

FINAL

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DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes .

DESIGN NOTE:
Example text for critical
design notes.

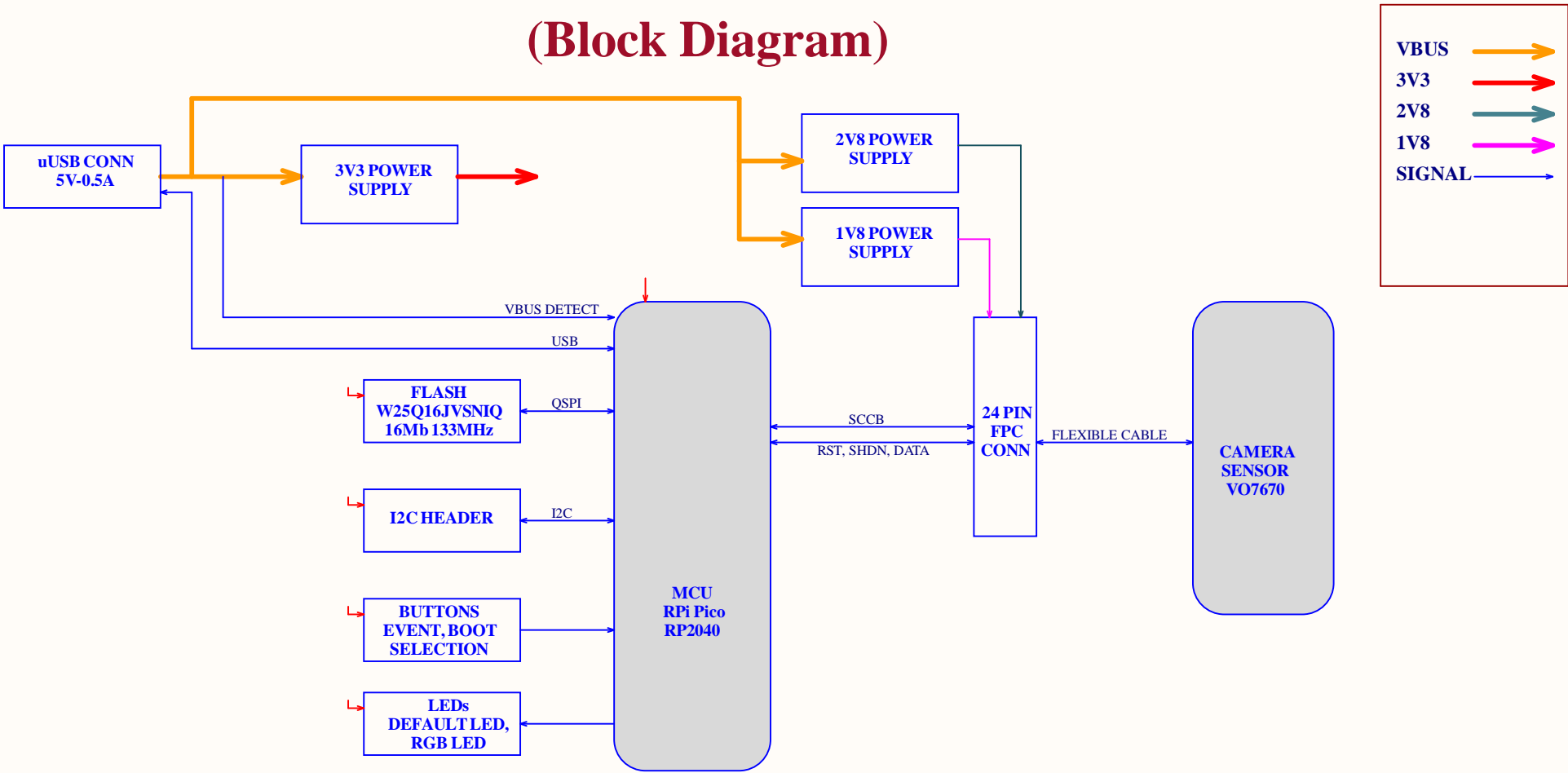
DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

Title: [01] - COVER PAGE.SchDoc					ITR VN Corp. Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam	
Size: A4	Number: 1	Revision: v1.0	Designer:	Name		
Date: 9/20/2023	Time: 11:30:12 AM	Sheet 1 of 7	Reviewer:	Name		
File: [01] - COVER PAGE.SchDoc			Approved:	Name		

Pico Camera PCB v1.0.PrjPcb

(Block Diagram)



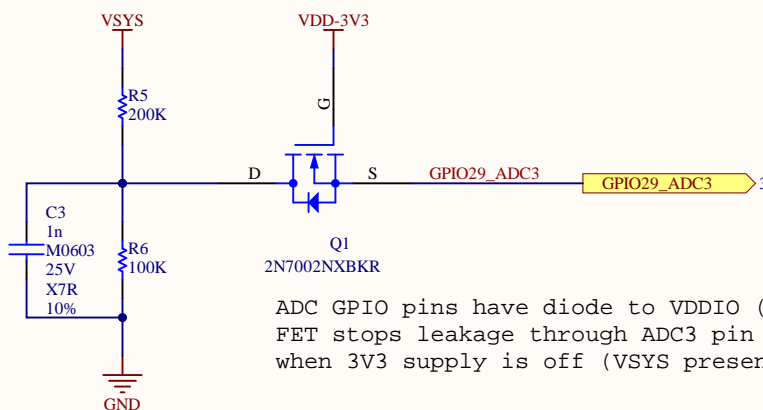
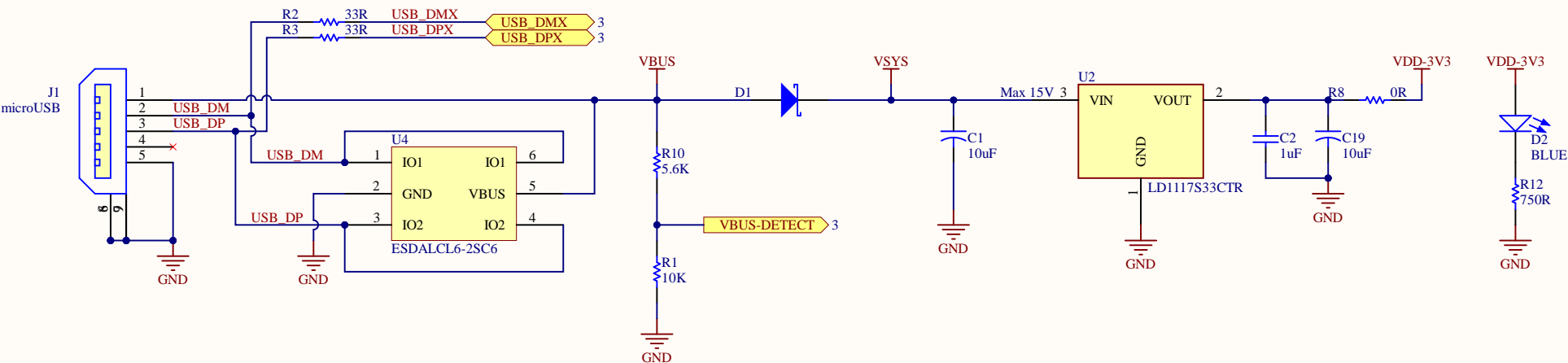
Title: [02] - BLOCK DIAGRAM.SchDoc					ITR VN Corp. Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam	
Size: A4	Number: 2	Revision: v1.0	Designer:	Name		
Date: 9/20/2023	Time: 11:30:12 AM	Sheet 2 of 7	Reviewer:	Name		
File: [02] - BLOCK DIAGRAM.SchDoc			Approved:	Name		

B

D

ITR VN Corp.
Room 904, Saigon Paragon Building,
Tan Phu Ward, District 7,
Ho Chi Minh City, 700000,
Vietnam

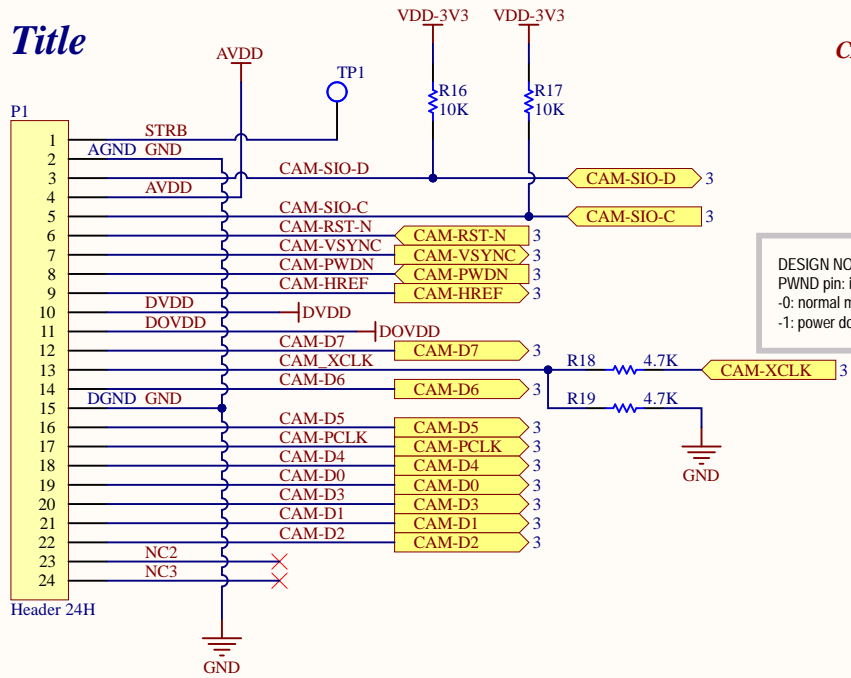
POWER SUPPLY



ADC GPIO pins have diode to VDDIO (other GPIO do not)
FET stops leakage through ADC3 pin diode into 3V3 net
when 3V3 supply is off (VSYS present but 3V3_EN low)

Title: [04] - POWER SUPPLY.SchDoc					ITR VN Corp.	
Size: A4	Number: 4	Revision: v1.0	Designer:	Name	Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam	
Date: 9/20/2023	Time: 11:30:12 AM	Sheet 4 of 7	Reviewer:	Name		
File: [04] - POWER SUPPLY.SchDoc			Approved:	Name		

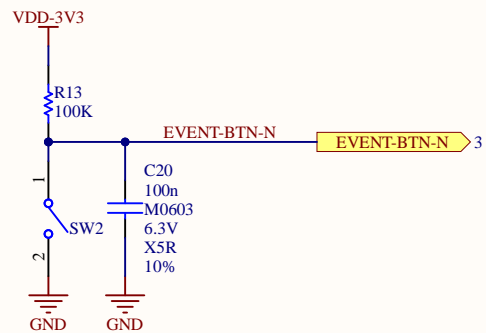
Title



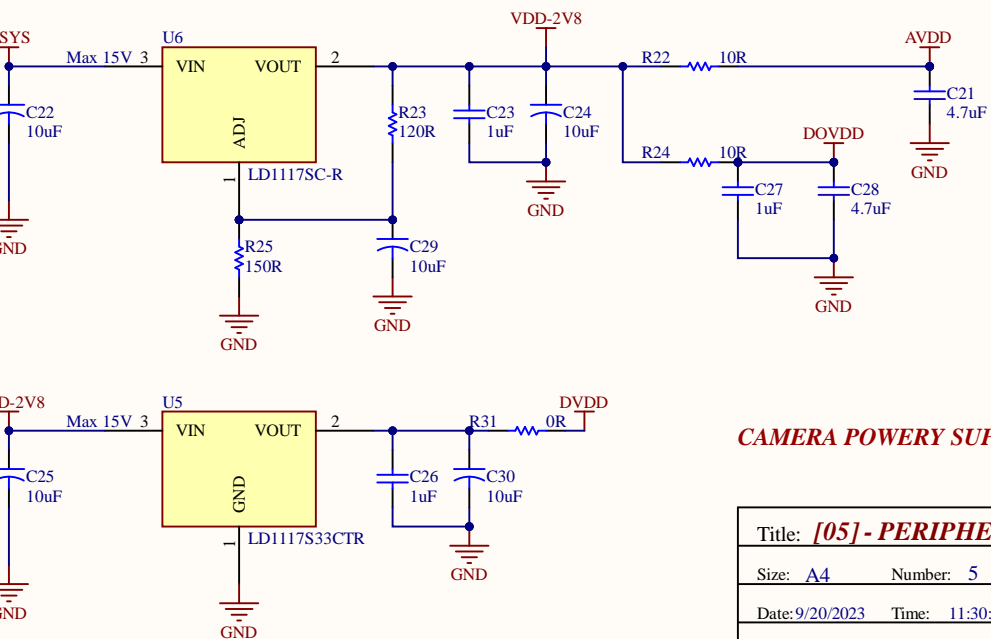
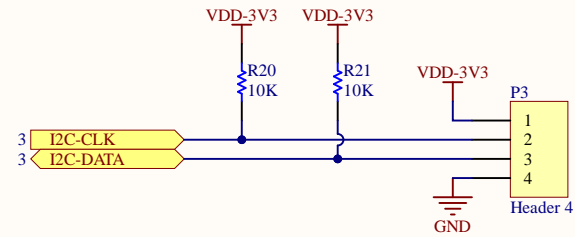
CAMERA SENSOR

DESIGN NOTE:
PWND pin: internal pull-down
-0: normal mode
-1: power down mode

EVENT BUTTON

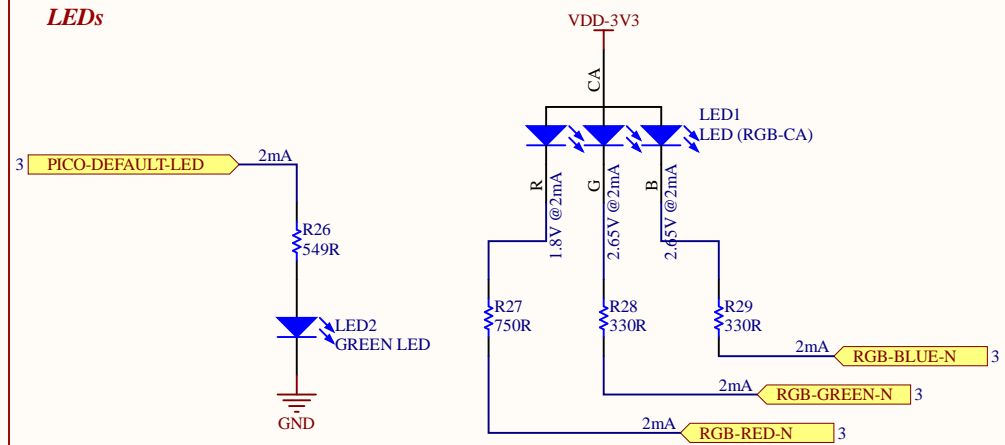


I2C HEADER



CAMERA POWERY SUPPLY

LEDs



Title: [05] - PERIPHERAL.SchDoc				ITR VN Corp.			
Size: A4	Number: 5	Revision: v1.0	Designer:	Name			
Date: 9/20/2023	Time: 11:30:12 AM	Sheet 5 of 7	Reviewer:	Name			
File: [05] - PERIPHERAL.SchDoc				Approved:	Name		

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Vietnam

DOC: REVISION HISTORY

Title: [06] - DOC REVISION HISTORY.SchDoc			<i>ITR VN Corp.</i>	
Size: A4	Number: 6	Revision: v1.0	Designer:	Name
Date: 9/20/2023	Time: 11:30:12 AM	Sheet 6 of 7	Reviewer:	Name
File: [06] - DOC REVISION HISTORY.SchDoc			Approved:	Name

	1	2	3	4							
A	<div>Designator [01] - COVER PAGE.SchDoc</div> <div></div>	<div>Designator [02] - BLOCK DIAGRAM.SchDoc</div> <div></div>	<div>Designator [03] - MCU - RP2040.SchDoc</div> <div></div>	<div>Designator [04] - POWER SUPPLY.SchDoc</div> <div></div>	<div>Designator [05] - PERIPHERAL.SchDoc</div> <div></div>	A					
B					<div>Designator [06] - DOC REVISION HISTORY.SchDoc</div> <div></div>	B					
C						C					
D	<div>Schematic Status Explanation</div> <div>DRAFT - Very early stage of schematic, ignore details.</div> <div>PRELIMINARY - Close to final schematic.</div> <div>CHECKED - There should not be any mistakes. Tell the engineer if you find one.</div> <div>RELEASED - A board with this schematic has been sent to production.</div>				<table><tr><td>Title: <i>Pico Camera PCB v1.0 Project.SchDoc</i></td><td rowspan="4"><i>ITR VN Corp.</i> <i>Room 904, Saigon Paragon Building,</i> <i>Tan Phu Ward, District 7,</i> <i>Ho Chi Minh City, 700000,</i> <i>Vietnam</i></td></tr><tr><td>Size: <u>A4</u> Number: <u>7</u> Revision: <u>v1.0</u></td></tr><tr><td>Date:<u>9/20/2023</u> Time: <u>11:30:12 AM</u> Sheet <u>7</u> of <u>7</u></td></tr><tr><td>File: <u>Pico Camera PCB v1.0 Project.SchDoc</u></td></tr></table>	Title: <i>Pico Camera PCB v1.0 Project.SchDoc</i>	<i>ITR VN Corp.</i> <i>Room 904, Saigon Paragon Building,</i> <i>Tan Phu Ward, District 7,</i> <i>Ho Chi Minh City, 700000,</i> <i>Vietnam</i>	Size: <u>A4</u> Number: <u>7</u> Revision: <u>v1.0</u>	Date: <u>9/20/2023</u> Time: <u>11:30:12 AM</u> Sheet <u>7</u> of <u>7</u>	File: <u>Pico Camera PCB v1.0 Project.SchDoc</u>	D
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