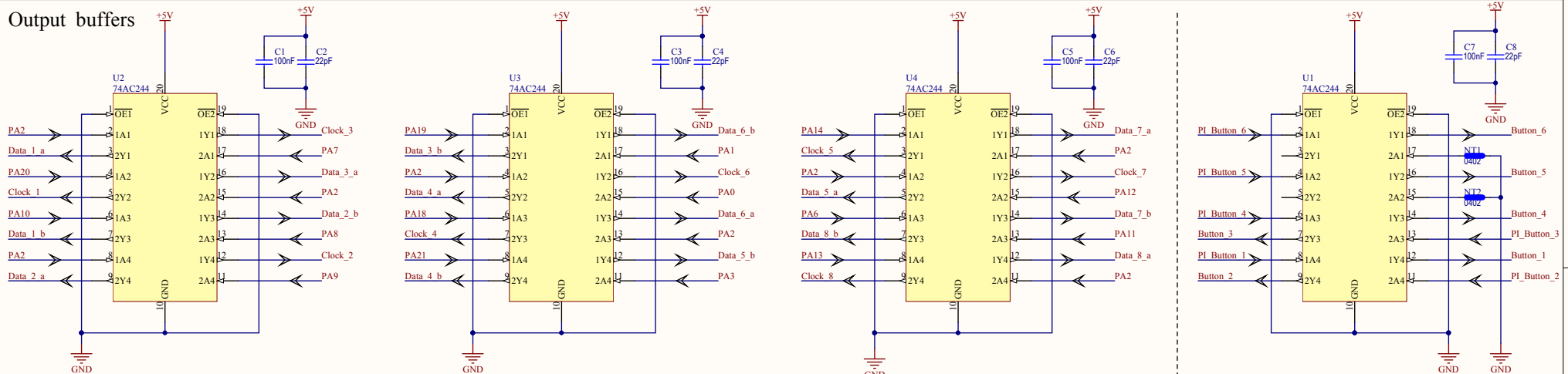
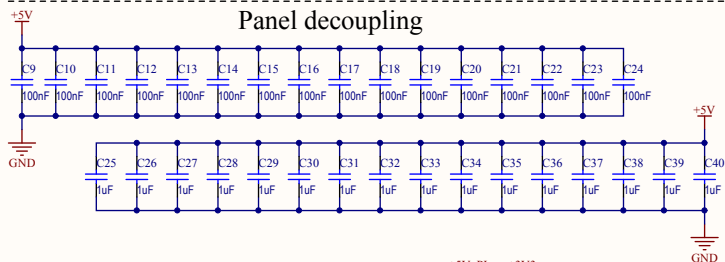


Output buffers



Panel decoupling



Panel Pin assignments

#	Pin	Panel #	LED line
1	PA0	Panel 4	Data 4 a
2	PA1	Panel 3	Data 3 b
3	PA2	all	Clock
4	PA3	Panel 4	Data 4 b
5	PA6	Panel 7	Data 7 b
6	PA7	Panel 1	Data 1 a
7	PA8	Panel 1	Data 1 b
8	PA9	Panel 2	Data 2 a
9	PA10	Panel 2	Data 2 b
10	PA11	Panel 8	Data 8 b
11	PA12	Panel 5	Data 5 a
12	PA13	Panel 8	Data 8 a
13	PA14	Panel 7	Data 7 a
14	PA18	Panel 6	Data 6 a
15	PA19	Panel 6	Data 6 b
16	PA20	Panel 3	Data 3 a
17	PA21	Panel 5	Data 5 b

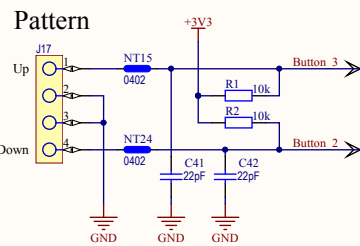
Button Pin assignments

1	PG9	Button 3	Pattern	Up
2	PG6	Button 2	Pattern	Down
3	PG7	Button 1	Speed	Up
4	PG8	Button 4	Speed	Down
5	PC7	Button 5	Brightness	Up
6	PC4	Button 6	Brightness	Down

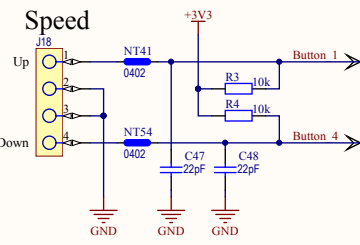
Not tracked to connector
PA4
PA5
PA16
PA15
PA17

INPUT CONNECTORS

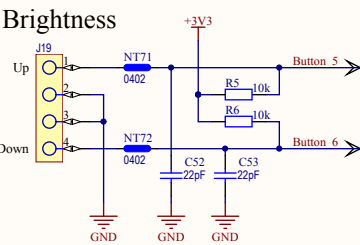
Pattern



Speed

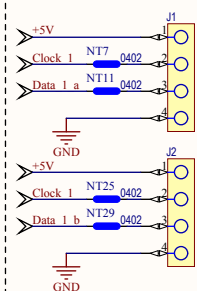


Brightness

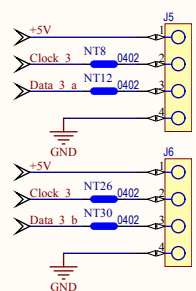


OUTPUT CONNECTORS

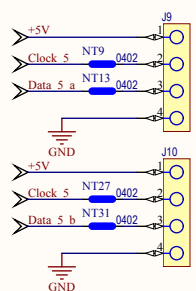
Panel 1



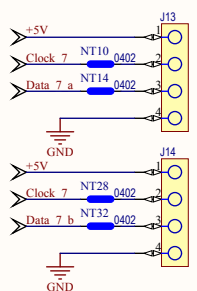
Panel 3



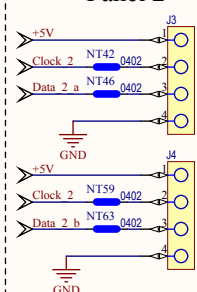
Panel 5



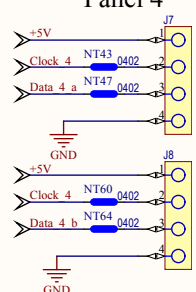
Panel 7



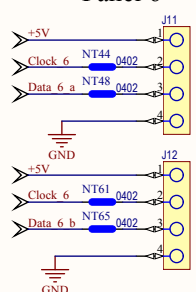
Panel 2



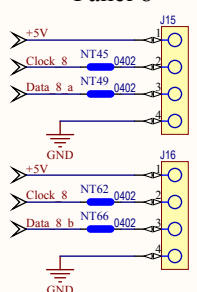
Panel 4



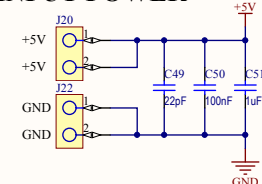
Panel 6



Panel 8



INPUT POWER



Title		
Size	Number	Revision
A3		
Date:	26/02/2017	Sheet of
File:	C:\Users\... \LED Panel Interface.SchDoc	Drawn By: