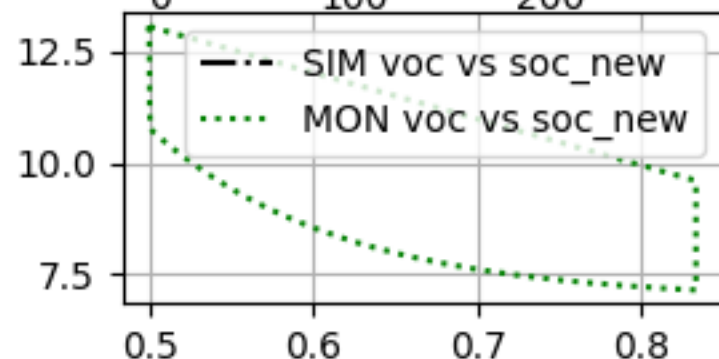
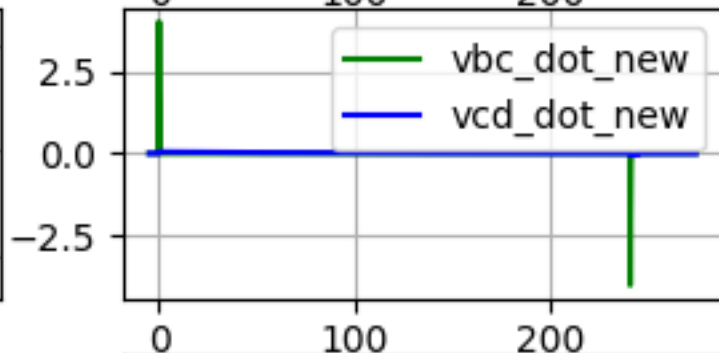
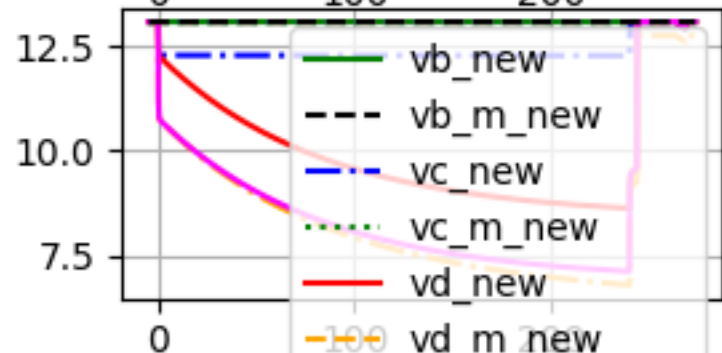
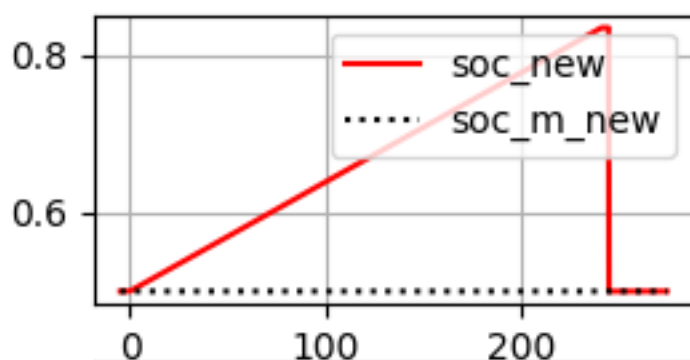
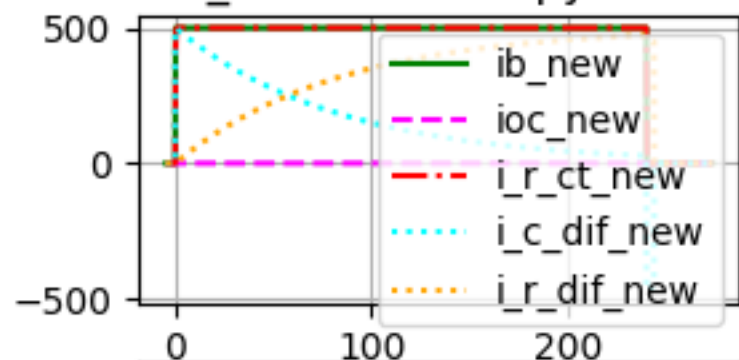
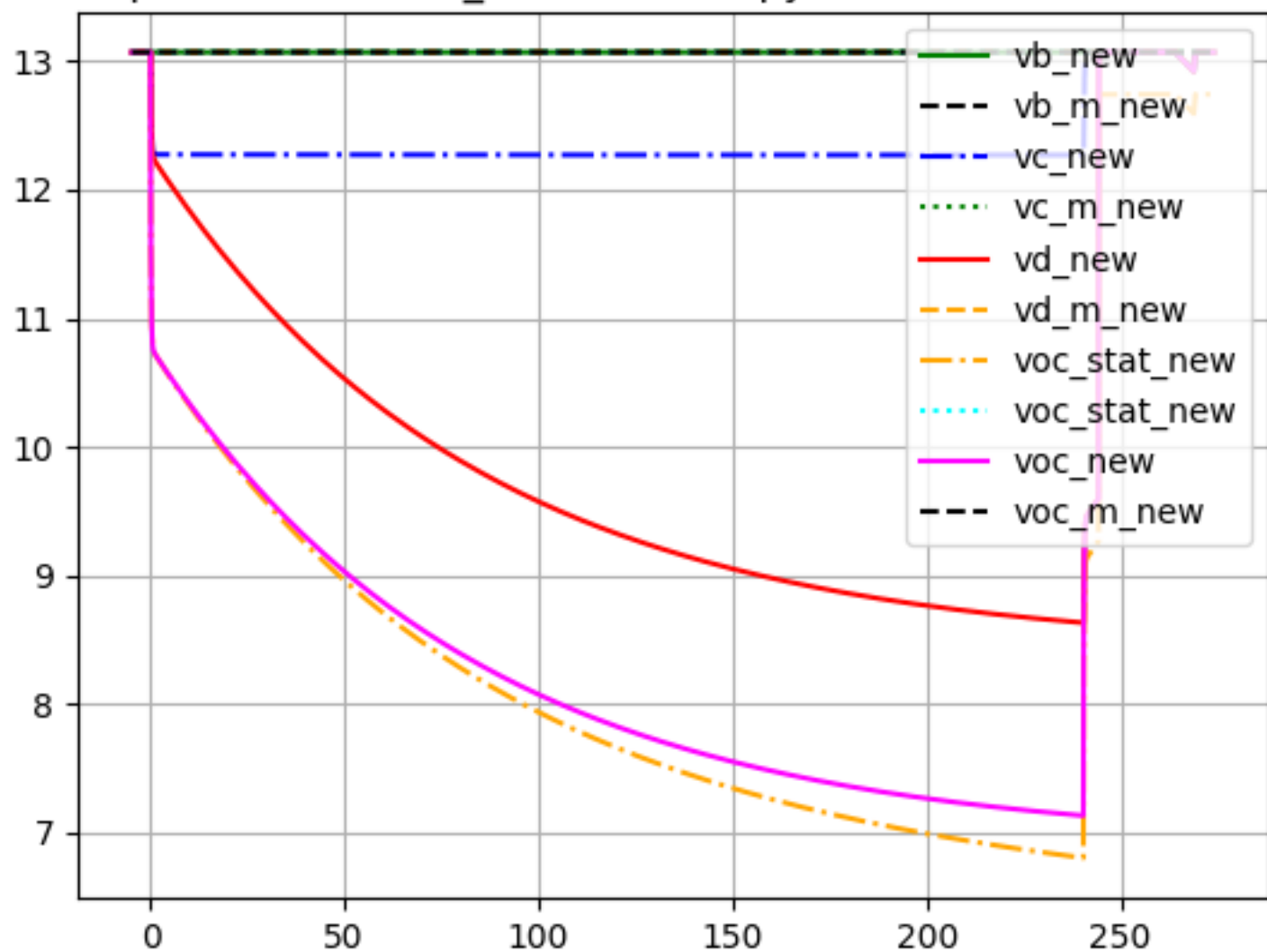


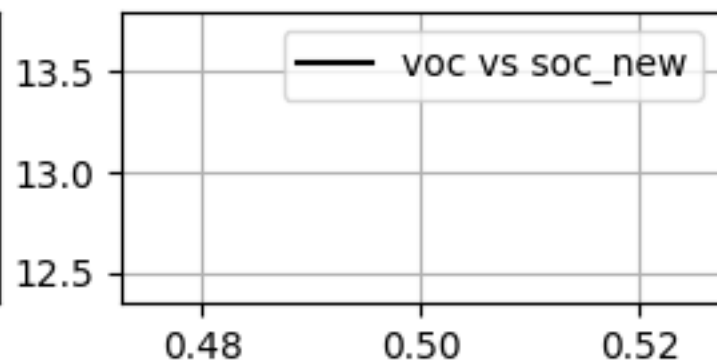
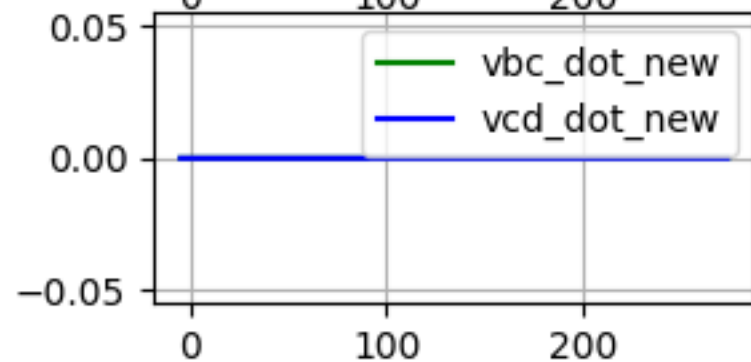
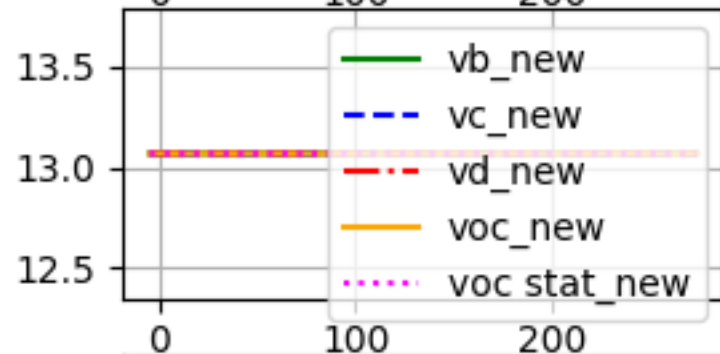
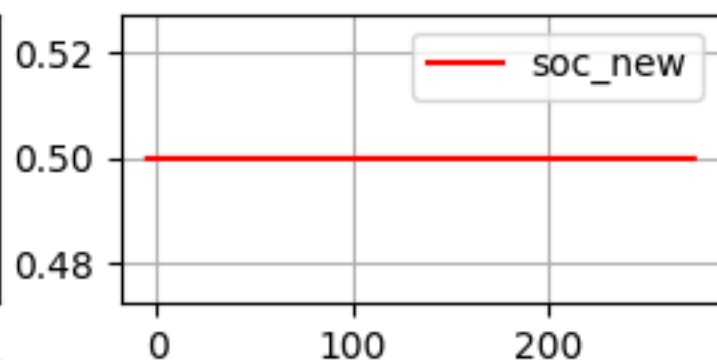
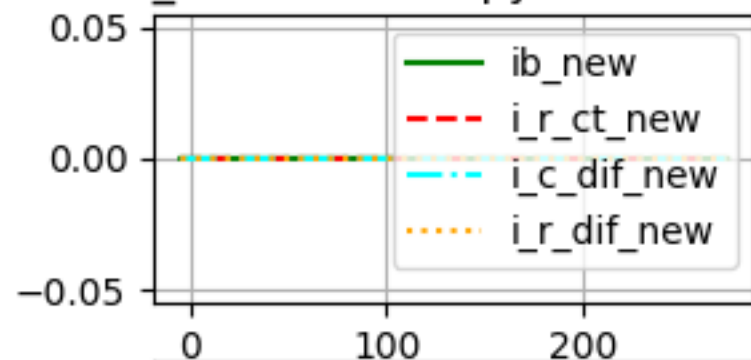
Fail20220804_mon-MonSim.py 2022-08-04T13-15-13



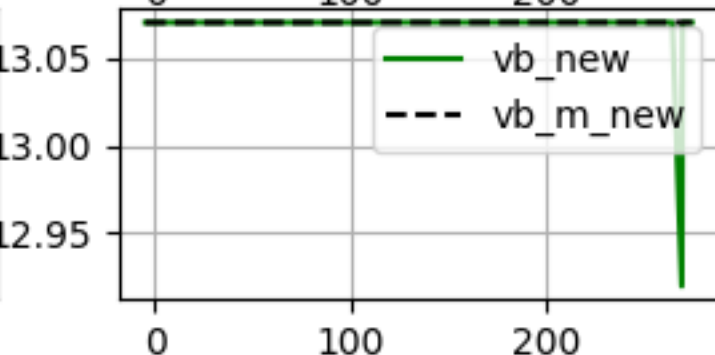
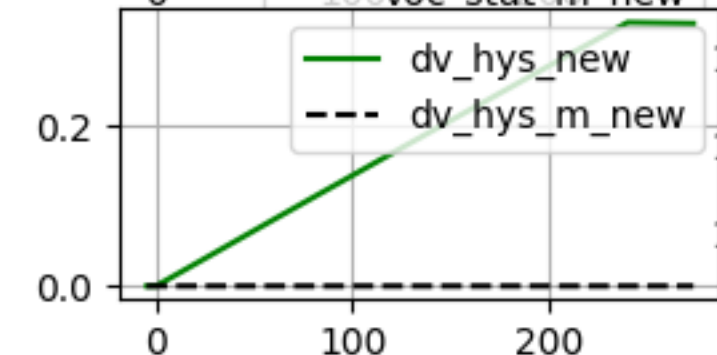
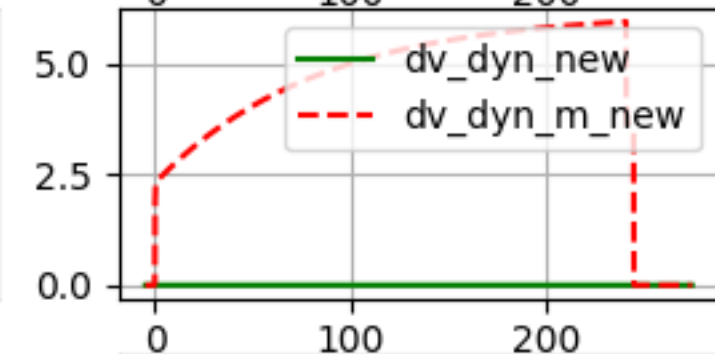
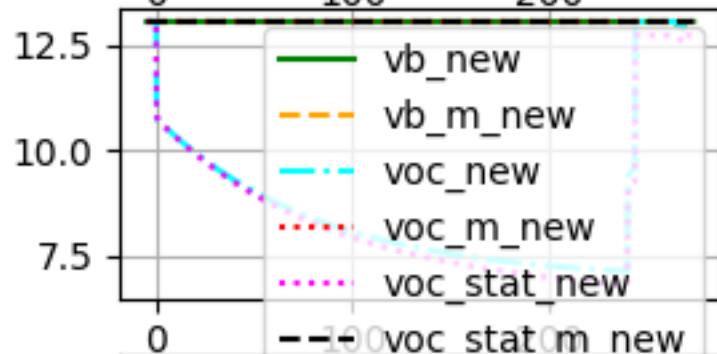
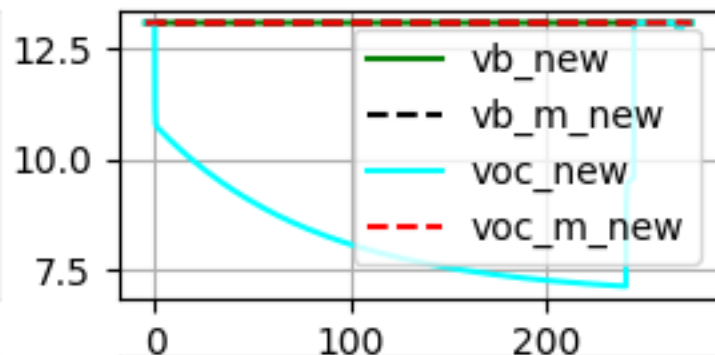
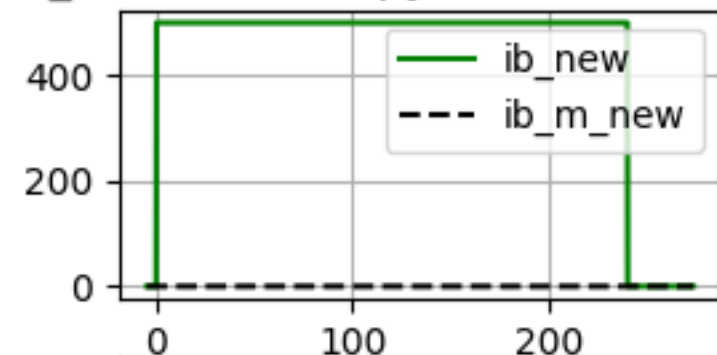
ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13



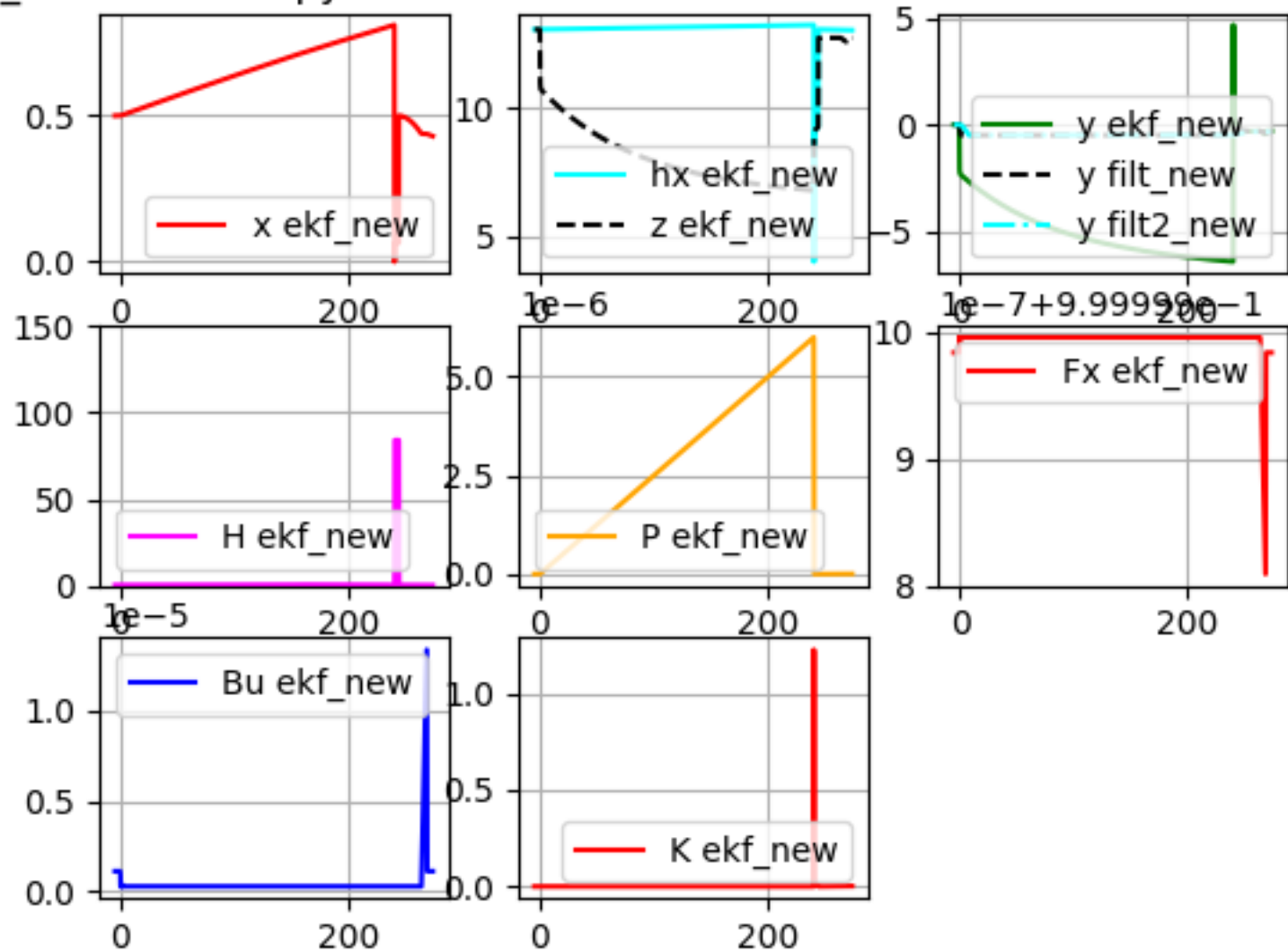
20220804_mon-MonSim.py 2022-08-04T13-15-13 **SIM



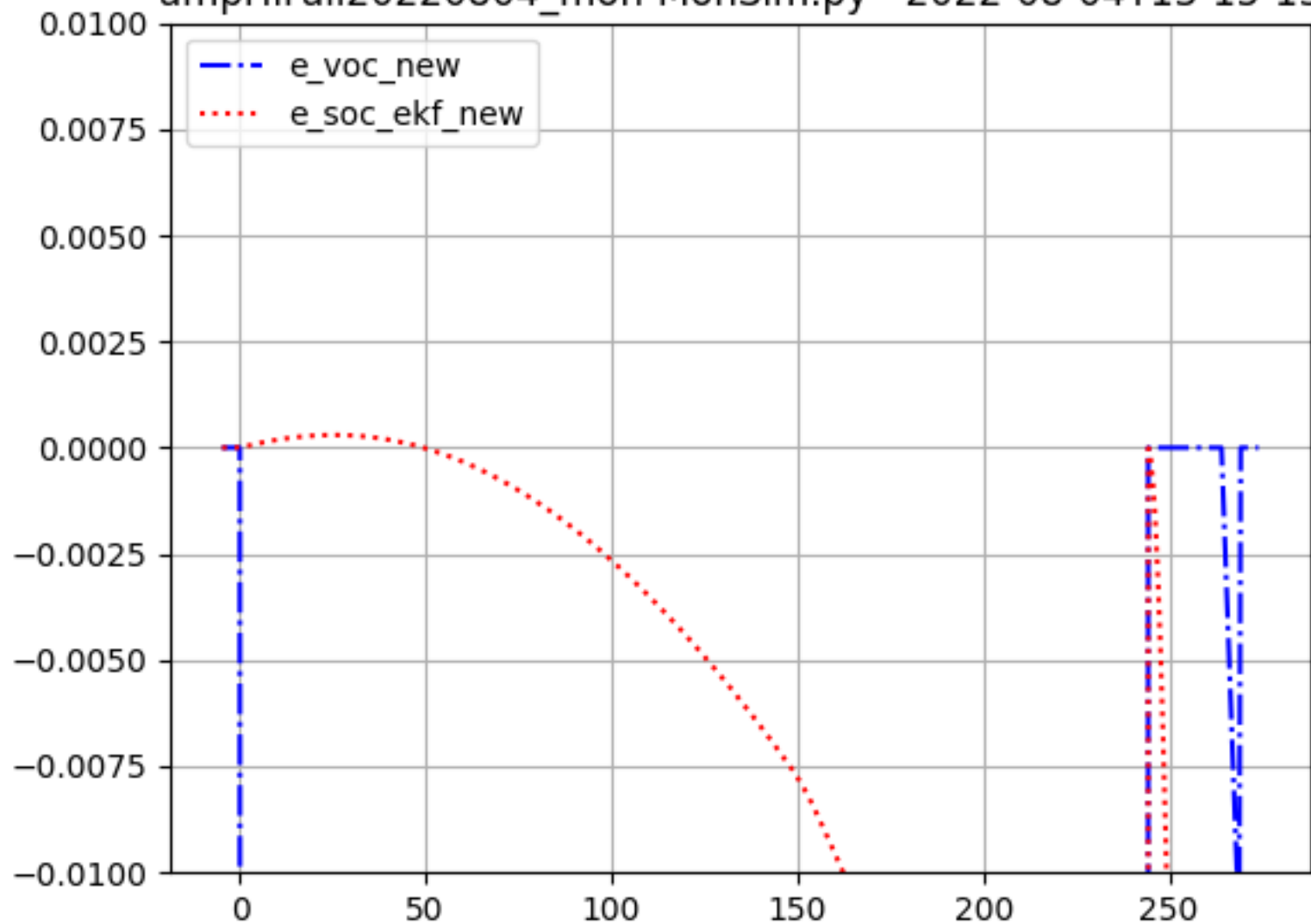
20804_mon-MonSim.py 2022-08-04T13-15-13 MON vs SIM



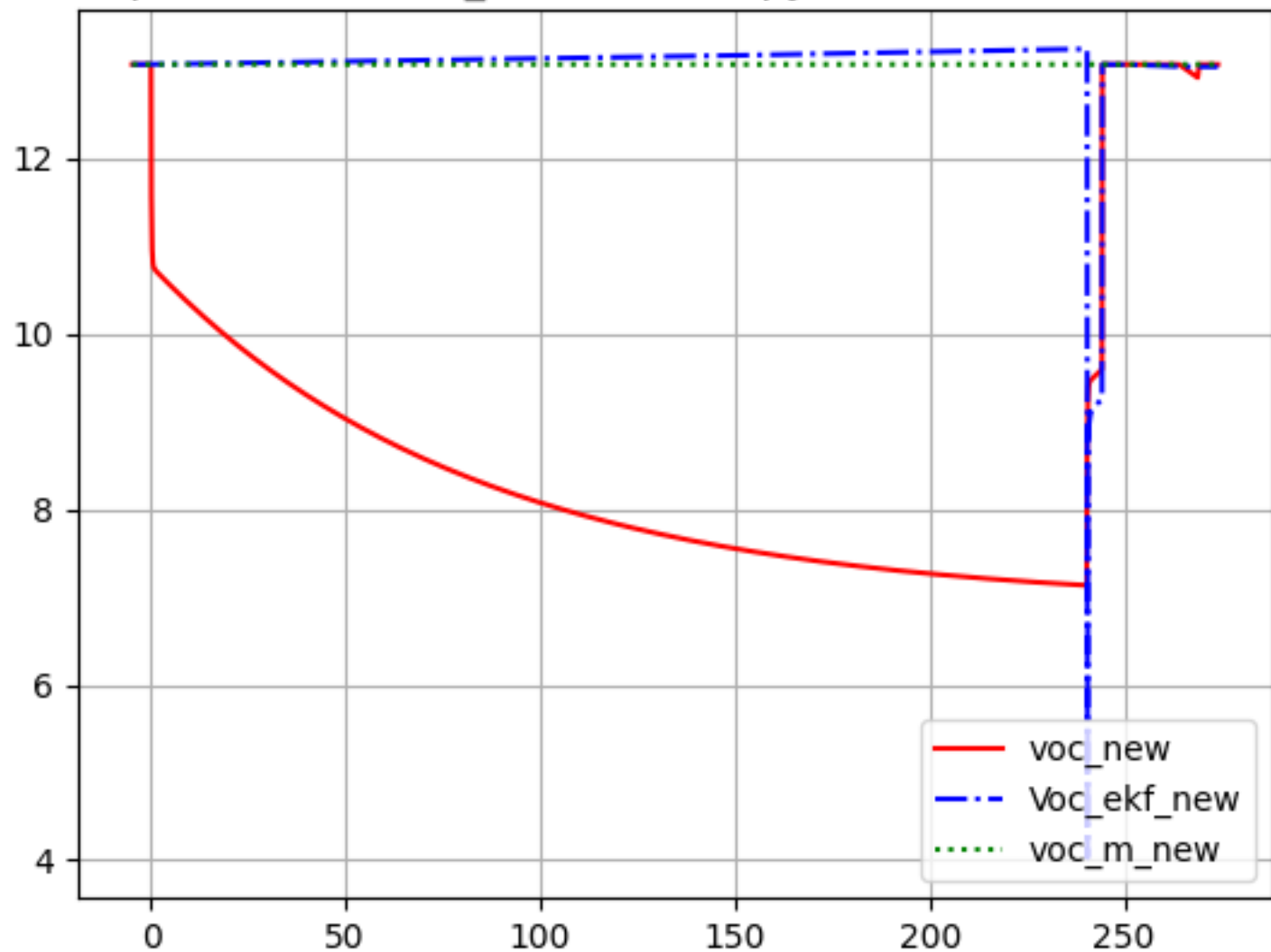
0804_mon-MonSim.py 2022-08-04T13-15-13 **EKF



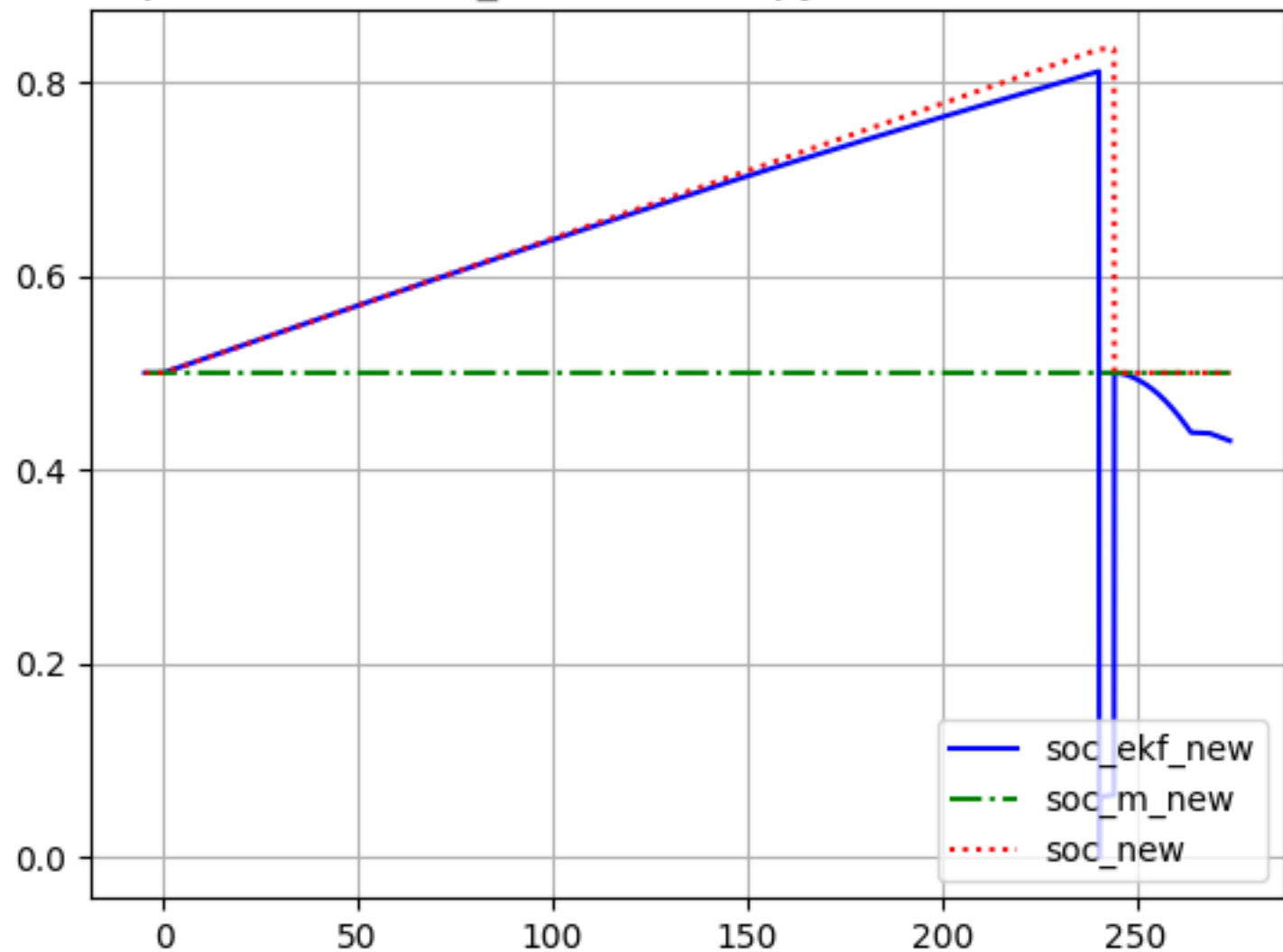
ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13



ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13



ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13



ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13

