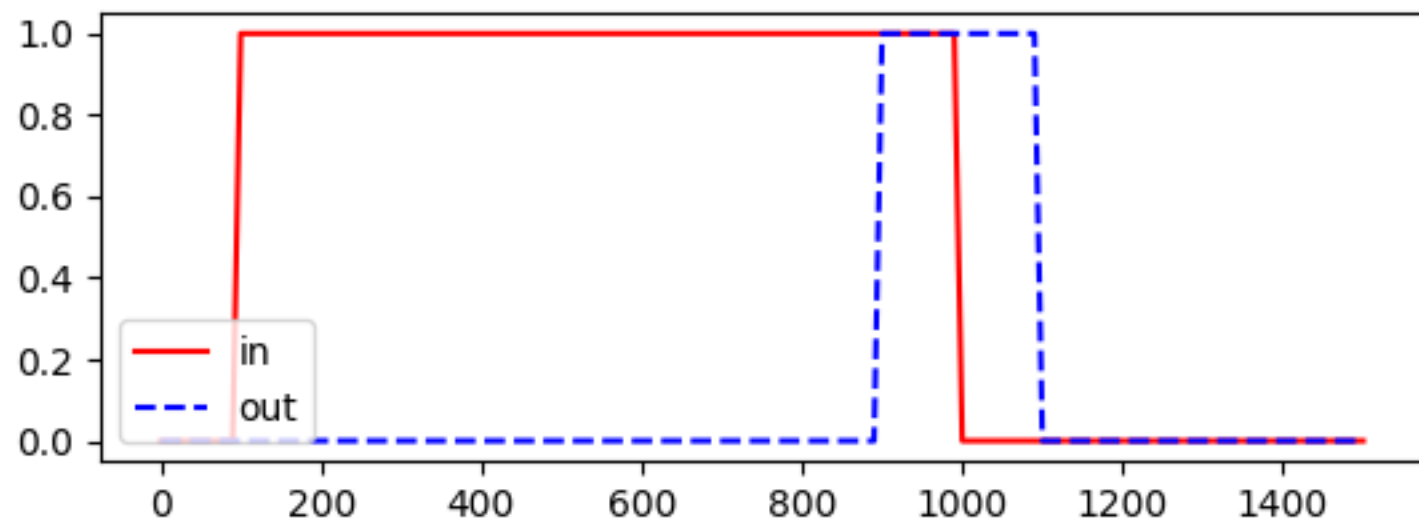
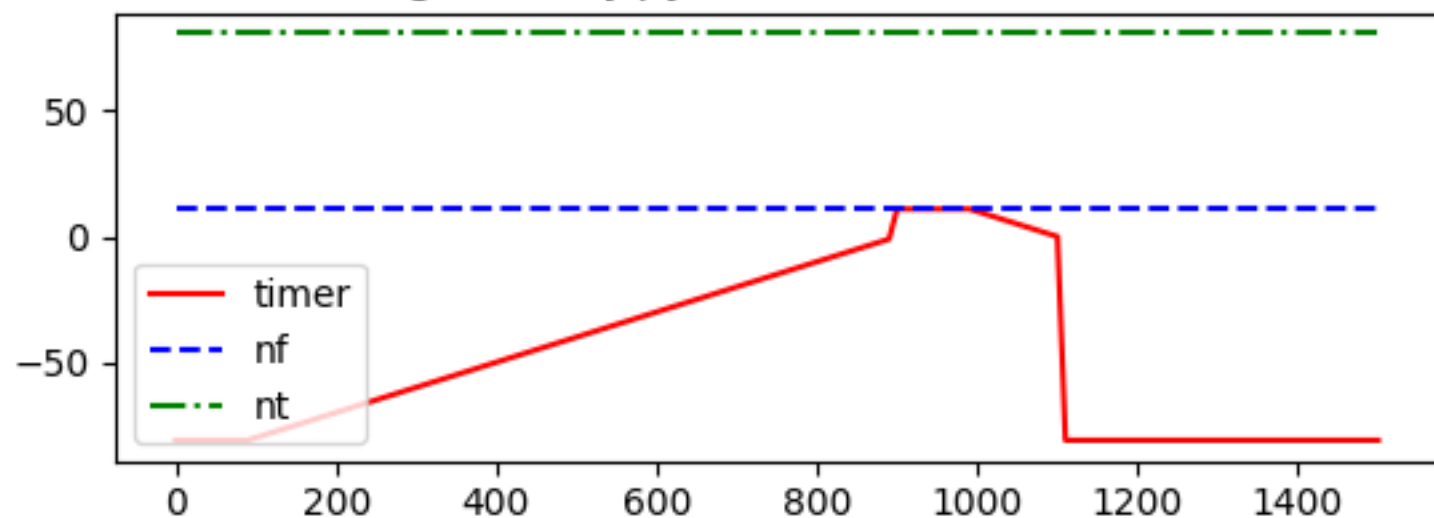
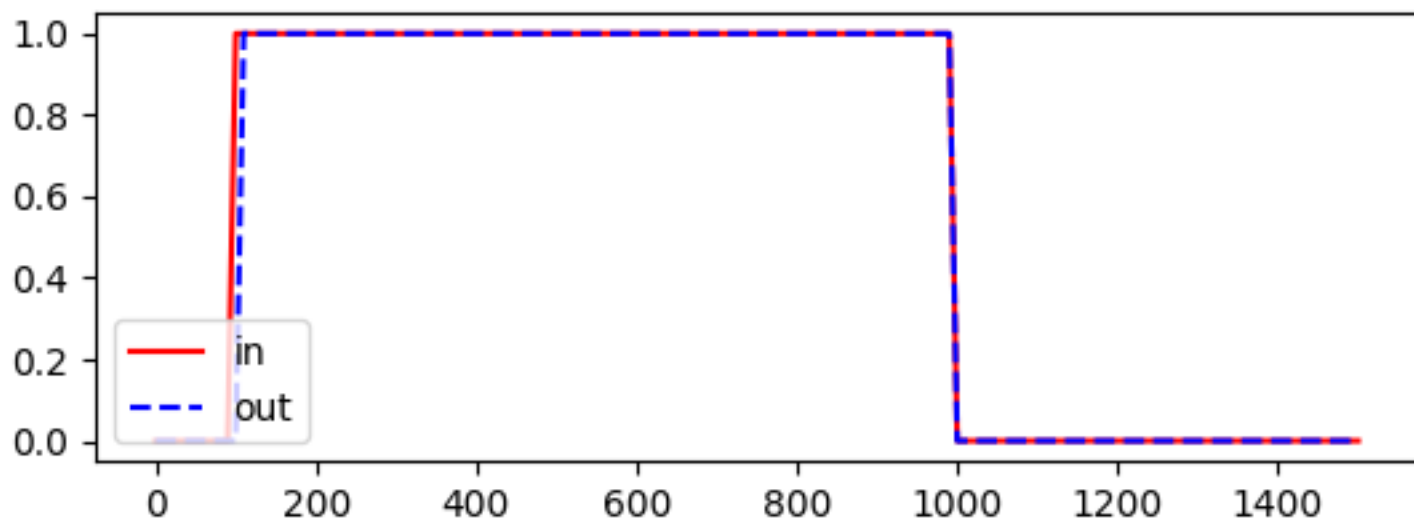
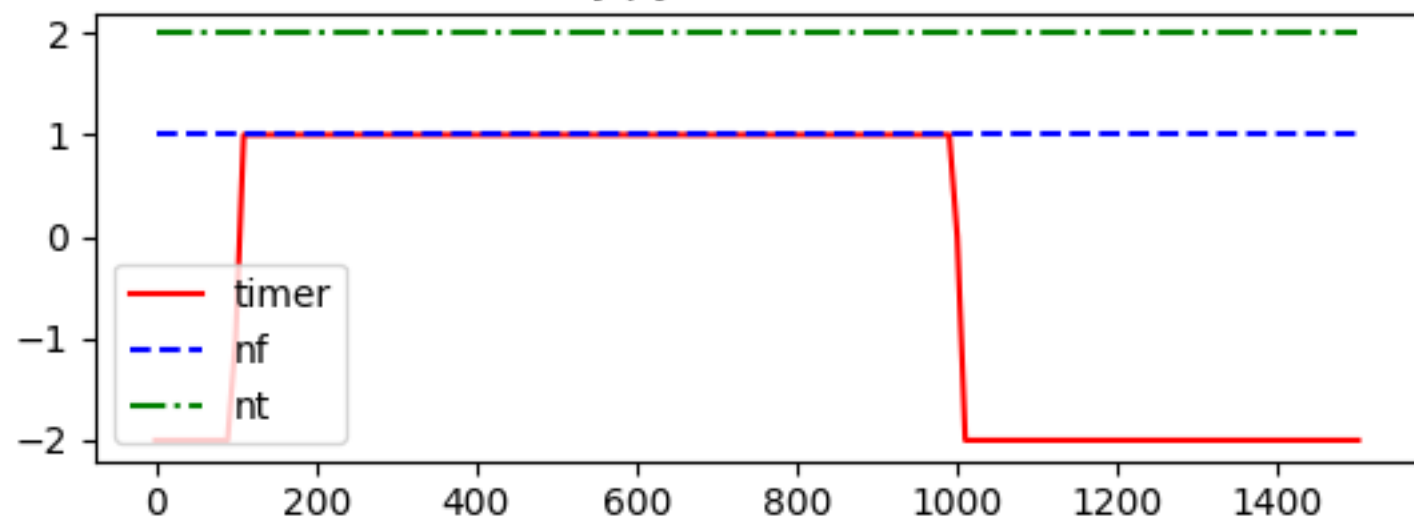


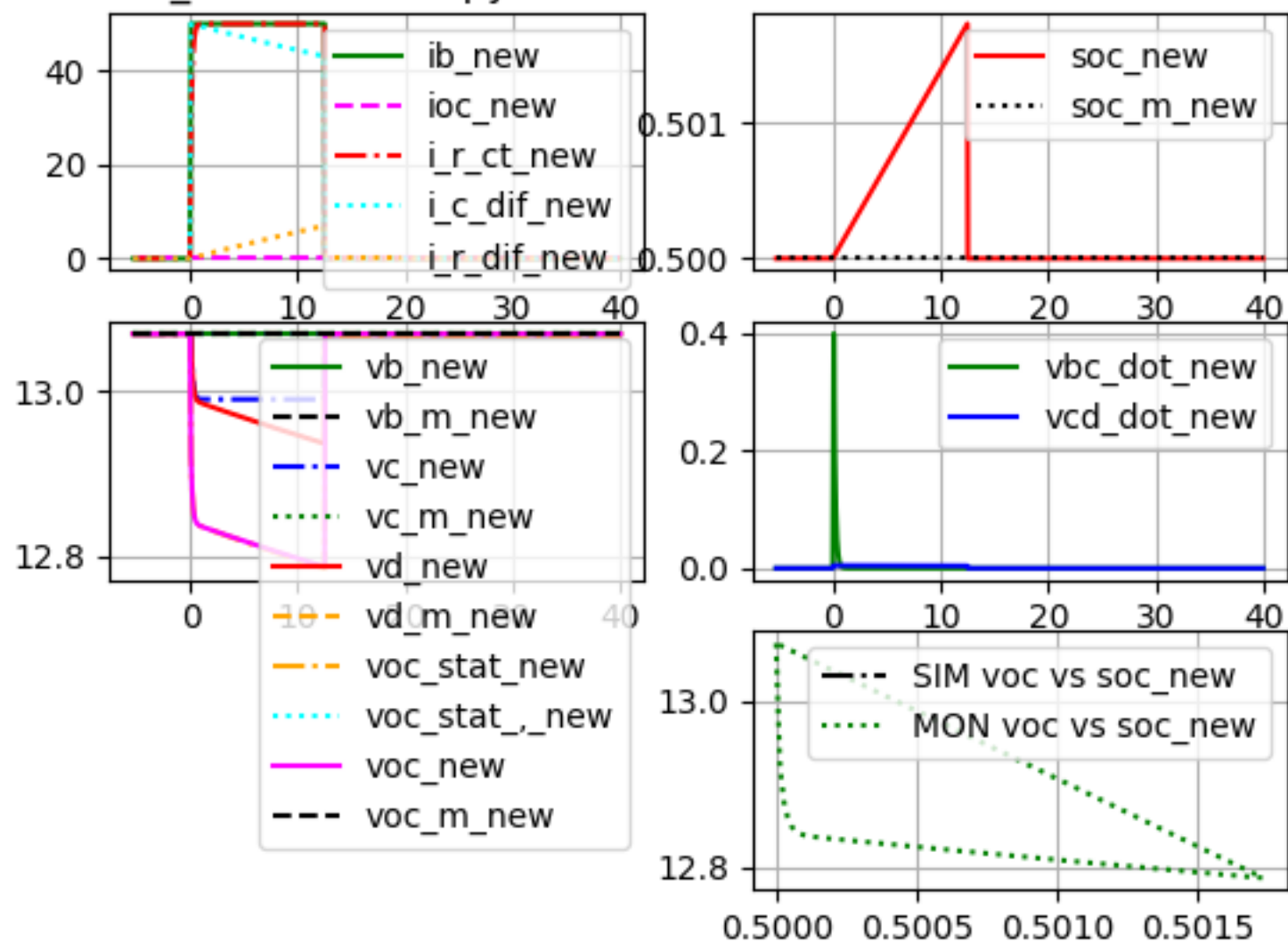
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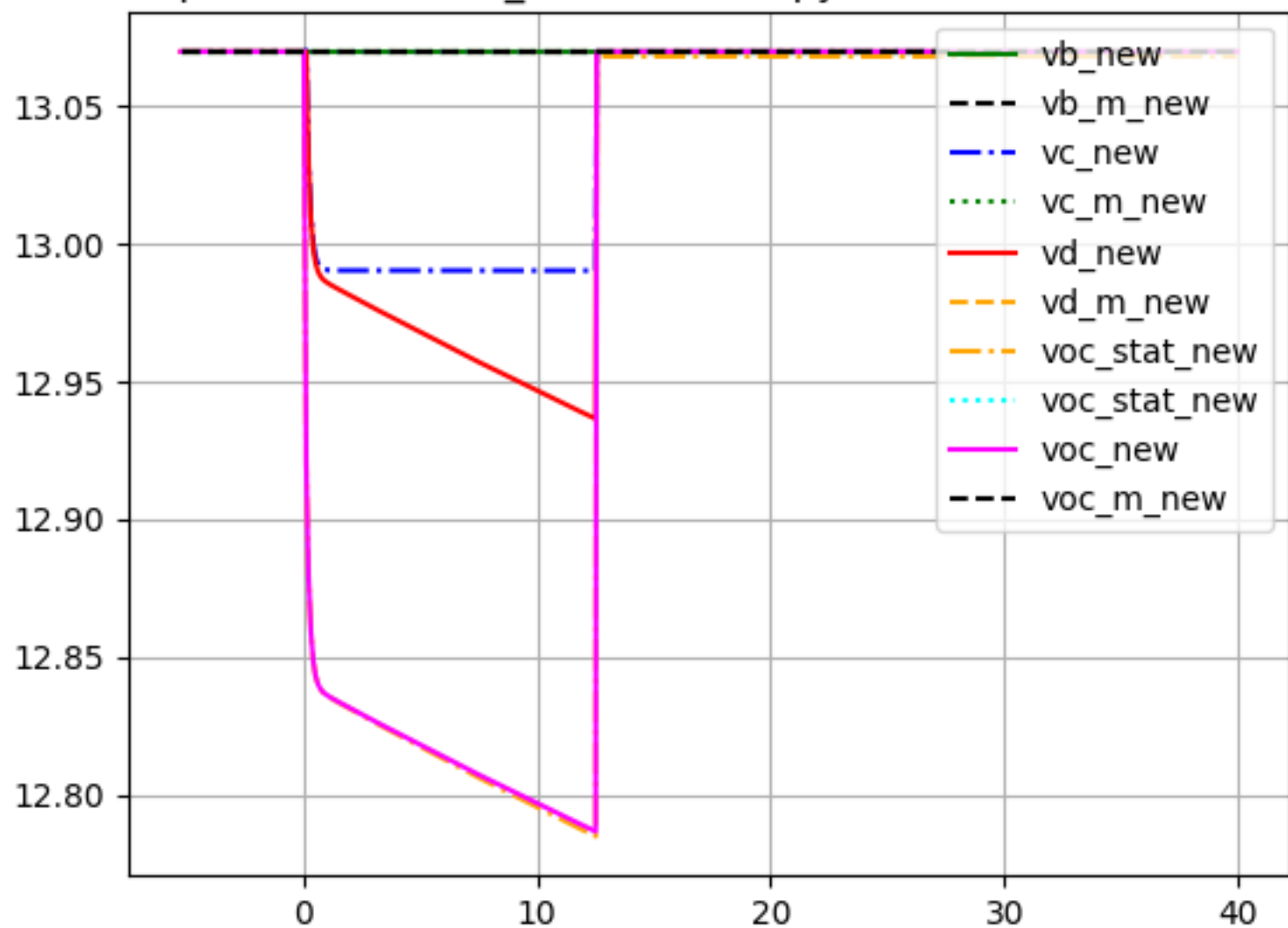
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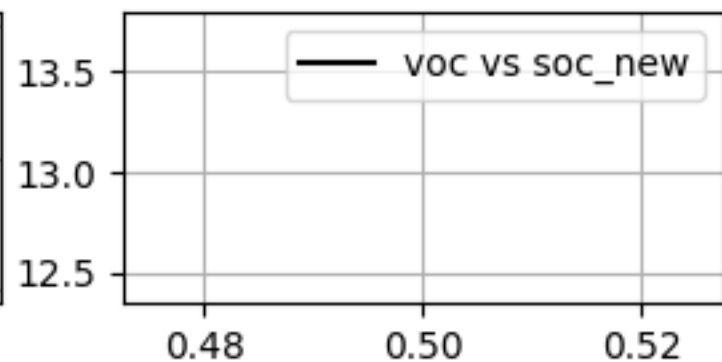
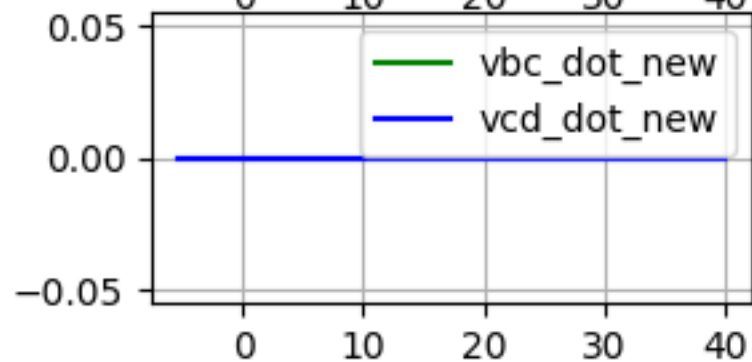
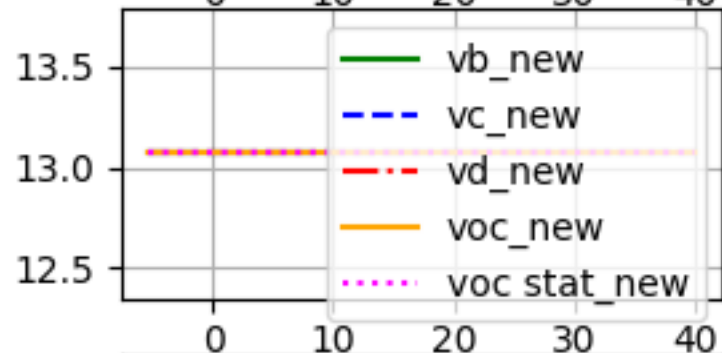
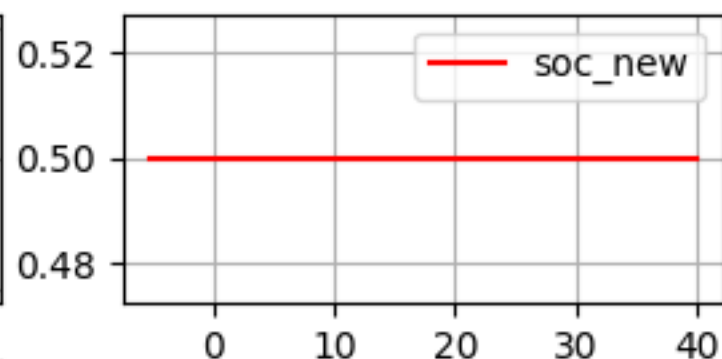
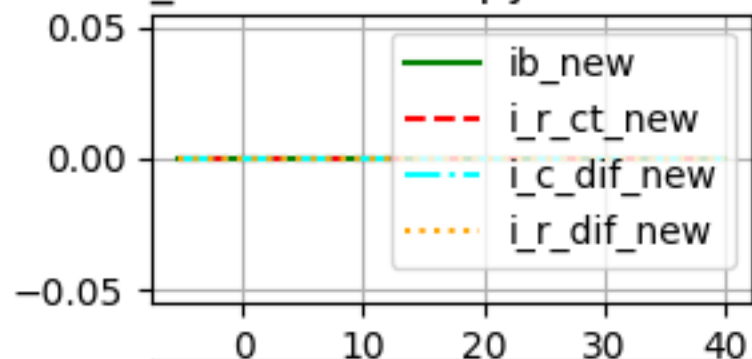
Fail20220819_mon-MonSim.py 2022-08-19T05-21-27



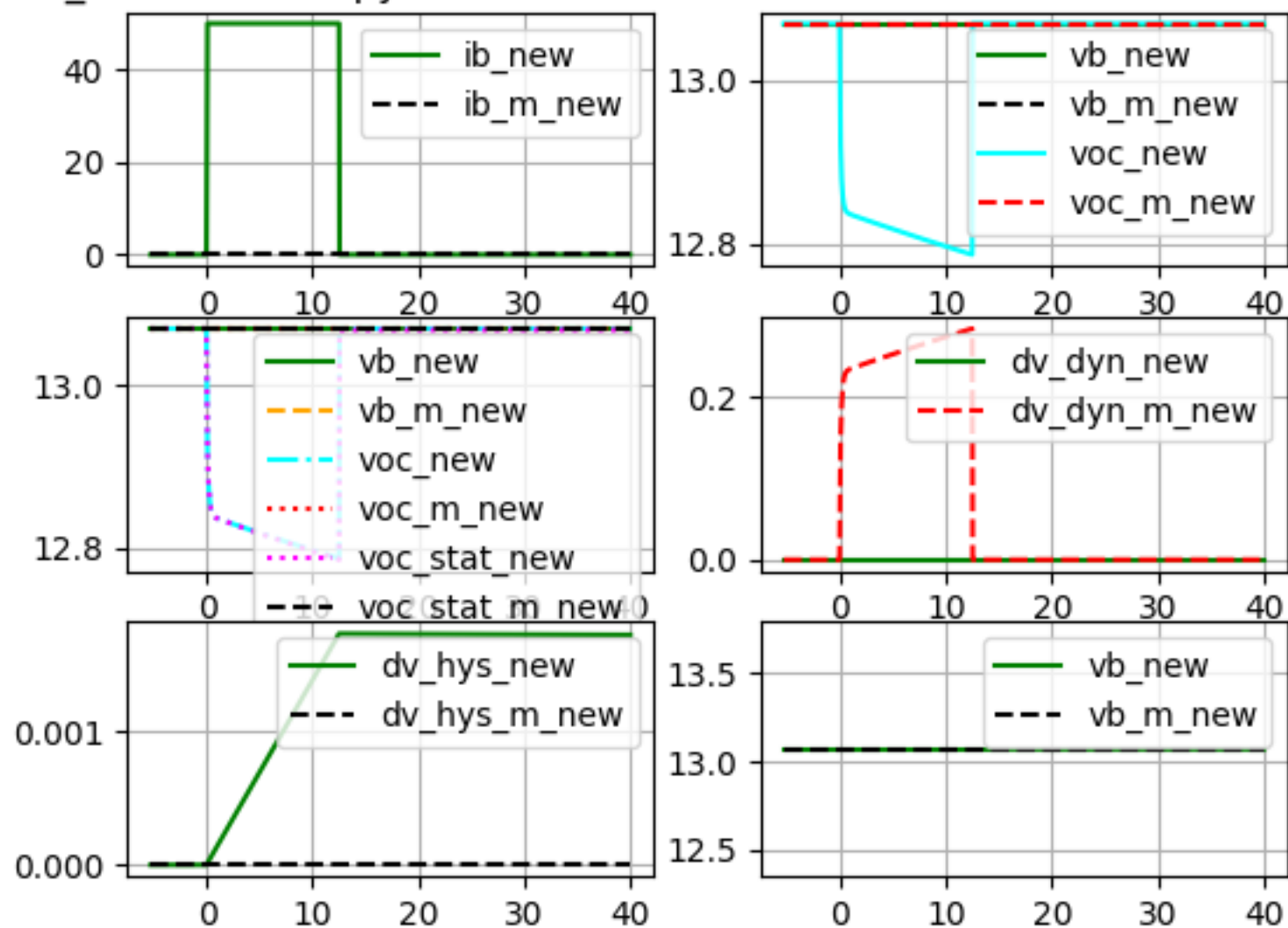
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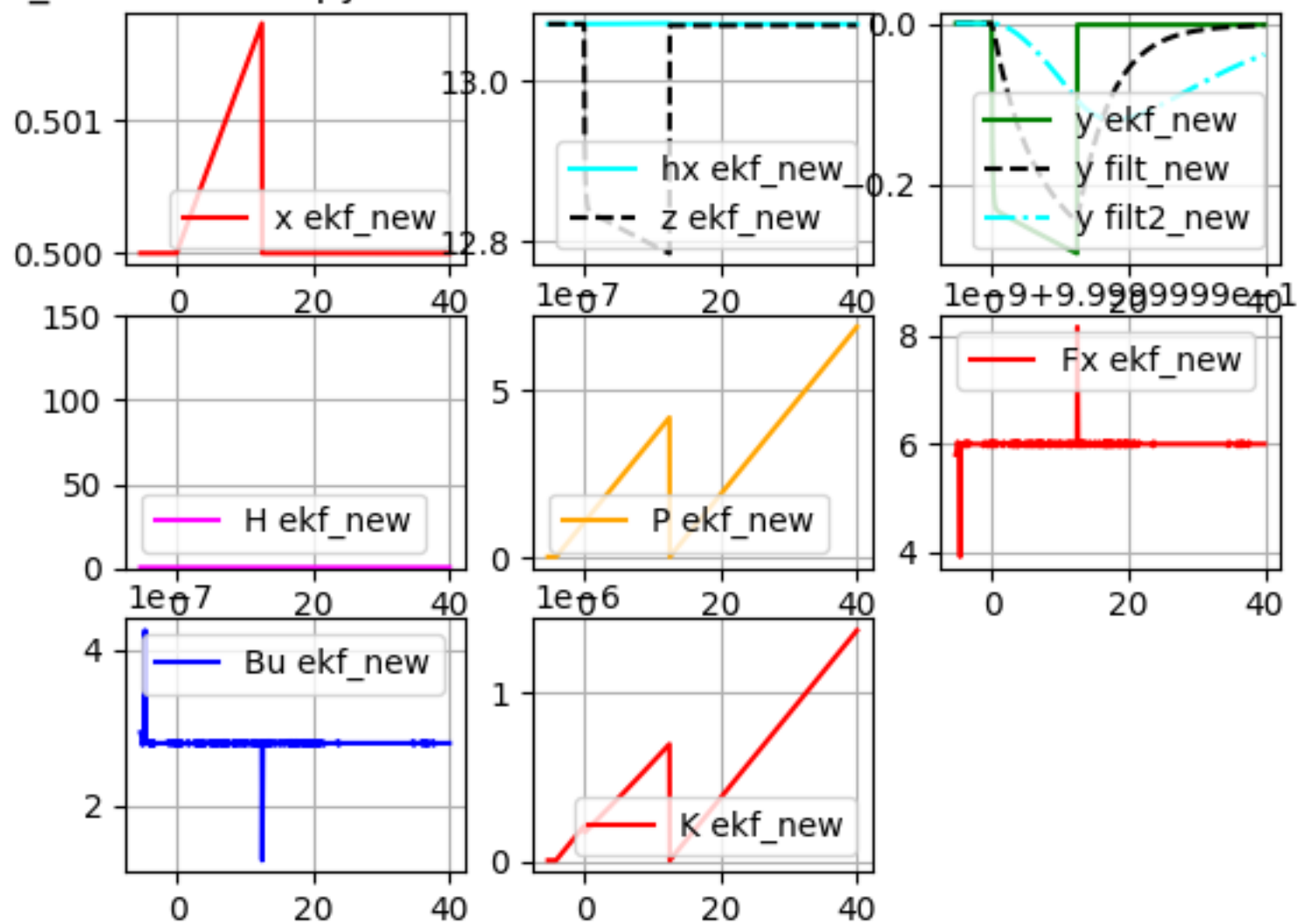
20220819_mon-MonSim.py 2022-08-19T05-21-27 **SIM



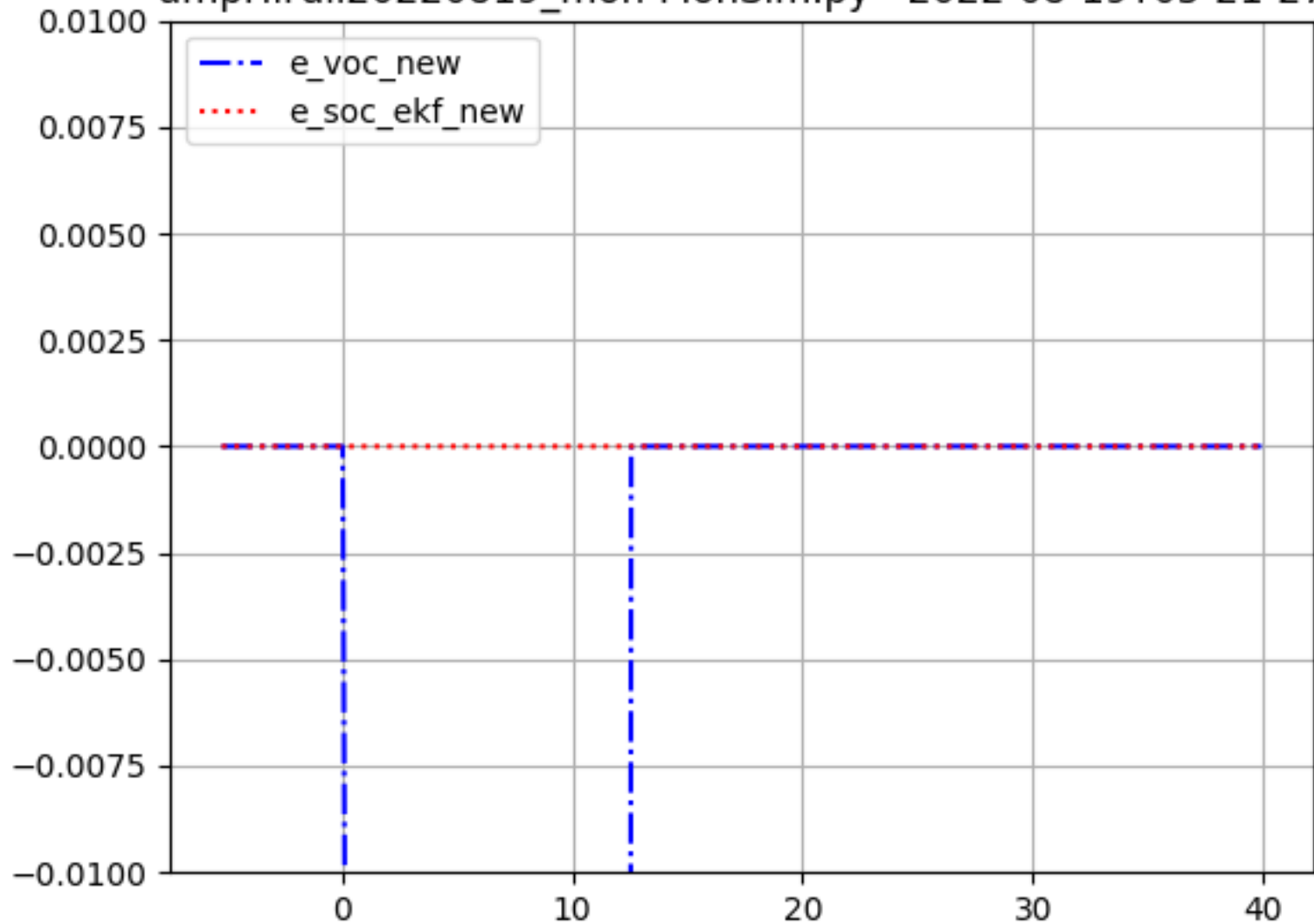
20819_mon-MonSim.py 2022-08-19T05-21-27 MON vs SIM



0819_mon-MonSim.py 2022-08-19T05-21-27 **EKF



ampHiFail20220819_mon-MonSim.py 2022-08-19T05-21-27



ampHiFail20220819_mon-MonSim.py 2022-08-19T05-21-27

