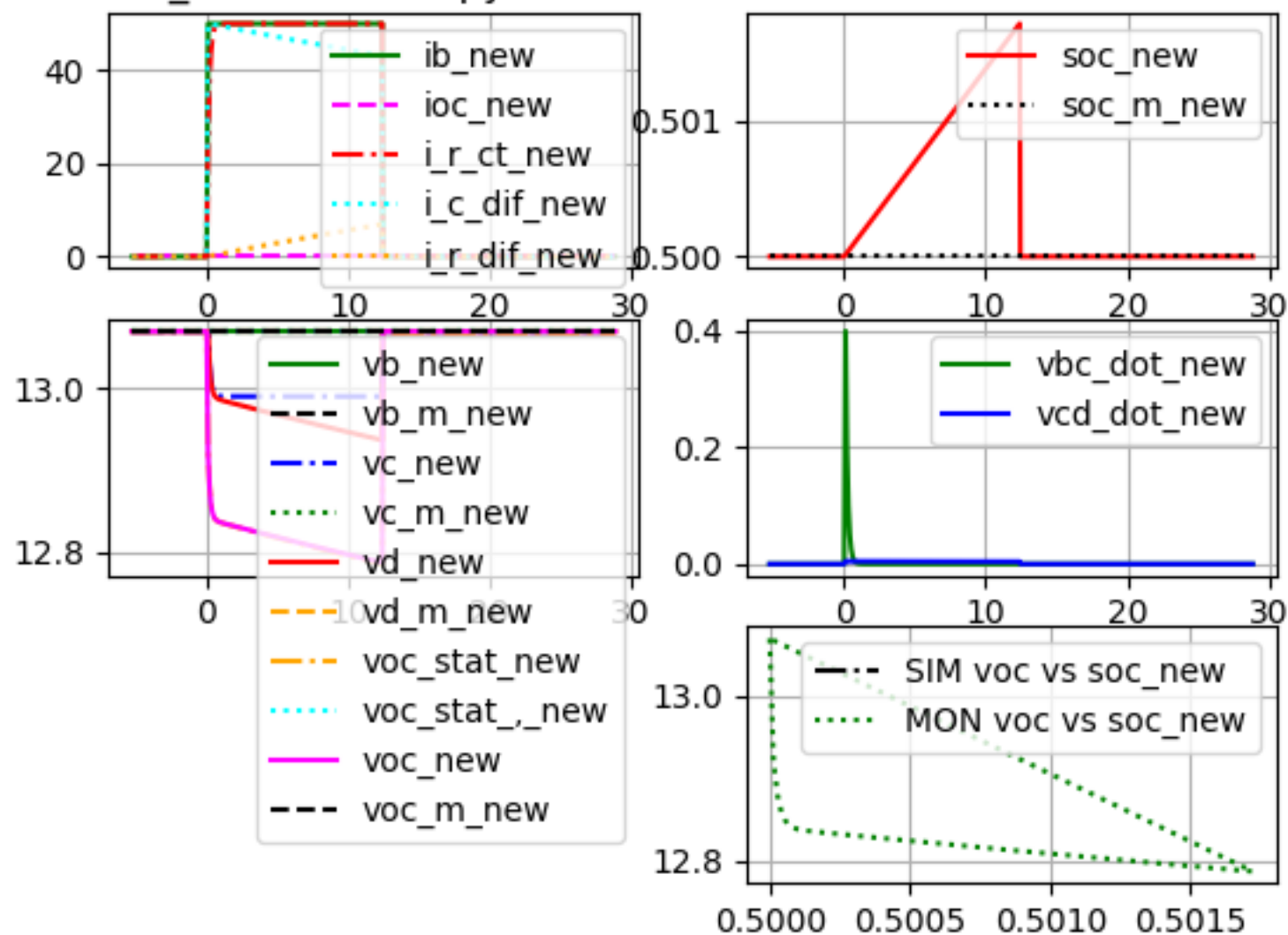
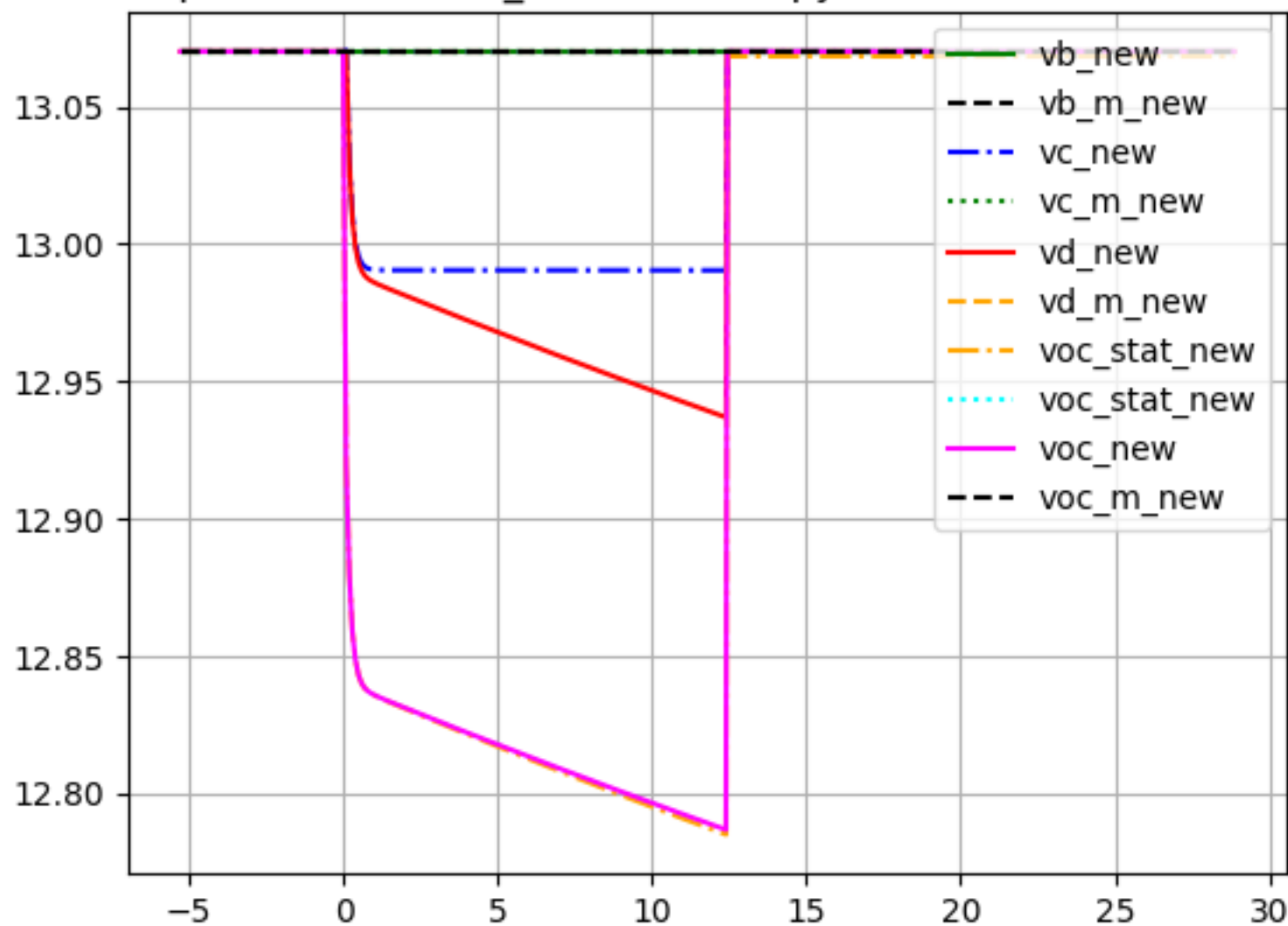


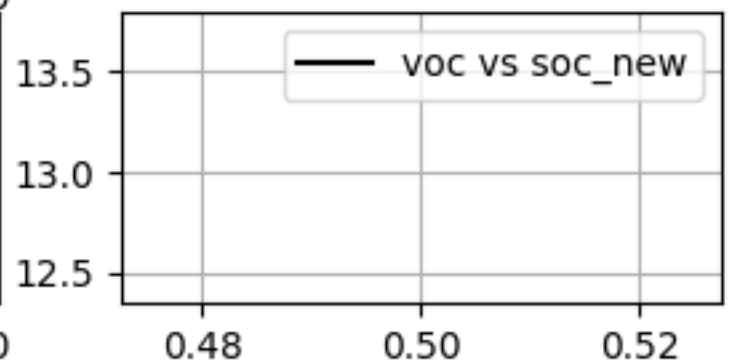
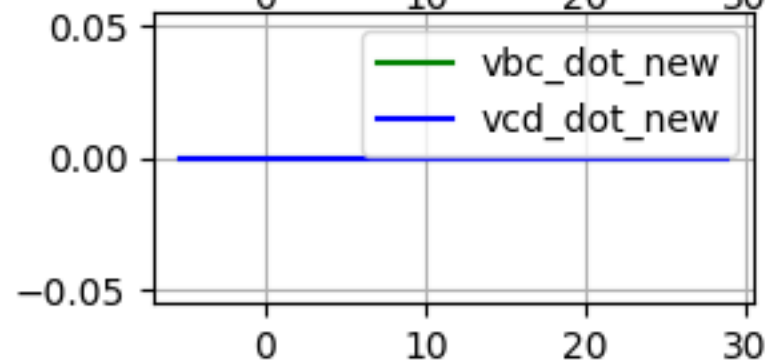
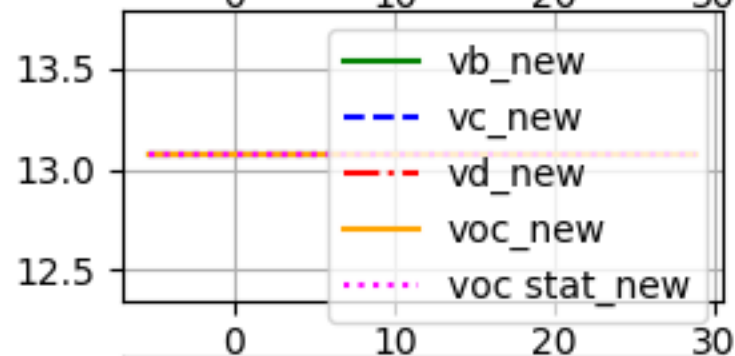
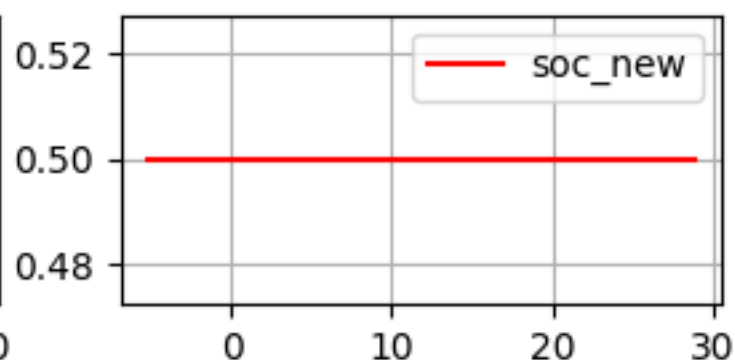
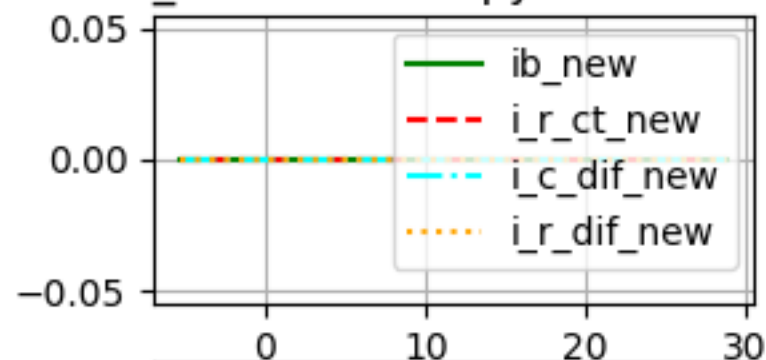
Fail20220816_mon-MonSim.py 2022-08-16T17-27-41



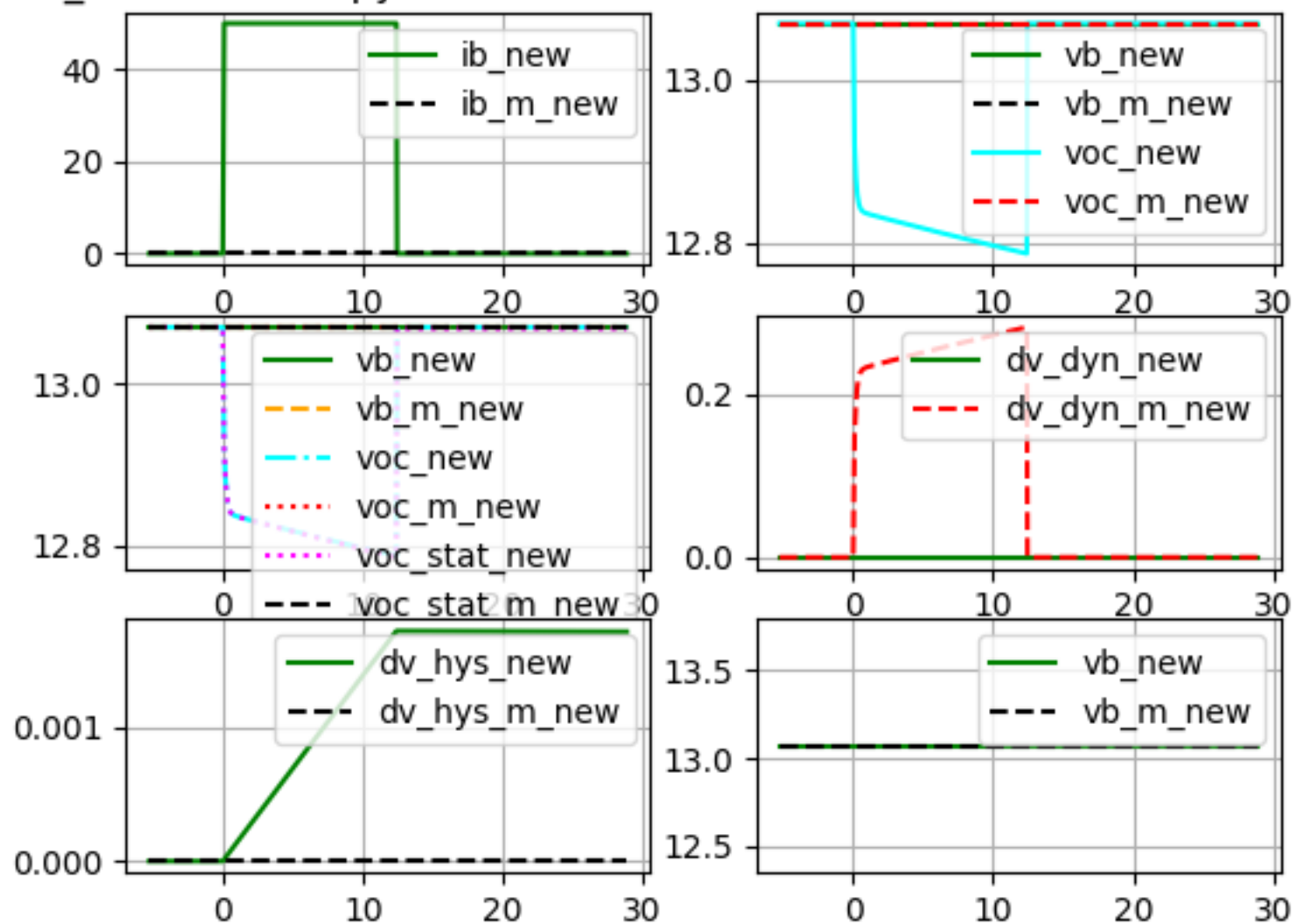
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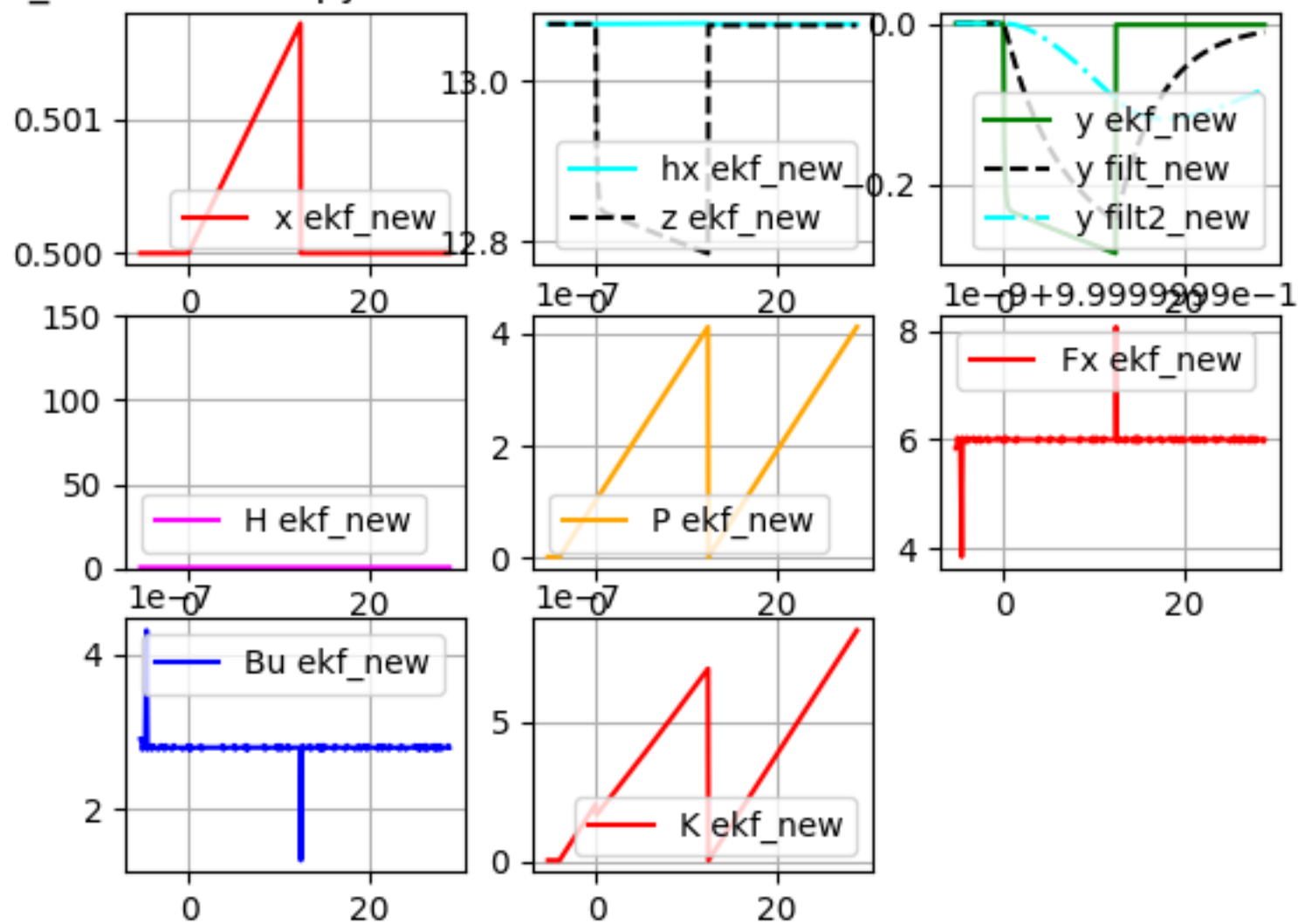
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20816_mon-MonSim.py 2022-08-16T17-27-41 MON vs SIM



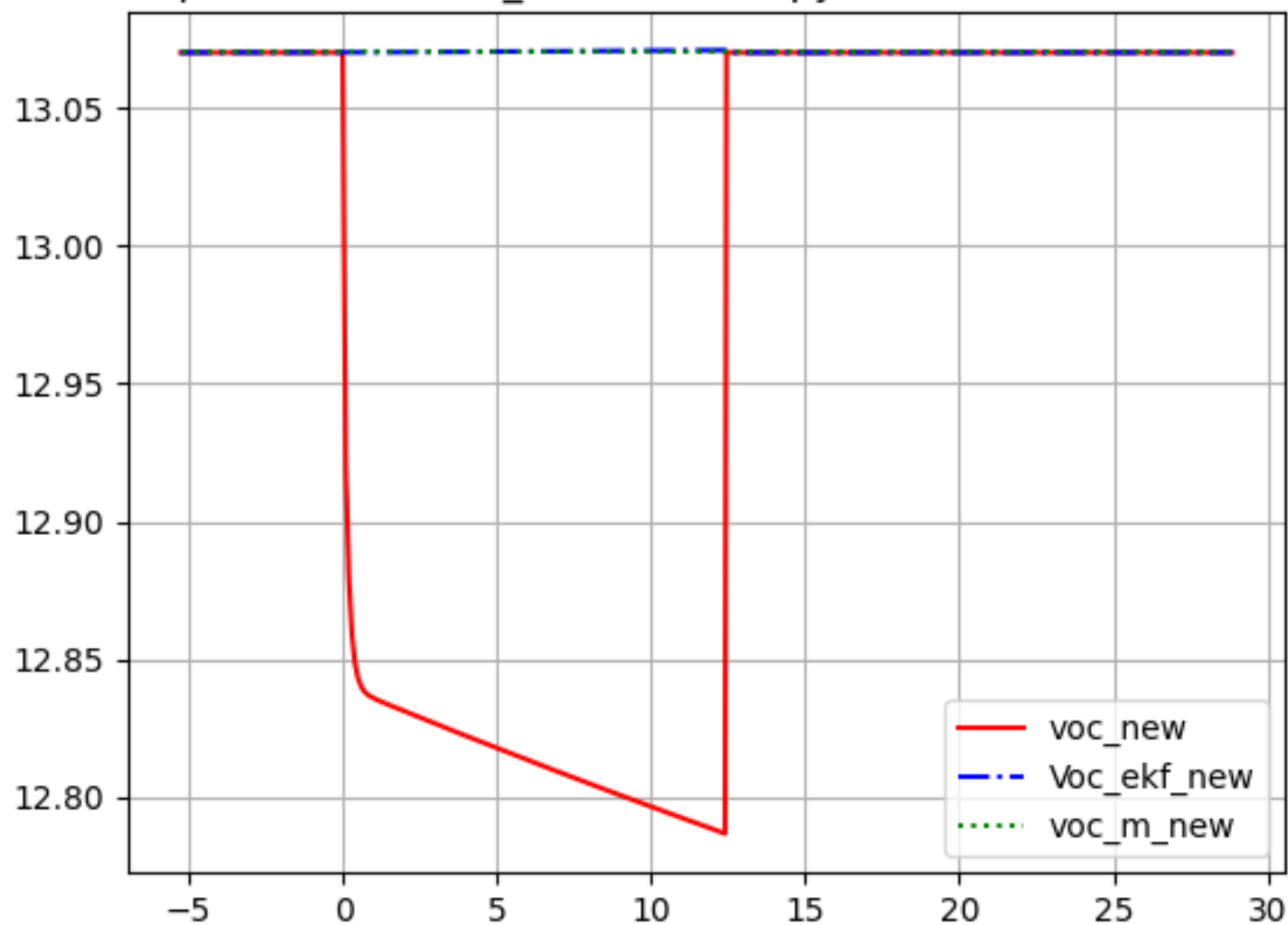
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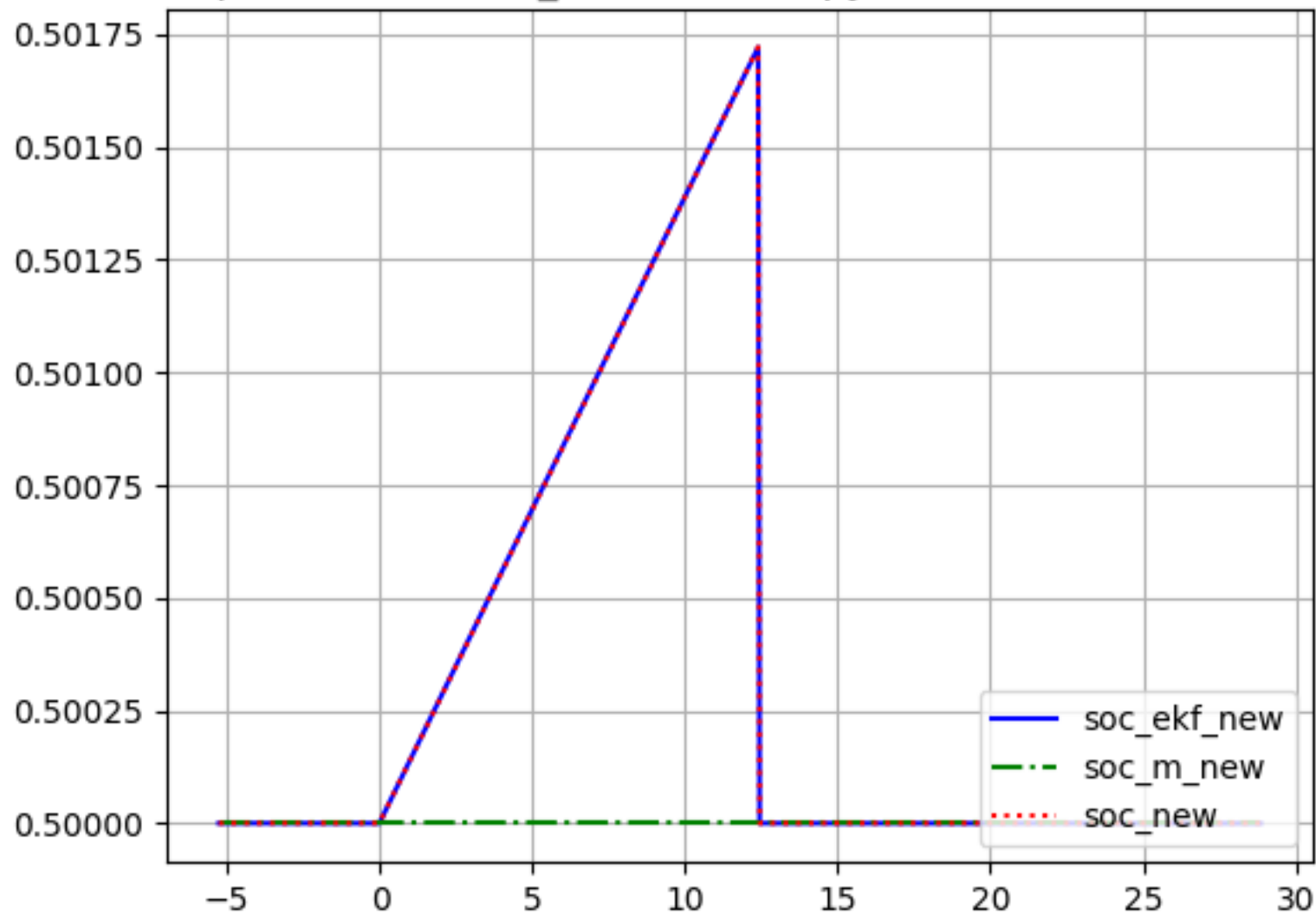
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ampHiFail20220816_mon-MonSim.py 2022-08-16T17-27-41



ampHiFail20220816_mon-MonSim.py 2022-08-16T17-27-41



ampHiFail20220816_mon-MonSim.py 2022-08-16T17-27-41

