

lowHalfTweakRegressionTest20220829\_mon-MonSim.py 2022-08-30T05-13vb\_new vb\_m\_new vc\_new 14.4 vc\_m\_new vd\_new vd\_m\_new 14.2 voc\_stat\_new voc stat new voc\_new voc\_m\_new 14.0 13.8

400

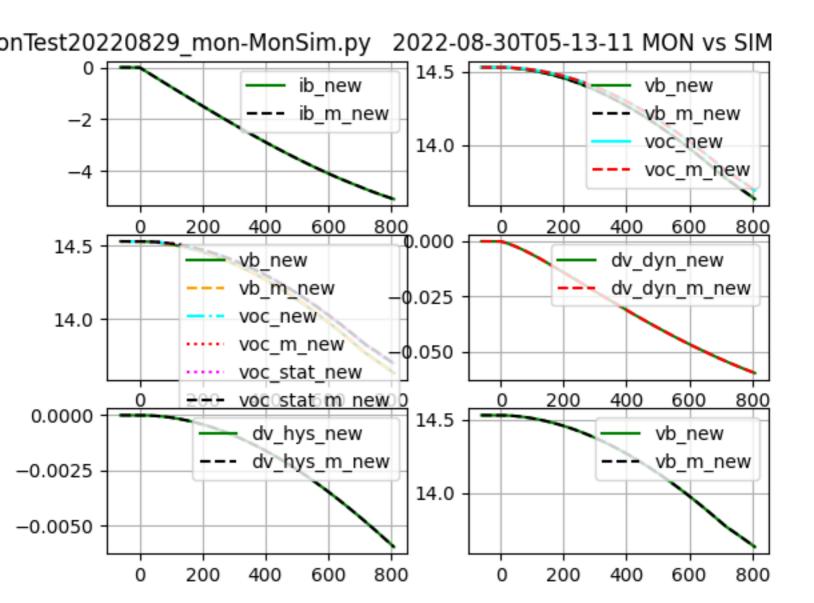
200

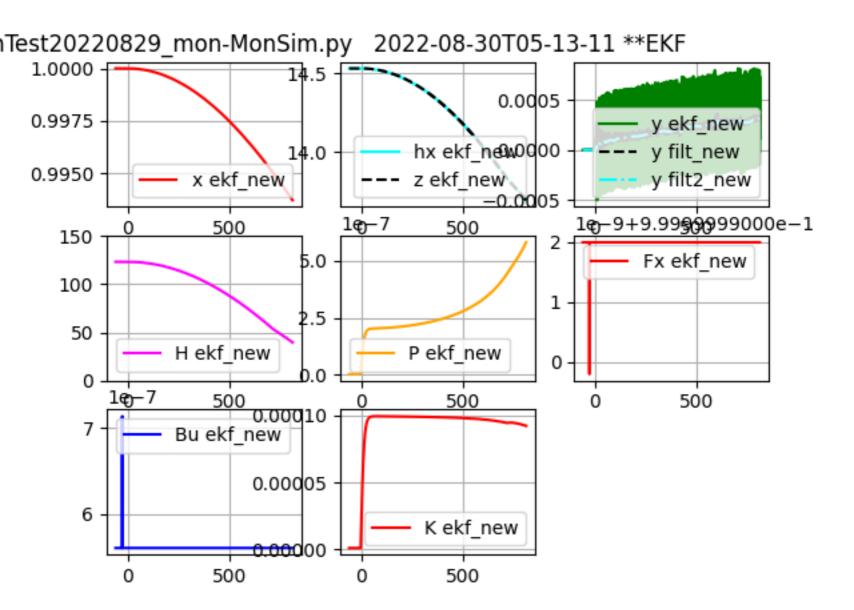
600

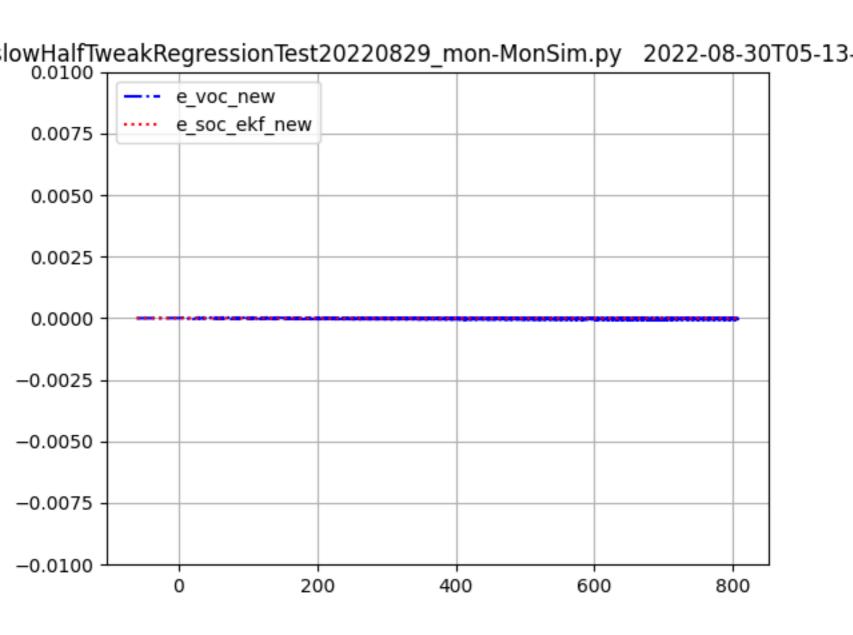
800

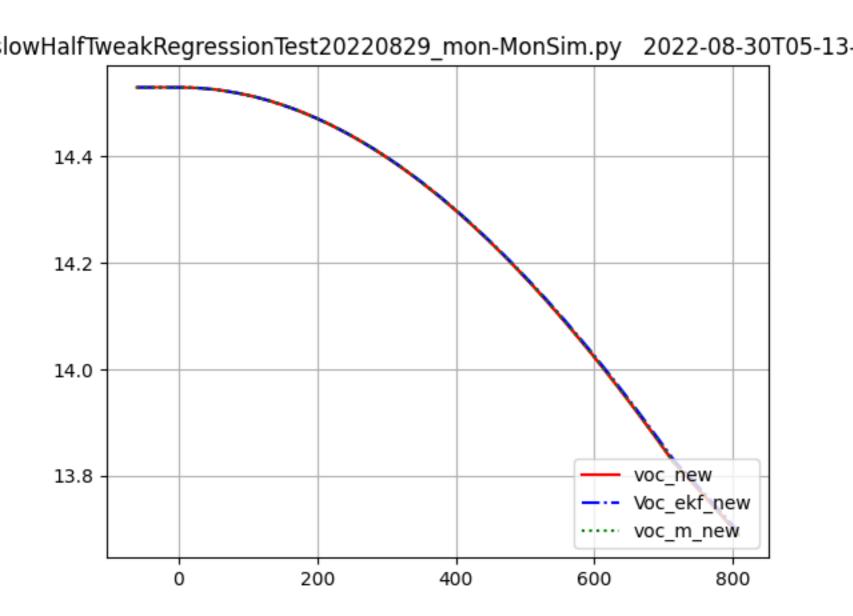
13.6

ssionTest20220829\_mon-MonSim.py 2022-08-30T05-13-11 \*\*SIM T.0000 ib\_new soc\_new i\_r\_ct\_new 0.9975 -2 i\_c\_dif\_new i\_r\_dif\_new 0.9950 -4200 400 200 400 600 600 800 0 800 14.5 vb\_new vc new vd new 14.0 voc\_new voc stat new 1e-<del>0</del>5 200 400 600 800 14.5 vbc\_dot\_new voc vs soc new vcd\_dot\_new -2 14.0 -40 200 400 600 800 0.994 0.996 0.998 1.000









lowHalfTweakRegressionTest20220829\_mon-MonSim.py 2022-08-30T05-13-1.000 0.999 0.998 0.997 0.996 0.995 soc\_ekf\_new soc\_m\_new 0.994 soc\_new 600 800 200 400

lowHalfTweakRegressionTest20220829\_mon-MonSim.py 2022-08-30T05-13-

