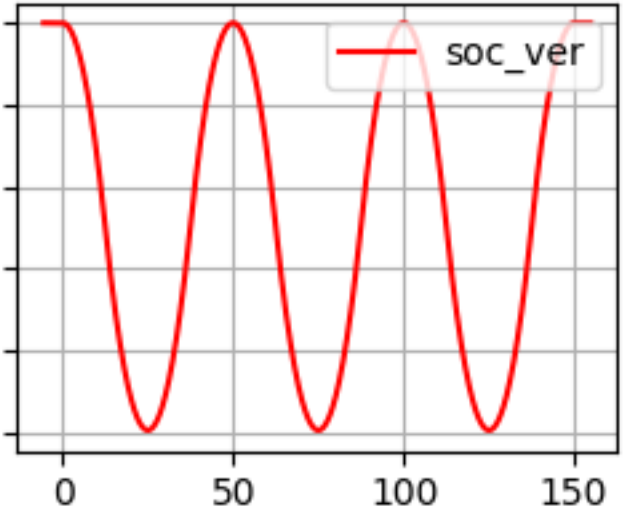
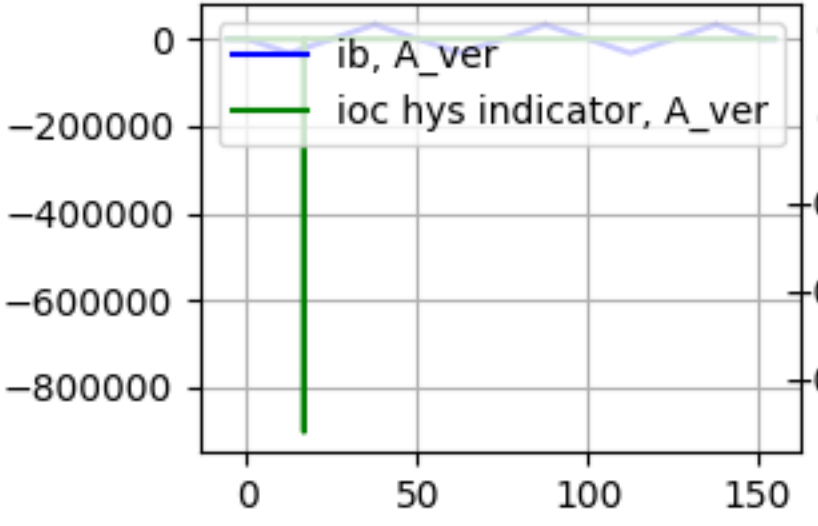
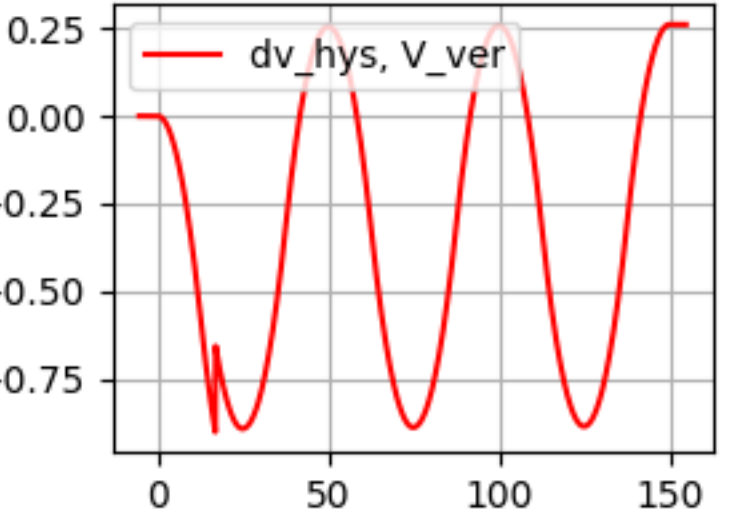
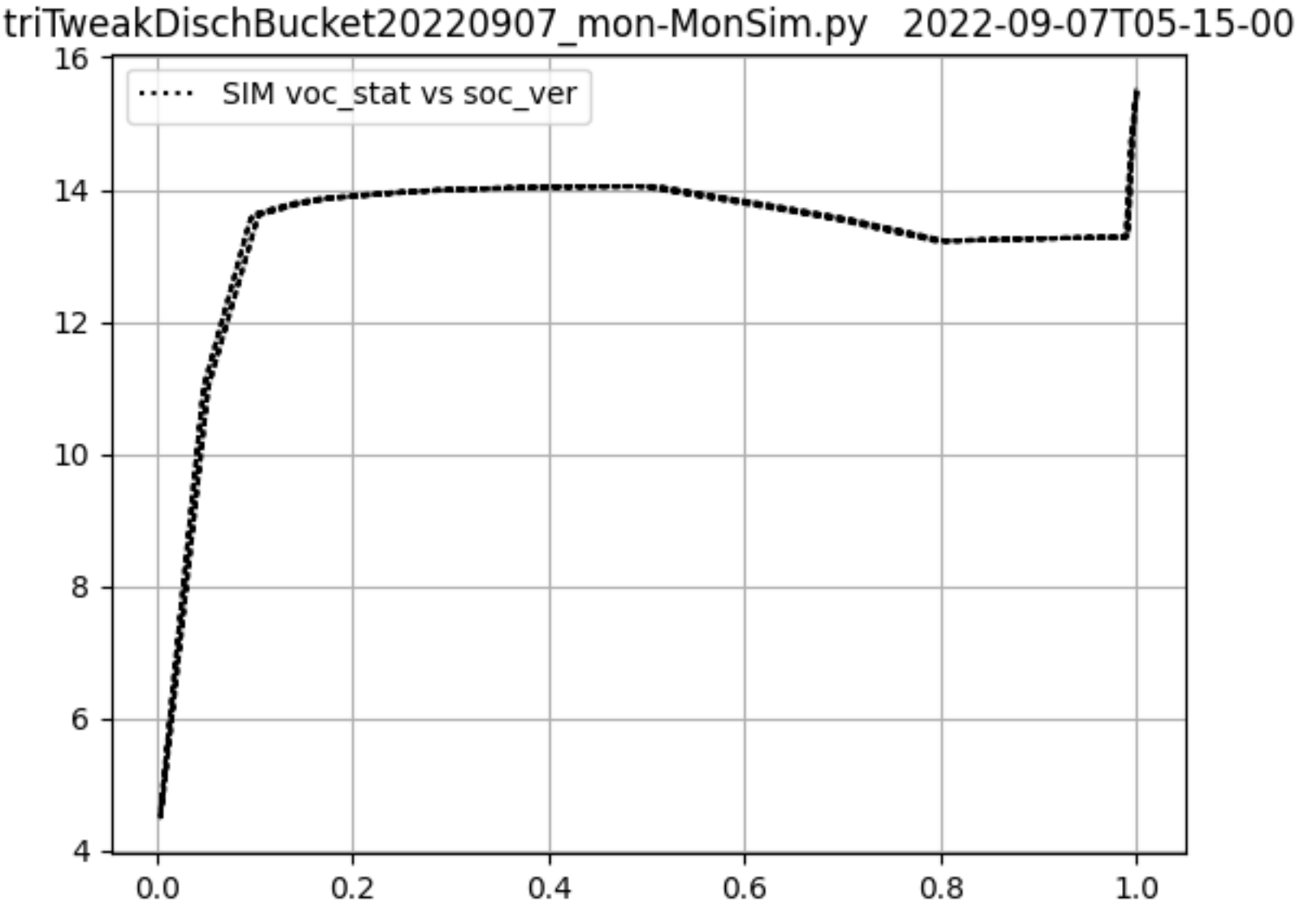
hBucket20220907_mon-MonSim.py 2022-09-07T05-15-00

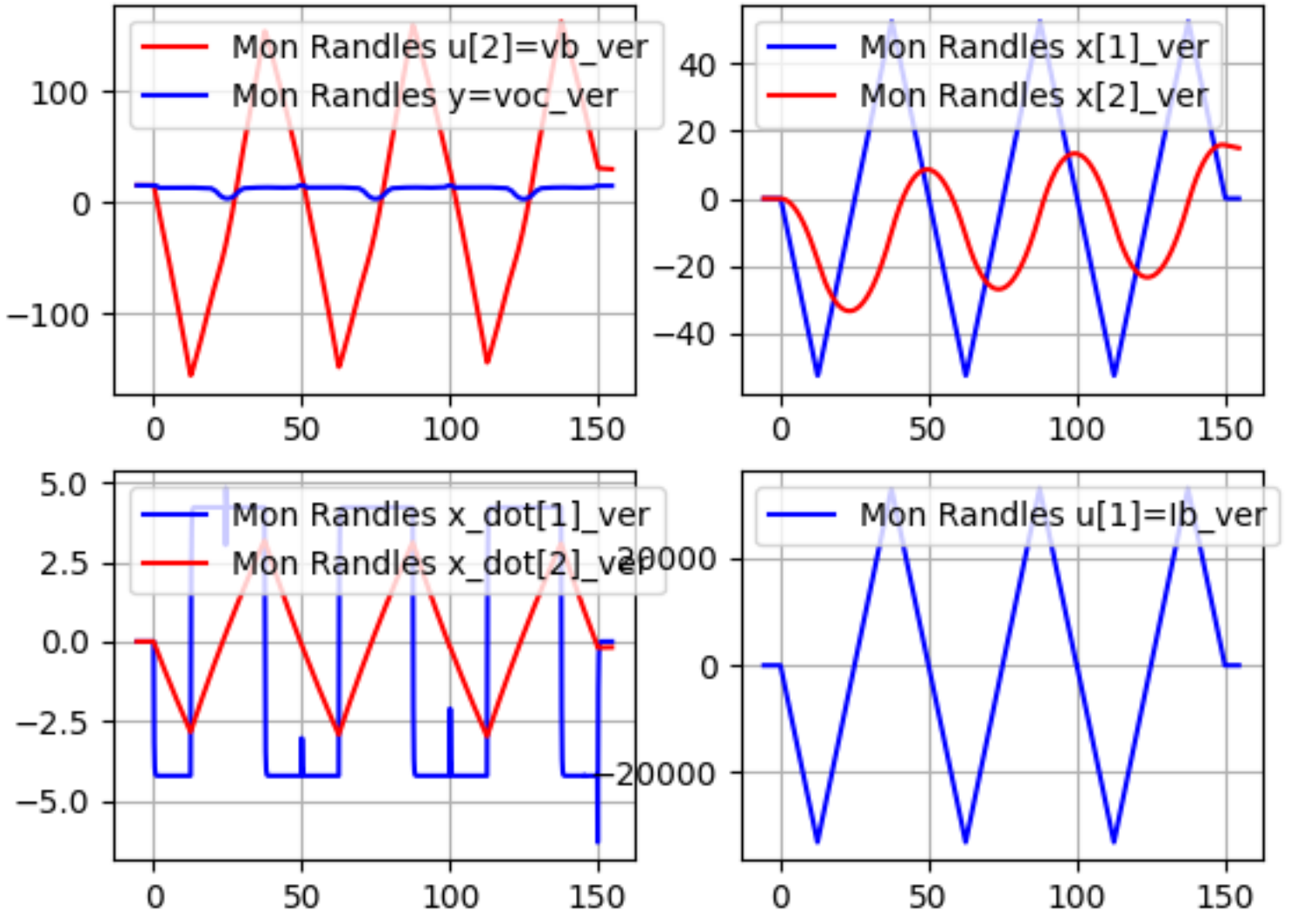


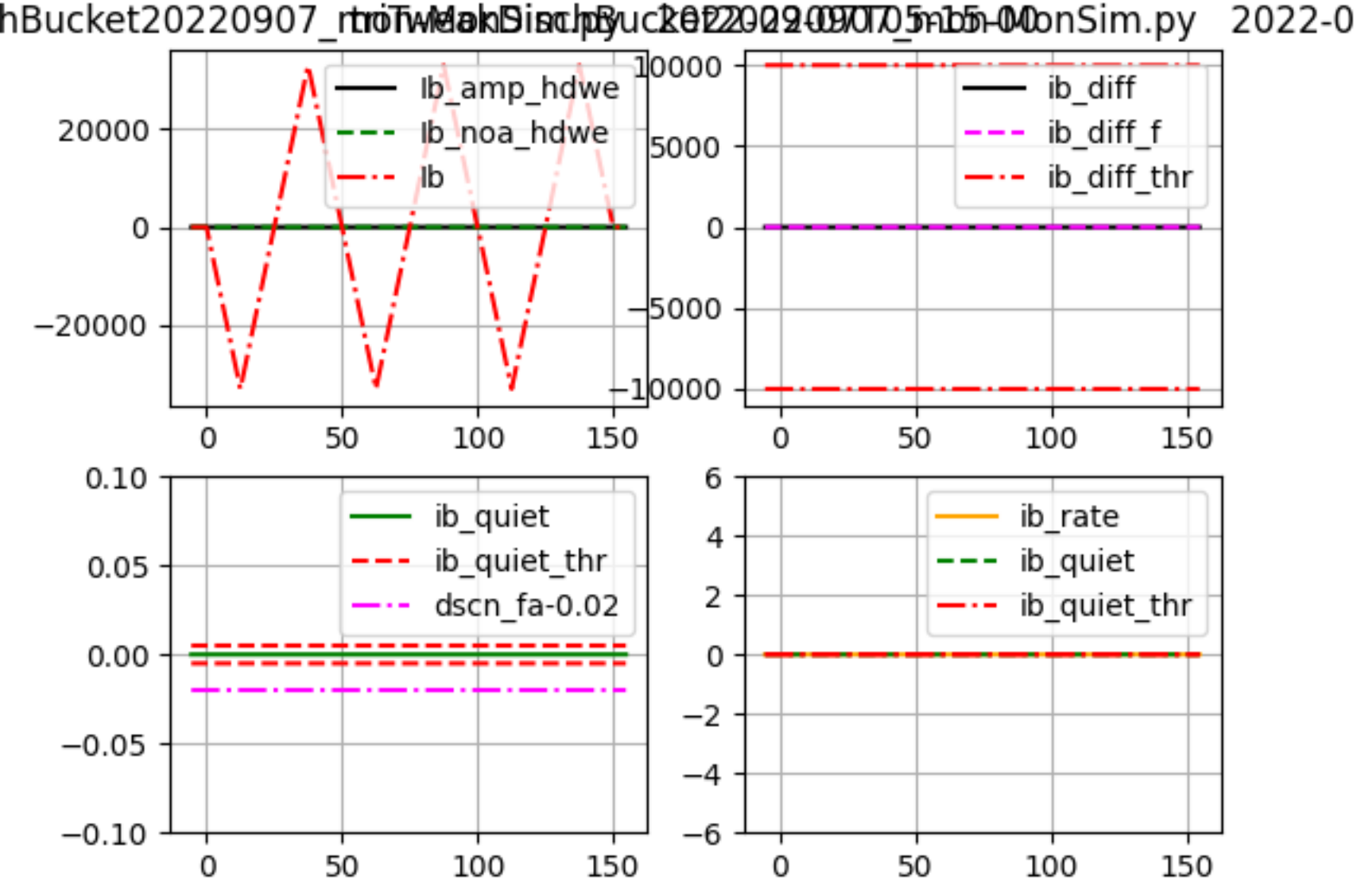




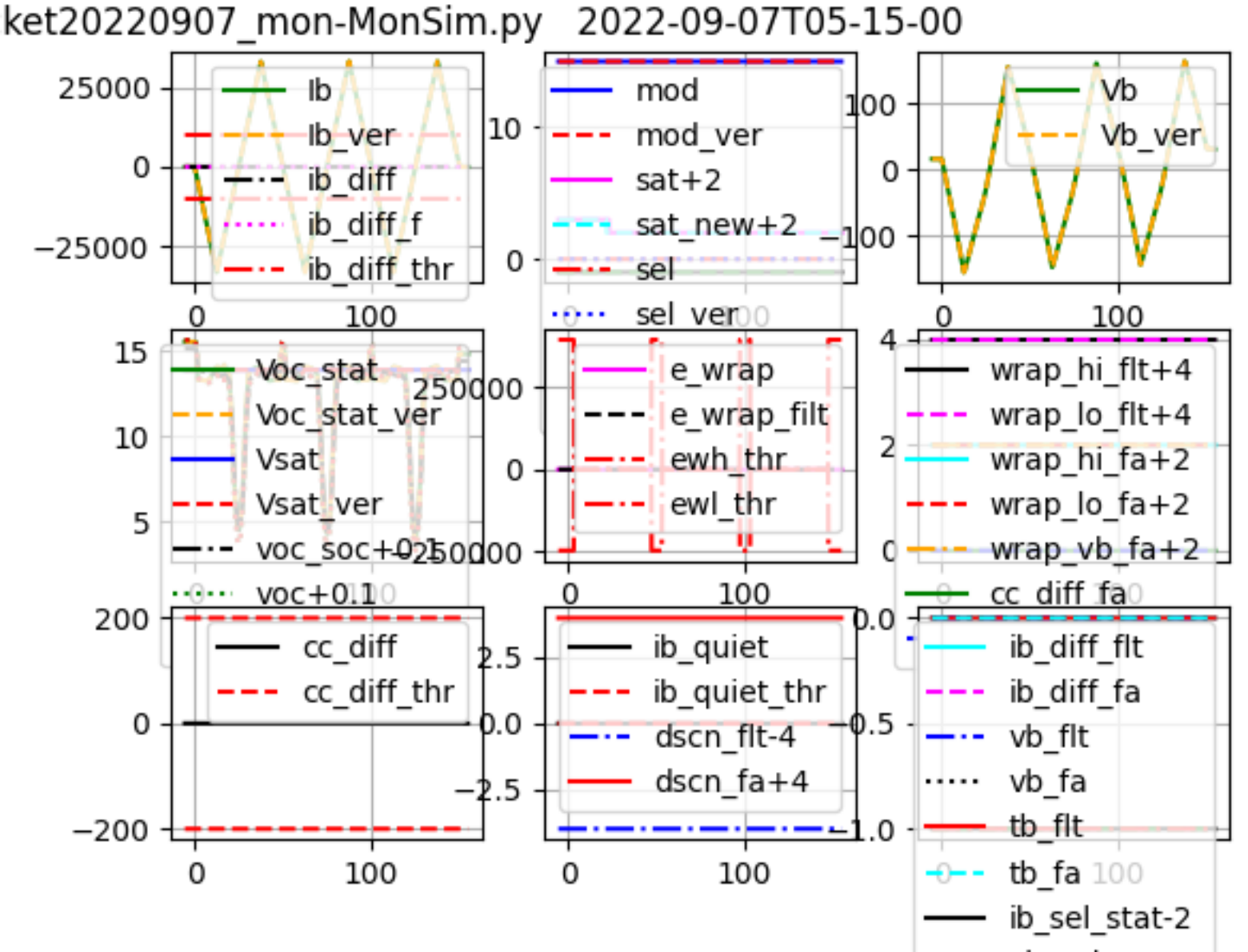

triTweakDischBucket20220907_mon-MonSim.py 2022-09-07T05-15-00
SIM voc_stat vs soc_ver
16
14
12
10
8
6
4
0.0
0.2
0.4
0.6
0.8
1.0

hBucket20220907_mon-MonSim.py 2022-09-07T05-15-00

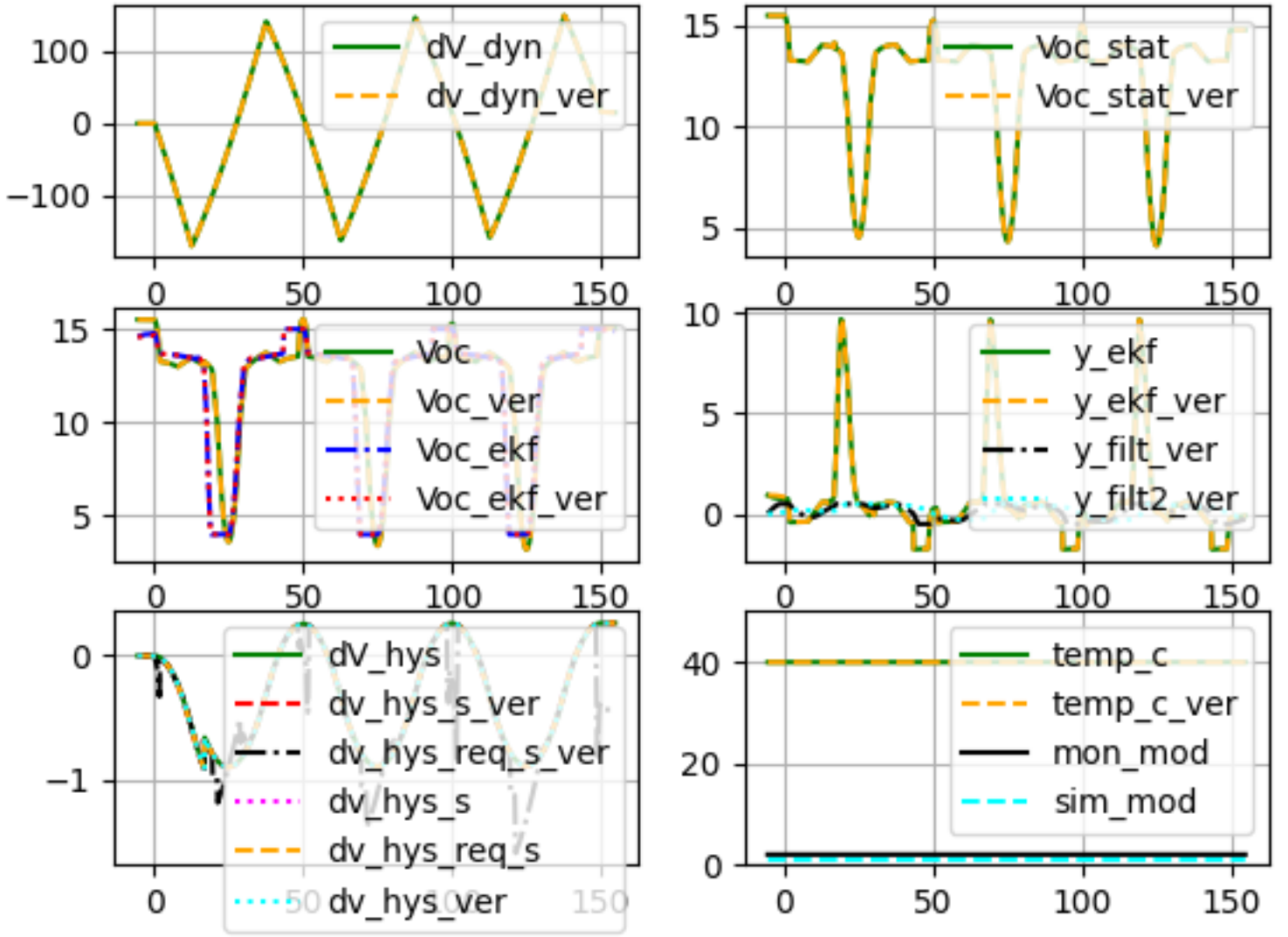




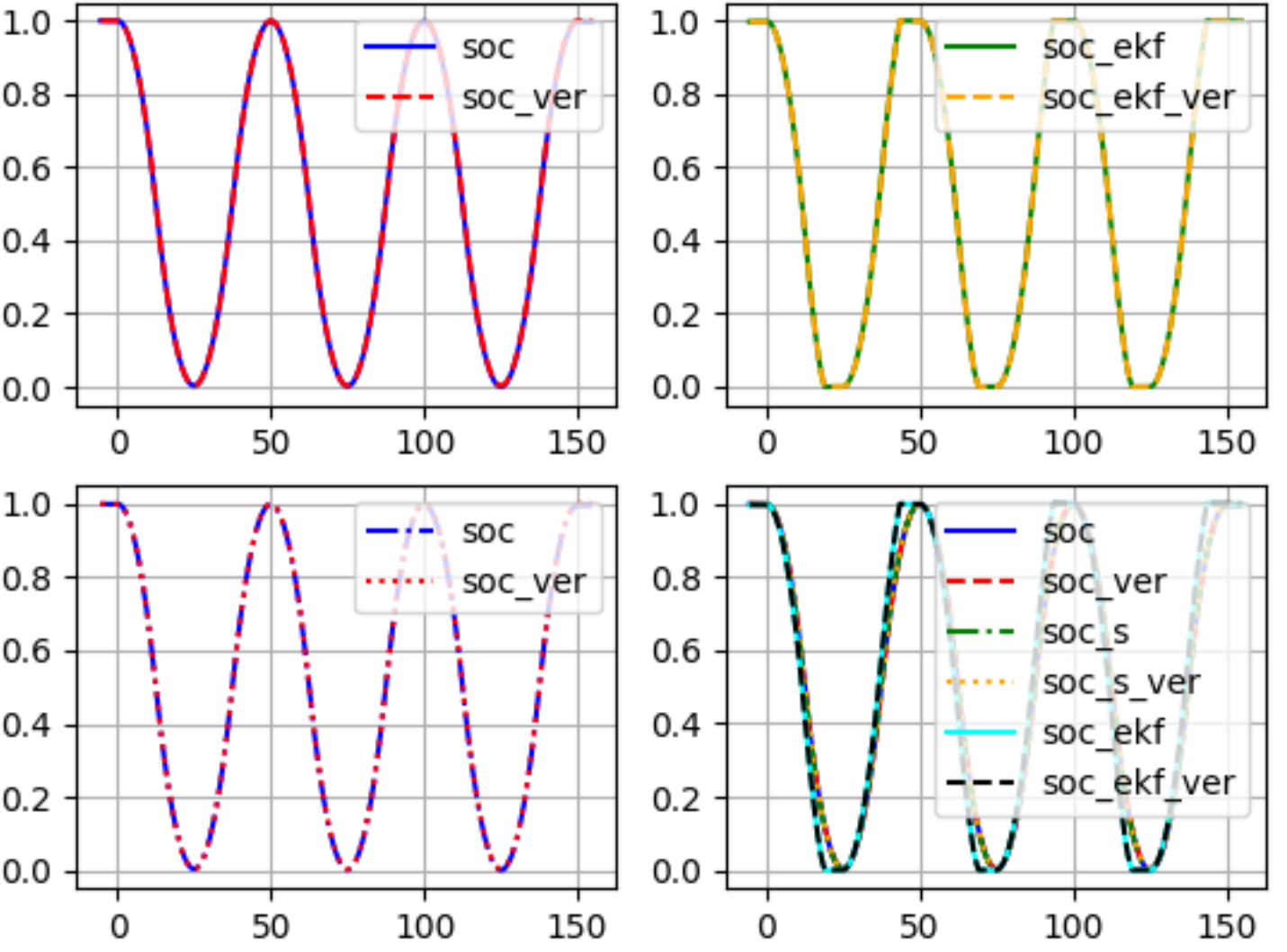
hBucket20220907_mon-MonSim.py
2022-0
Ib_amp_hdwe
Ib_noa_hdwe
Ib
ib_diff
ib_diff_f
ib_diff_thr
ib_quiet
ib_quiet_thr
dscn_fa-0.02
ib_rate
ib_quiet
ib_quiet_thr

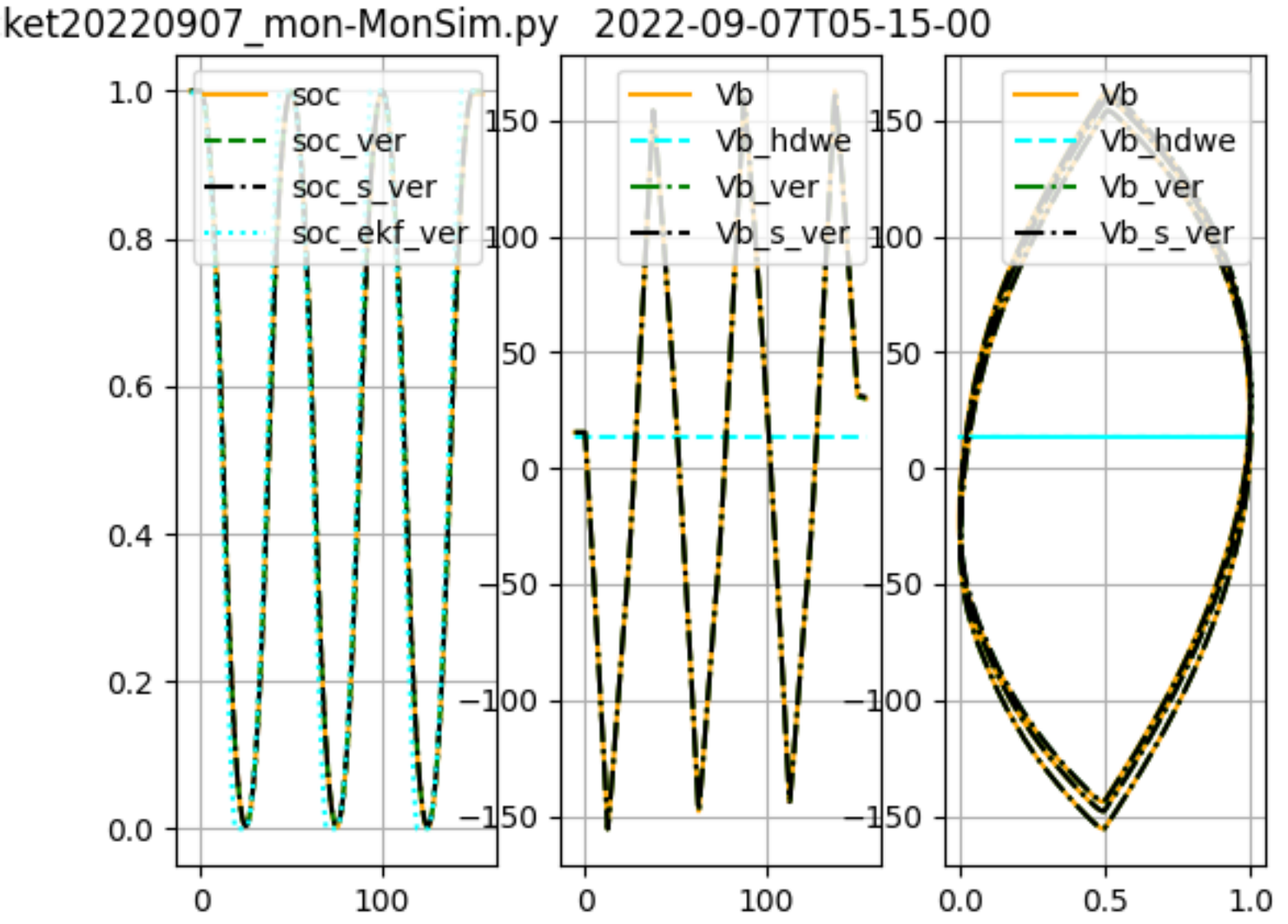
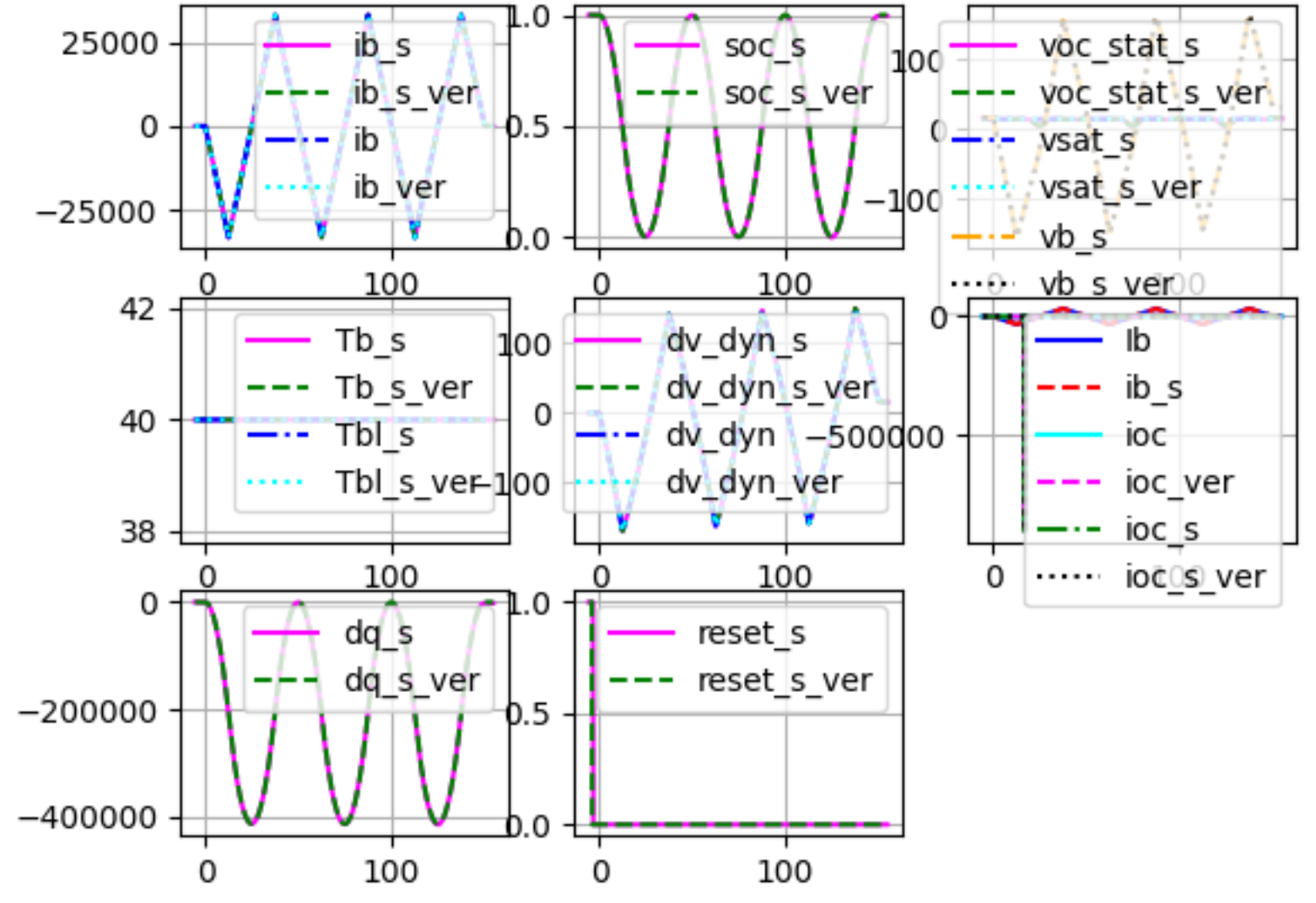

ket20220907_mon-MonSim.py 2022-09-07T05-15-00
Ib
Ib_ver
ib_diff
ib_diff_f
ib_diff_thr
mod
mod_ver
sat+2
sat_new+2
sel
sel_ver
Vb
Vb_ver
Voc_stat
Voc_stat_ver
Vsat
Vsat_ver
voc_soc+0.1
voc+0.1
e_wrap
e_wrap_filt
ewh_thr
ewl_thr
wrap_hi_flt+4
wrap_lo_flt+4
wrap_hi_fa+2
wrap_lo_fa+2
wrap_vb_fa+2
cc_diff_fa
cc_diff
cc_diff_thr
ib_quiet
ib_quiet_thr
dscn_flt-4
dscn_fa+4
ib_diff_flt
ib_diff_fa
vb_flt
vb_fa
tb_flt
tb_fa
ib_sel_stat-2

hBucket20220907_mon-MonSim.py 2022-09-07T05-15-00



hBucket20220907_mon-MonSim.py 2022-09-07T05-15-00




ket20220907_mon-MonSim.py 2022-09-07T05-15-00
soc
soc_ver
soc_s_ver
soc_ekf_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver

ket20220907_mon-MonSim.py 2022-09-07T05-15-00