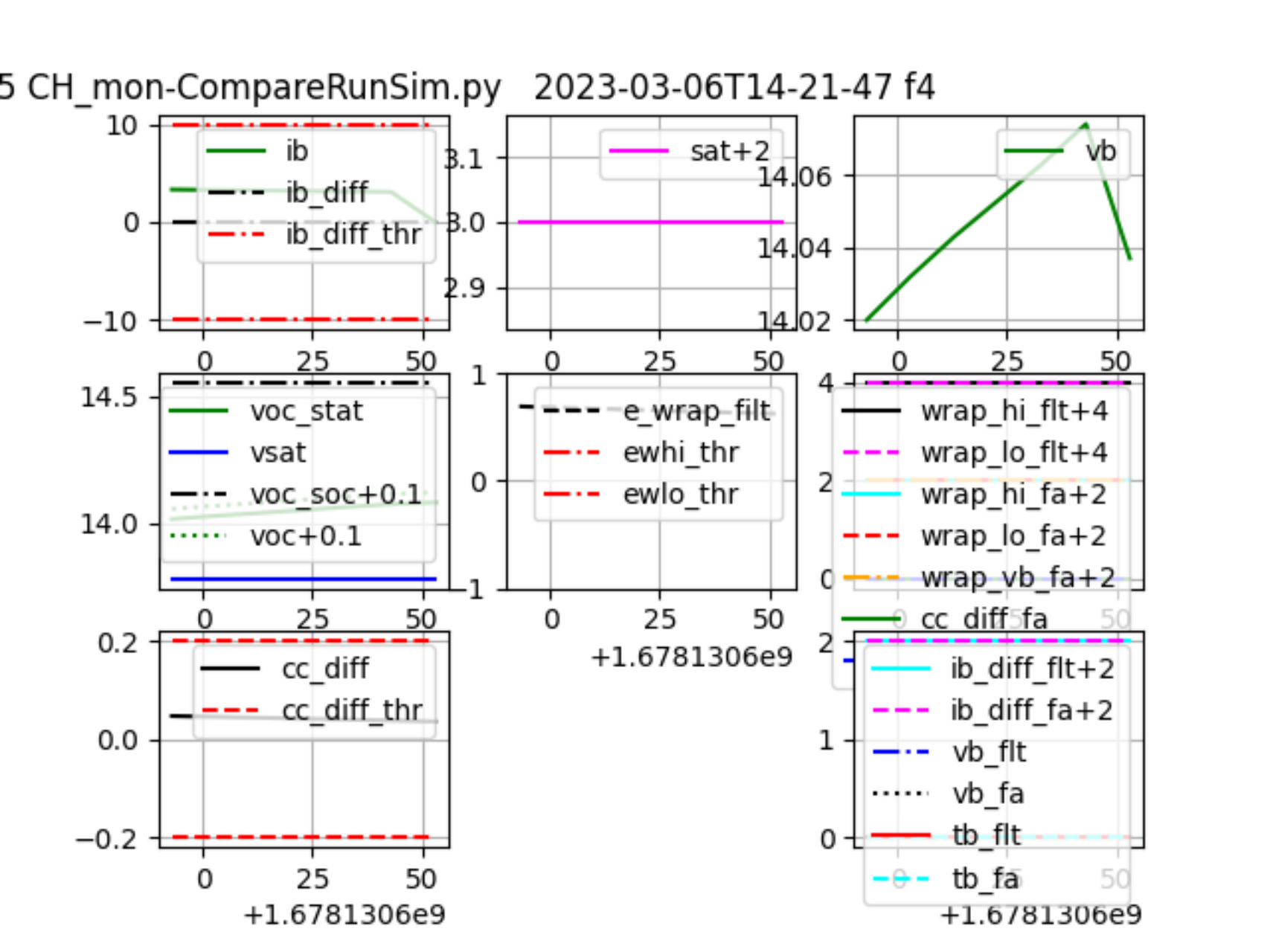
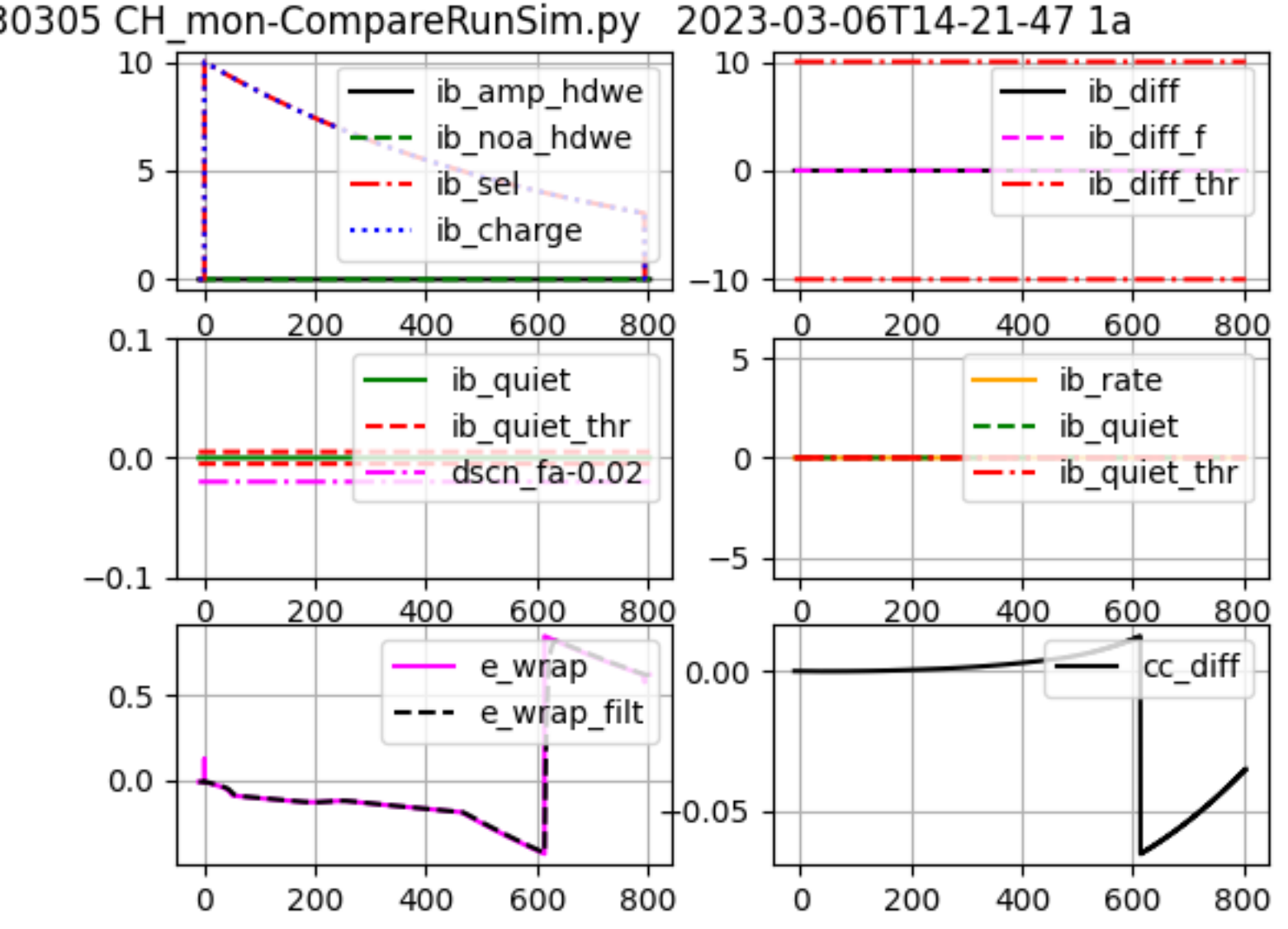
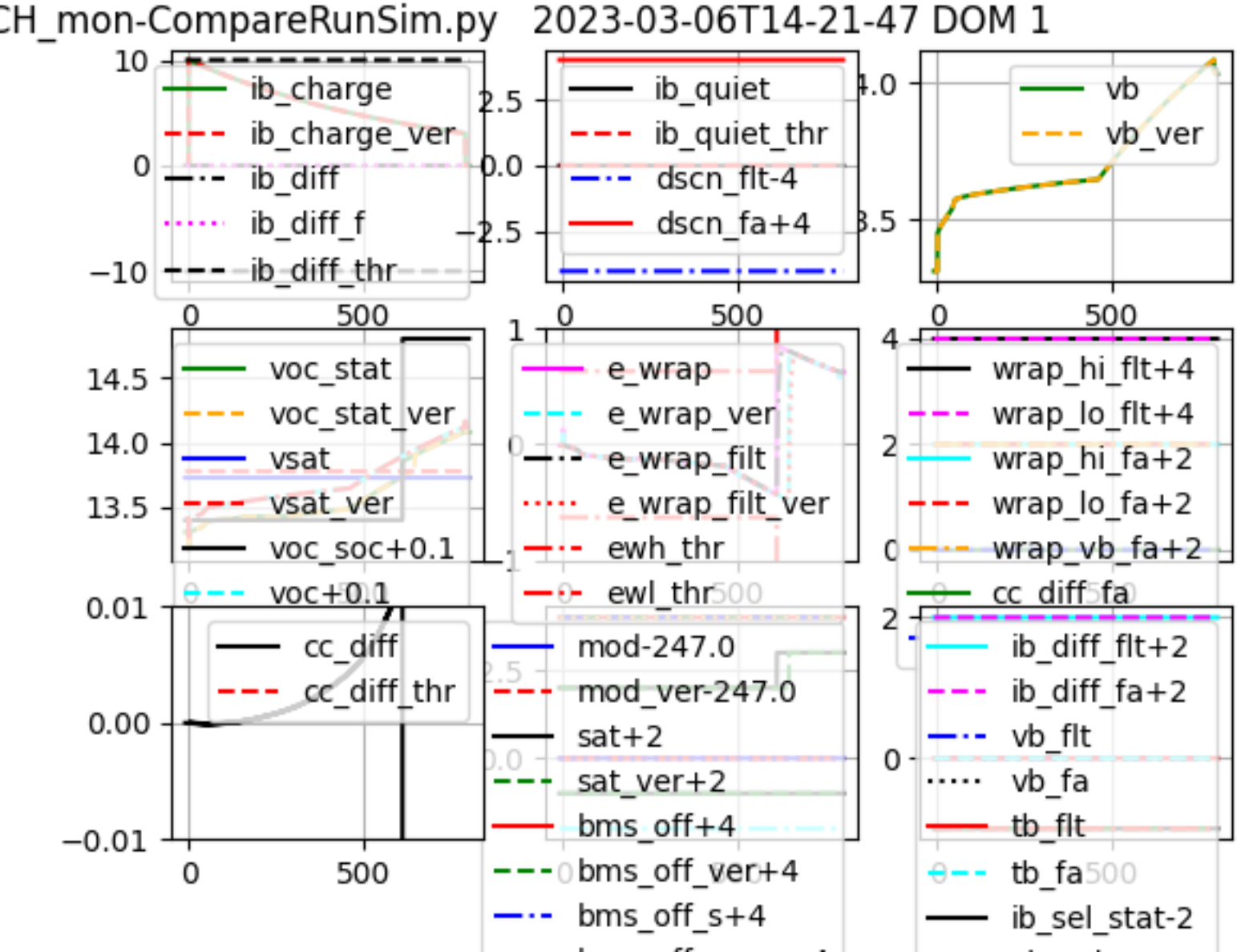
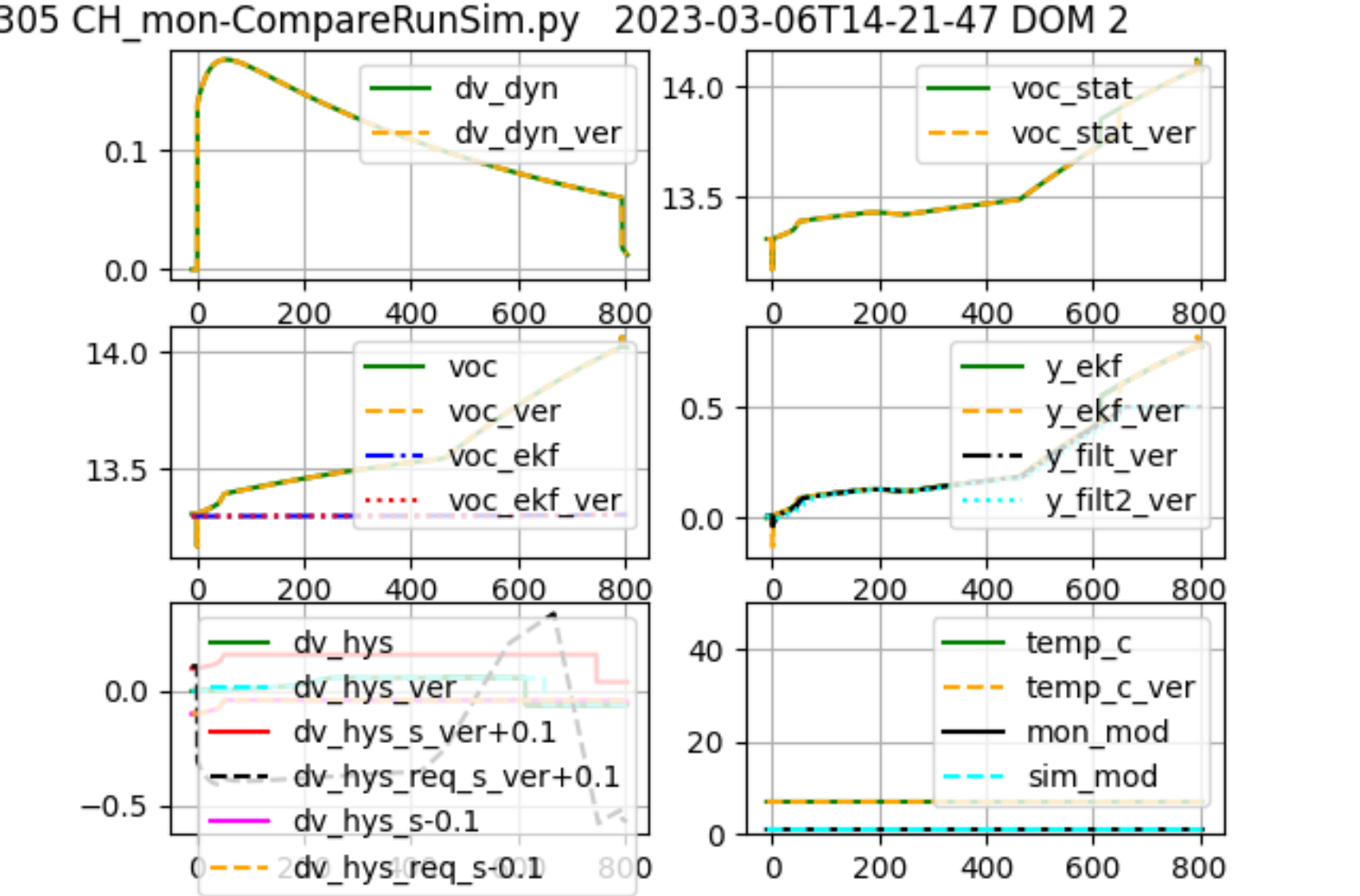
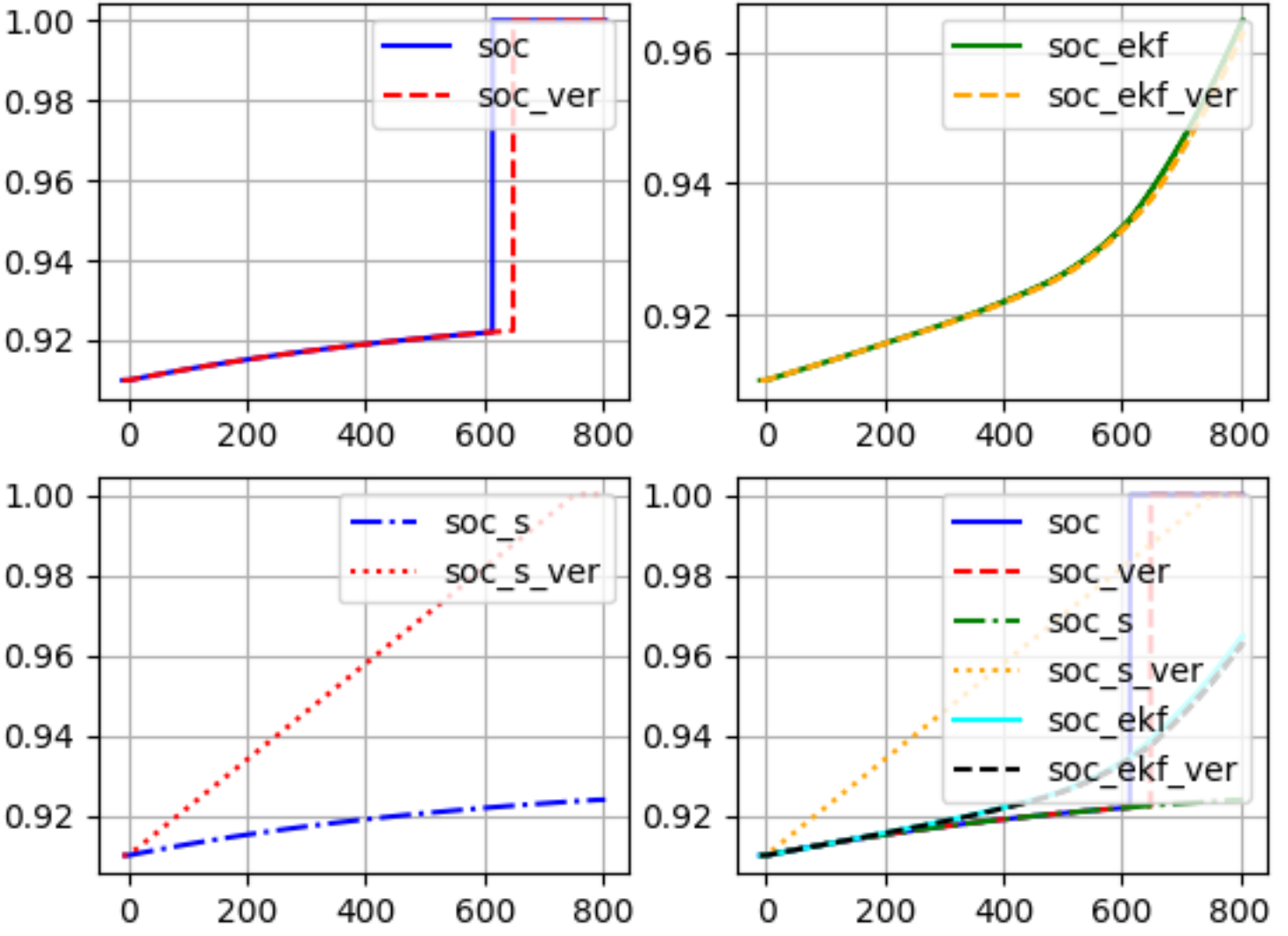

5 CH_mon-CompareRunSim.py 2023-03-06T14-21-47 f4
ib
ib_diff
ib_diff_thr
sat+2
vb
voc_stat
vsat
voc_soc+0.1
voc+0.1
e_wrap_filt
ewhi_thr
ewlo_thr
wrap_hi_flt+4
wrap_lo_flt+4
wrap_hi_fa+2
wrap_lo_fa+2
wrap_vb_fa+2
cc_diff_fa
cc_diff
cc_diff_thr
ib_diff_flt+2
ib_diff_fa+2
vb_flt
vb_fa
tb_flt
tb_fa
+1.6781306e9
+1.6781306e9
+1.6781306e9

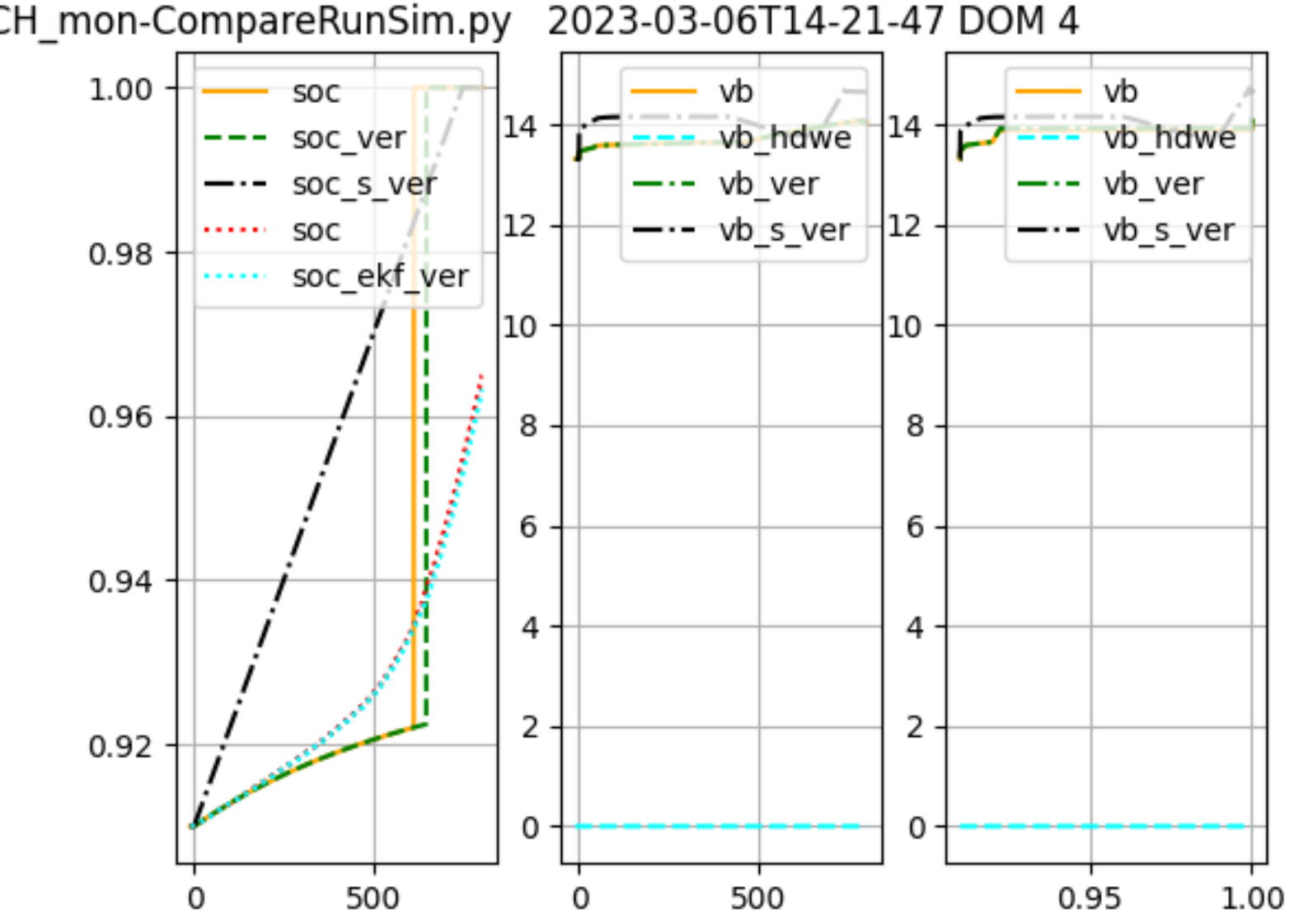

30305 CH_mon-CompareRunSim.py 2023-03-06T14-21-47 1a
ib_amp_hdwe
ib_noa_hdwe
ib_sel
ib_charge
ib_diff
ib_diff_f
ib_diff_thr
ib_quiet
ib_quiet_thr
dscn_fa-0.02
ib_rate
ib_quiet
ib_quiet_thr
e_wrap
e_wrap_filt
cc_diff

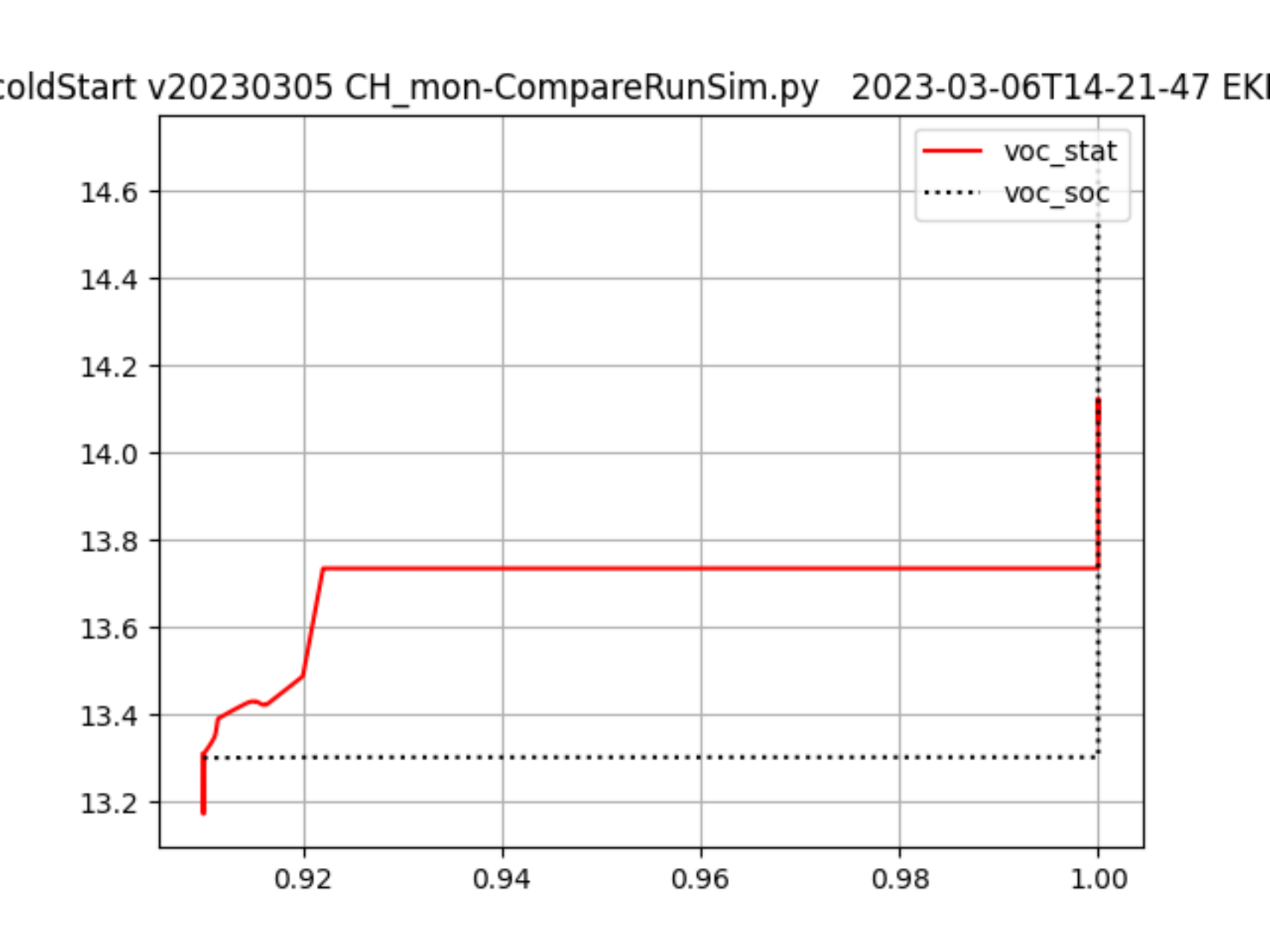

CH_mon-CompareRunSim.py 2023-03-06T14-21-47 DOM 1
ib_charge
ib_charge_ver
ib_diff
ib_diff_f
ib_diff_thr
ib_quiet
ib_quiet_thr
dscn_flt-4
dscn_fa+4
vb
vb_ver
voc_stat
voc_stat_ver
vsat
vsat_ver
voc_soc+0.1
voc+0.1
e_wrap
e_wrap_ver
e_wrap_filt
e_wrap_filt_ver
ewh_thr
ewl_thr
wrap_hi_flt+4
wrap_lo_flt+4
wrap_hi_fa+2
wrap_lo_fa+2
wrap_vb_fa+2
cc_diff_fa
cc_diff
cc_diff_thr
mod-247.0
mod_ver-247.0
sat+2
sat_ver+2
bms_off+4
bms_off_ver+4
bms_off_s+4
ib_diff_flt+2
ib_diff_fa+2
vb_flt
vb_fa
tb_flt
tb_fa
ib_sel_stat-2

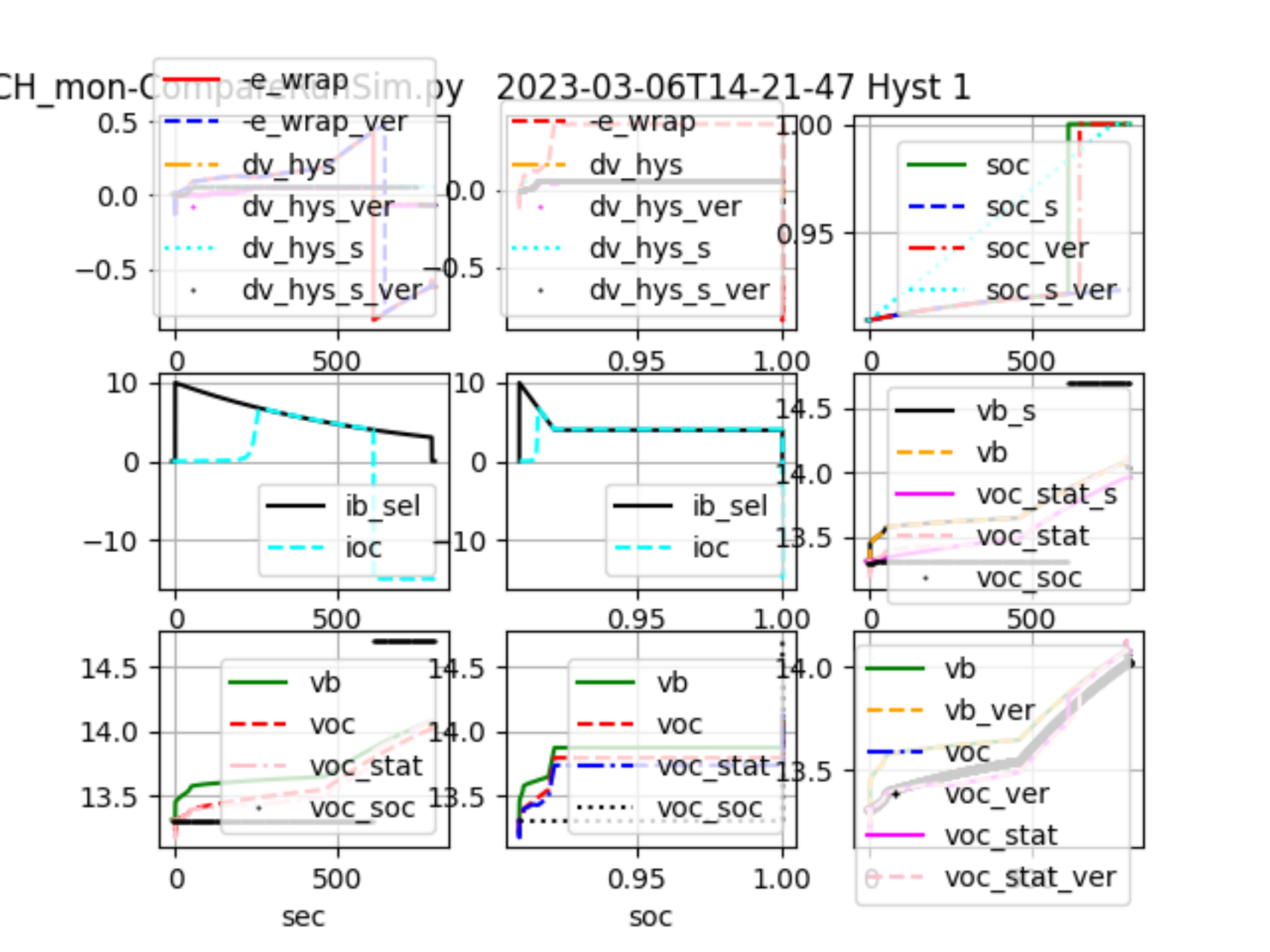

305 CH_mon-CompareRunSim.py 2023-03-06T14-21-47 DOM 2
dv_dyn
dv_dyn_ver
0.0
0.1
0
200
400
600
800
voc_stat
voc_stat_ver
13.5
14.0
voc
voc_ver
voc_ekf
voc_ekf_ver
y_ekf
y_ekf_ver
y_filt_ver
y_filt2_ver
0.5
dv_hys
dv_hys_ver
dv_hys_s_ver+0.1
dv_hys_req_s_ver+0.1
dv_hys_s-0.1
dv_hys_req_s-0.1
−0.5
temp_c
temp_c_ver
mon_mod
sim_mod
20
40

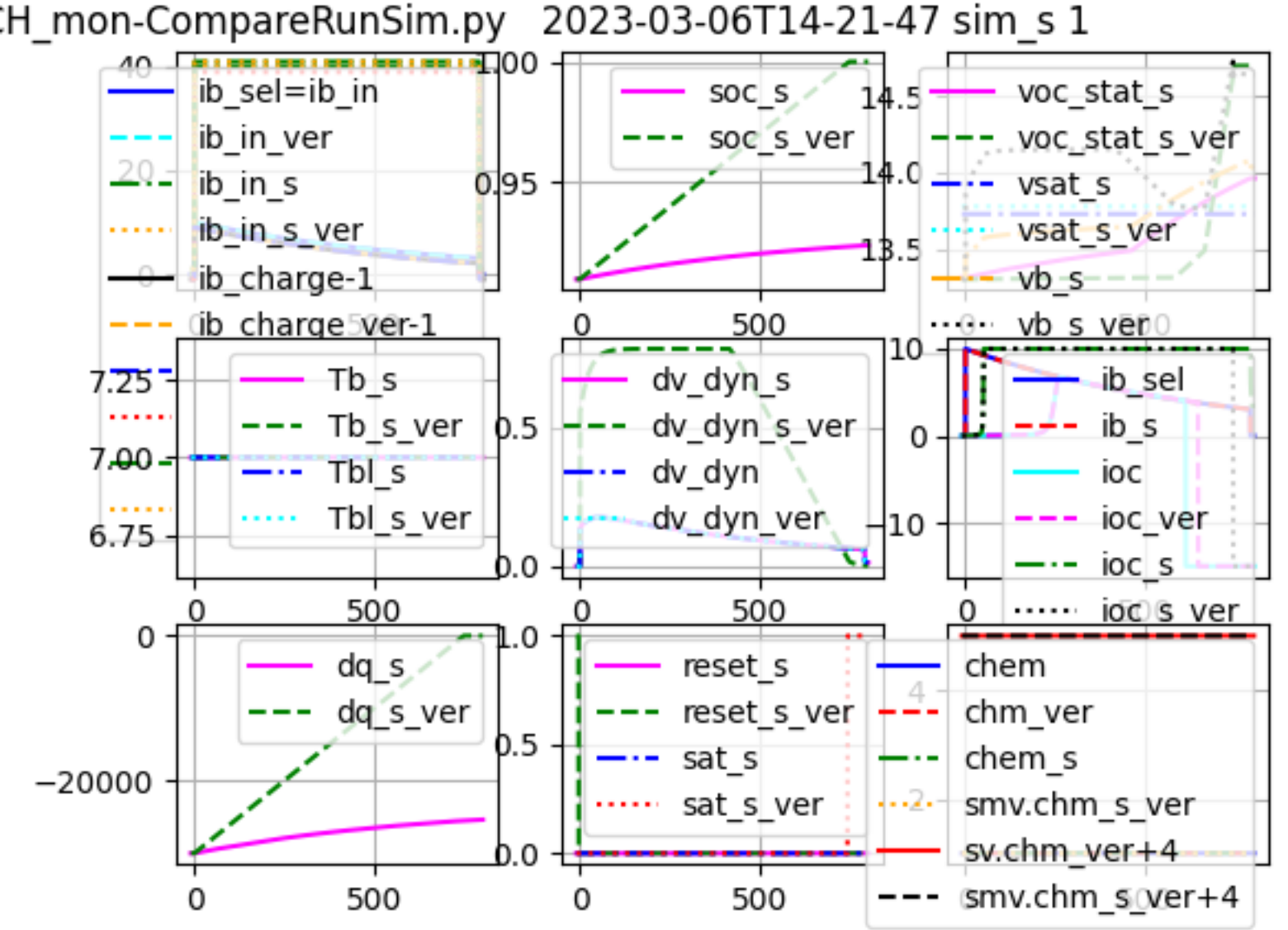
305 CH_mon-CompareRunSim.py 2023-03-06T14-21-47 DOM 3




CH_mon-CompareRunSim.py 2023-03-06T14-21-47 DOM 4
soc
soc_ver
soc_s_ver
soc
soc_ekf_ver
vb
vb_hdwe
vb_ver
vb_s_ver
vb
vb_hdwe
vb_ver
vb_s_ver
1.00
0.98
0.96
0.94
0.92
14
12
10
8
6
4
2
0
0
500
0
500
0.95
1.00


coldStart v20230305 CH_mon-CompareRunSim.py 2023-03-06T14-21-47 EK
voc_stat
voc_soc
14.6
14.4
14.2
14.0
13.8
13.6
13.4
13.2
0.92
0.94
0.96
0.98
1.00


CH_mon-CompareRunSim.py 2023-03-06T14-21-47 Hyst 1
-e_wrap
-e_wrap_ver
dv_hys
dv_hys_ver
dv_hys_s
dv_hys_s_ver
soc
soc_s
soc_ver
soc_s_ver
ib_sel
ioc
vb_s
vb
voc_stat_s
voc_stat
voc_soc
vb
voc
voc_stat
voc_soc
vb_ver
voc_ver
voc_stat_ver
sec
soc


CH_mon-CompareRunSim.py 2023-03-06T14-21-47 sim_s 1
ib_sel=ib_in
ib_in_ver
ib_in_s
ib_in_s_ver
ib_charge-1
ib_charge_ver-1
soc_s
soc_s_ver
voc_stat_s
voc_stat_s_ver
vsat_s
vsat_s_ver
vb_s
vb_s_ver
Tb_s
Tb_s_ver
Tbl_s
Tbl_s_ver
dv_dyn_s
dv_dyn_s_ver
dv_dyn
dv_dyn_ver
ib_sel
ib_s
ioc
ioc_ver
ioc_s
ioc_s_ver
dq_s
dq_s_ver
reset_s
reset_s_ver
sat_s
sat_s_ver
chem
chm_ver
chem_s
smv.chm_s_ver
sv.chm_ver+4
smv.chm_s_ver+4