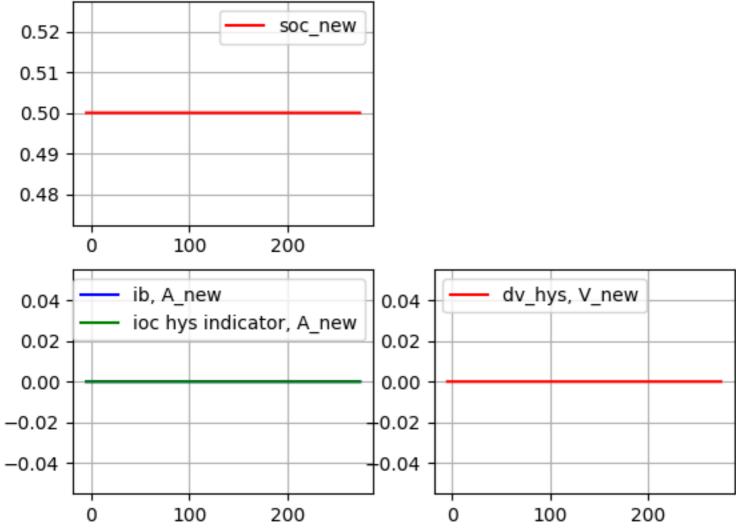
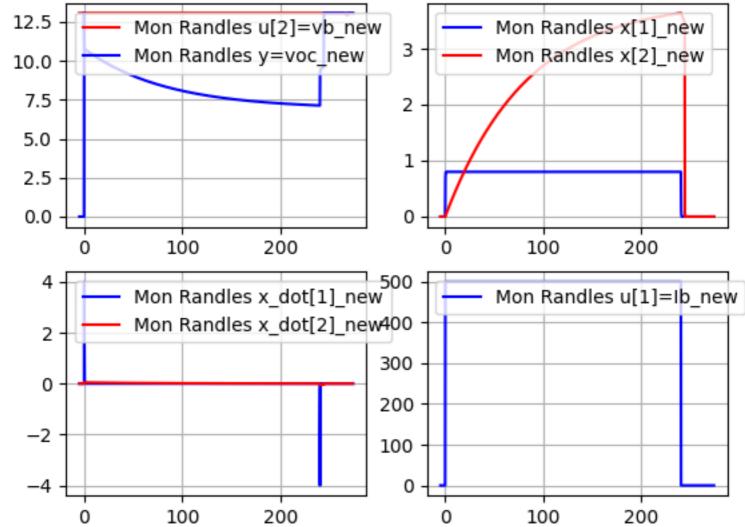
ail20220804_mon-MonSim.py 2022-08-04T13-15-13

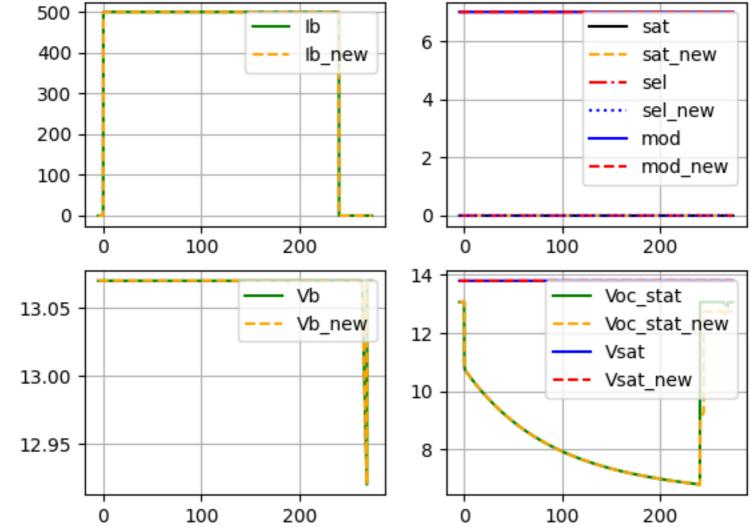


ampHiFail20220804_mon-MonSim.py 2022-08-04T13-15-13 SIM voc vs soc_new 13.6 13.4 13.2 13.0 12.8 12.6 12.4 0.48 0.49 0.50 0.51 0.52

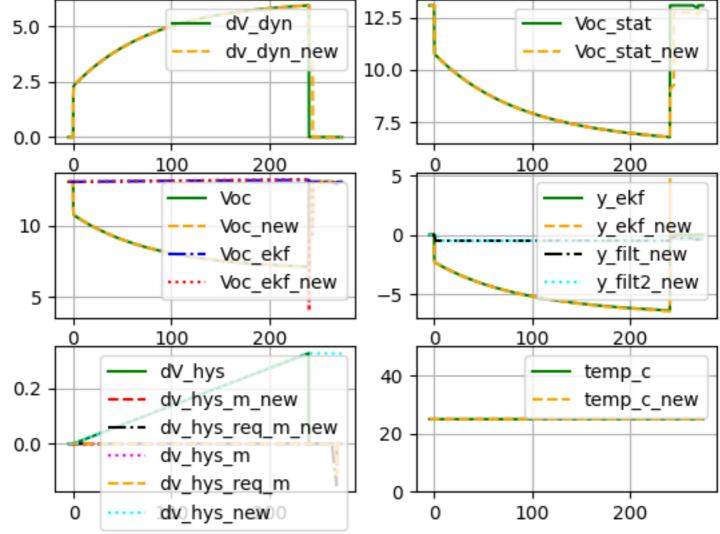
ail20220804_mon-MonSim.py 2022-08-04T13-15-13



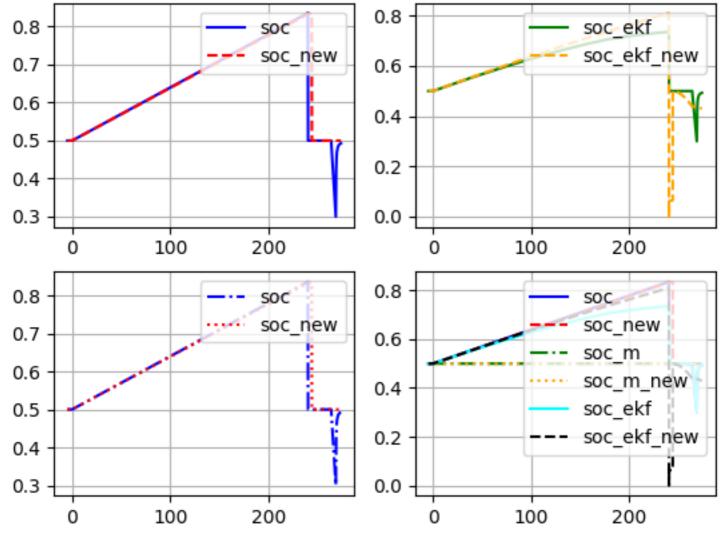
ail20220804_mon-MonSim.py 2022-08-04T13-15-13



ail20220804_mon-MonSim.py 2022-08-04T13-15-13



ail20220804_mon-MonSim.py 2022-08-04T13-15-13



220804_mon-MonSim.py 2022-08-04T13-15-13 ٧b ٧b SOC 0.8 13.06 soc_new Vb_new 13.06 Vb_new Vb_m_new Vb_m_new soc_m_new soc_ekf_new13.04 **1**8.04 0.6 13.02 18.02 13.00 **1**8.00 0.4 12.98 12.98 12.96 12.96 0.2 12.94 12.94 0.0 12.92 12.92

200

0.4

0.6

0.8

200

