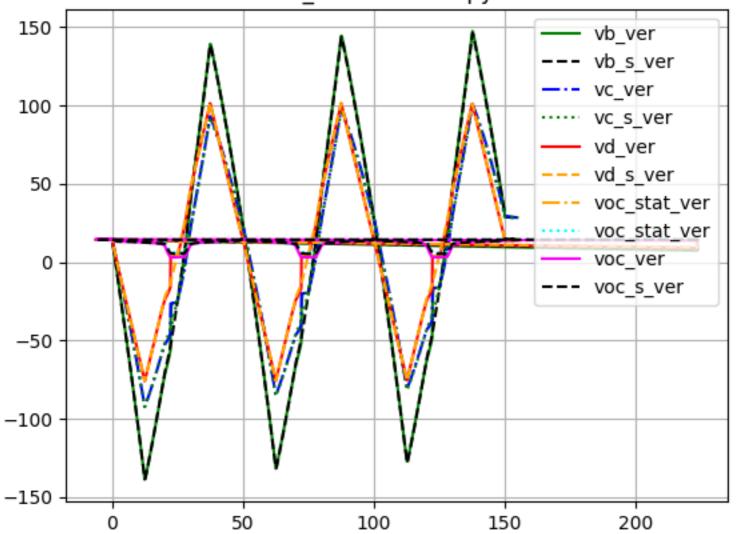
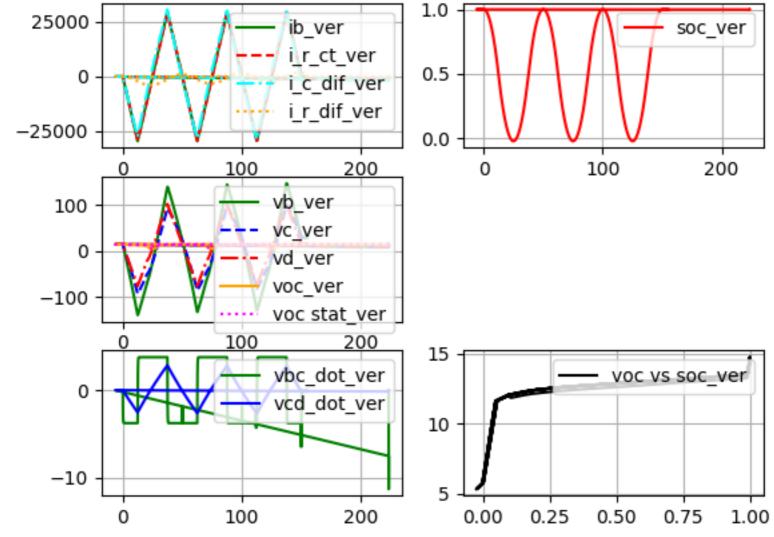
Disch20220910\_mon-MonSim.py 2022-09-11T04-04-03 ib\_ver soc\_ver ioc\_ver soc\_s\_ver 0.5 i r ct ver -500000i\_c\_dif\_ver i r dif ver 0.0 100 200 100 200 50 vb ver vbc dot ver 100 vcd\_dot\_ver vb s ver 25 0 vc ver vc s ver -1000 vd ver <u>vd s ver 200</u> 100 200 15 0.05 SIM voc\_stat vs soc\_ver chm\_ver chm s ver MON voc soc ver vs soc 0.00 10 -0.05100 200 0.00 0.25 0.50 0.75 1.00

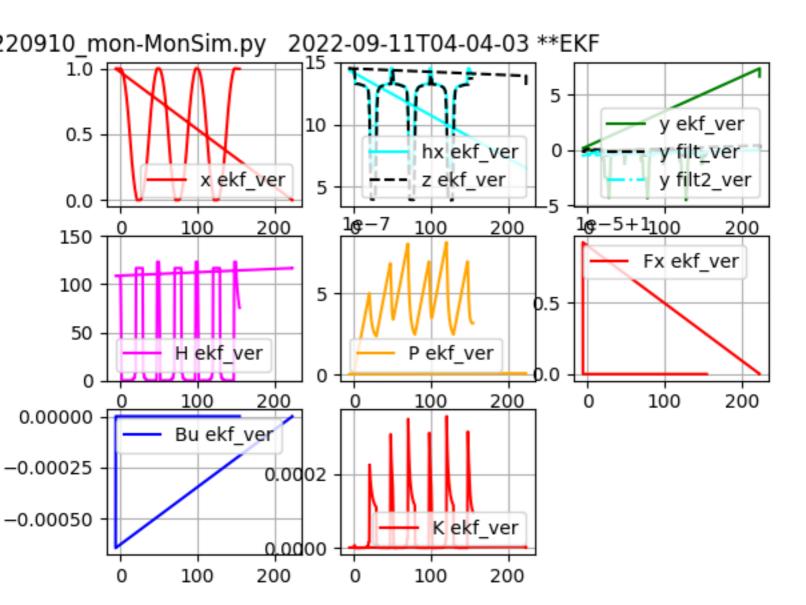
triTweakDisch20220910\_mon-MonSim.py 2022-09-11T04-04-03

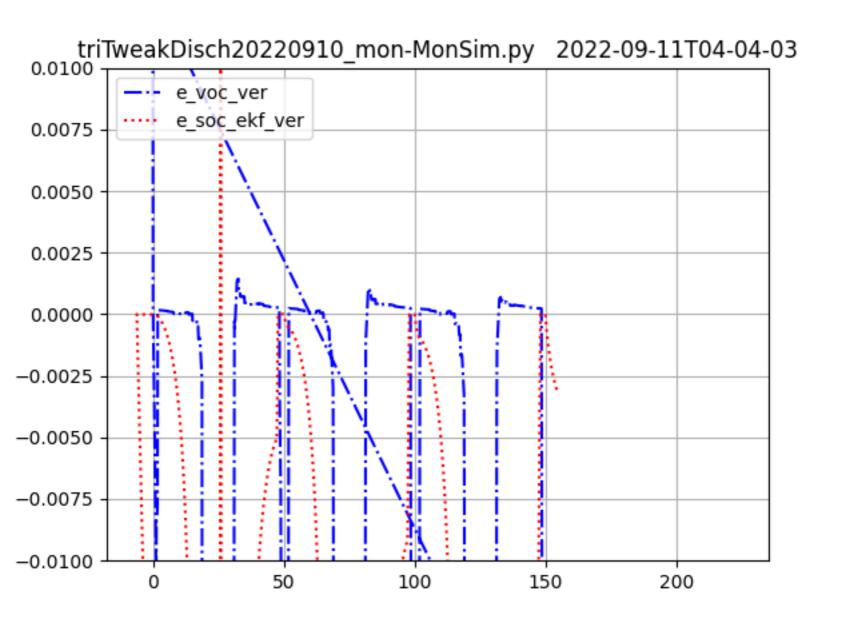


h20220910\_mon-MonSim.py 2022-09-11T04-04-03 \*\*SIM

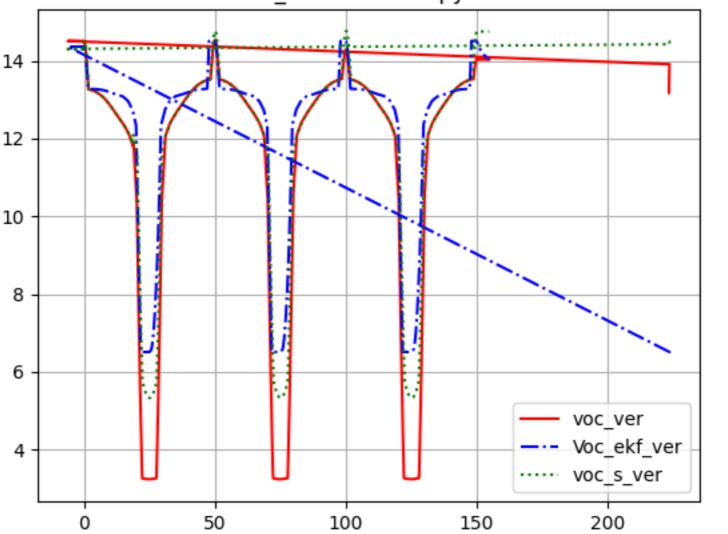


0220910\_mon-MonSim.py 2022-09-11T04-04-03 MON vs SIM 25000 vb ver ib\_ver 100 ib\_s\_ver vb\_s\_ver 0 0 voc\_ver voc s ver -100 -25000100 200 100 200 vb\_ver 100 dv\_dyn\_ver 100 dv\_dyn\_s\_ver vb\_s\_ver 0 0 voc\_ver voc\_s\_ver -100 -100voc stat ver voc stat s2ver 100 200 dv\_hys\_ver vb\_ver 100 0.0 dv hys\_s\_ver vb\_s\_ver 0 -0.5-100 100 200 100 200





triTweakDisch20220910\_mon-MonSim.py 2022-09-11T04-04-03



triTweakDisch20220910\_mon-MonSim.py 2022-09-11T04-04-03 1.0 0.8 0.6 0.4 0.2 soc\_ekf\_ver soc s ver 0.0 soc ver

100

150

200

50

triTweakDisch20220910\_mon-MonSim.py 2022-09-11T04-04-03

