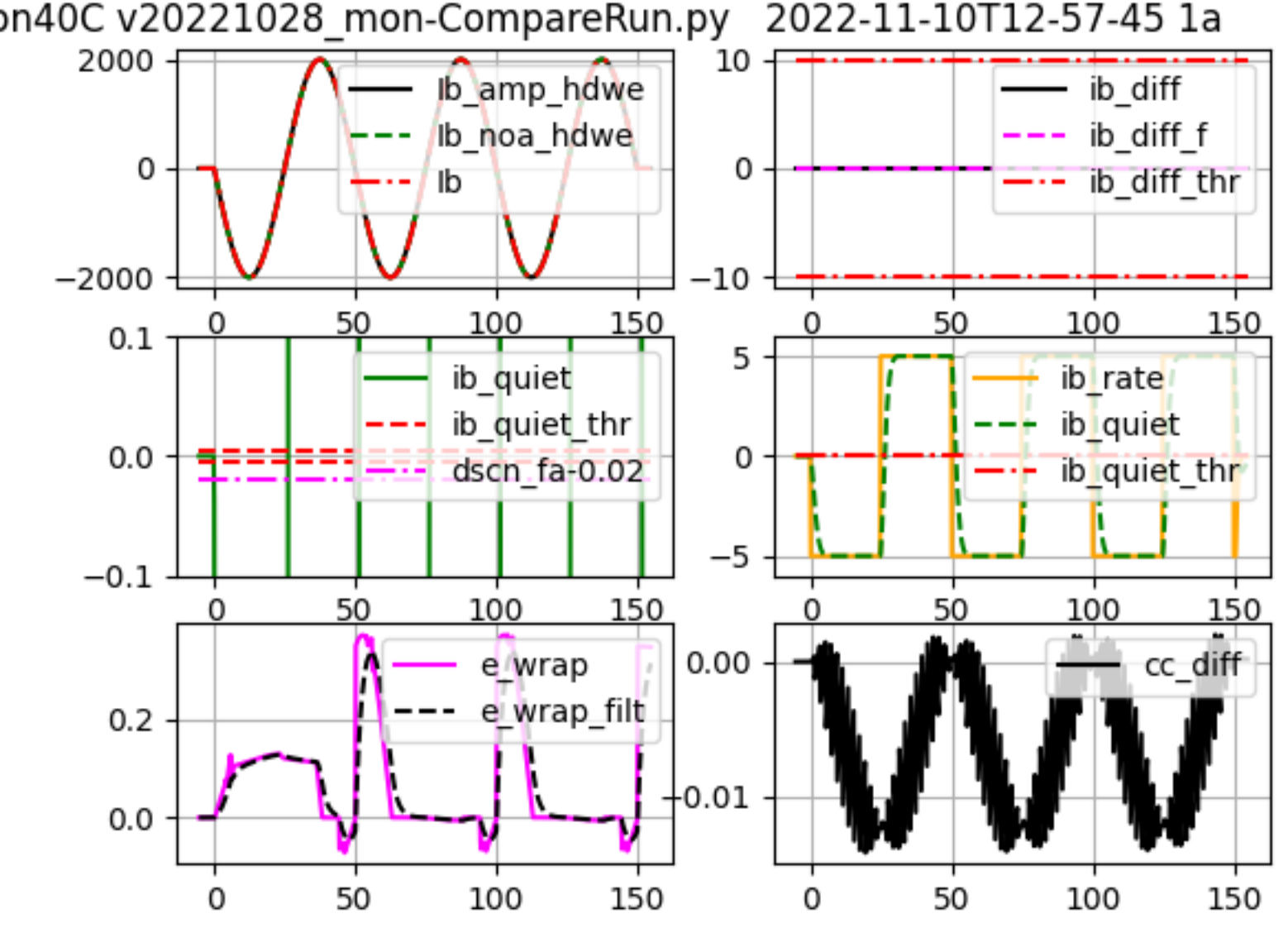
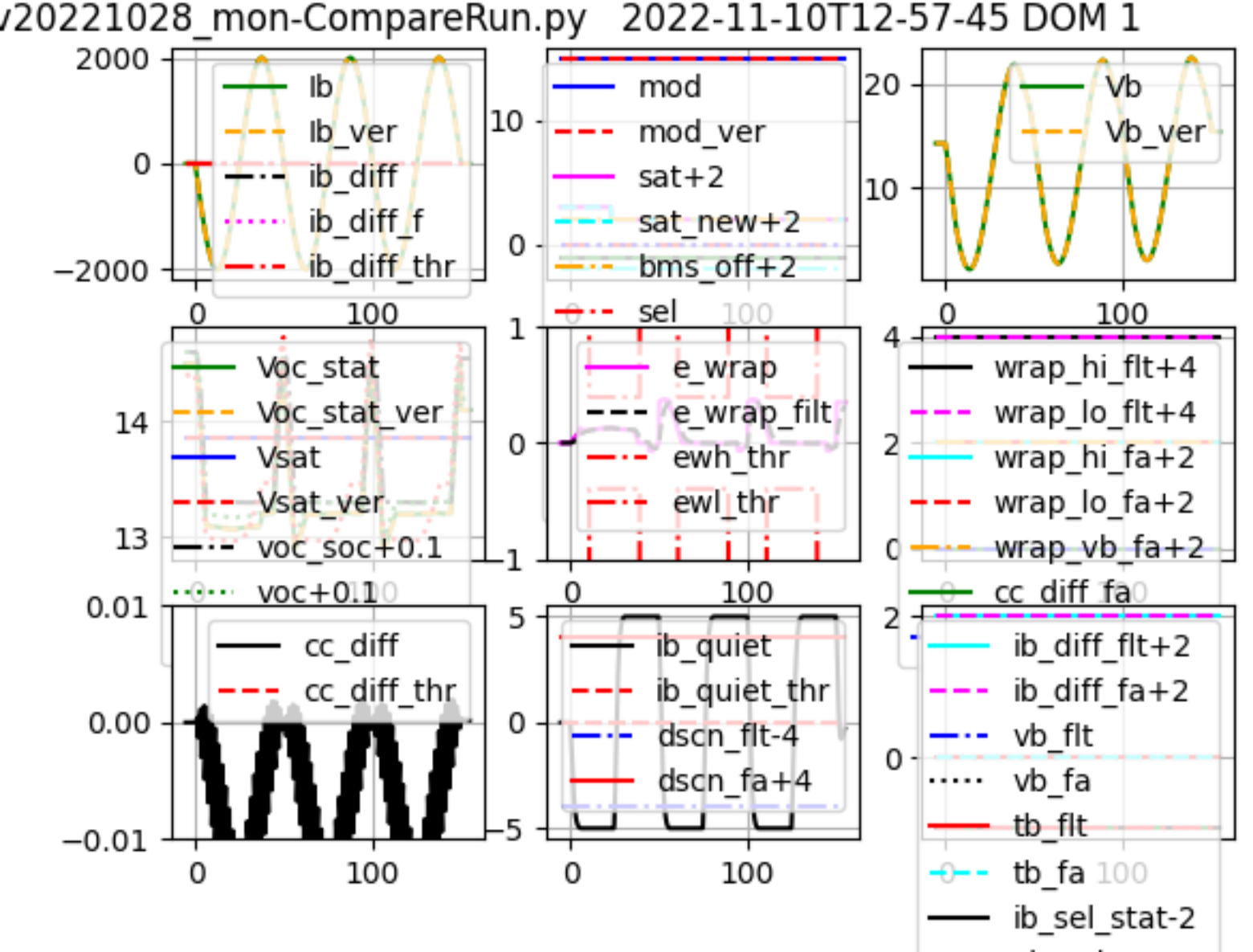
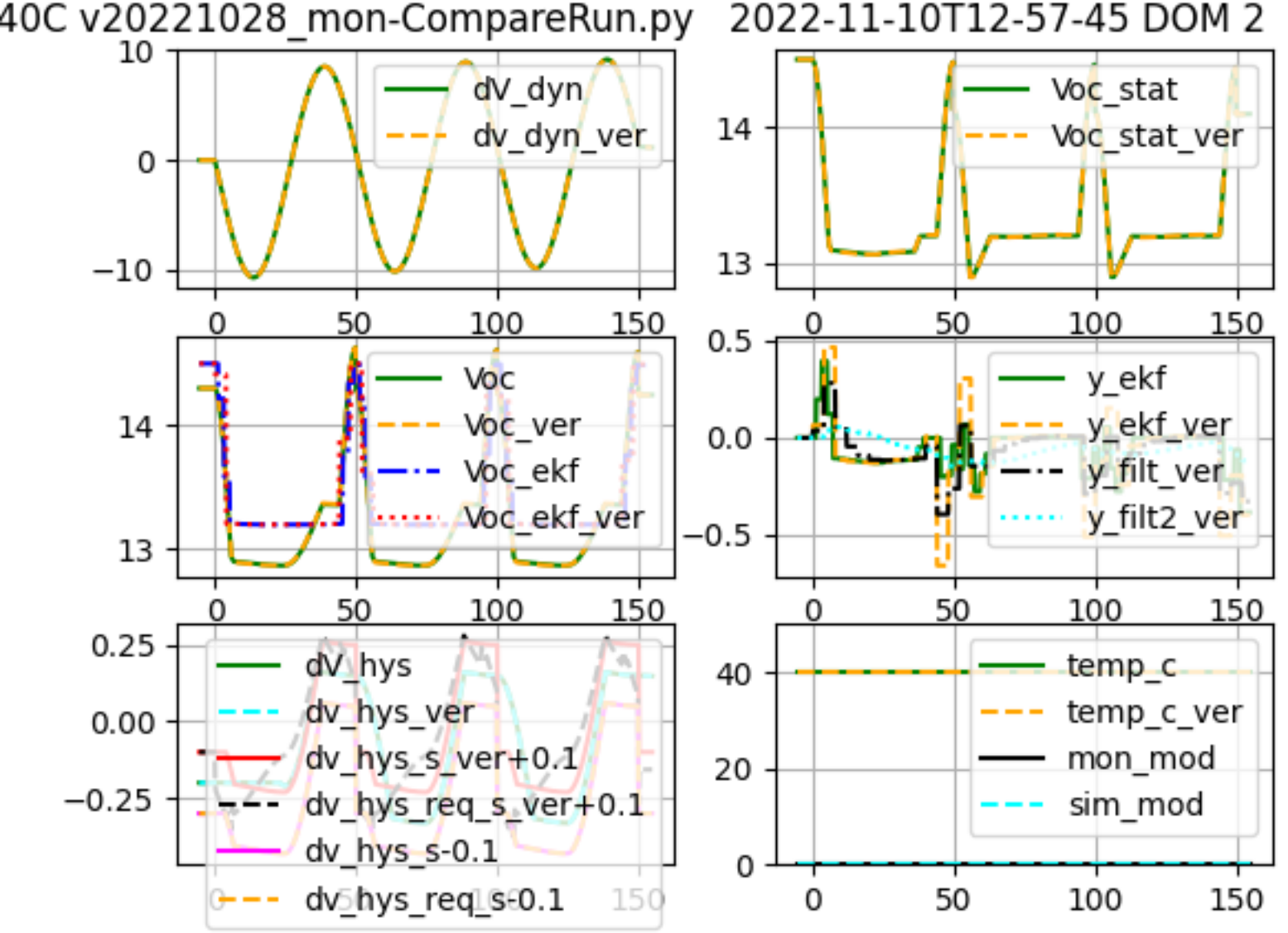
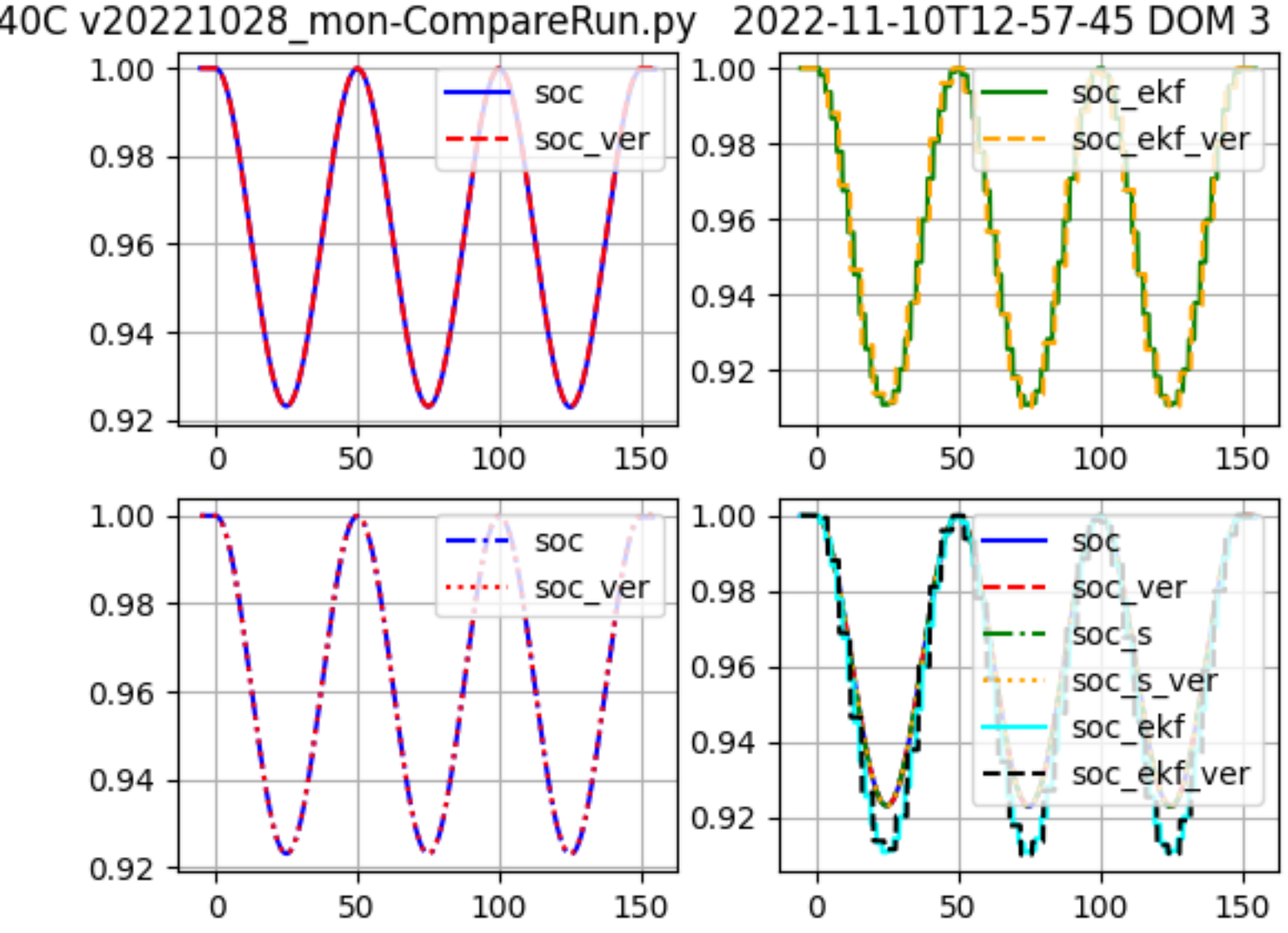
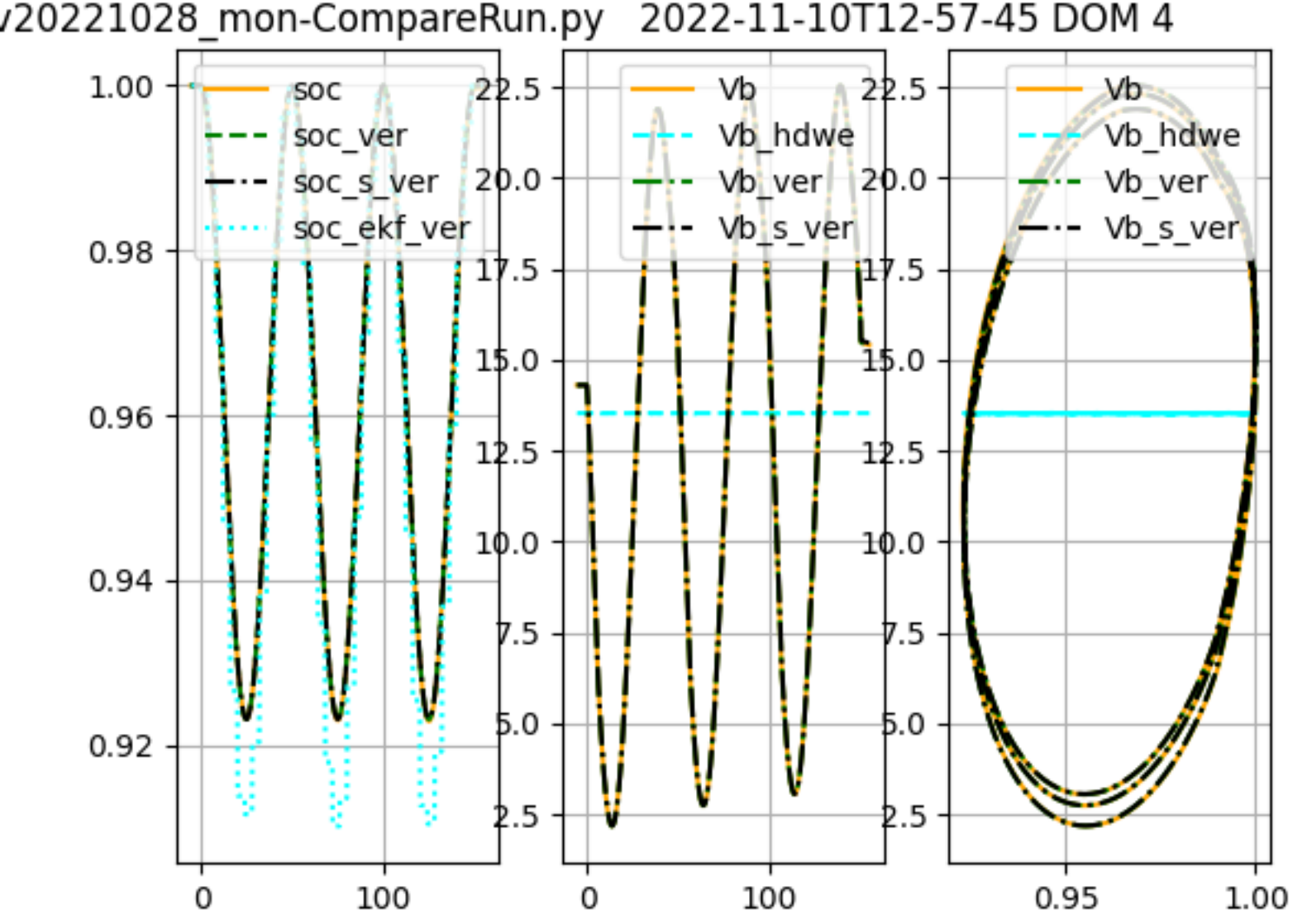
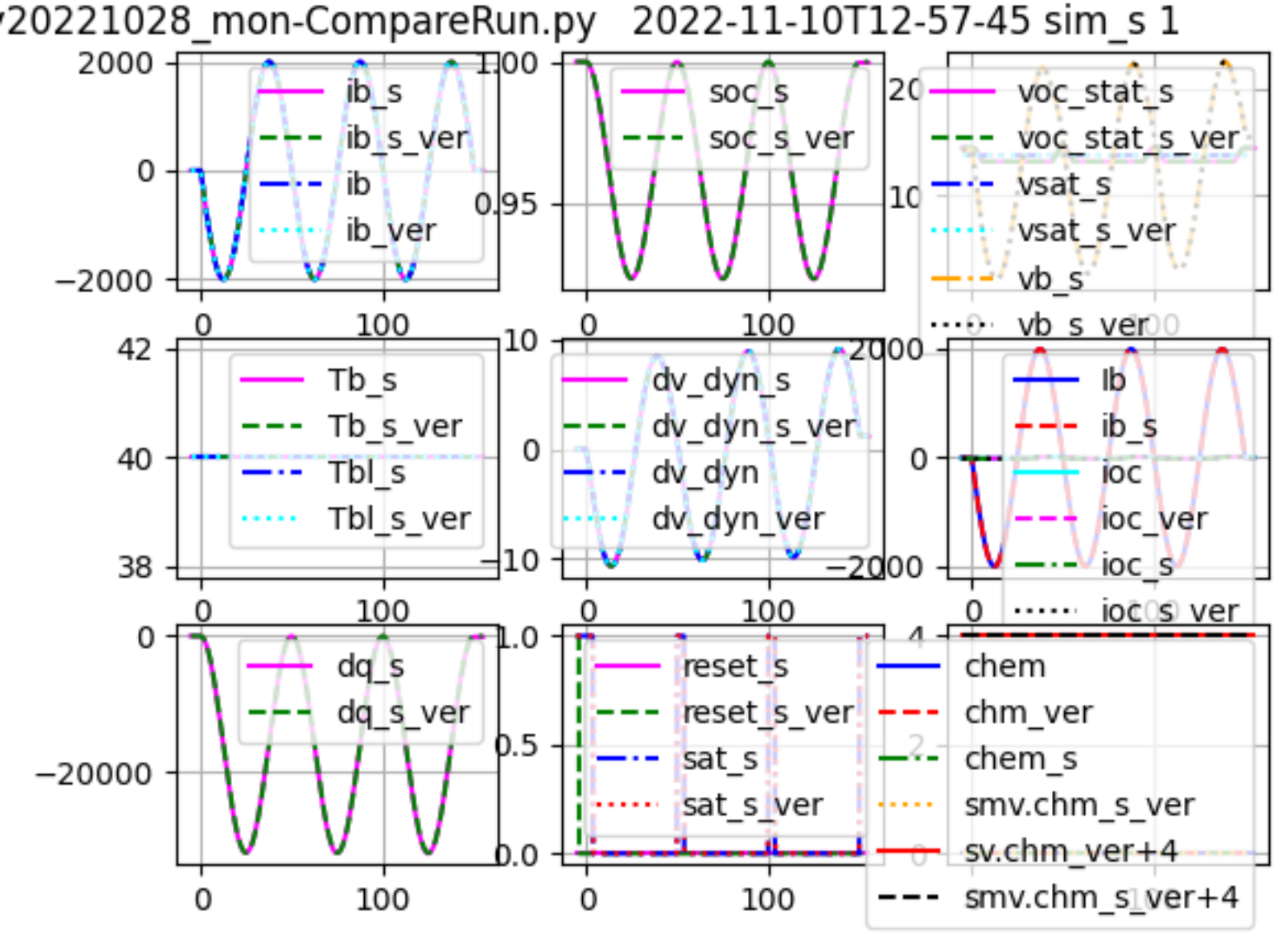

on40C v20221028_mon-CompareRun.py 2022-11-10T12-57-45 1a
2000
0
−2000
Ib_amp_hdwe
Ib_noa_hdwe
Ib
10
0
−10
ib_diff
ib_diff_f
ib_diff_thr
0.1
0.0
−0.1
ib_quiet
ib_quiet_thr
dscn_fa-0.02
5
0
−5
ib_rate
ib_quiet
ib_quiet_thr
0.2
0.0
e_wrap
e_wrap_filt
0.00
−0.01
cc_diff
0
50
100
150

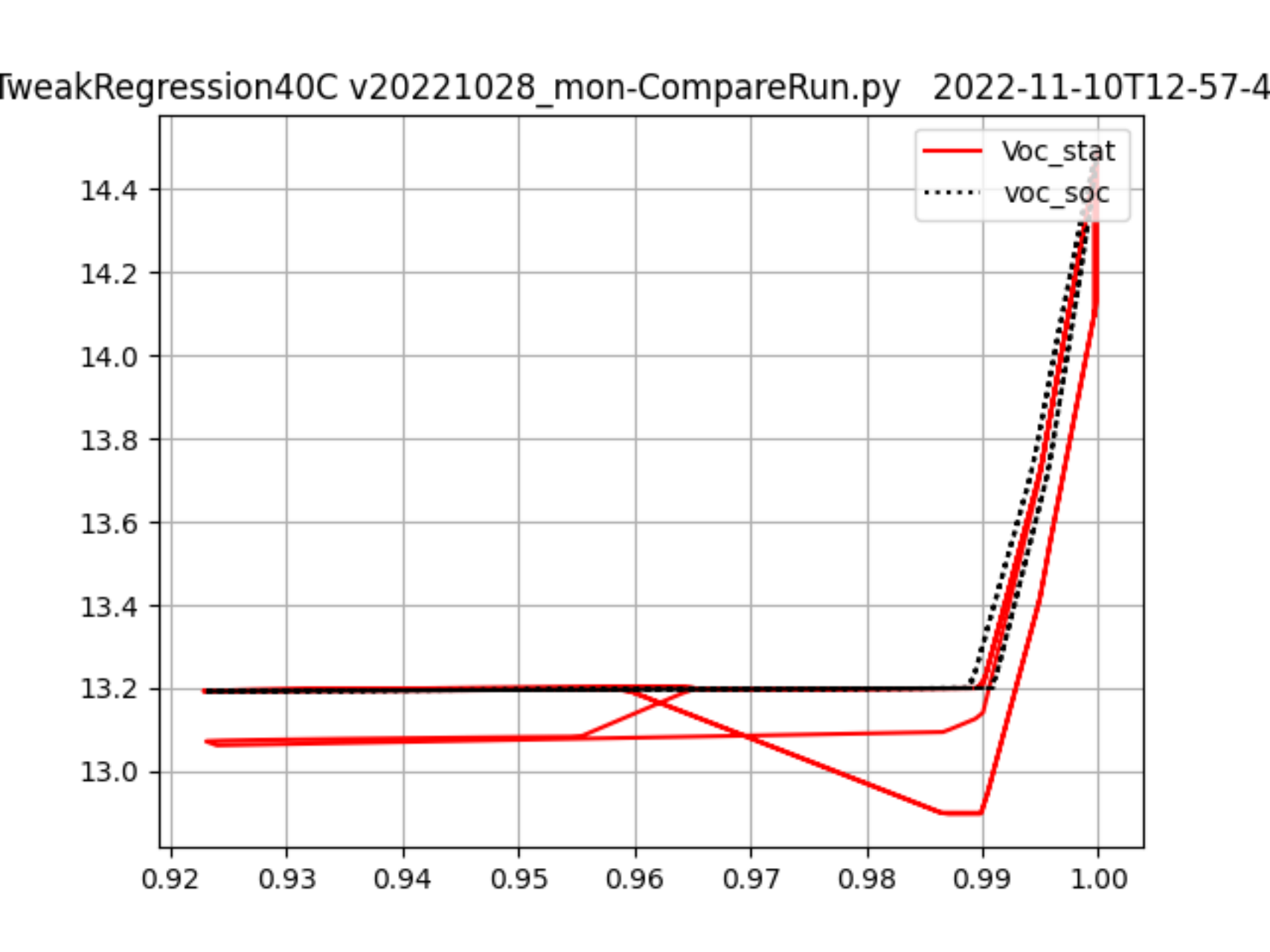

v20221028_mon-CompareRun.py 2022-11-10T12-57-45 DOM 1
Ib
Ib_ver
ib_diff
ib_diff_f
ib_diff_thr
mod
mod_ver
sat+2
sat_new+2
bms_off+2
sel
Vb
Vb_ver
Voc_stat
Voc_stat_ver
Vsat
Vsat_ver
voc_soc+0.1
voc+0.1
e_wrap
e_wrap_filt
ewh_thr
ewl_thr
wrap_hi_flt+4
wrap_lo_flt+4
wrap_hi_fa+2
wrap_lo_fa+2
wrap_vb_fa+2
cc_diff_fa
cc_diff
cc_diff_thr
ib_quiet
ib_quiet_thr
dscn_flt-4
dscn_fa+4
ib_diff_flt+2
ib_diff_fa+2
vb_flt
vb_fa
tb_flt
tb_fa
ib_sel_stat-2

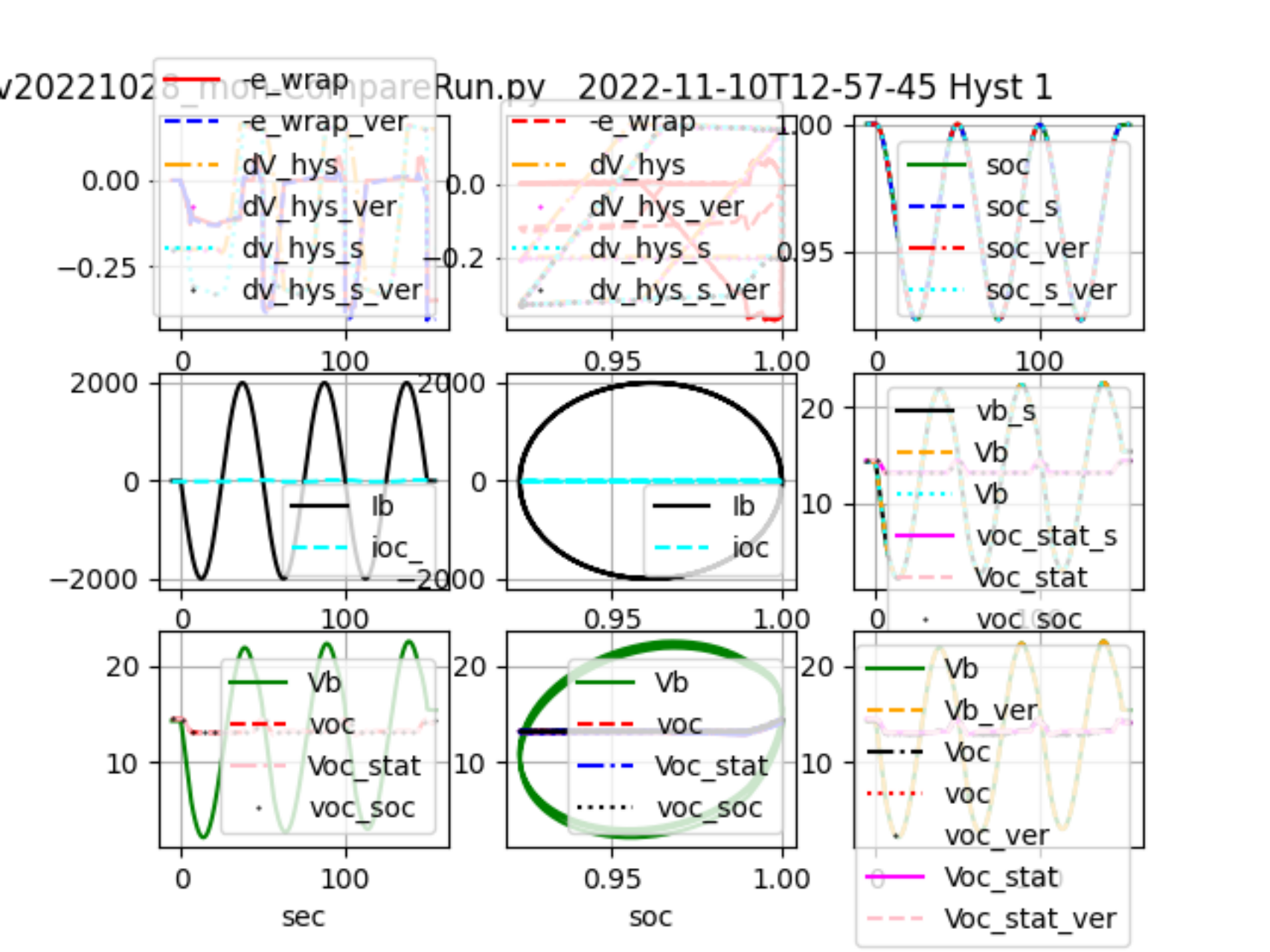

40C v20221028_mon-CompareRun.py 2022-11-10T12-57-45 DOM 2
dV_dyn
dv_dyn_ver
Voc_stat
Voc_stat_ver
Voc
Voc_ver
Voc_ekf
Voc_ekf_ver
y_ekf
y_ekf_ver
y_filt_ver
y_filt2_ver
dV_hys
dv_hys_ver
dv_hys_s_ver+0.1
dv_hys_req_s_ver+0.1
dv_hys_s-0.1
dv_hys_req_s-0.1
temp_c
temp_c_ver
mon_mod
sim_mod

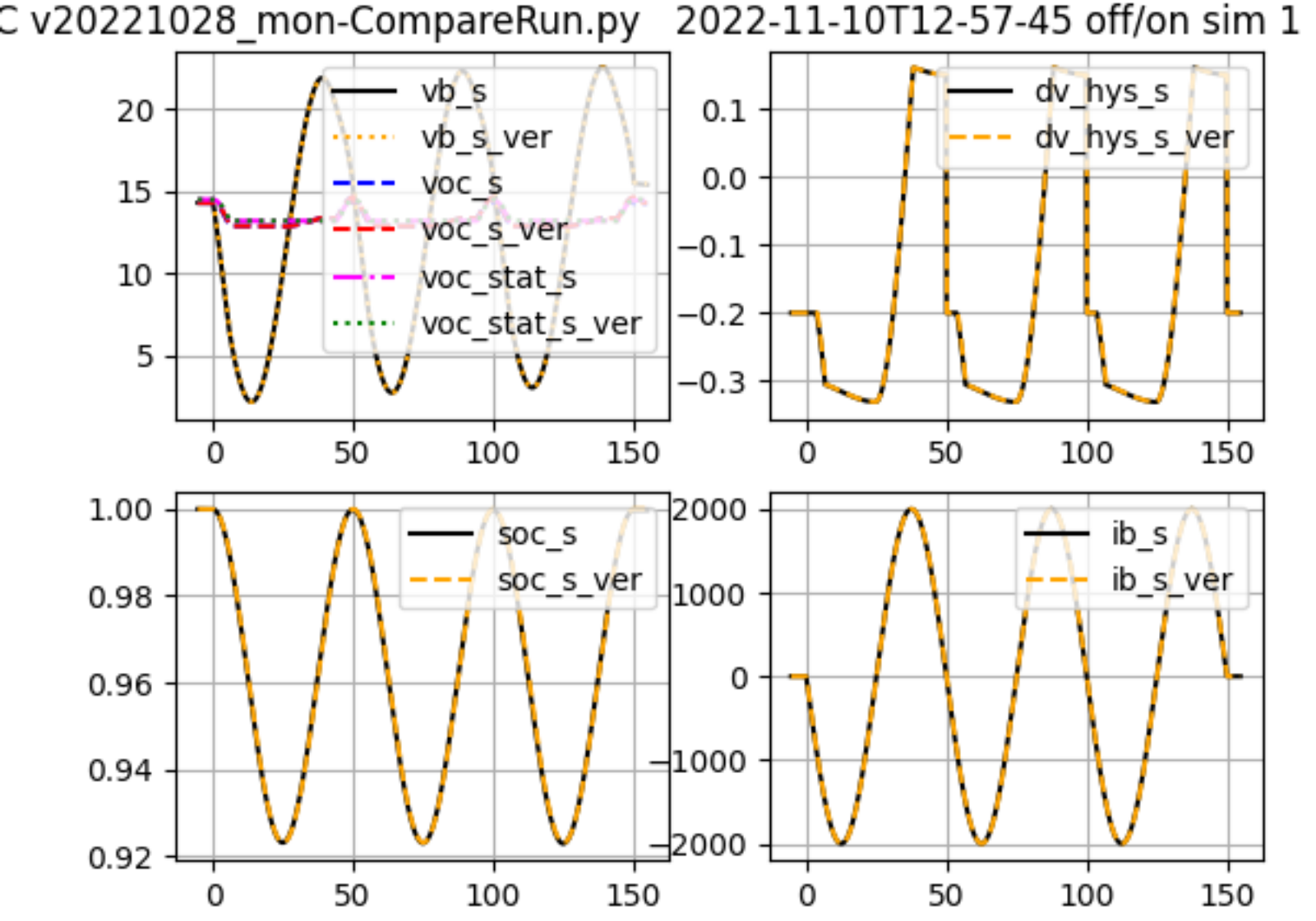

40C v20221028_mon-CompareRun.py 2022-11-10T12-57-45 DOM 3
soc
soc_ver
soc_ekf
soc_ekf_ver
soc
soc_ver
soc
soc_ver
soc_s
soc_s_ver
soc_ekf
soc_ekf_ver
1.00
0.98
0.96
0.94
0.92
0
50
100
150


v20221028_mon-CompareRun.py 2022-11-10T12-57-45 DOM 4
soc
soc_ver
soc_s_ver
soc_ekf_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver
1.00
0.98
0.96
0.94
0.92
22.5
20.0
17.5
15.0
12.5
10.0
7.5
5.0
2.5
0
100
0
100
0.95
1.00


r20221028_mon-CompareRun.py 2022-11-10T12-57-45 sim_s 1
ib_s
ib_s_ver
ib
ib_ver
soc_s
soc_s_ver
voc_stat_s
voc_stat_s_ver
vsat_s
vsat_s_ver
vb_s
vb_s_ver
Tb_s
Tb_s_ver
Tbl_s
Tbl_s_ver
dv_dyn_s
dv_dyn_s_ver
dv_dyn
dv_dyn_ver
Ib
ib_s
ioc
ioc_ver
ioc_s
ioc_s_ver
dq_s
dq_s_ver
reset_s
reset_s_ver
sat_s
sat_s_ver
chem
chm_ver
chem_s
smv.chm_s_ver
sv.chm_ver+4
smv.chm_s_ver+4


TweakRegression40C v20221028_mon-CompareRun.py 2022-11-10T12-57-4
Voc_stat
voc_soc
14.4
14.2
14.0
13.8
13.6
13.4
13.2
13.0
0.92
0.93
0.94
0.95
0.96
0.97
0.98
0.99
1.00


v20221028_modCompareRun.py 2022-11-10T12-57-45 Hyst 1
e_wrap
e_wrap_ver
dV_hys
dV_hys_ver
dv_hys_s
dv_hys_s_ver
soc
soc_s
soc_ver
soc_s_ver
Ib
ioc
vb_s
Vb
Vb
voc_stat_s
Voc_stat
voc_soc
Vb
voc
Voc_stat
voc_soc
Vb_ver
Voc
voc_ver
Voc_stat_ver
sec
soc


C v20221028_mon-CompareRun.py 2022-11-10T12-57-45 off/on sim 1
vb_s
vb_s_ver
voc_s
voc_s_ver
voc_stat_s
voc_stat_s_ver
dv_hys_s
dv_hys_s_ver
soc_s
soc_s_ver
ib_s
ib_s_ver