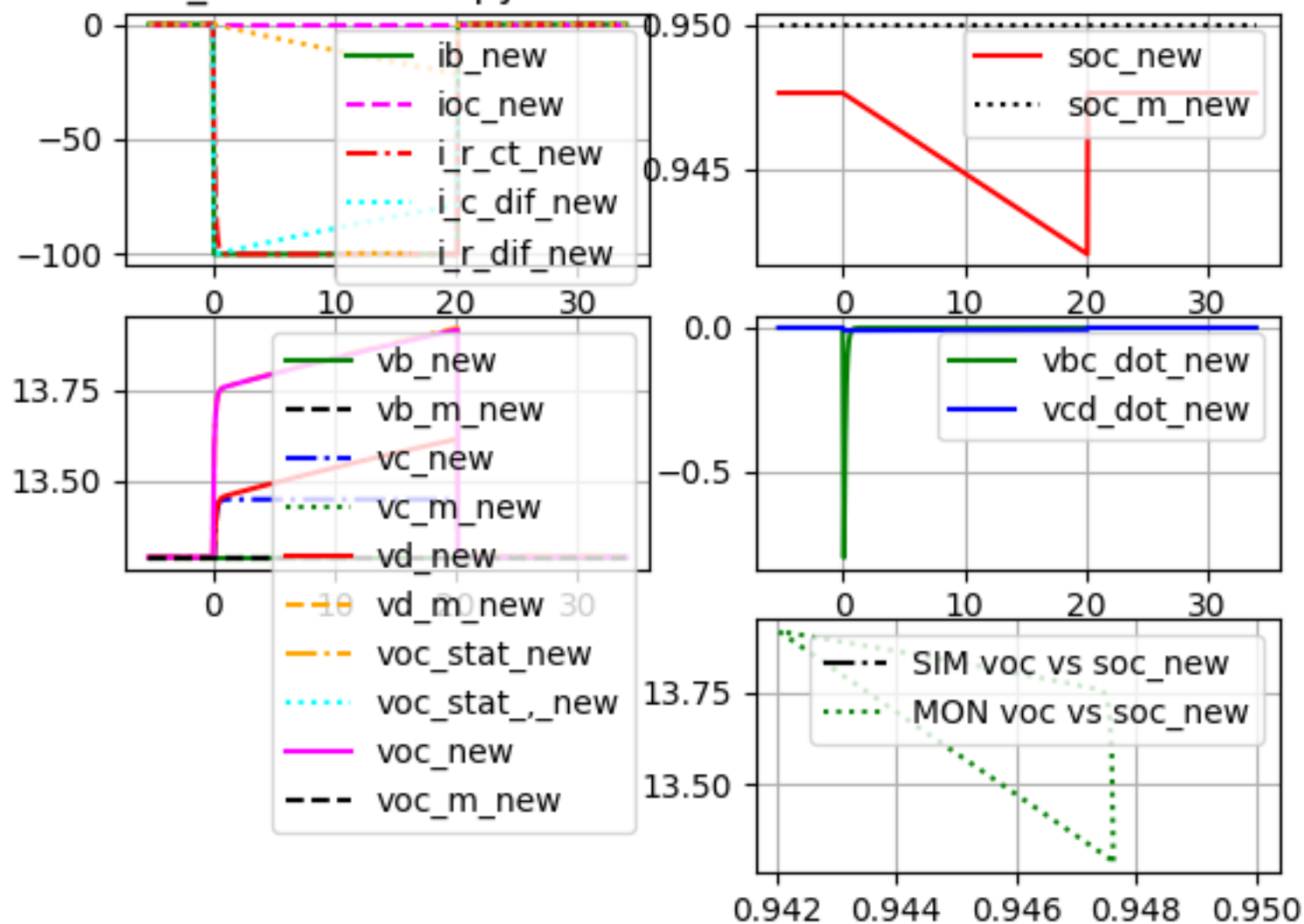
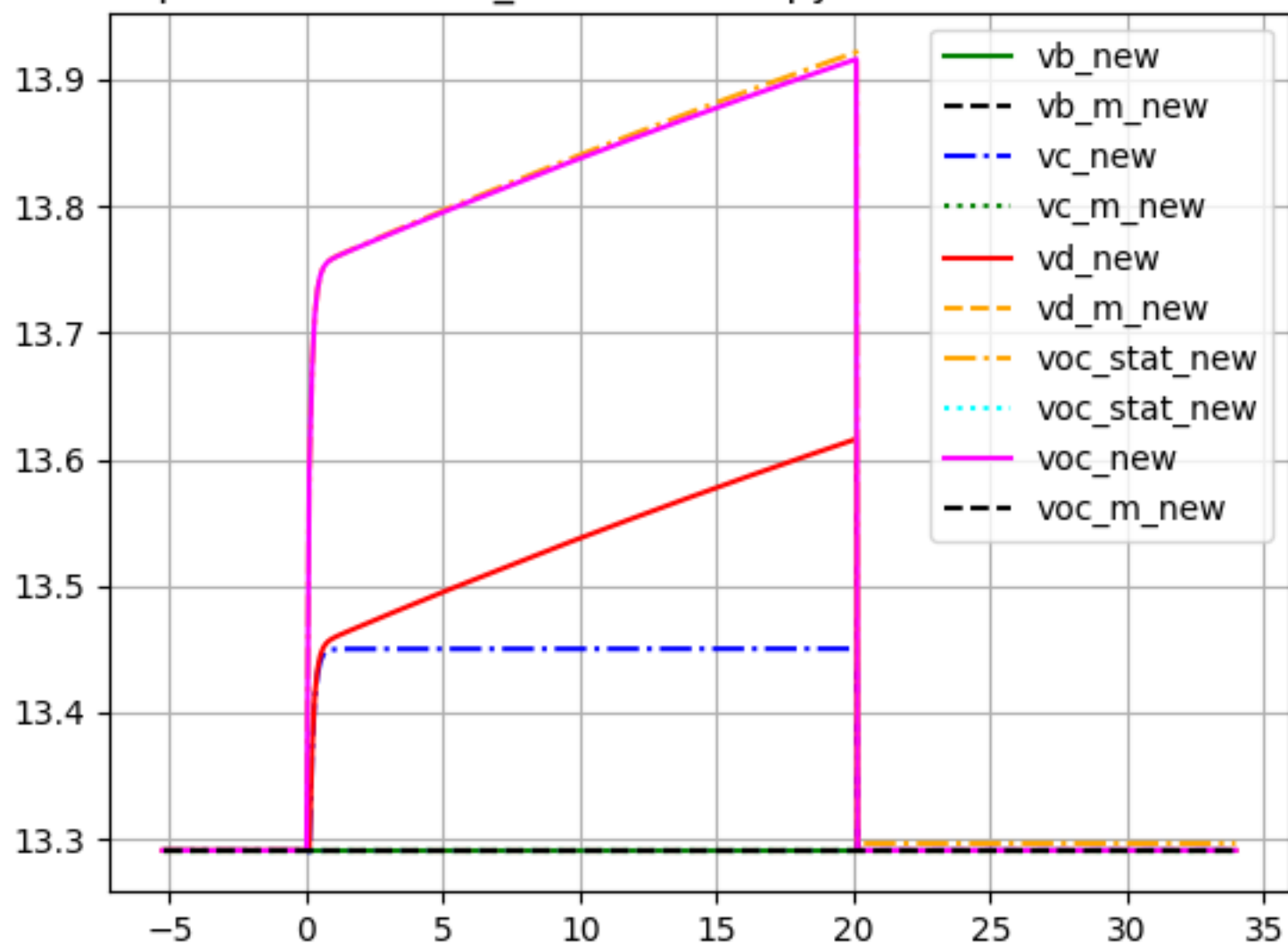


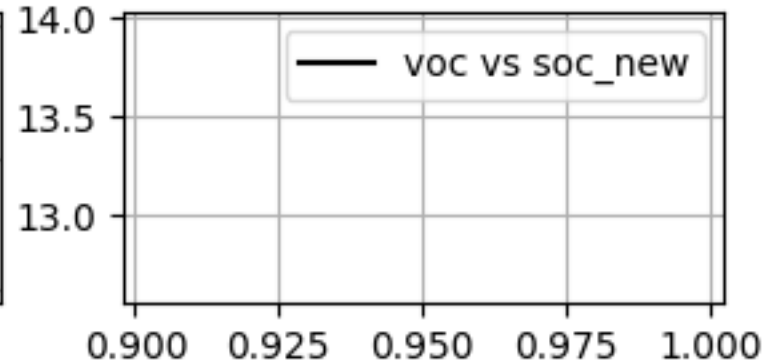
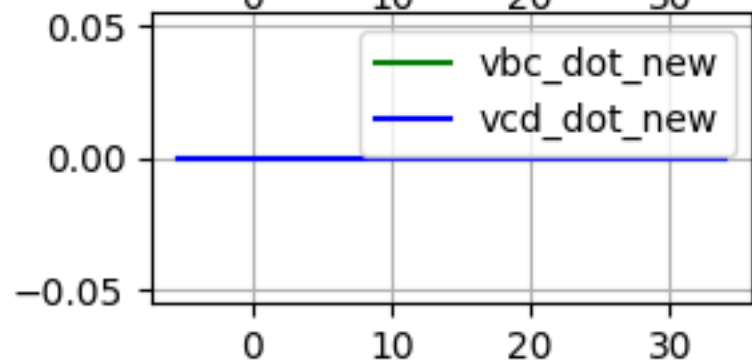
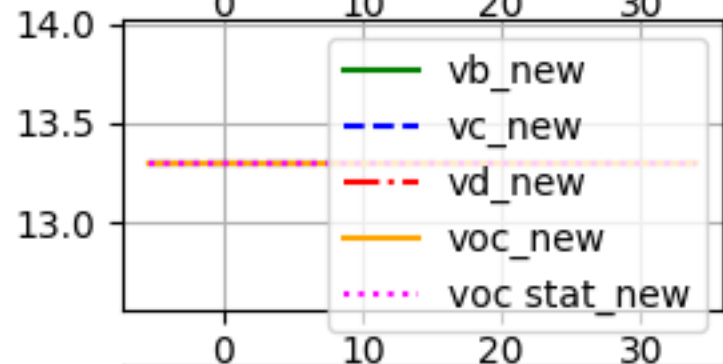
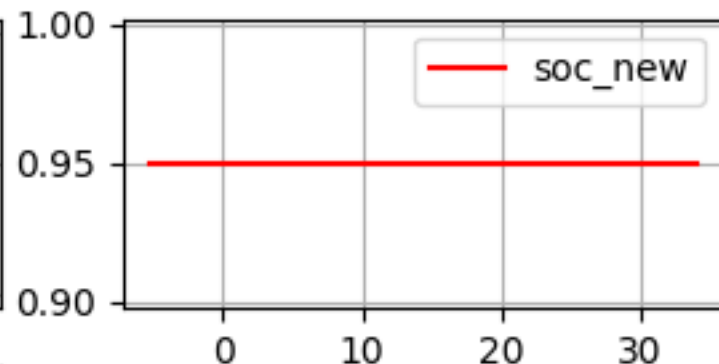
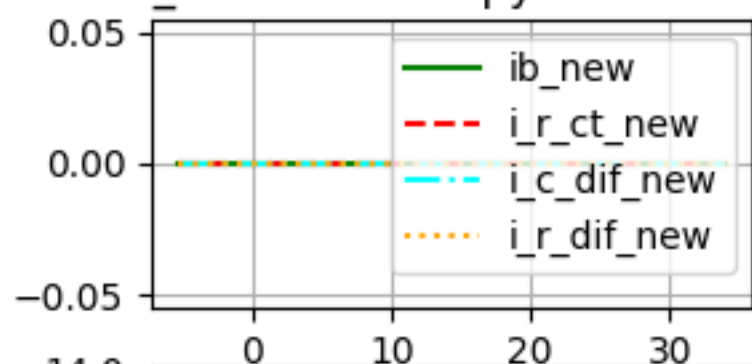
Fail20220809\_mon-MonSim.py 2022-08-09T05-14-55



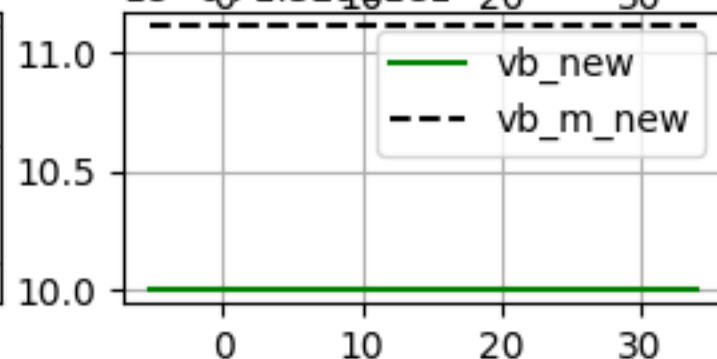
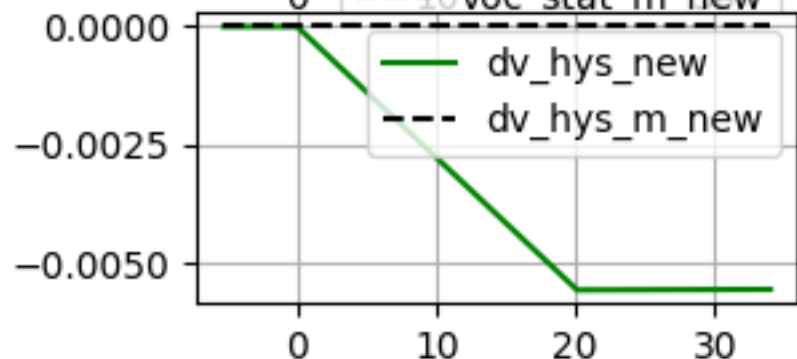
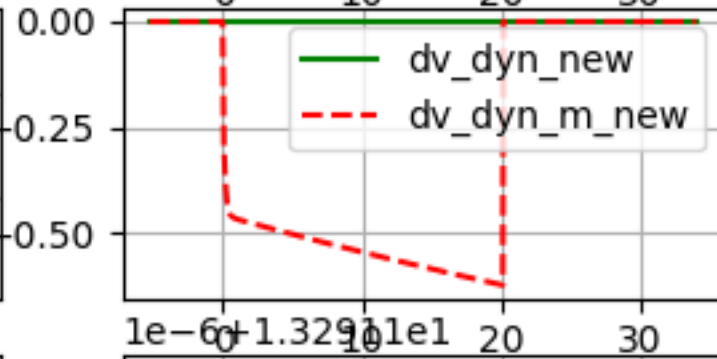
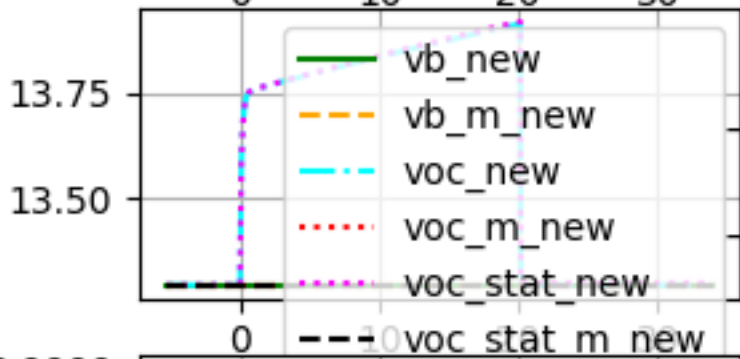
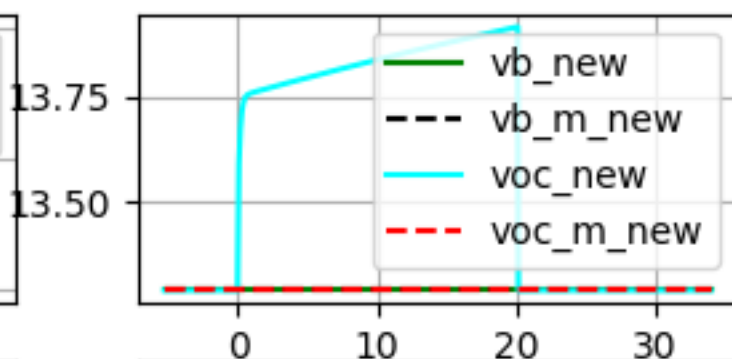
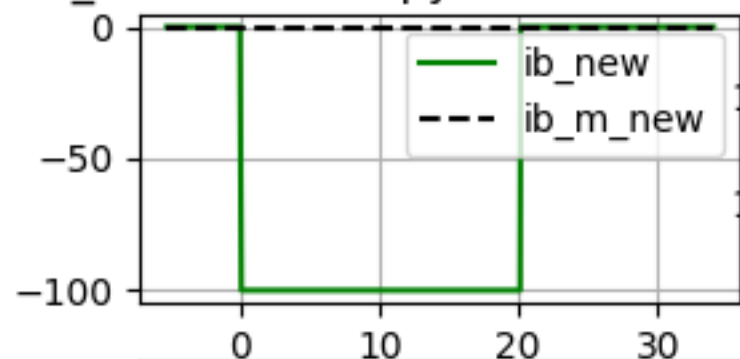
ampLoFail20220809 mon-MonSim.py 2022-08-09T05-14-55



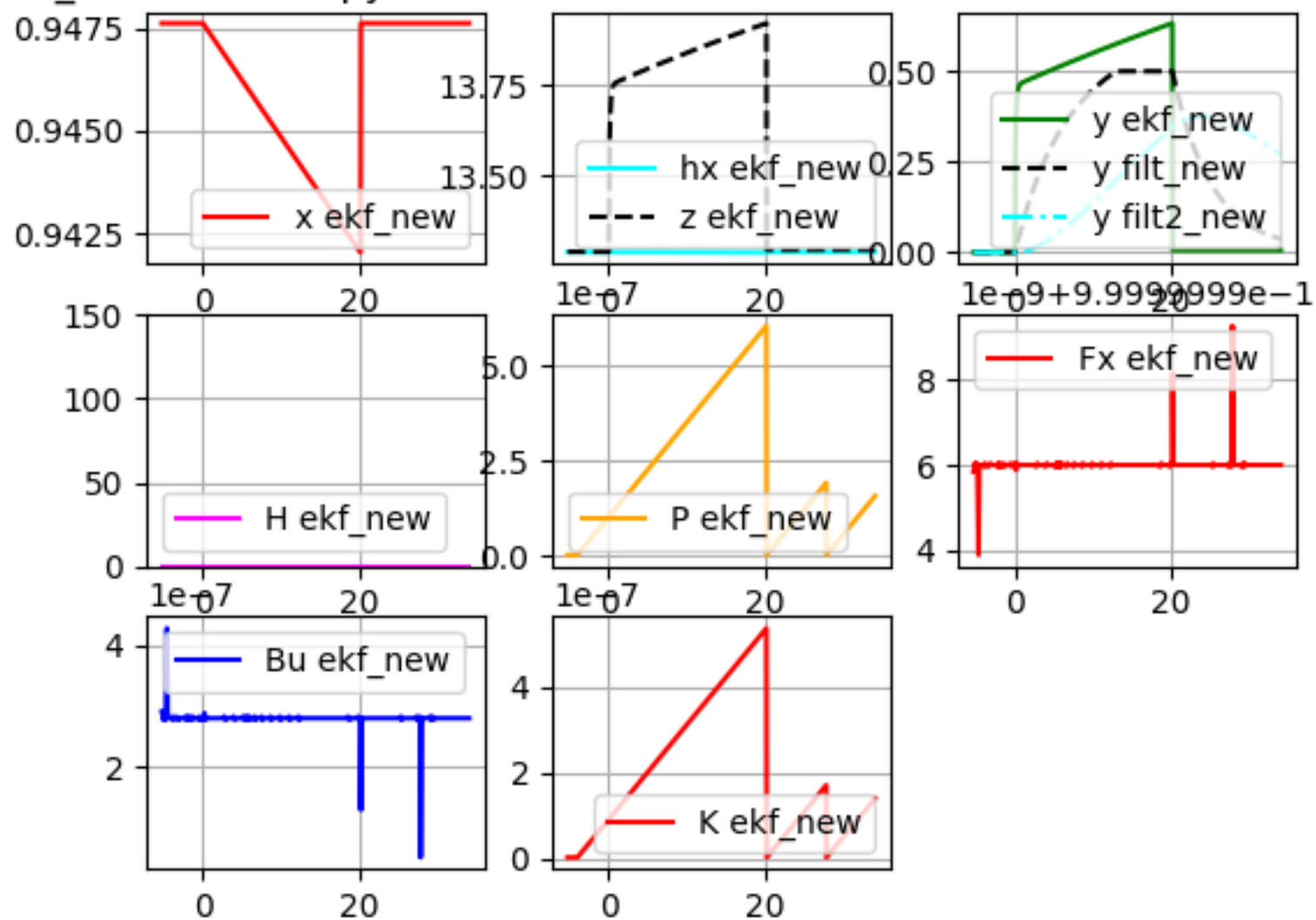
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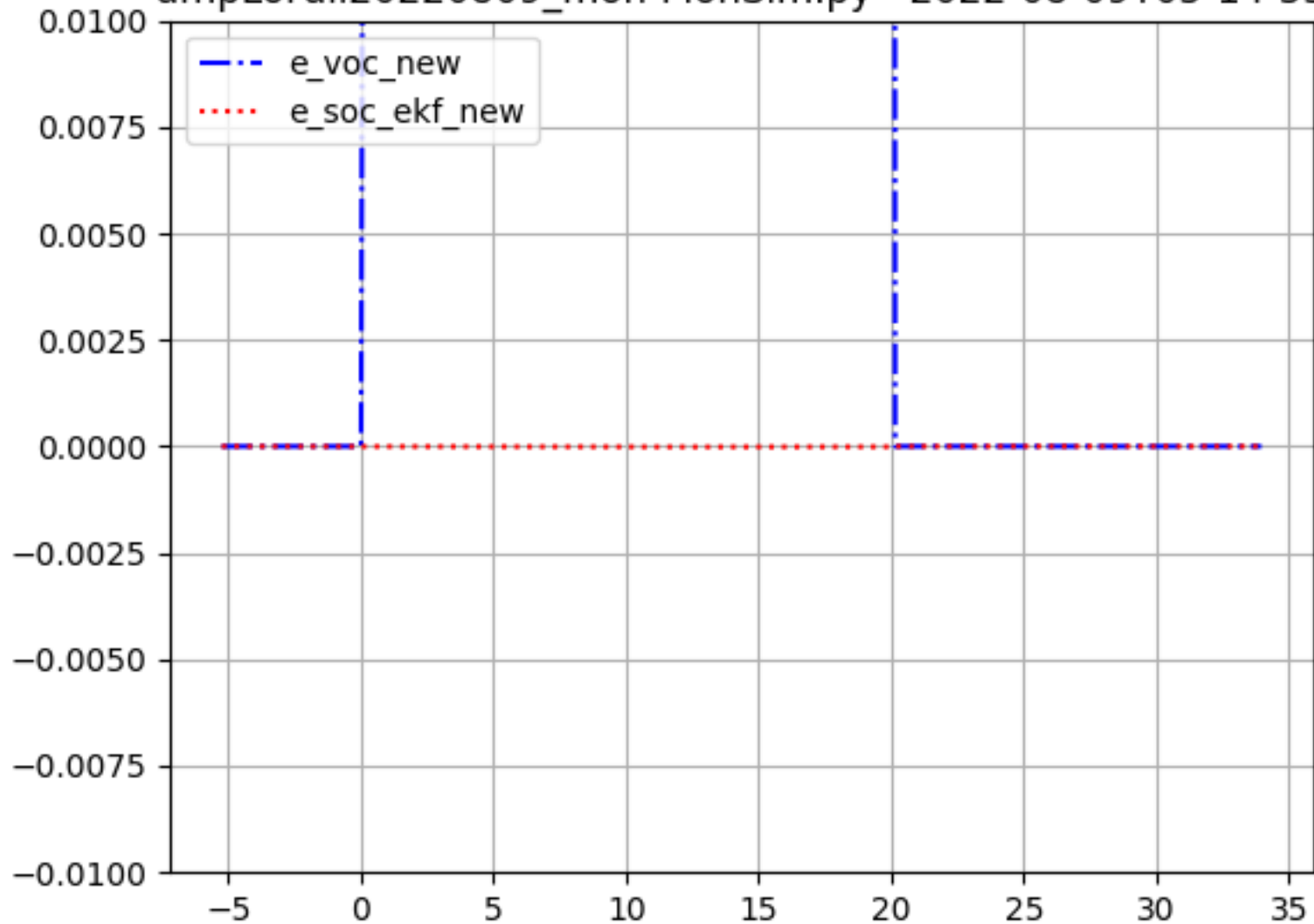
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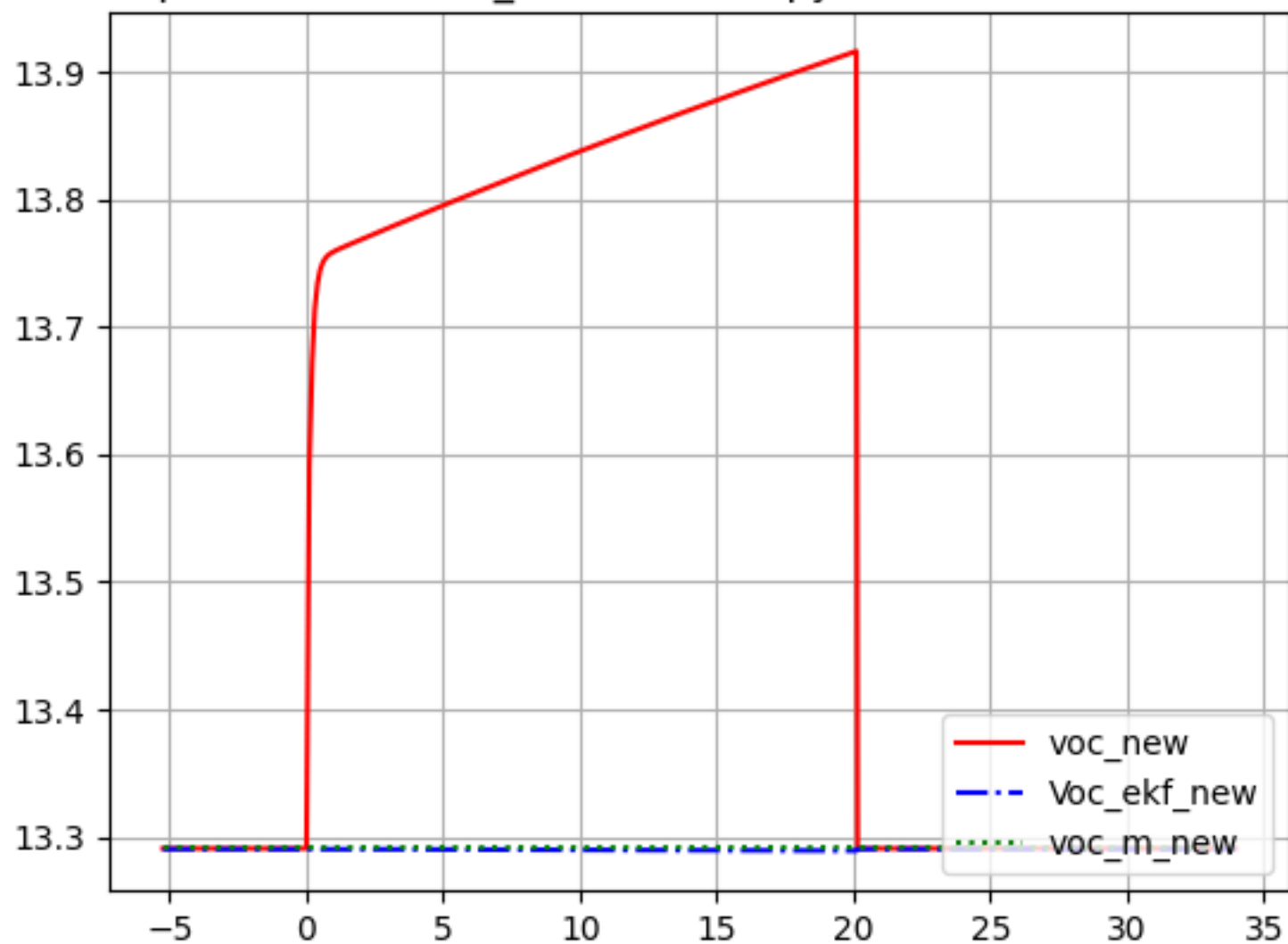
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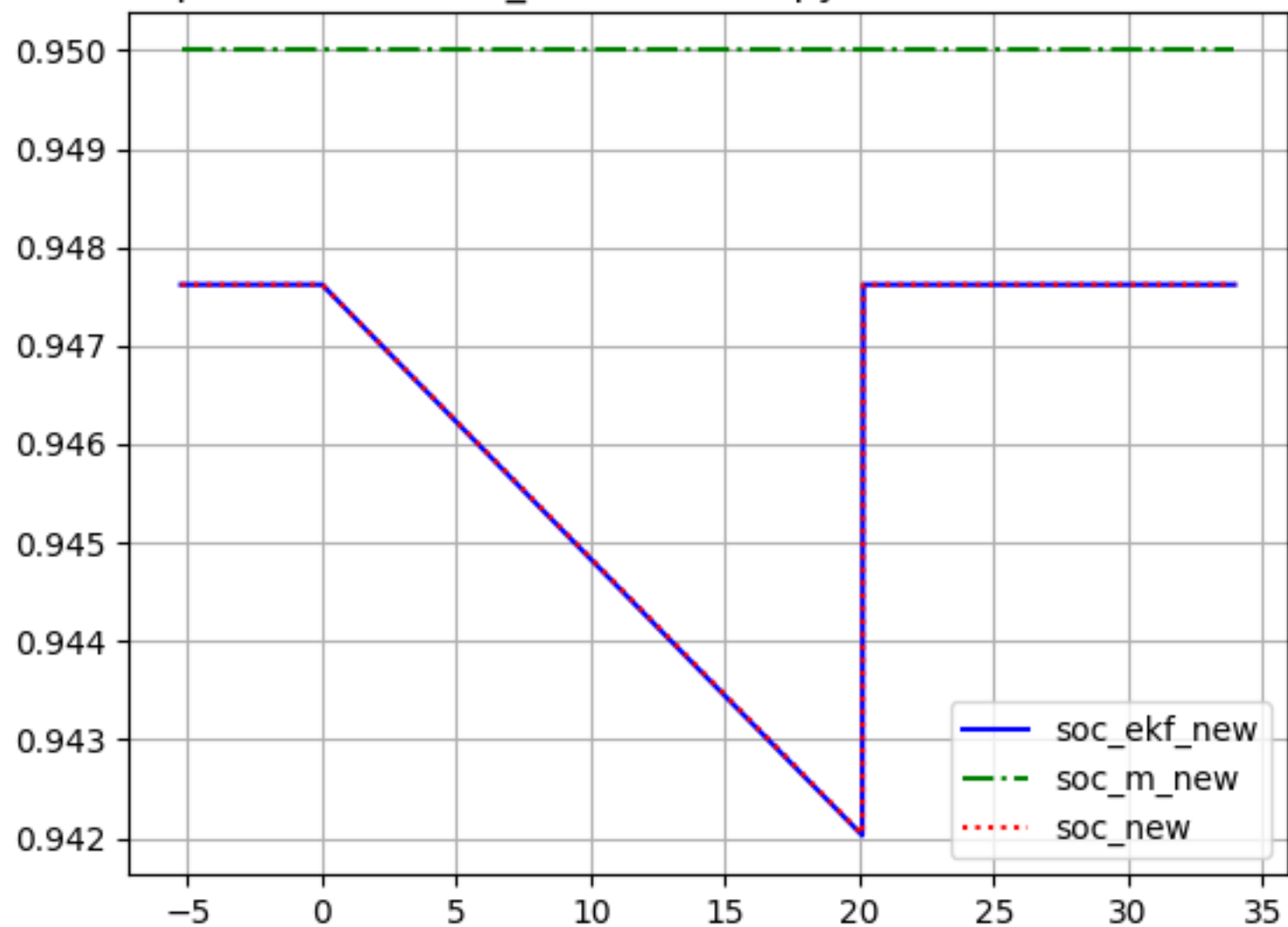
ampLoFail20220809\_mon-MonSim.py 2022-08-09T05-14-55



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ampLoFail20220809\_mon-MonSim.py 2022-08-09T05-14-55





ampLoFail20220809\_mon-MonSim.py 2022-08-09T05-14-55

