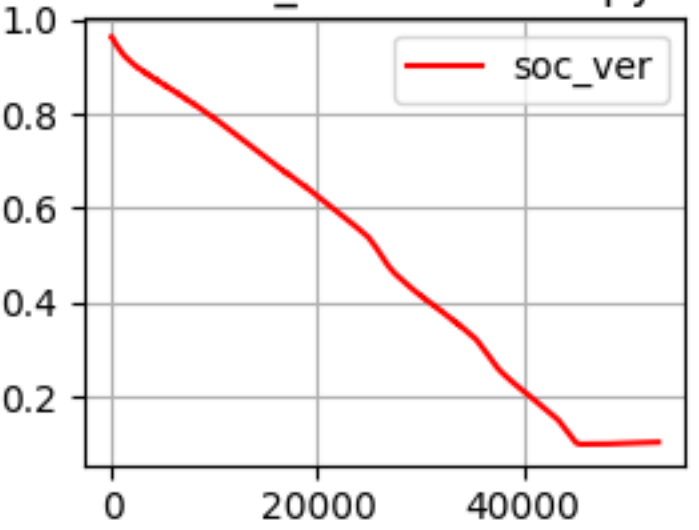
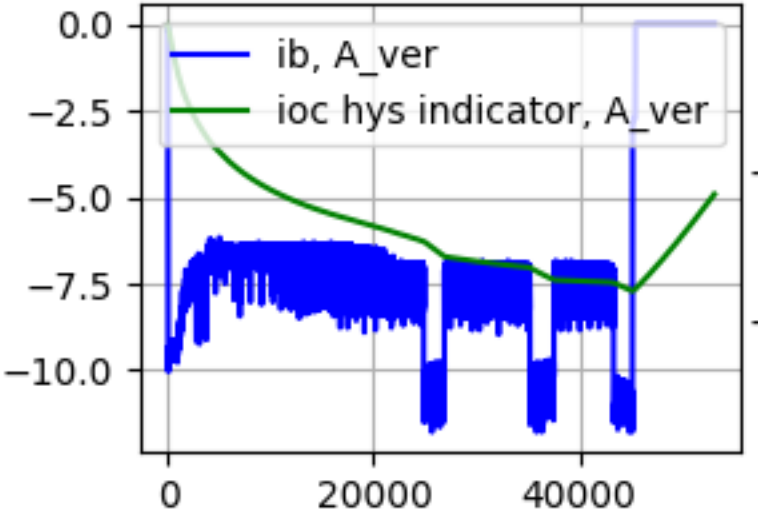
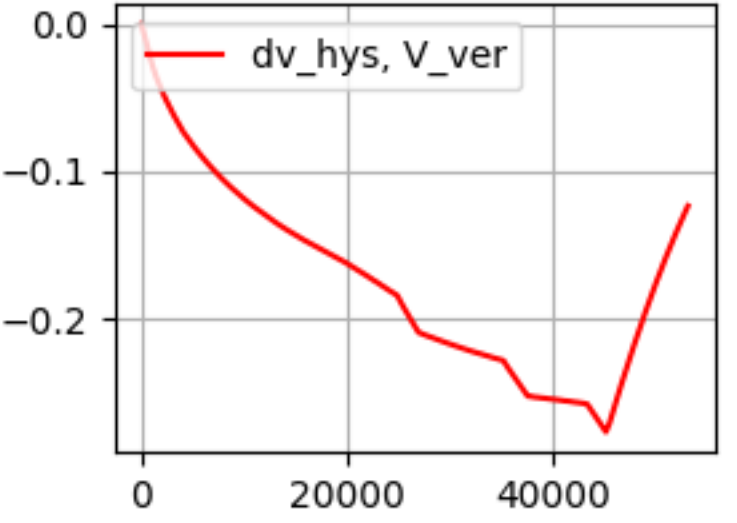
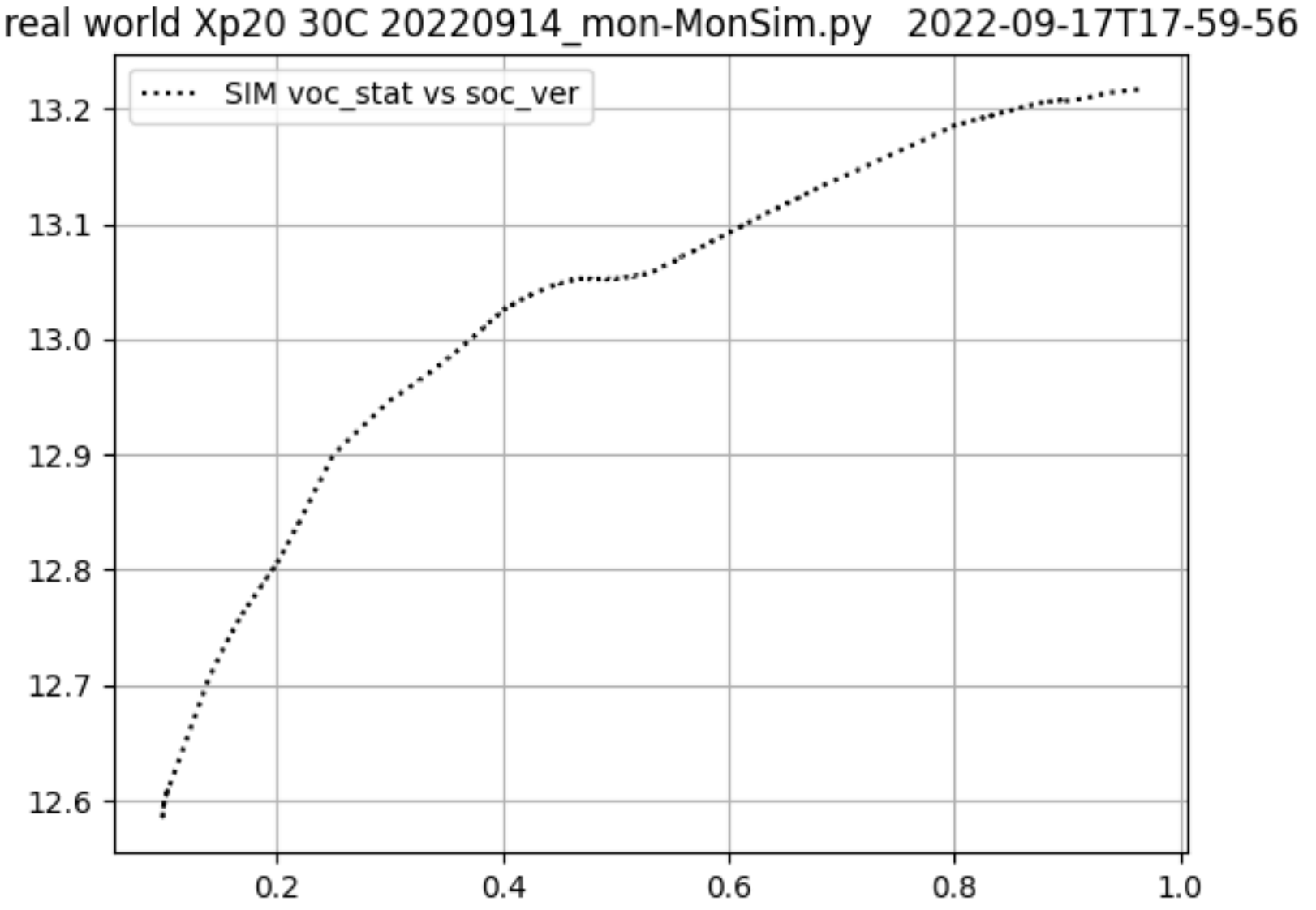
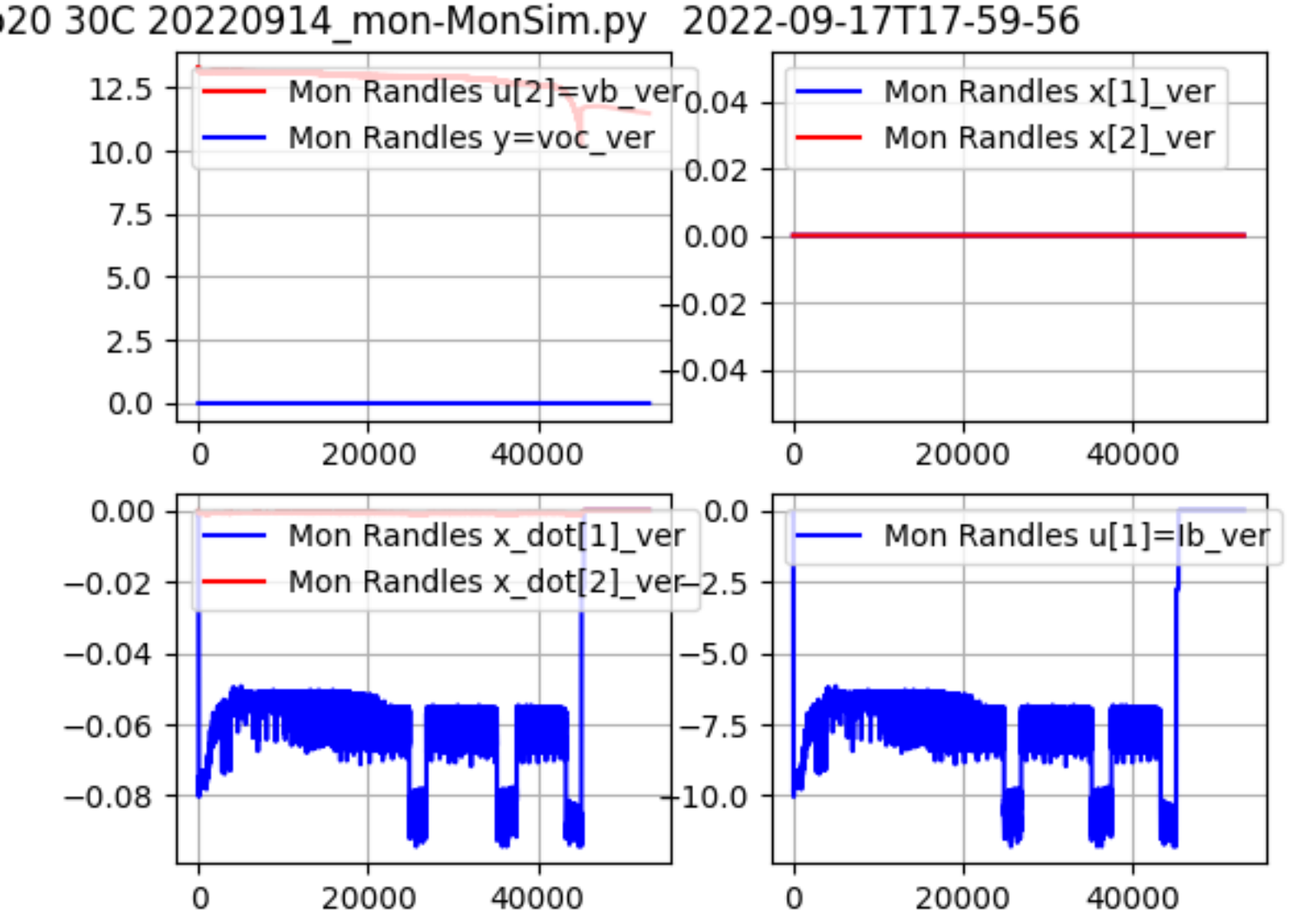
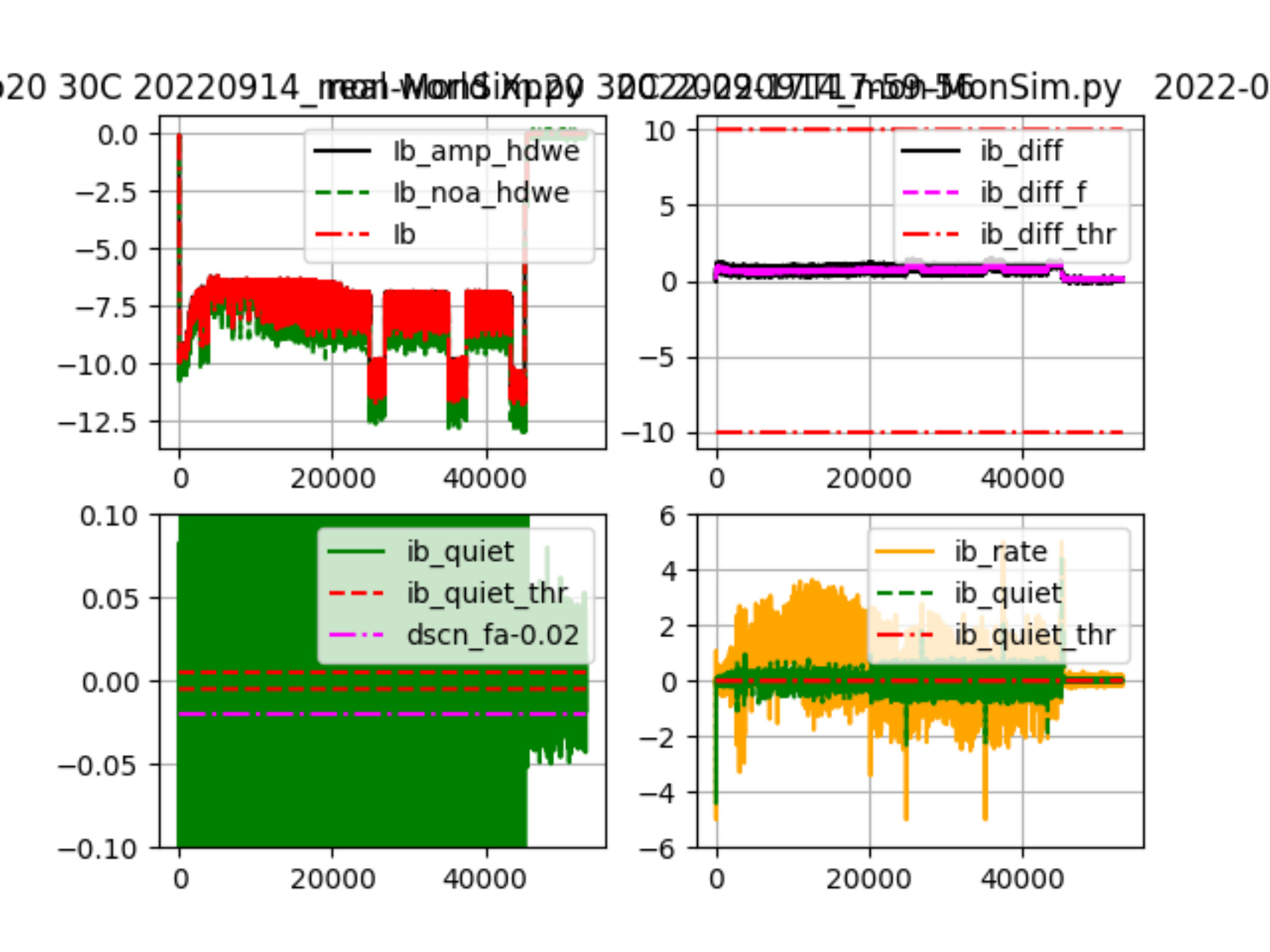

soc_ver
1.0
0.8
0.6
0.4
0.2
0
20000
40000

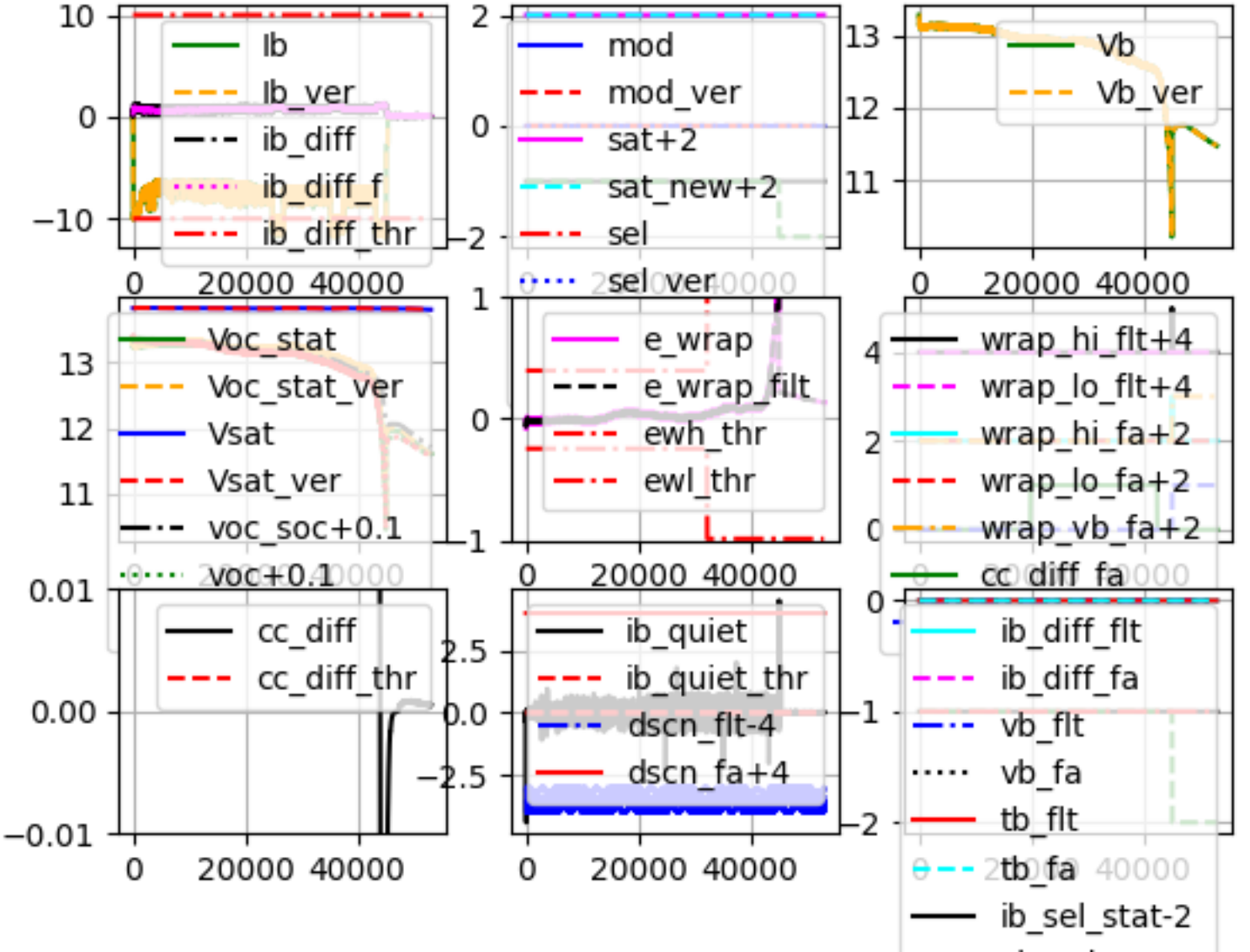

ib, A_ver
ioc hys indicator, A_ver
0.0
−2.5
−5.0
−7.5
−10.0
0
20000
40000

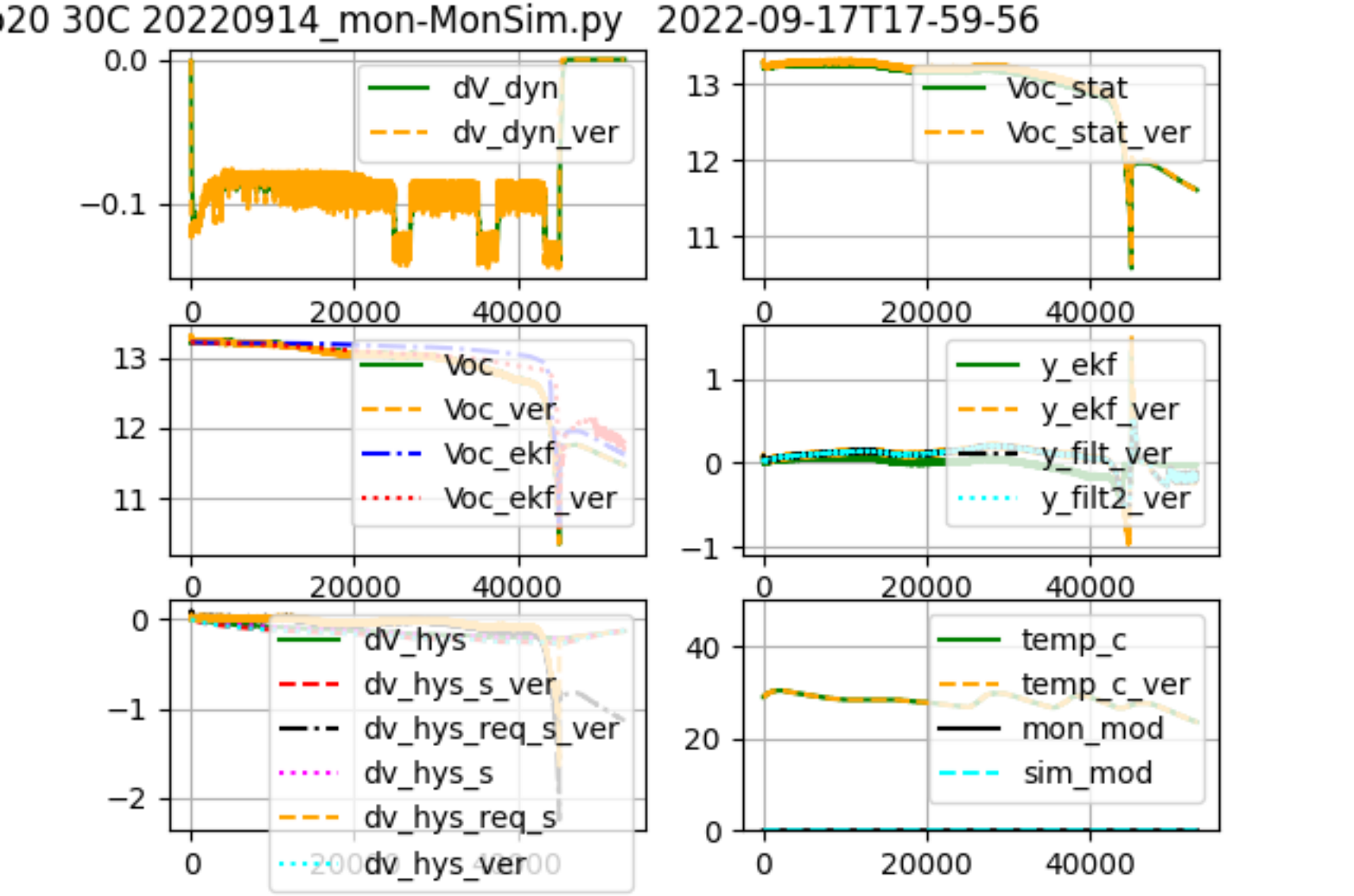

dv_hys, V_ver
0.0
−0.1
−0.2
0
20000
40000

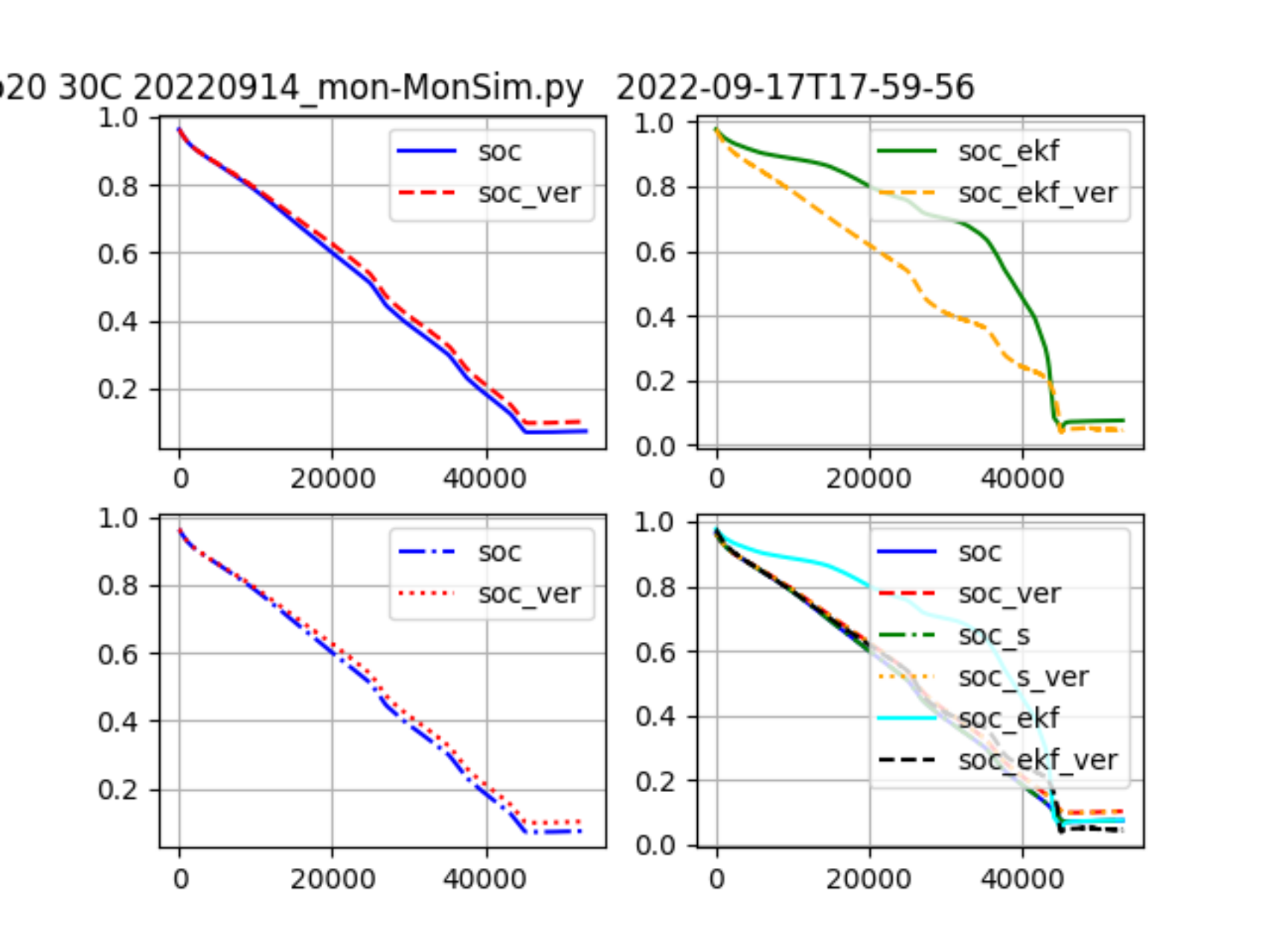

real world Xp20 30C 20220914_mon-MonSim.py 2022-09-17T17-59-56
SIM voc_stat vs soc_ver
13.2
13.1
13.0
12.9
12.8
12.7
12.6
0.2
0.4
0.6
0.8
1.0

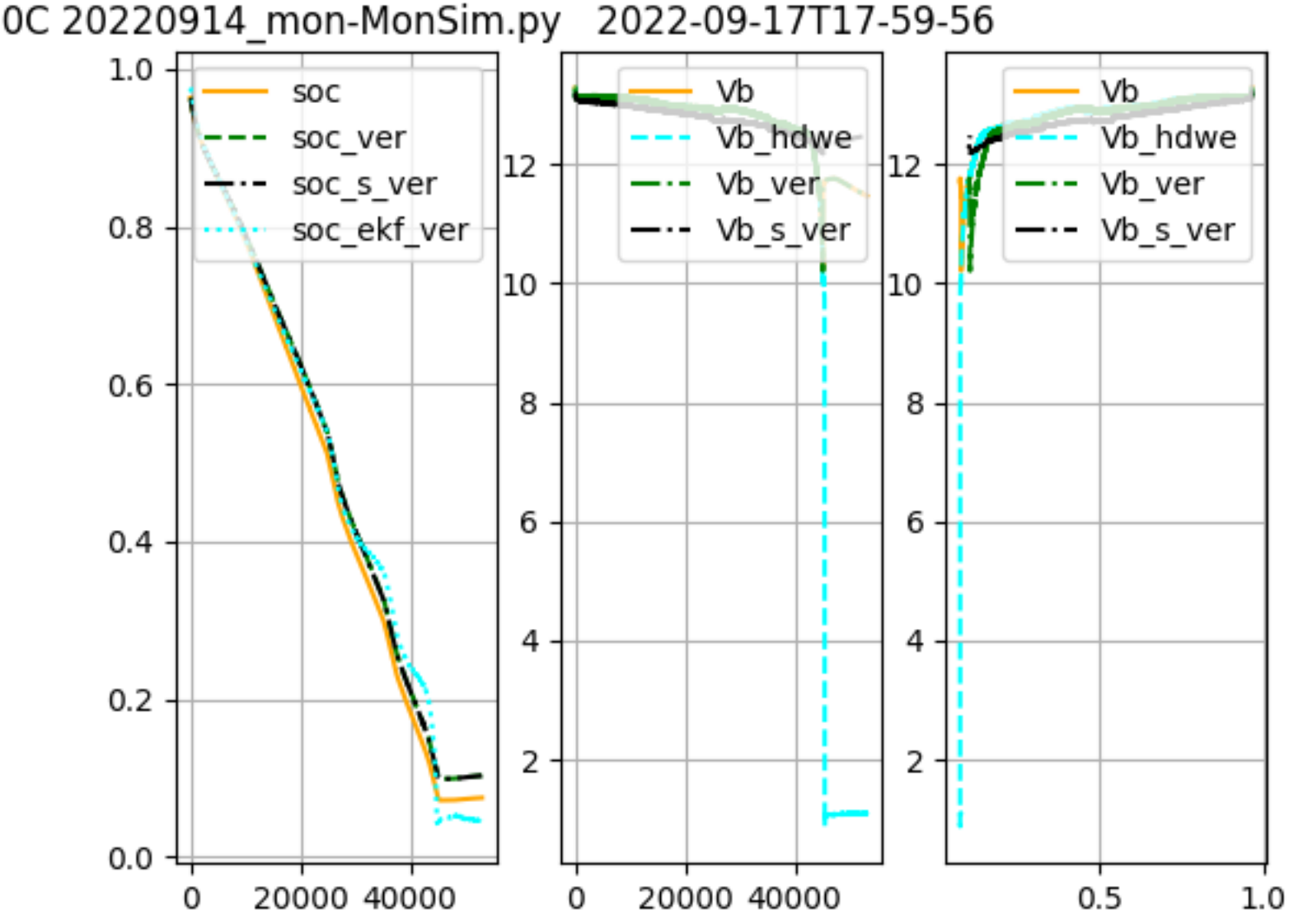

20 30C 20220914_mon-MonSim.py 2022-09-17T17-59-56
Mon Randles u[2]=vb_ver
Mon Randles y=voc_ver
Mon Randles x[1]_ver
Mon Randles x[2]_ver
Mon Randles x_dot[1]_ver
Mon Randles x_dot[2]_ver
Mon Randles u[1]=Ib_ver

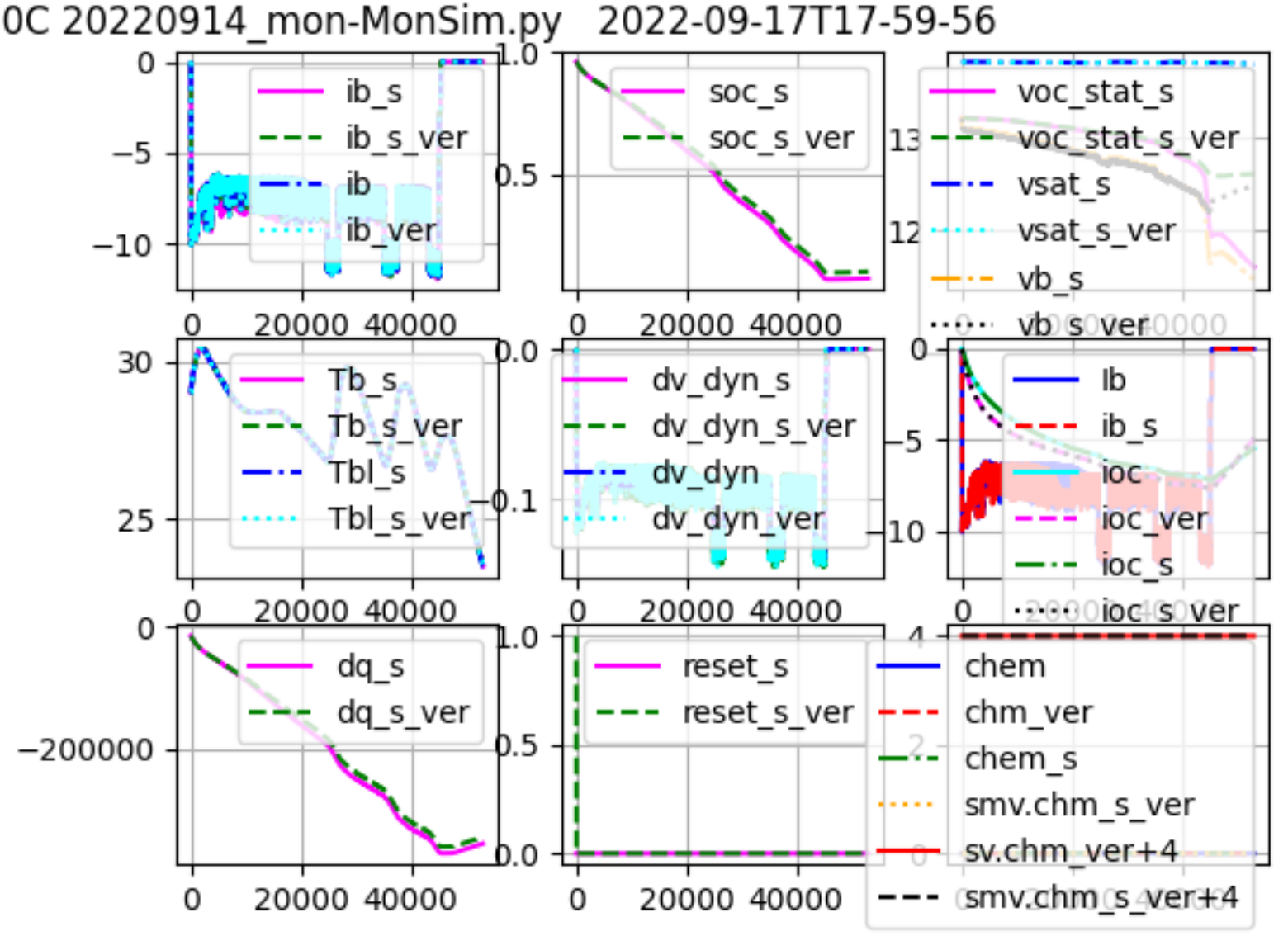

Ib_amp_hdwe
Ib_noa_hdwe
Ib
ib_diff
ib_diff_f
ib_diff_thr
ib_quiet
ib_quiet_thr
dscn_fa-0.02
ib_rate
ib_quiet
ib_quiet_thr


0C 20220914_mon-MonSim.py 2022-09-17T17-59-56
Ib
Ib_ver
ib_diff
ib_diff_f
ib_diff_thr
mod
mod_ver
sat+2
sat_new+2
sel
sel_ver
Vb
Vb_ver
Voc_stat
Voc_stat_ver
Vsat
Vsat_ver
voc_soc+0.1
voc+0.1
e_wrap
e_wrap_filt
ewh_thr
ewl_thr
wrap_hi_flt+4
wrap_lo_flt+4
wrap_hi_fa+2
wrap_lo_fa+2
wrap_vb_fa+2
cc_diff_fa
cc_diff
cc_diff_thr
ib_quiet
ib_quiet_thr
dscn_flt-4
dscn_fa+4
ib_diff_flt
ib_diff_fa
vb_flt
vb_fa
tb_flt
tb_fa
ib_sel_stat-2
0
20000
40000


20 30C 20220914_mon-MonSim.py 2022-09-17T17-59-56
dV_dyn
dv_dyn_ver
Voc_stat
Voc_stat_ver
Voc
Voc_ver
Voc_ekf
Voc_ekf_ver
y_ekf
y_ekf_ver
y_filt_ver
y_filt2_ver
dV_hys
dv_hys_s_ver
dv_hys_req_s_ver
dv_hys_s
dv_hys_req_s
dv_hys_ver
temp_c
temp_c_ver
mon_mod
sim_mod


20 30C 20220914_mon-MonSim.py 2022-09-17T17-59-56
soc
soc_ver
soc_ekf
soc_ekf_ver
soc_s
soc_s_ver
0
20000
40000
0.0
0.2
0.4
0.6
0.8
1.0


0C 20220914_mon-MonSim.py 2022-09-17T17-59-56
soc
soc_ver
soc_s_ver
soc_ekf_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver
Vb
Vb_hdwe
Vb_ver
Vb_s_ver
1.0
0.8
0.6
0.4
0.2
0.0
0
20000
40000
12
10
8
6
4
2
0
20000
40000
12
10
8
6
4
2
0.5
1.0


0C 20220914_mon-MonSim.py 2022-09-17T17-59-56
ib_s
ib_s_ver
ib
ib_ver
soc_s
soc_s_ver
voc_stat_s
voc_stat_s_ver
vsat_s
vsat_s_ver
vb_s
vb_s_ver
Tb_s
Tb_s_ver
Tbl_s
Tbl_s_ver
dv_dyn_s
dv_dyn_s_ver
dv_dyn
dv_dyn_ver
Ib
ib_s
ioc
ioc_ver
ioc_s
ioc_s_ver
dq_s
dq_s_ver
reset_s
reset_s_ver
chem
chm_ver
chem_s
smv.chm_s_ver
sv.chm_ver+4
smv.chm_s_ver+4