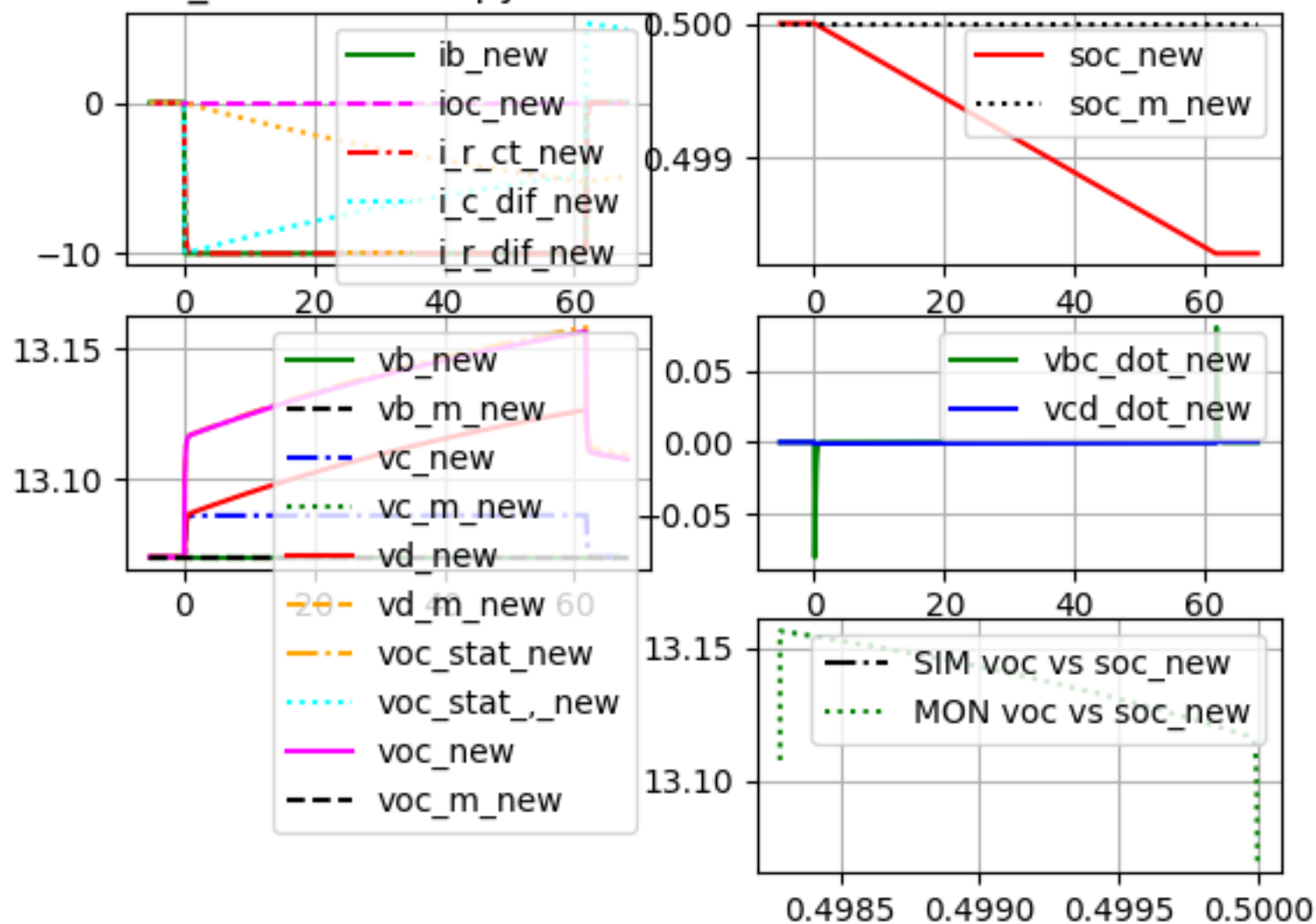
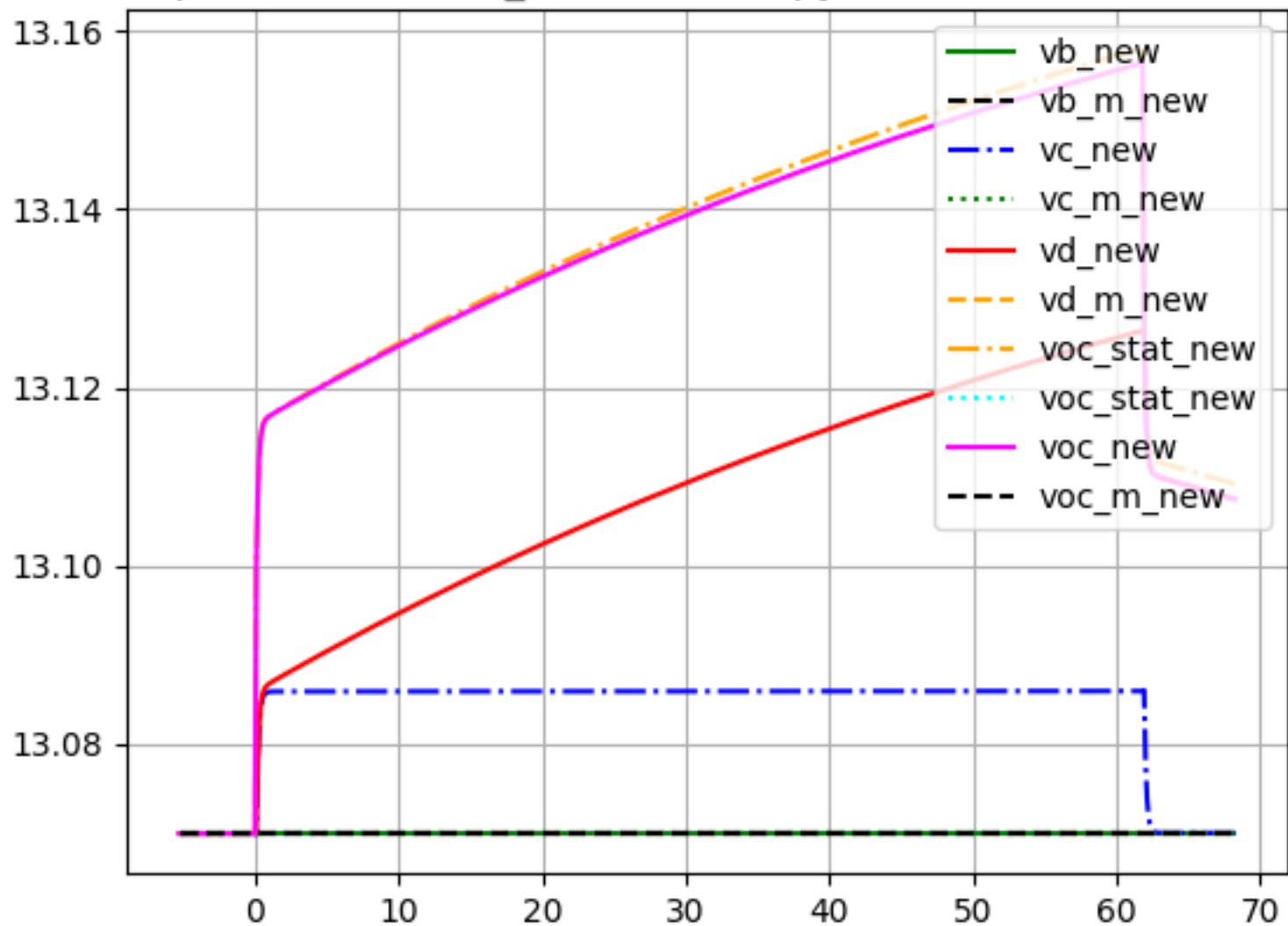


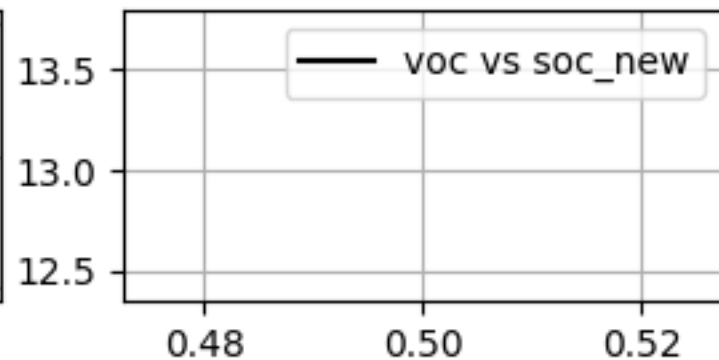
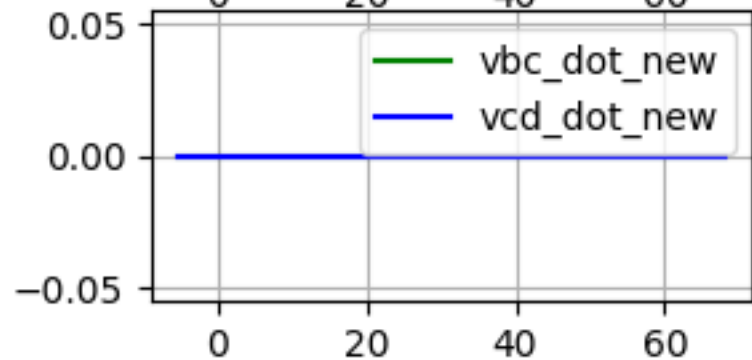
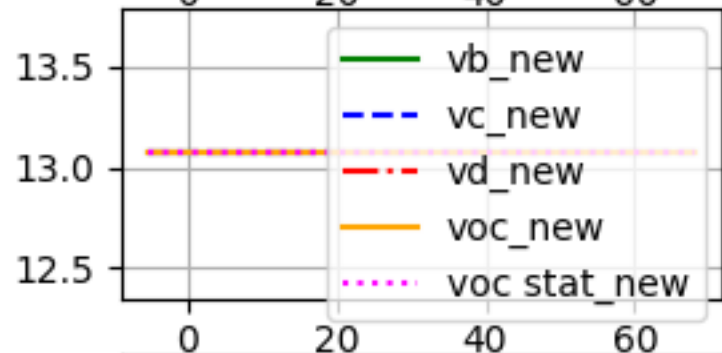
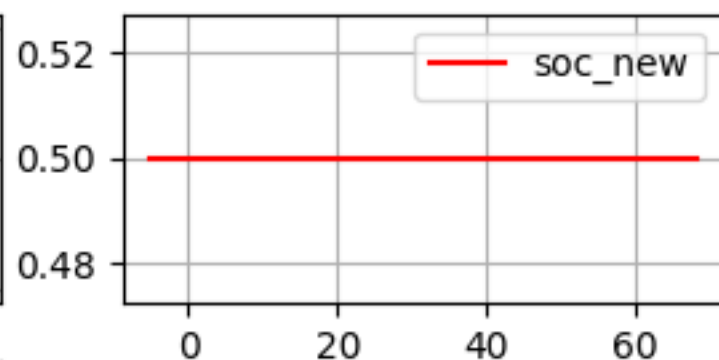
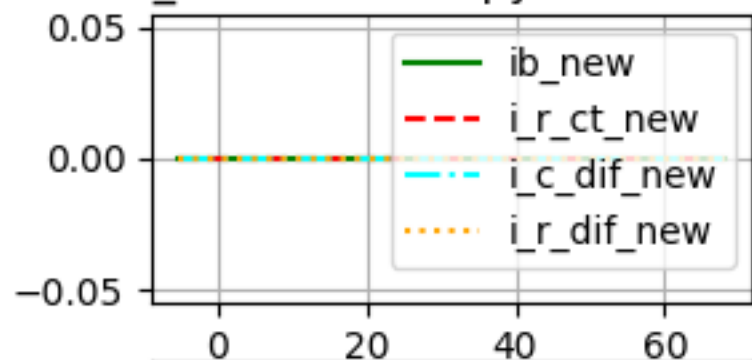
Fail20220809_mon-MonSim.py 2022-08-09T04:42:14



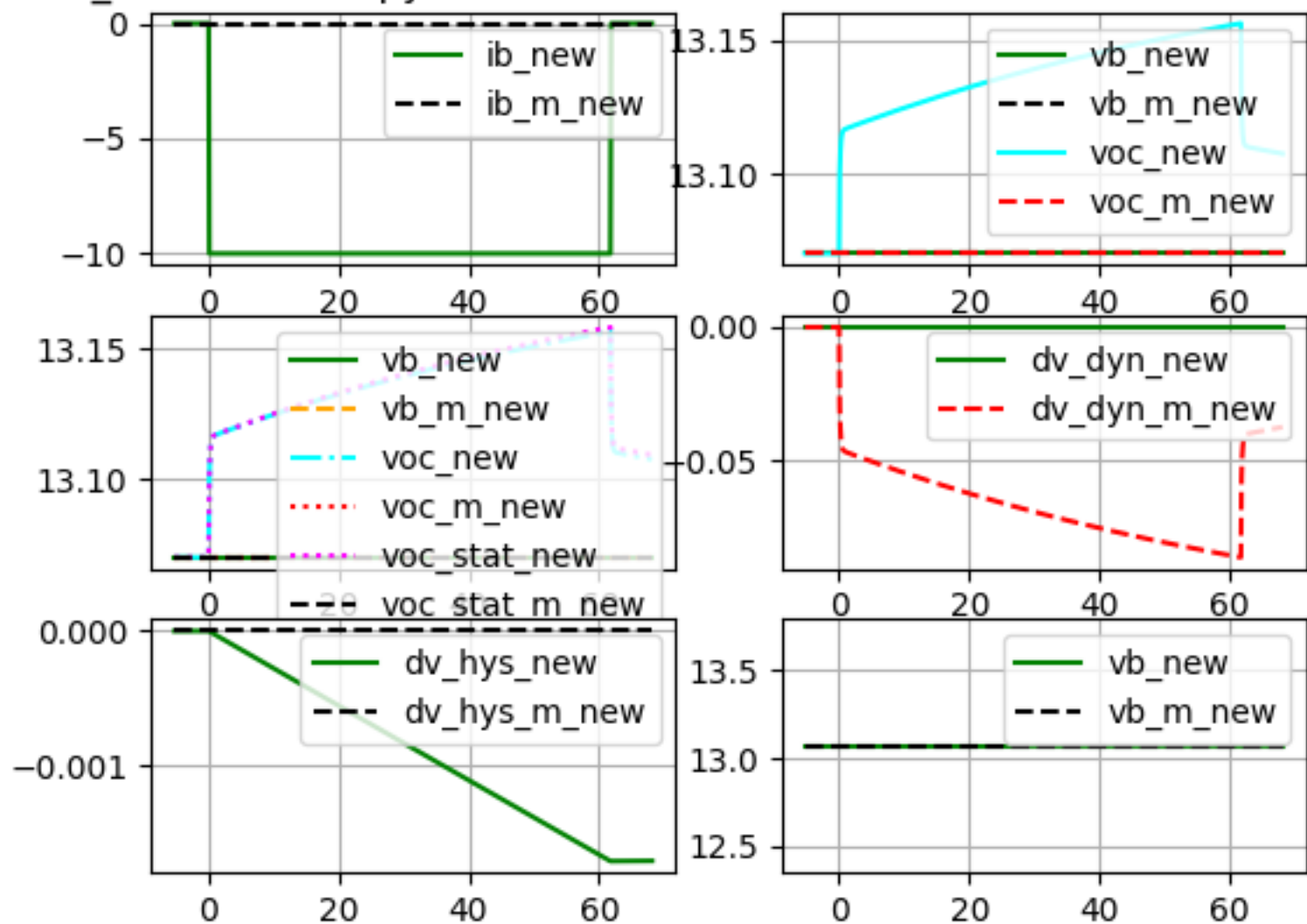
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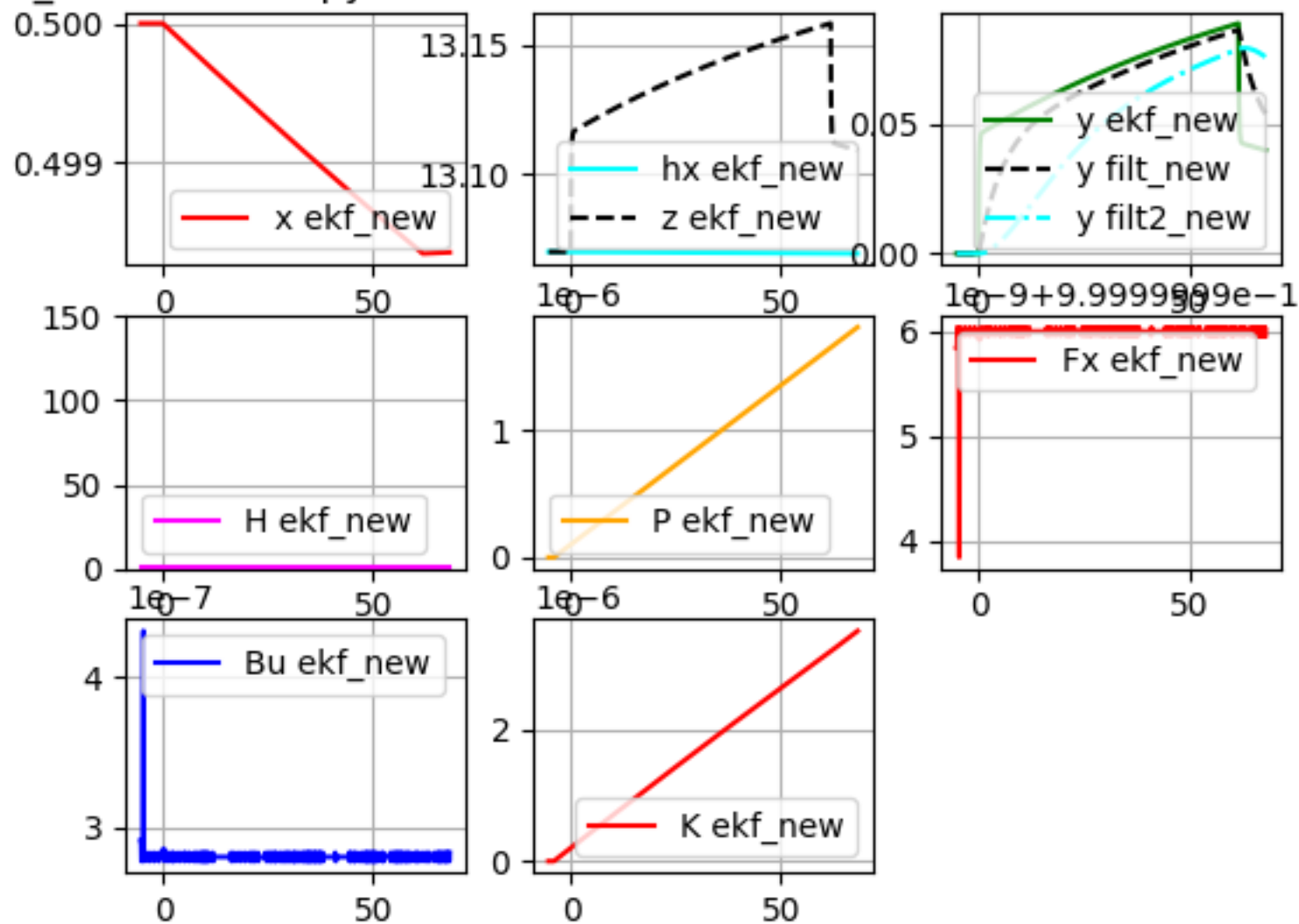
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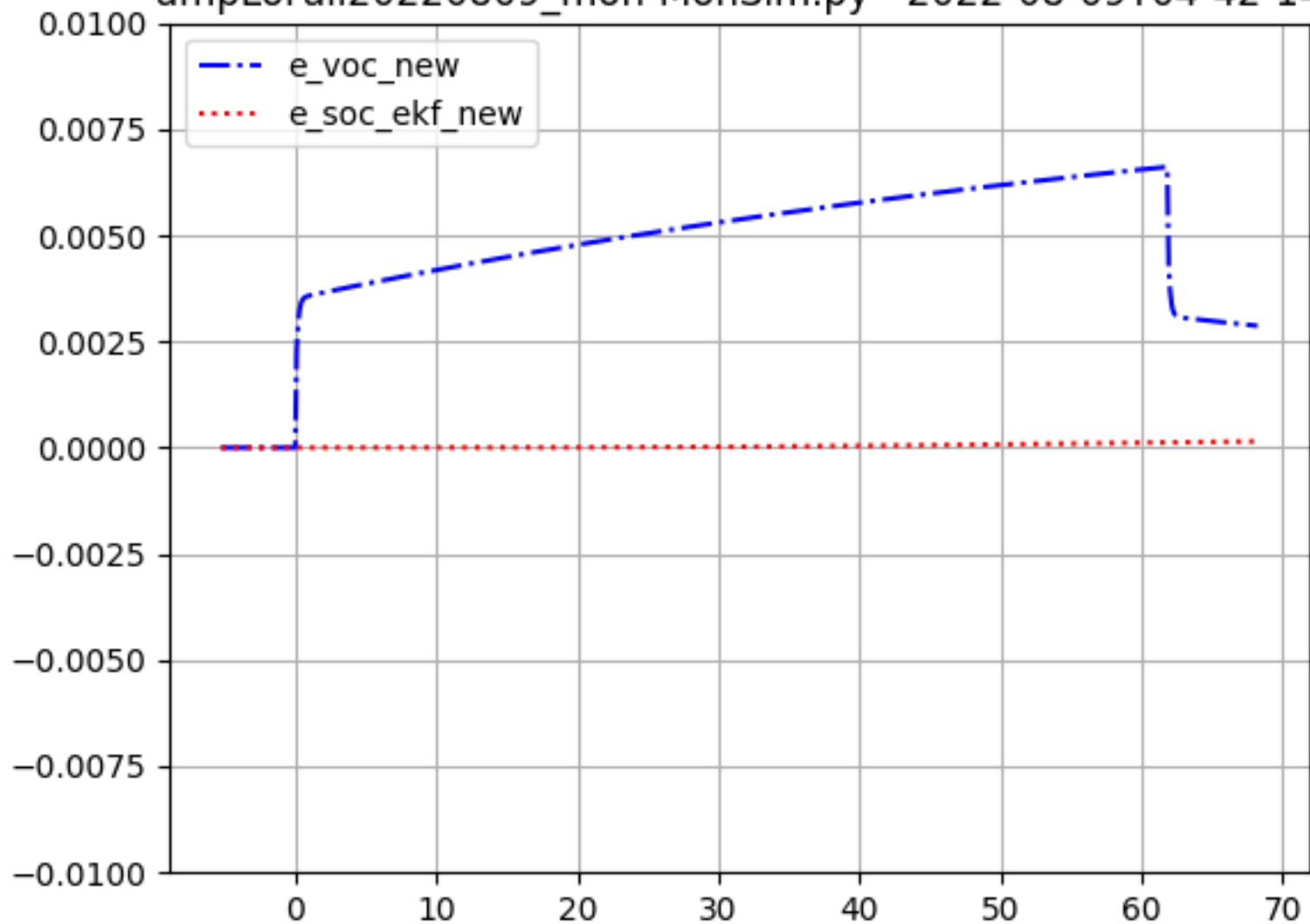
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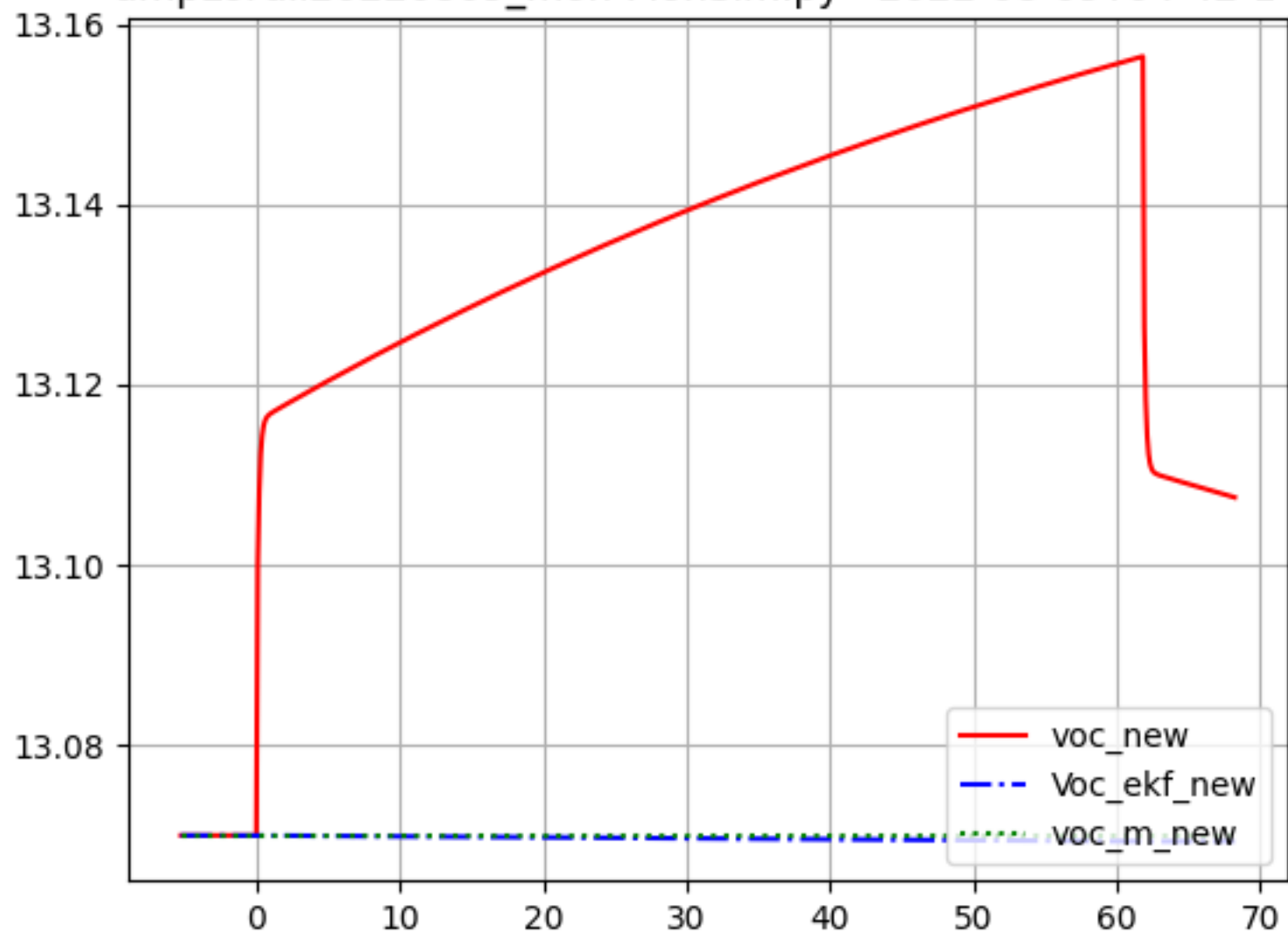
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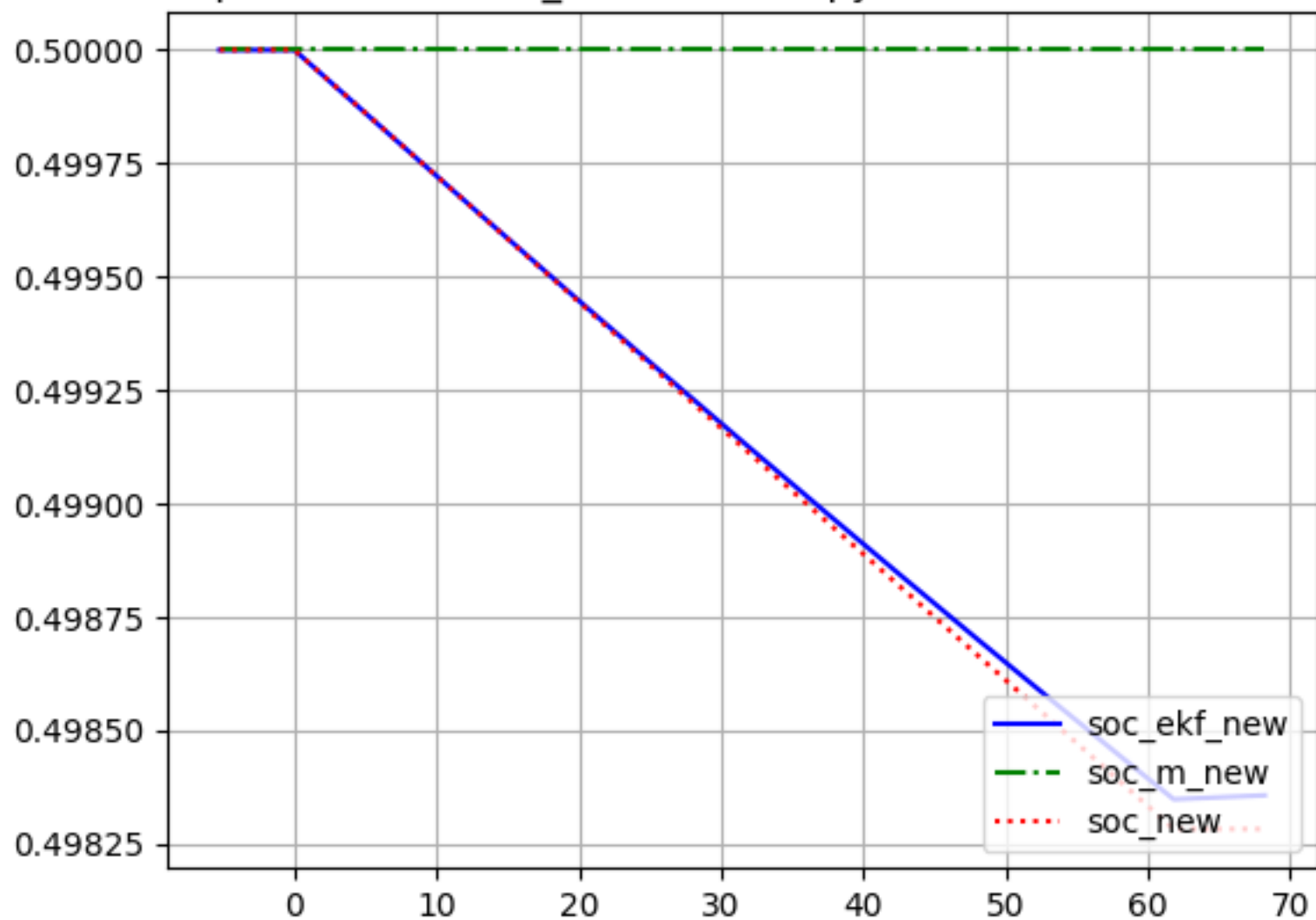
ampLoFail20220809_mon-MonSim.py 2022-08-09T04-42-14



ampLoFail20220809_mon-MonSim.py 2022-08-09T04:42:14



ampLoFail20220809_mon-MonSim.py 2022-08-09T04-42-14



ampLoFail20220809_mon-MonSim.py 2022-08-09T04-42-14

