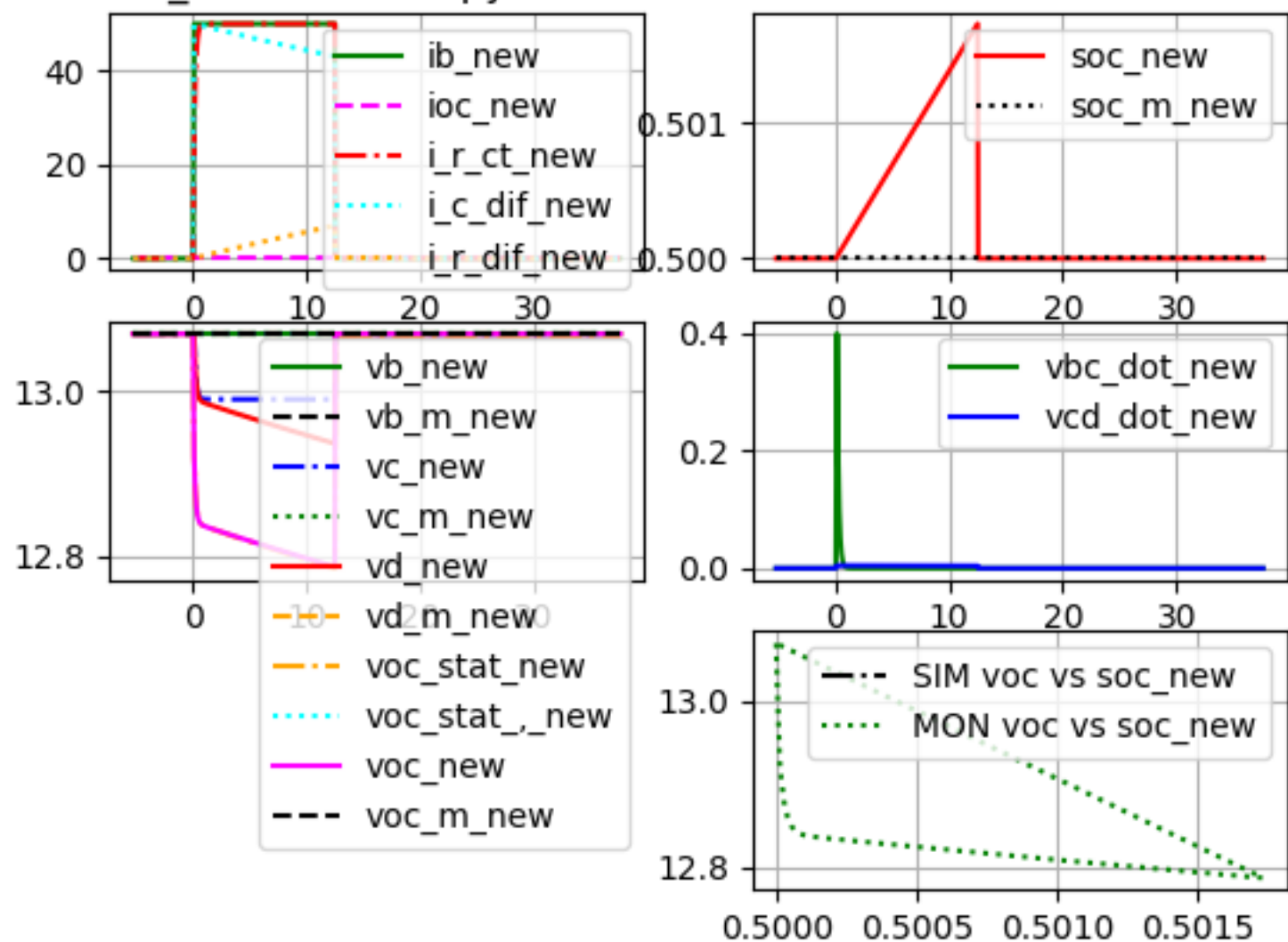
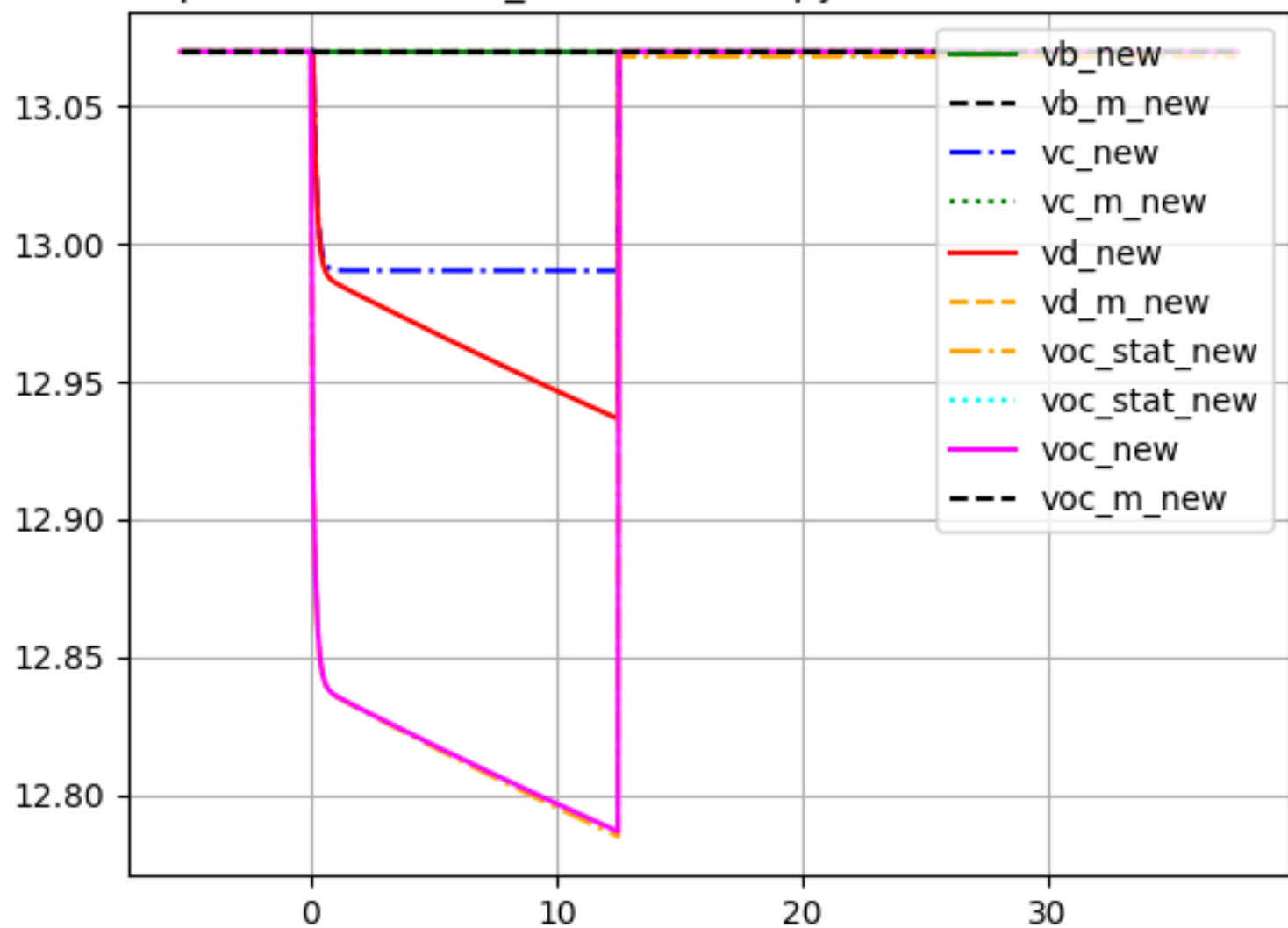


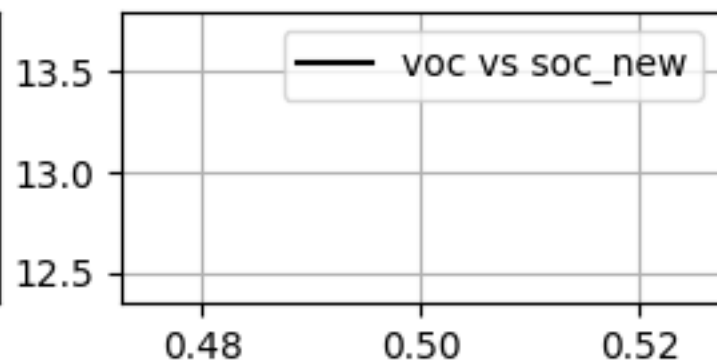
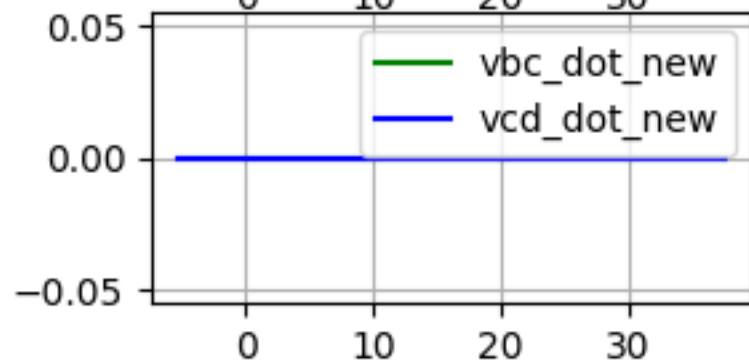
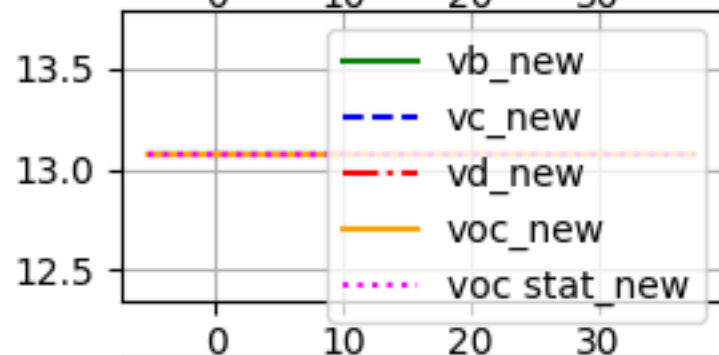
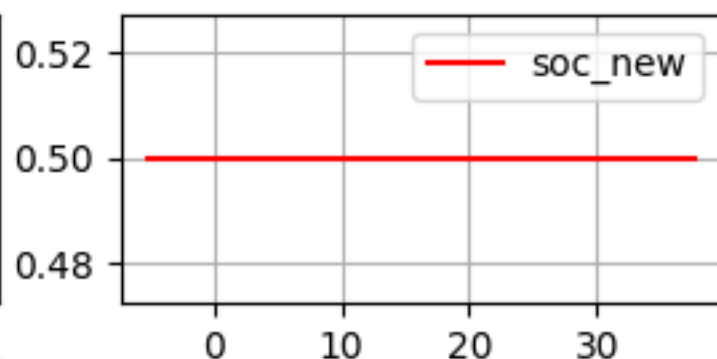
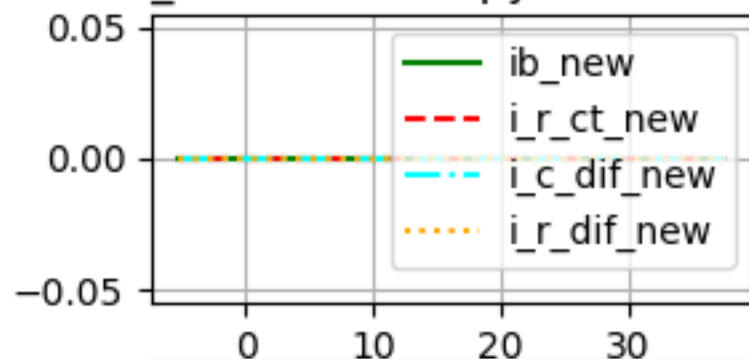
Fail20220821_mon-MonSim.py 2022-08-21T07-44-12



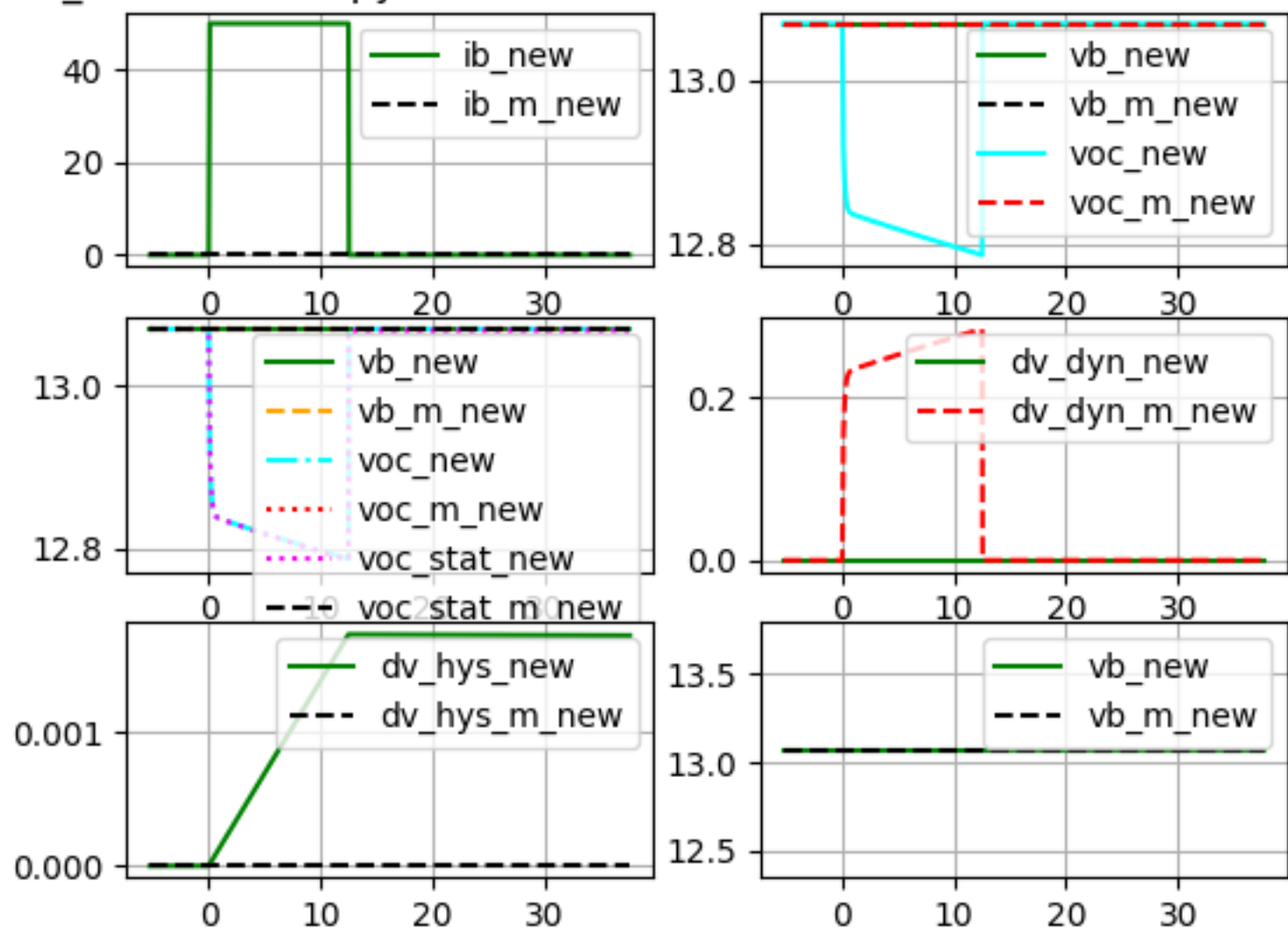
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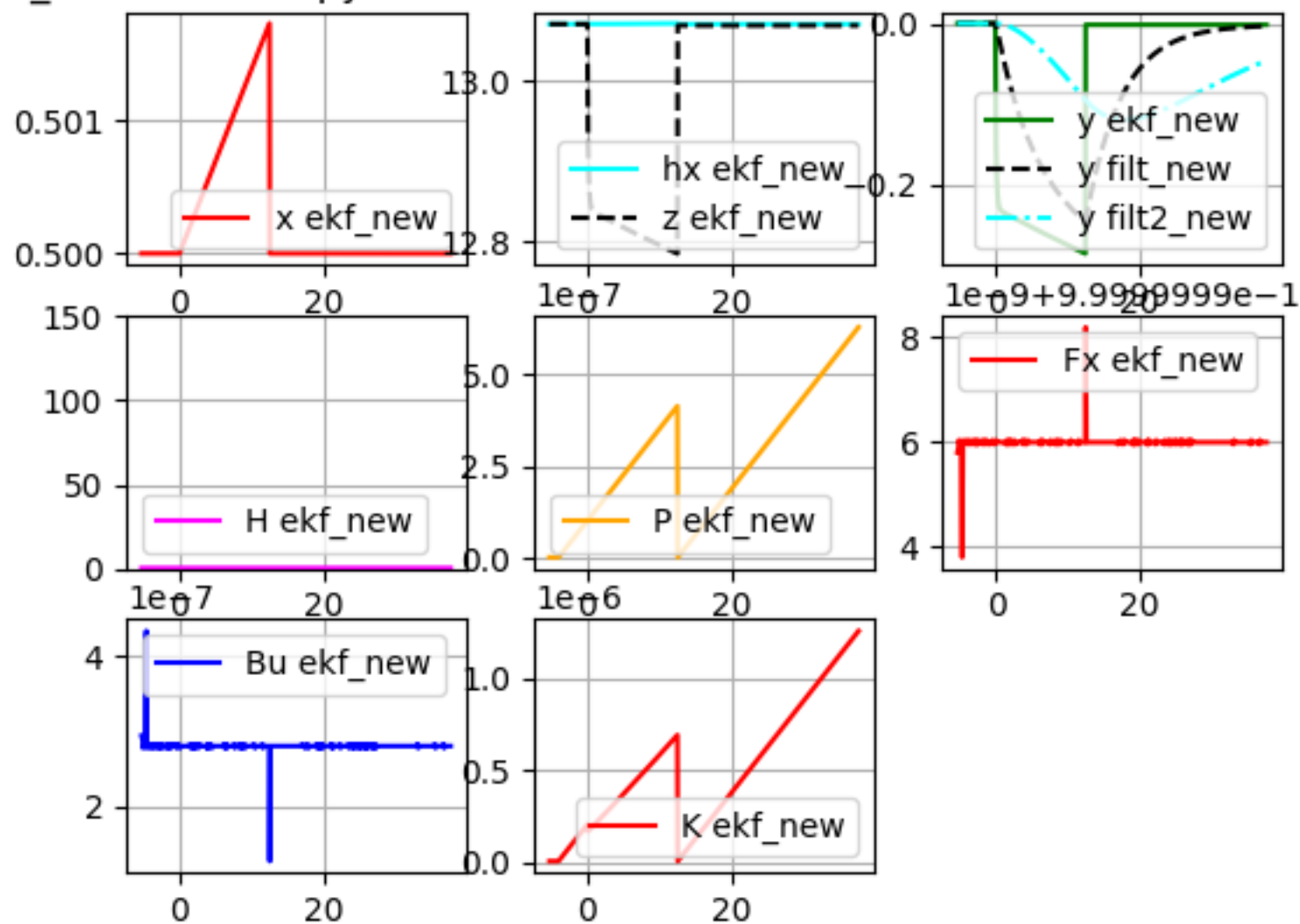
20220821_mon-MonSim.py 2022-08-21T07:44-12 **SIM



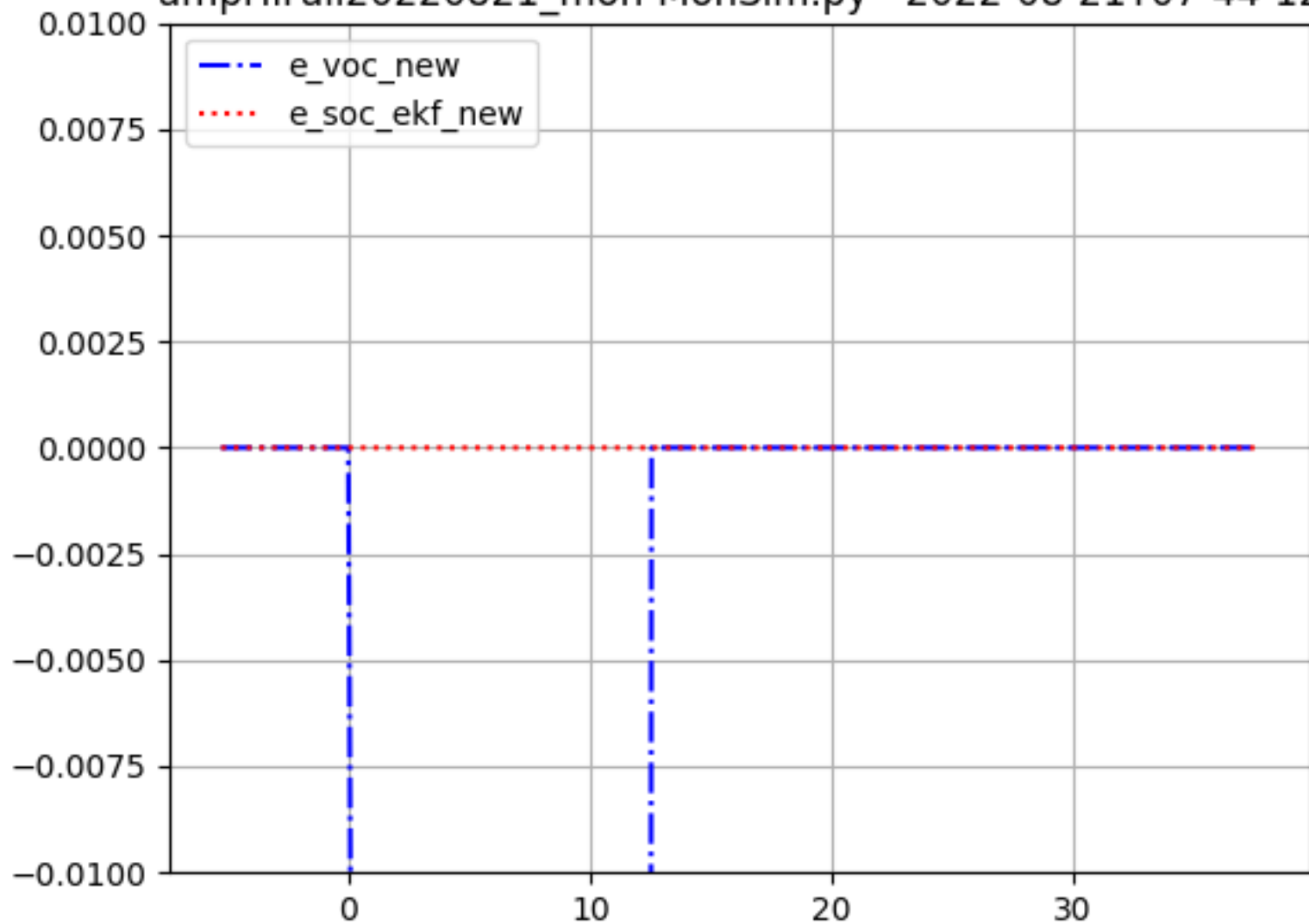
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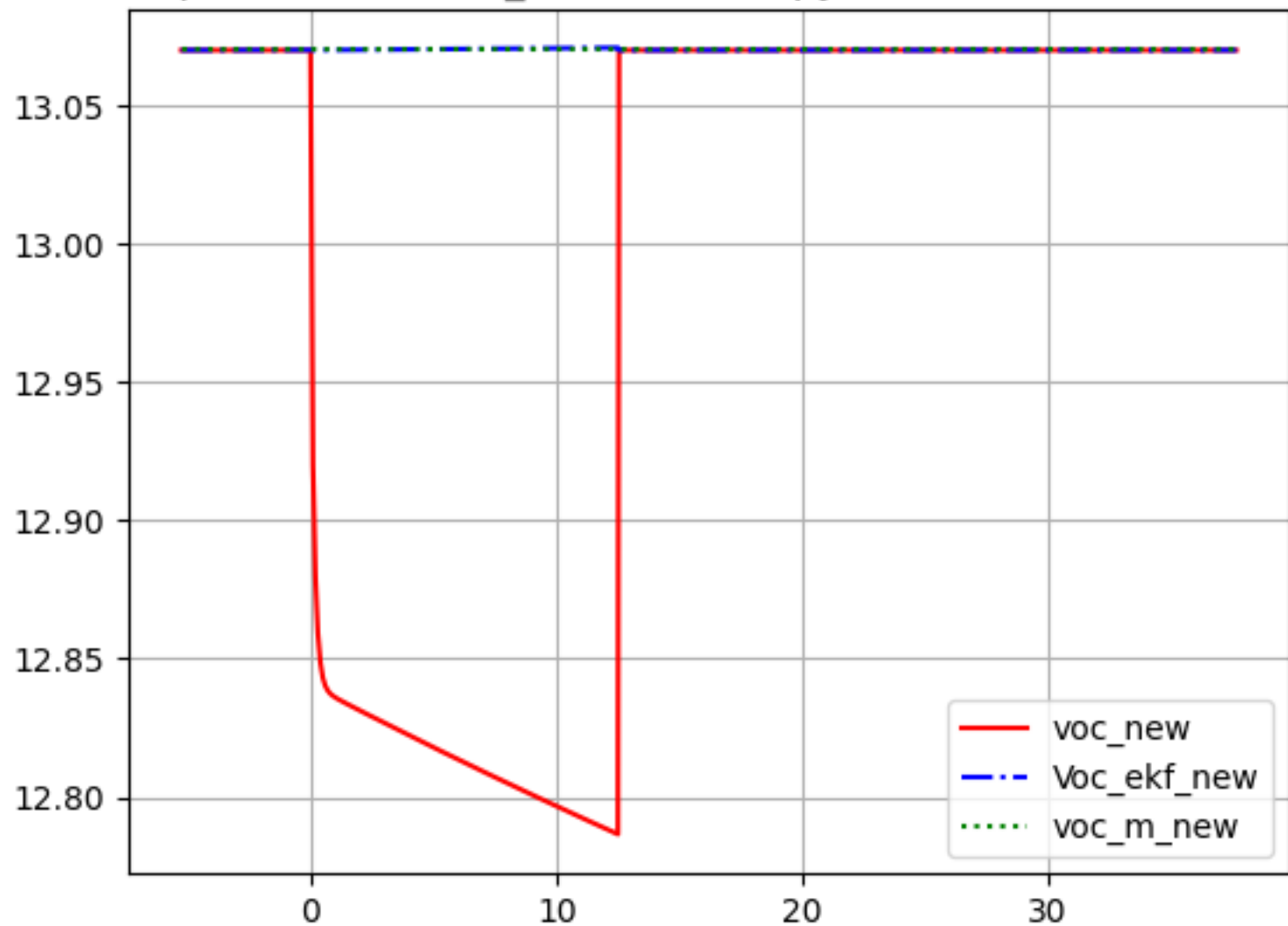
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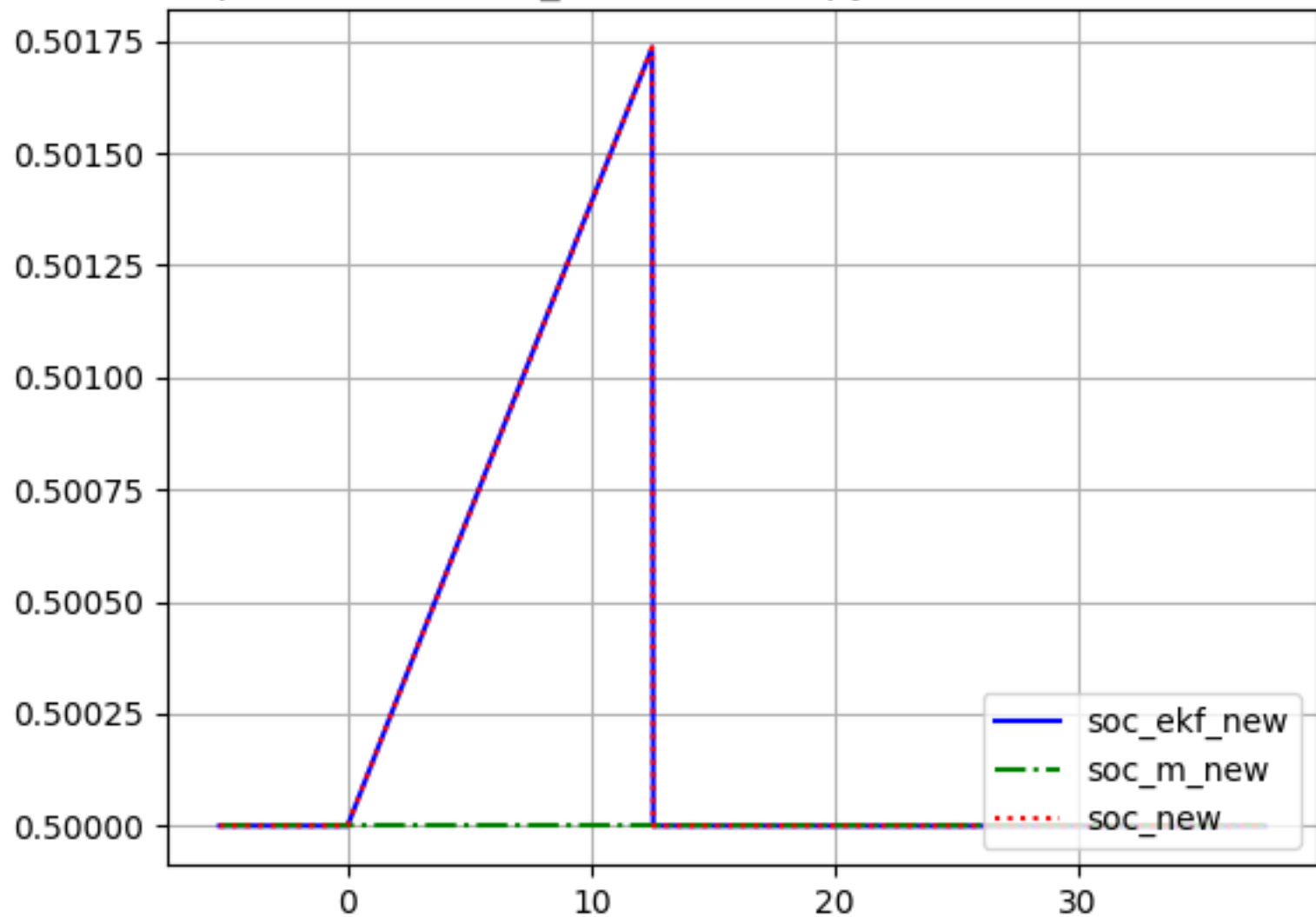
ampHiFail20220821_mon-MonSim.py 2022-08-21T07-44-12



ampHiFail20220821_mon-MonSim.py 2022-08-21T07-44-12



ampHiFail20220821_mon-MonSim.py 2022-08-21T07-44-12



ampHiFail20220821_mon-MonSim.py 2022-08-21T07-44-12

