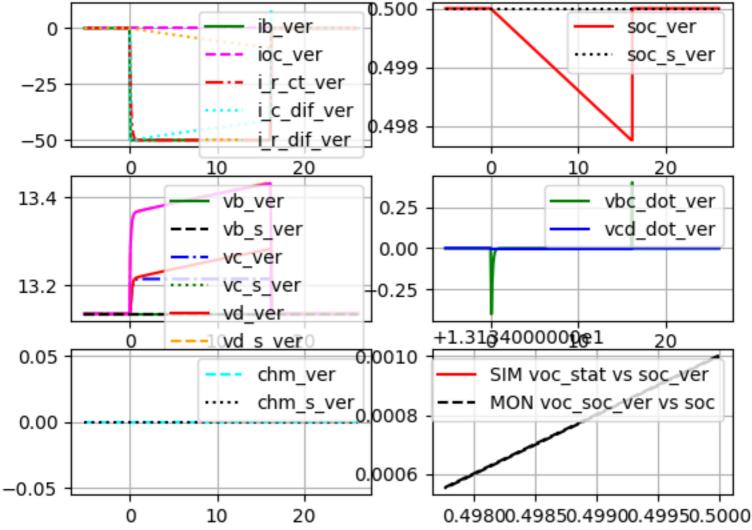
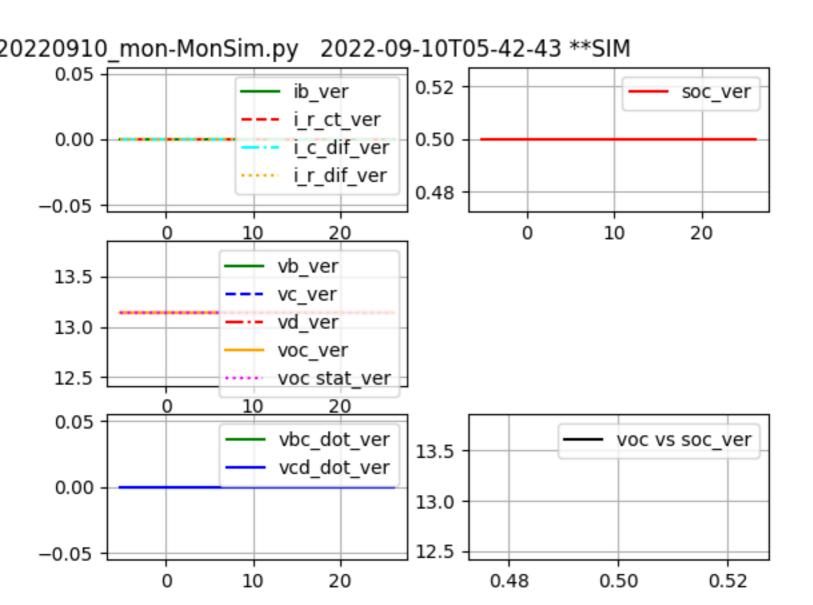
ail20220910_mon-MonSim.py 2022-09-10T05-42-43



ampLoFail20220910_mon-MonSim.py 2022-09-10T05-42-43 vb_ver vb_s_ver 13.40 vc_ver vc_s_ver vd ver 13.35 vd s ver voc_stat_ver voc_stat_ver 13.30 voc_ver voc s ver 13.25 13.20 13.15 15 10 20 25



20910_mon-MonSim.py 2022-09-10T05-42-43 MON vs SIM

