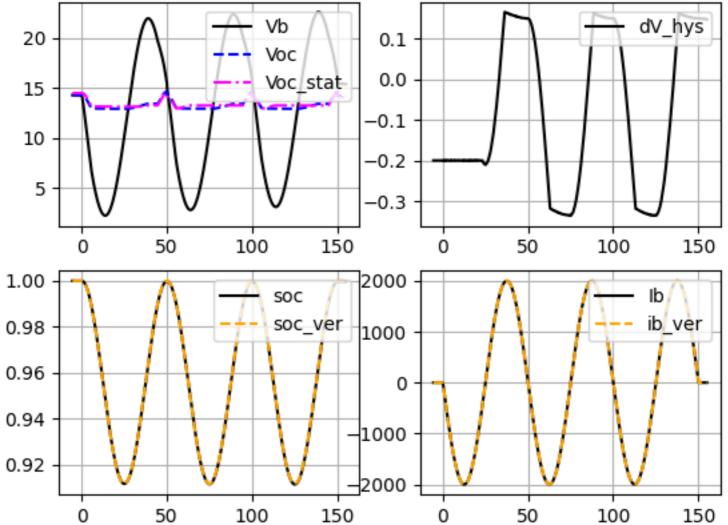
/20221028_mon-CompareRun.py 2022-11-10T03-11-39 off/on mon 1

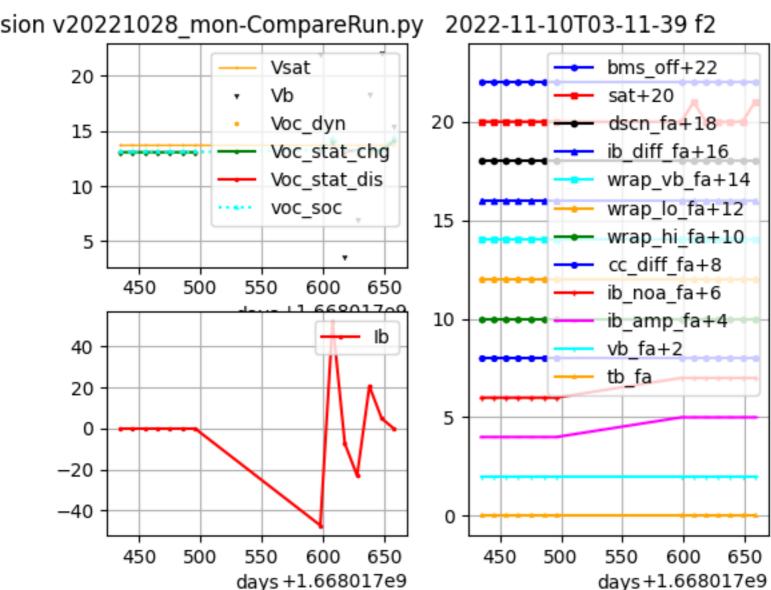


on v20221028_mon-CompareRun.py 2022-11-10T03-11-39 GP 1 vb s dv_hys_s 20 0.1 dv_hys_s_ver vb_s_ver 0.0 voc s 15 voc s ver -0.1 10 voc stat s -0.2voc stat s ver 5 -0.3 50 100 150 50 100 150 2000 1.00 ib s SOC S ib_s_ver soc_s_ver 0.98 1000 0.96 0 0.94 1000 0.92 2000 0 150 50 100 50 100 150 on v20221028_mon-CompareRun.py 2022-11-10T03-11-39 GP 2 dV_hys Vb 20 0.1 dv_hys_ver vb_ver 0.0 Voc 15 voc ver -0.110 Voc stat -0.2voc_stat_ver 5 -0.3 150 100 50 100 150 50 2000 1.00 Ιb SOC ib ver soc_ver 0.98 1000 0.96 0 0.94 -1000 0.92 2000 0 50 100 150 150 50 100

on v20221028_mon-CompareRun.py 2022-11-10T03-11-39 GP 3 vb ver dv_hys_ver 20 0.1 voc_ver 0.0 voc stat ver 15 -0.110 -0.25 -0.3150 50 100 150 50 100 1.00 2000 Ιb SOC ib ver soc_ver 0.98 1000 0.96 0 0.94 -1000 0.92 2000 0 50 100 150 50 100 150

faults v20221028_mon-CompareRun.py 2022-11-10T03-11-39 f1 1.0 -26 SOC Τb Ιb soc ekf 0.8 25 0 0.6 24 50 600 500 600 500 500 600 1.0 tweak sclr amp falw 10 tweak sclr noa **b**.5 5 soc_ekf -5 0 500 600 500 600 0.0 0.5 20 dv hys Vb e wrap _ dv_hys_rescaled wrap_vb_fa Voc dyn 0 wrap lo fa-1 Voc_stat_chg dy hys required 10 wrap hi fa+1 Voc stat dis e wrap dv hys remodel dv_hys5_redesign_chg 500 600 500 600 +day668017e9 +dax668017e9 dv hys redesign dis 9

faults



faults

