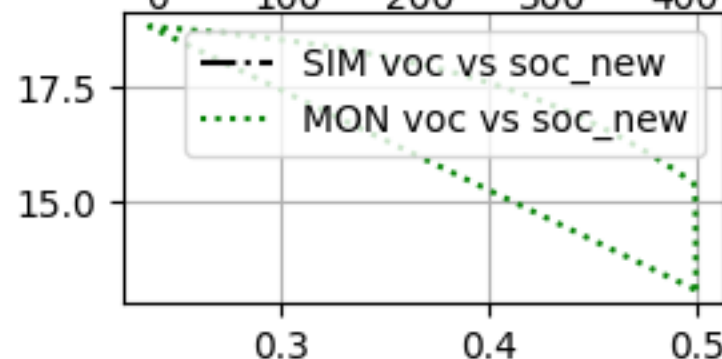
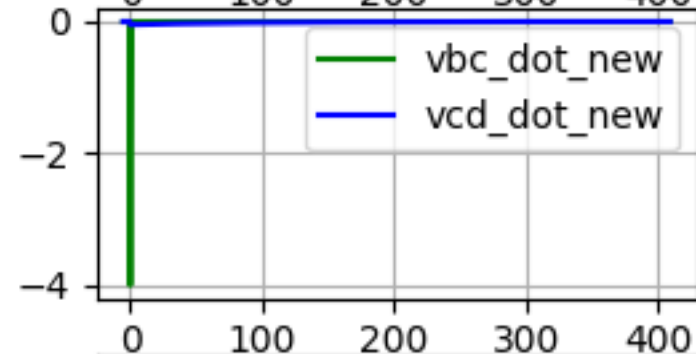
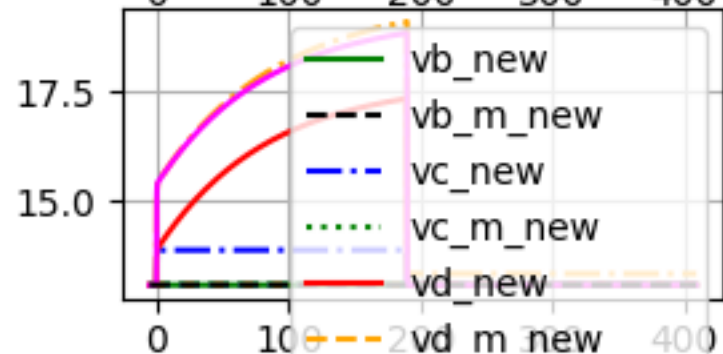
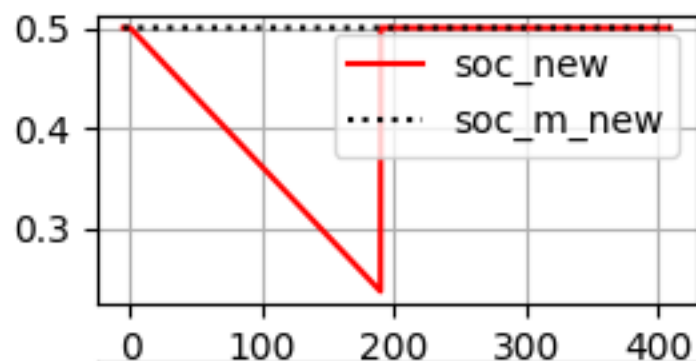
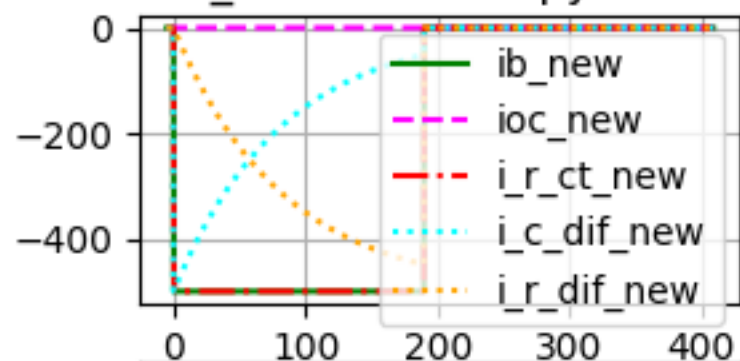
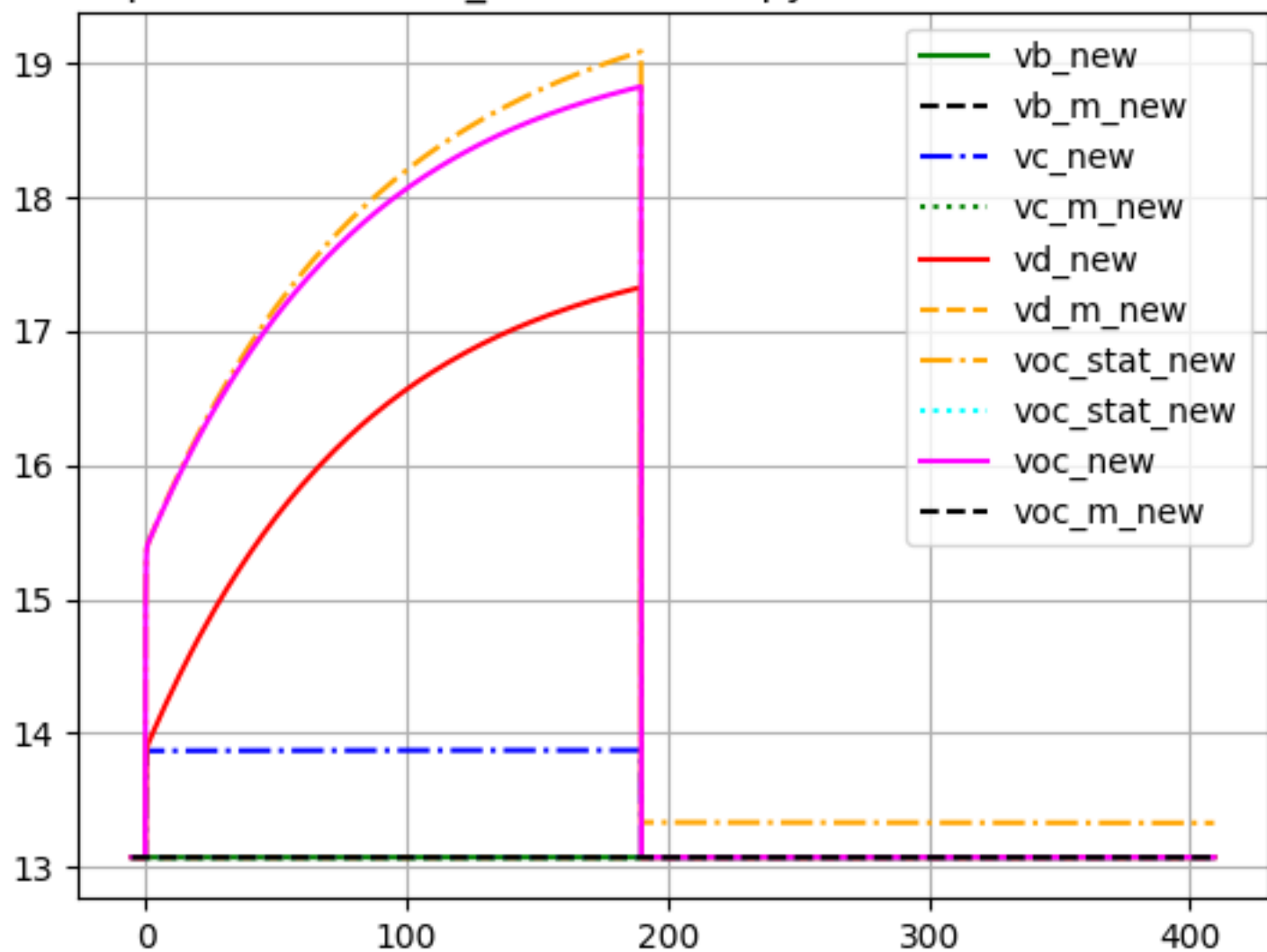


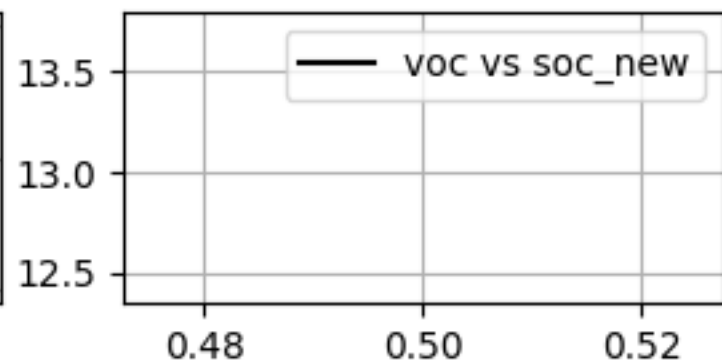
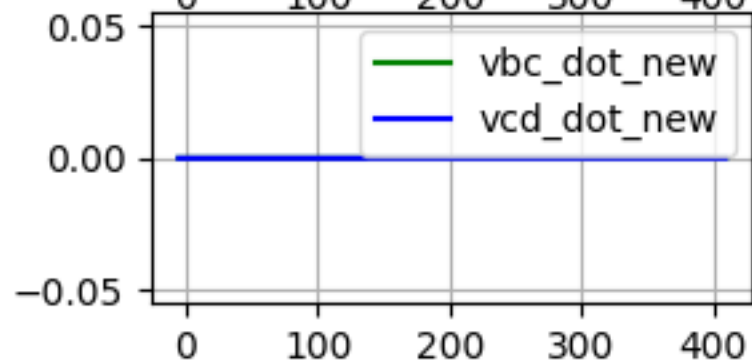
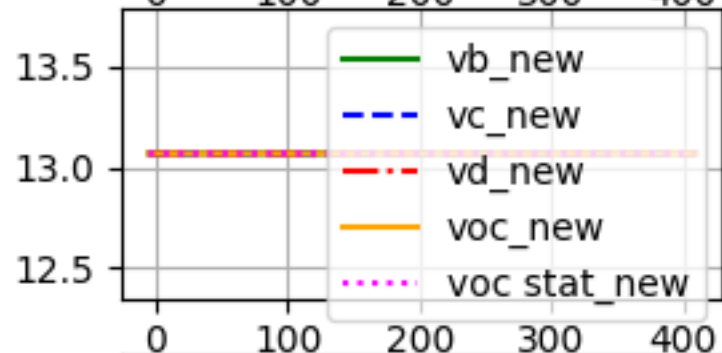
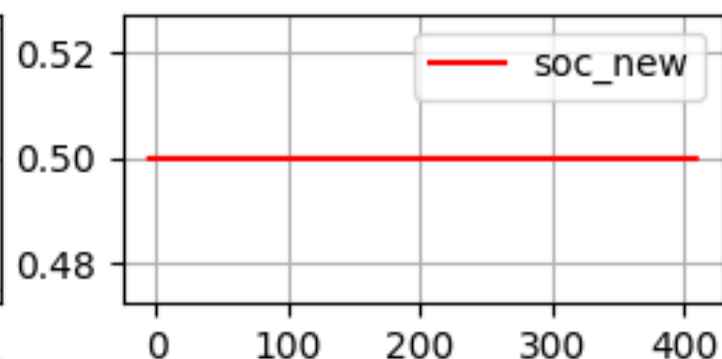
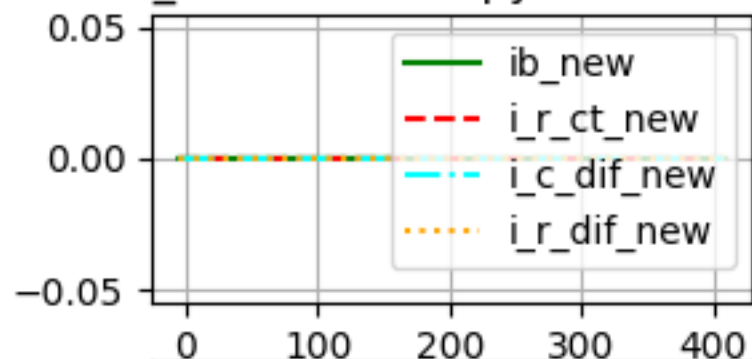
Fail20220807\_mon-MonSim.py 2022-08-07T08-38-29



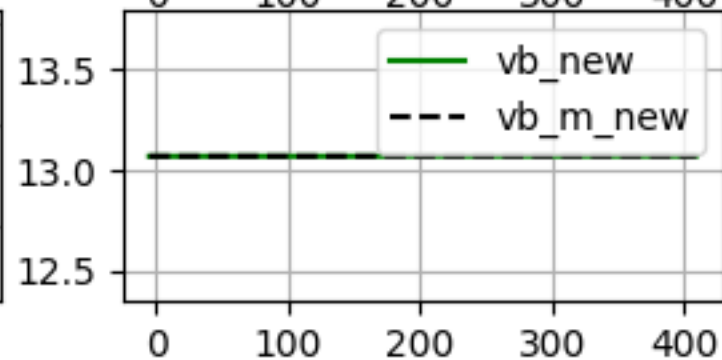
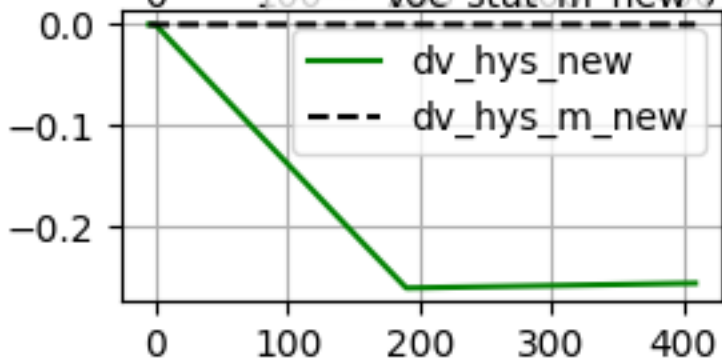
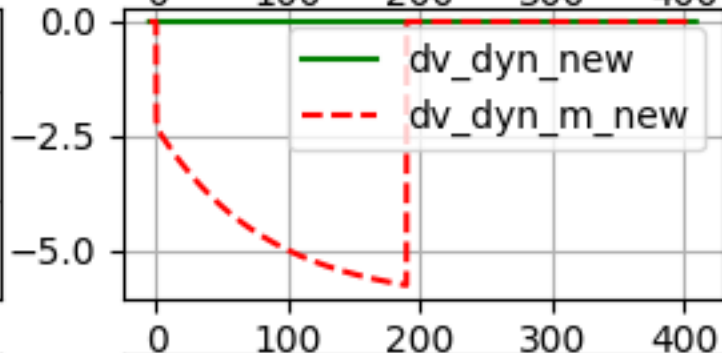
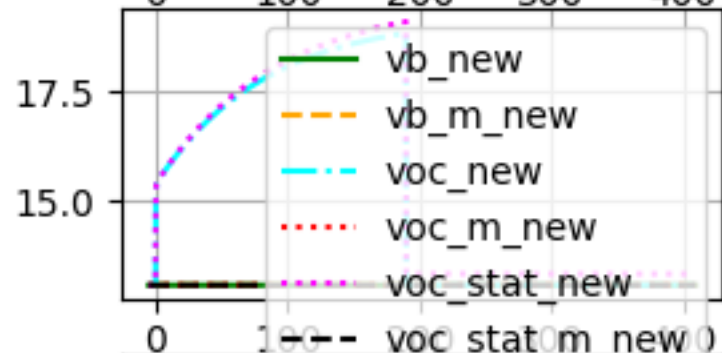
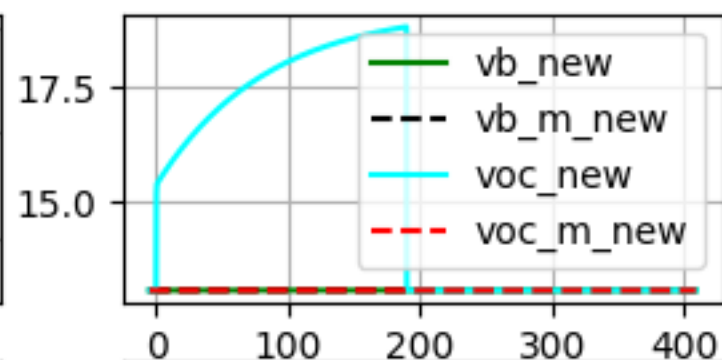
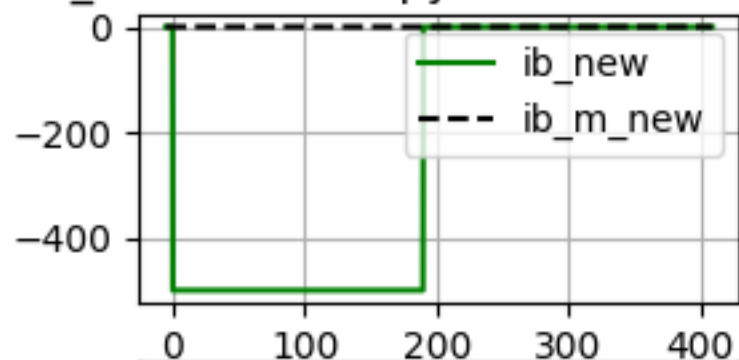
ampLoFail20220807\_mon-MonSim.py 2022-08-07T08-38-29



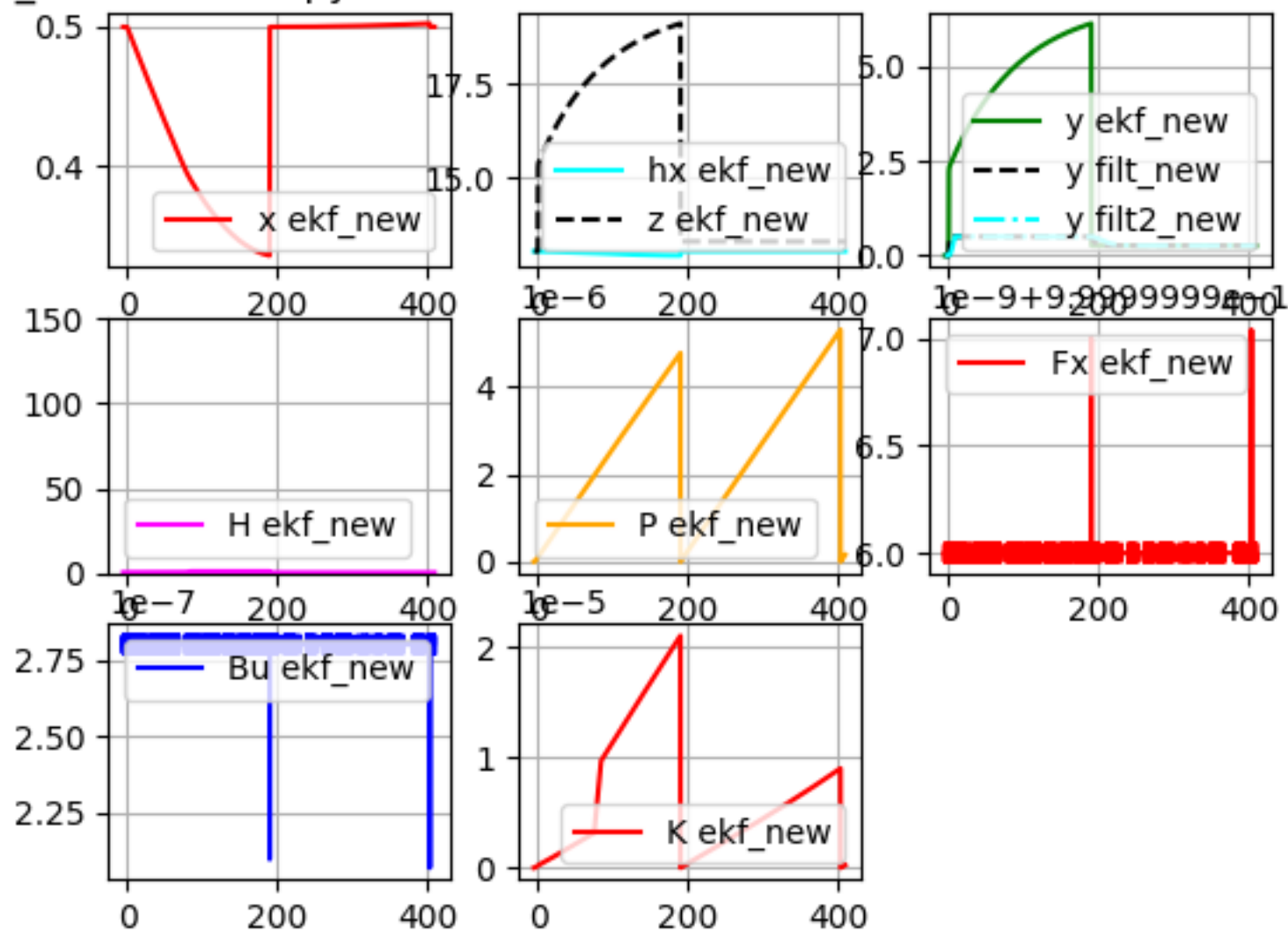
20220807\_mon-MonSim.py 2022-08-07T08-38-29 \*\*SIM



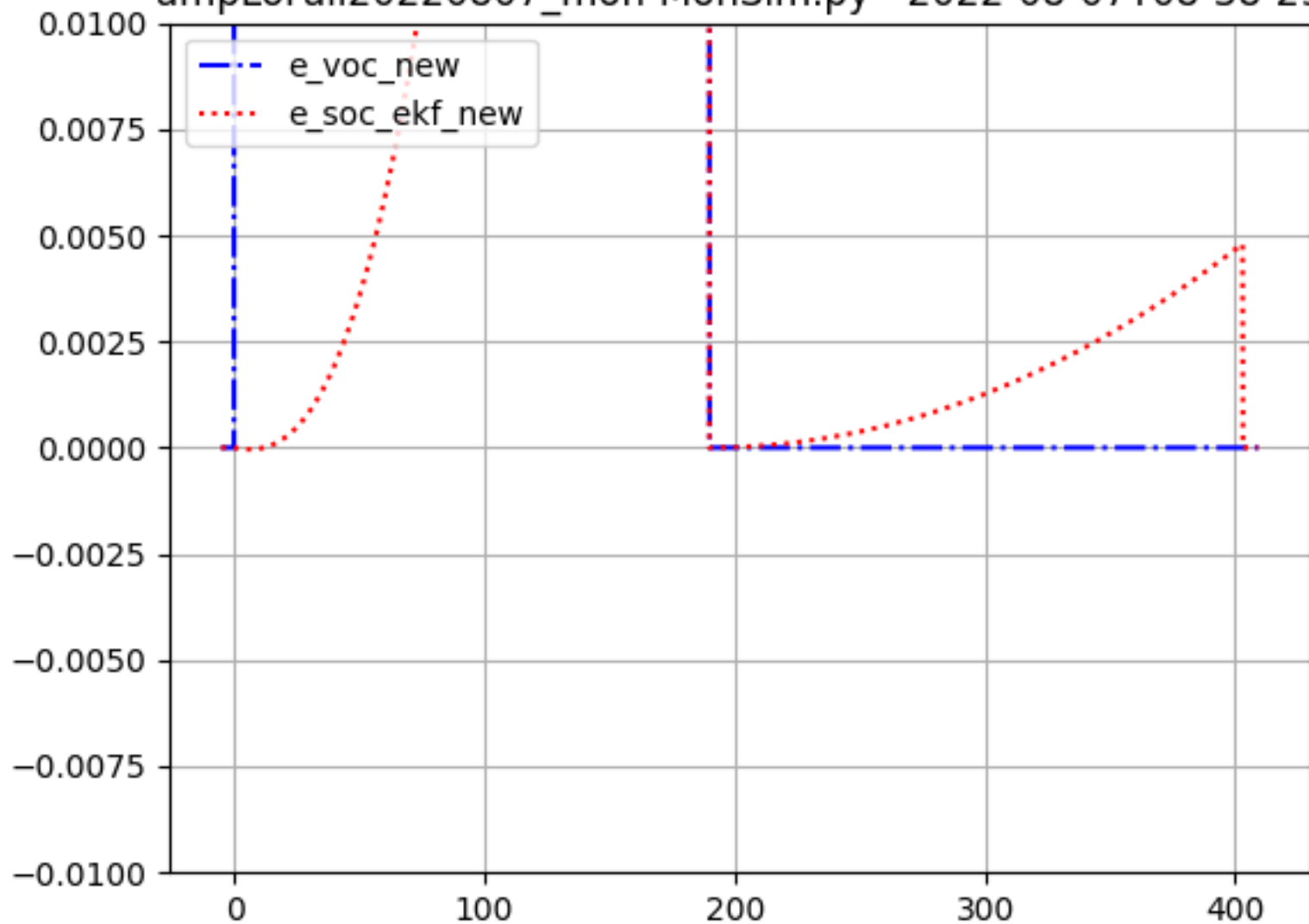
20807\_mon-MonSim.py 2022-08-07T08-38-29 MON vs SIM



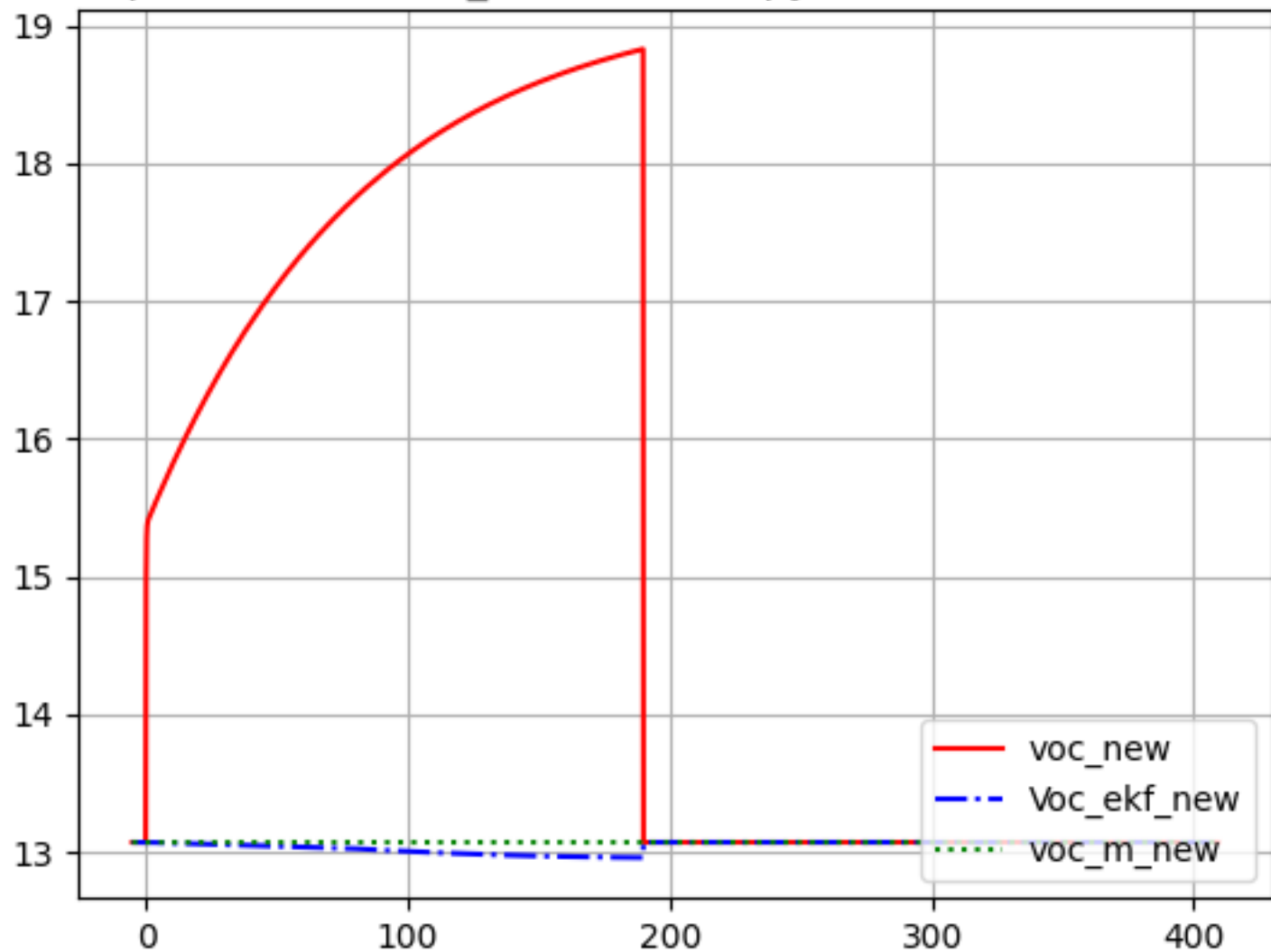
0807\_mon-MonSim.py 2022-08-07T08-38-29 \*\*EKF



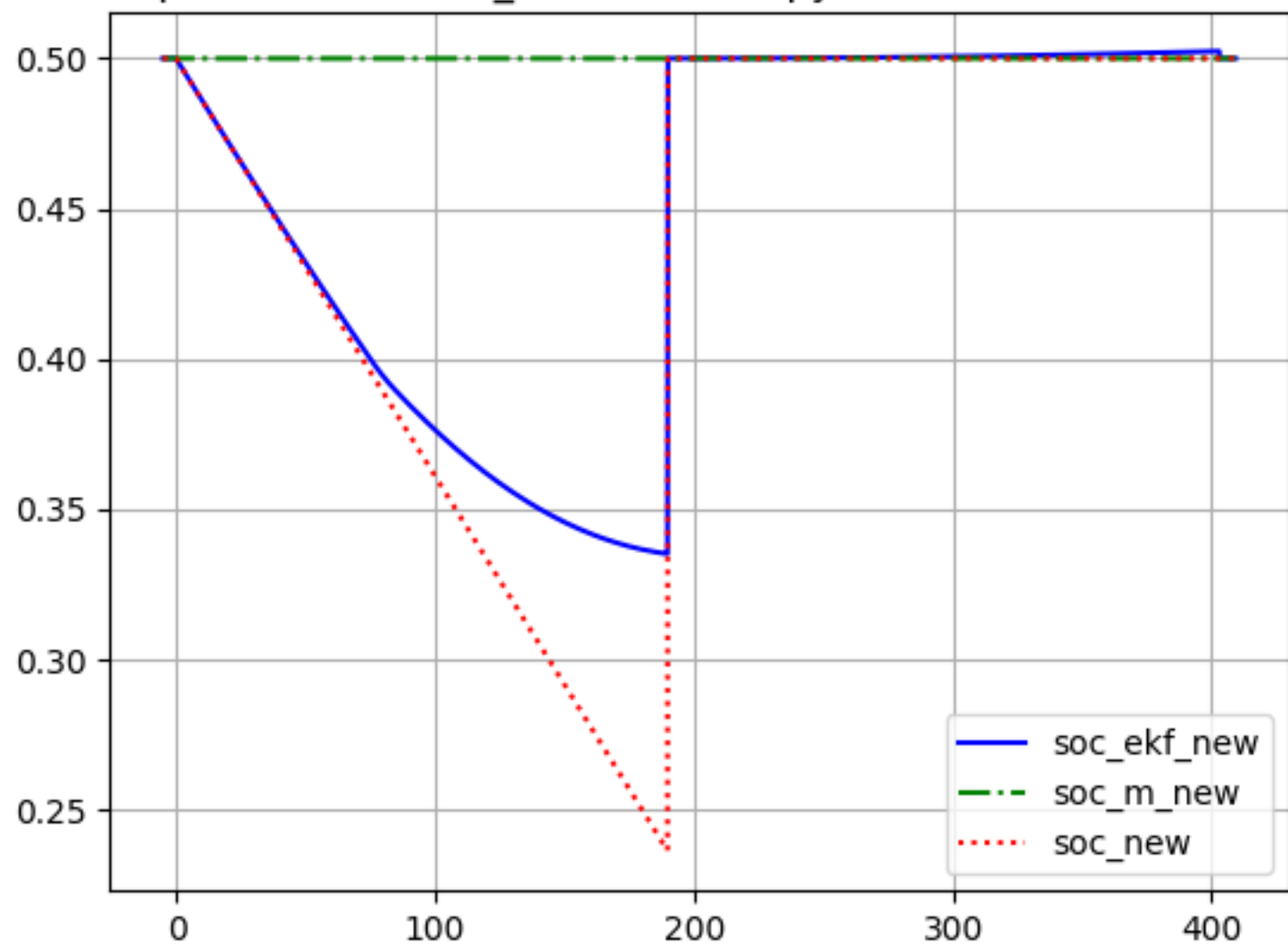
ampLoFail20220807\_mon-MonSim.py 2022-08-07T08-38-29



ampLoFail20220807\_mon-MonSim.py 2022-08-07T08-38-29



ampLoFail20220807\_mon-MonSim.py 2022-08-07T08-38-29





ampLoFail20220807\_mon-MonSim.py 2022-08-07T08-38-29

