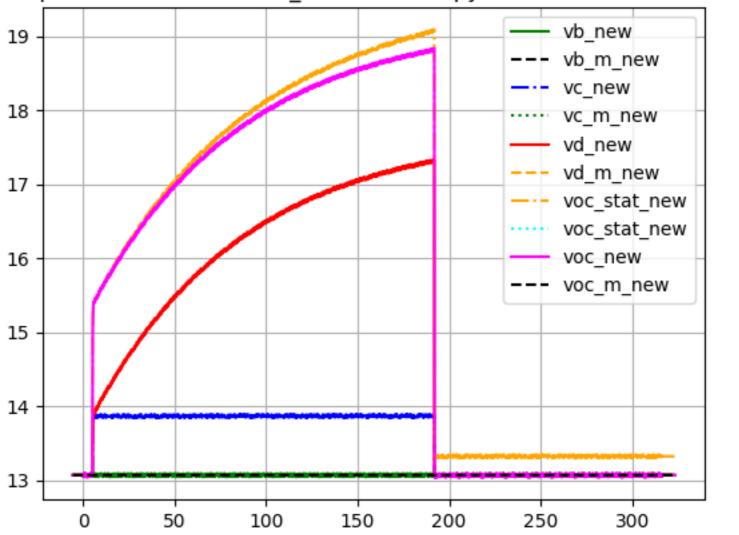
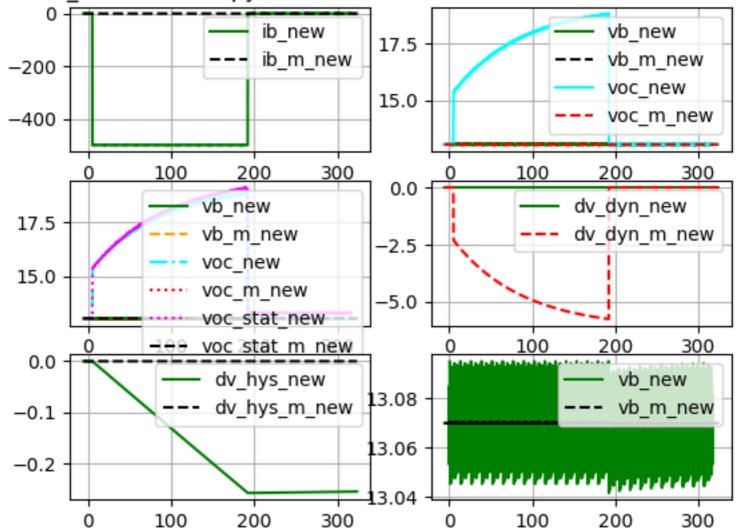


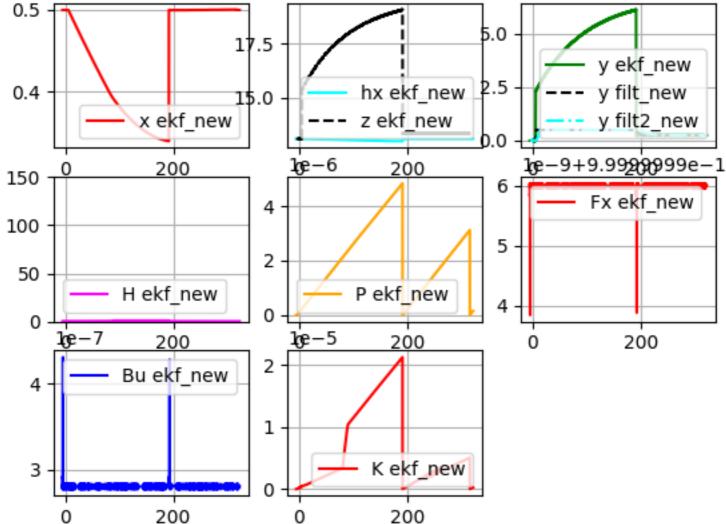
ampLoFailNoise20220808\_mon-MonSim.py 2022-08-08T04-03-15

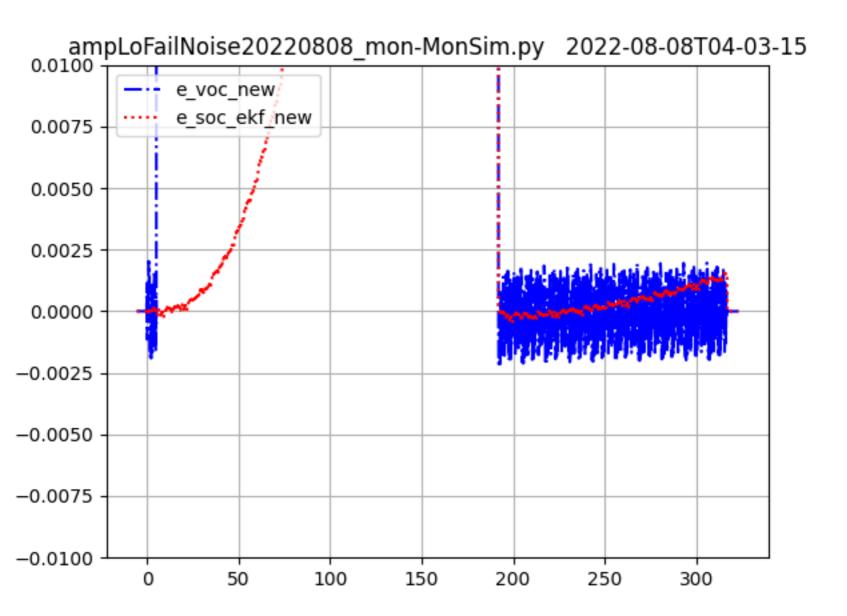


se20220808\_mon-MonSim.py 2022-08-08T04-03-15 \*\*SIM 0.05 ib new soc new 0.50000 i\_r\_ct\_new 0.00 i\_c\_dif\_new<sub>0.4</sub>9975 i\_r\_dif\_new -0.05<del>0.4</del>9950 +d.30699eh 200 100 300 0 200 300 vb new 0.0001 vc new vd new voc\_new -0.0001voc stat new +1.30699e1 100 200 300 0.05 vbc dot new voc vs soc\_new vcd\_dot\_new0.0001 0.00 -0.0001 -0.05100 200 0.4996 0.4998 0.5000 300

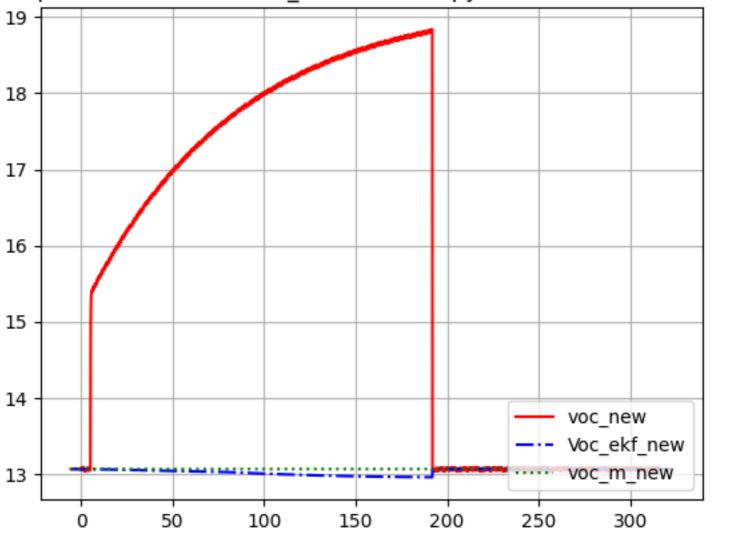
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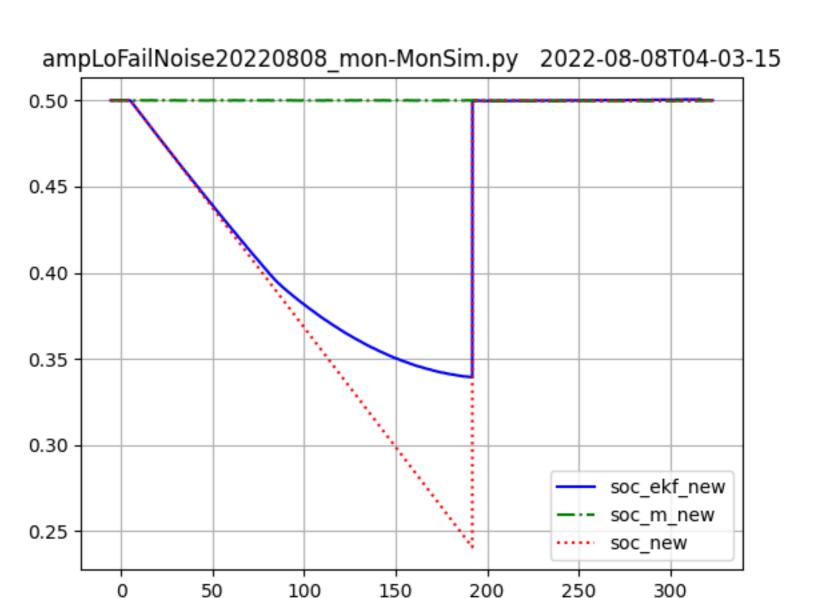






ampLoFailNoise20220808\_mon-MonSim.py 2022-08-08T04-03-15





ampLoFailNoise20220808\_mon-MonSim.py 2022-08-08T04-03-15

