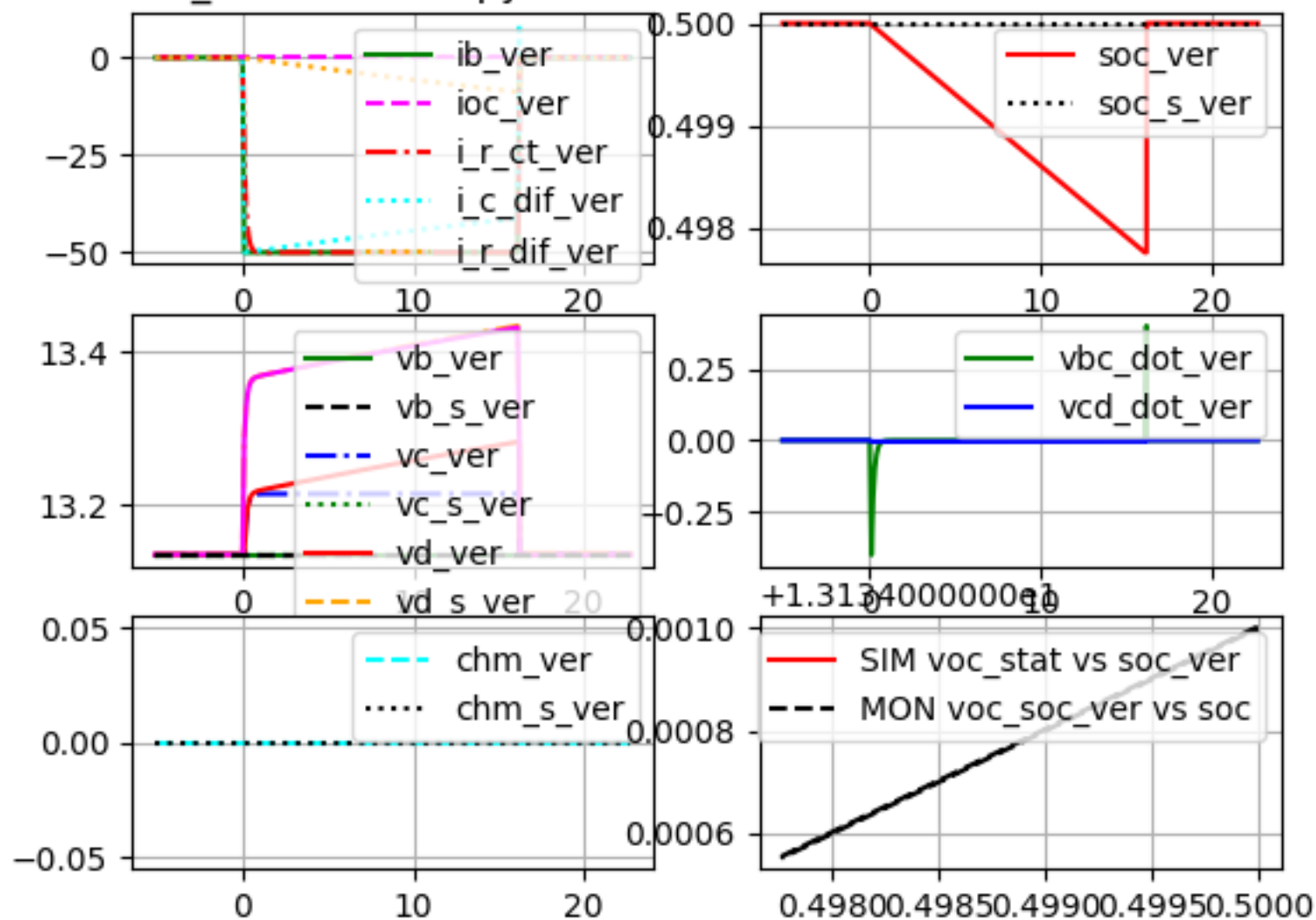
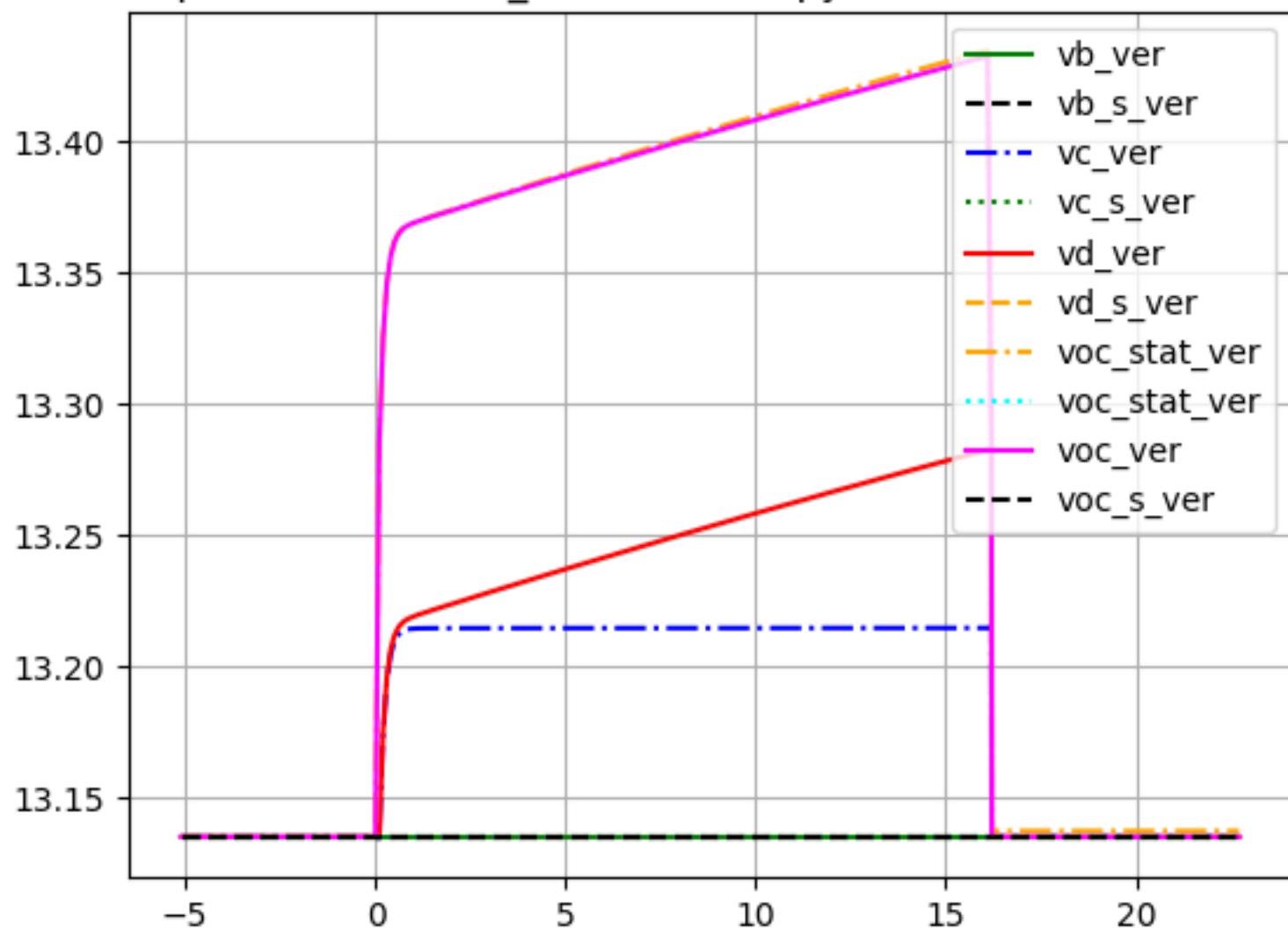


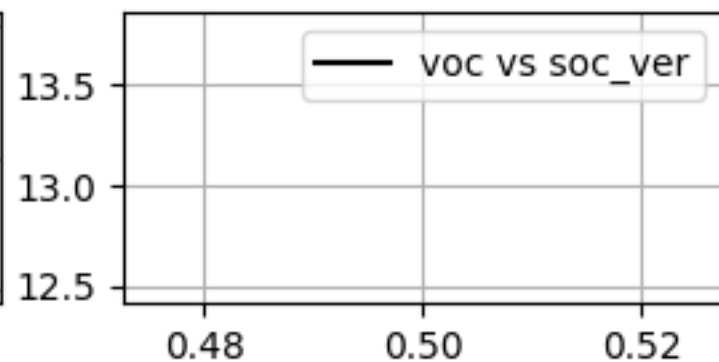
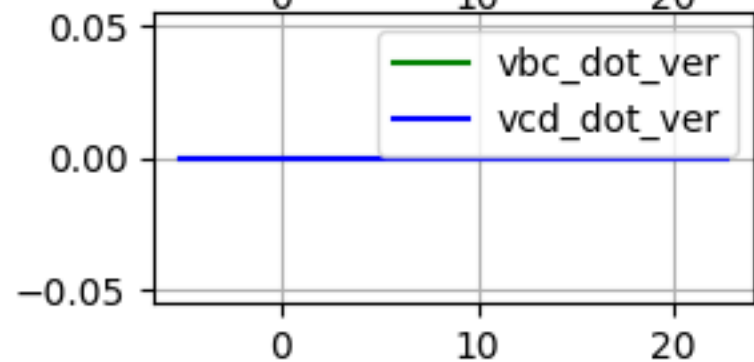
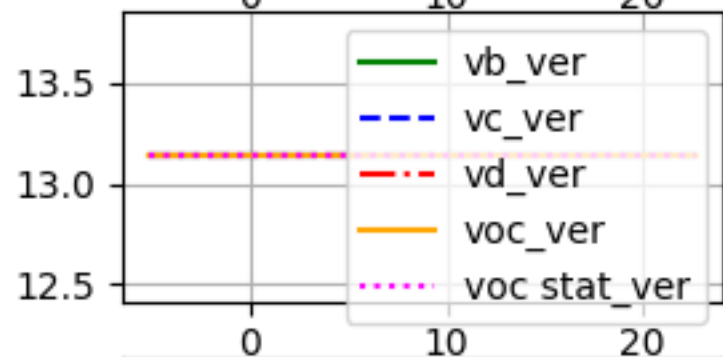
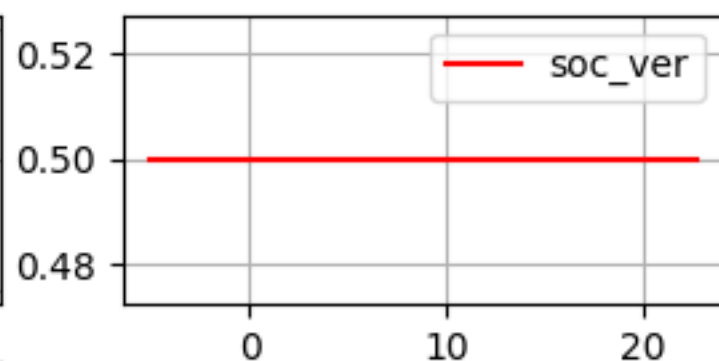
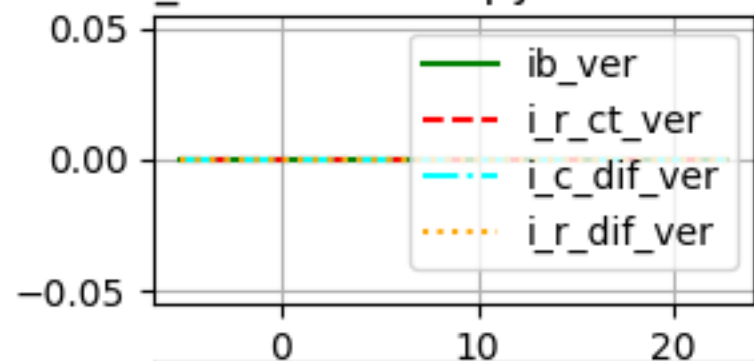
Fail20220910_mon-MonSim.py 2022-09-11T03-25-27



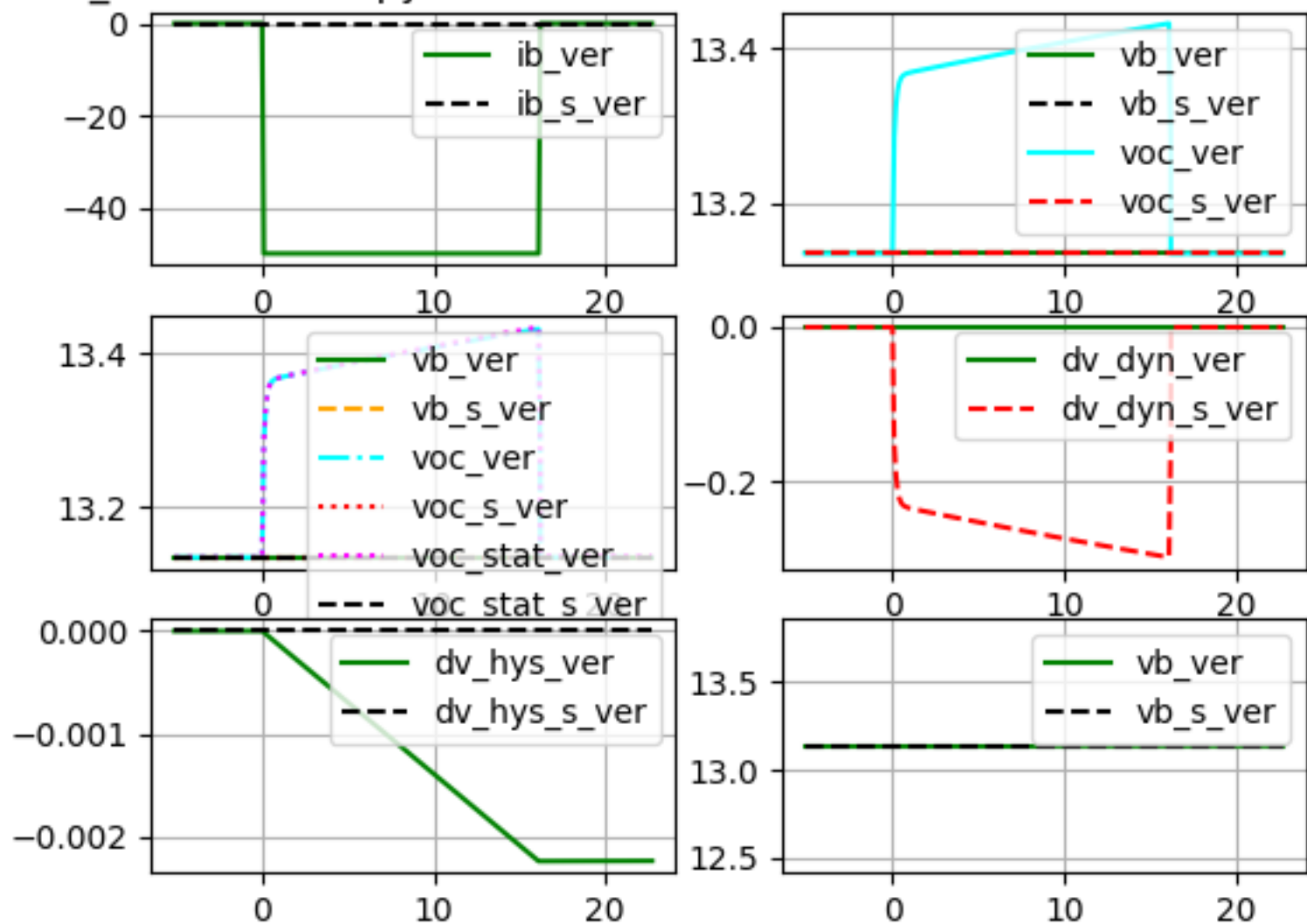
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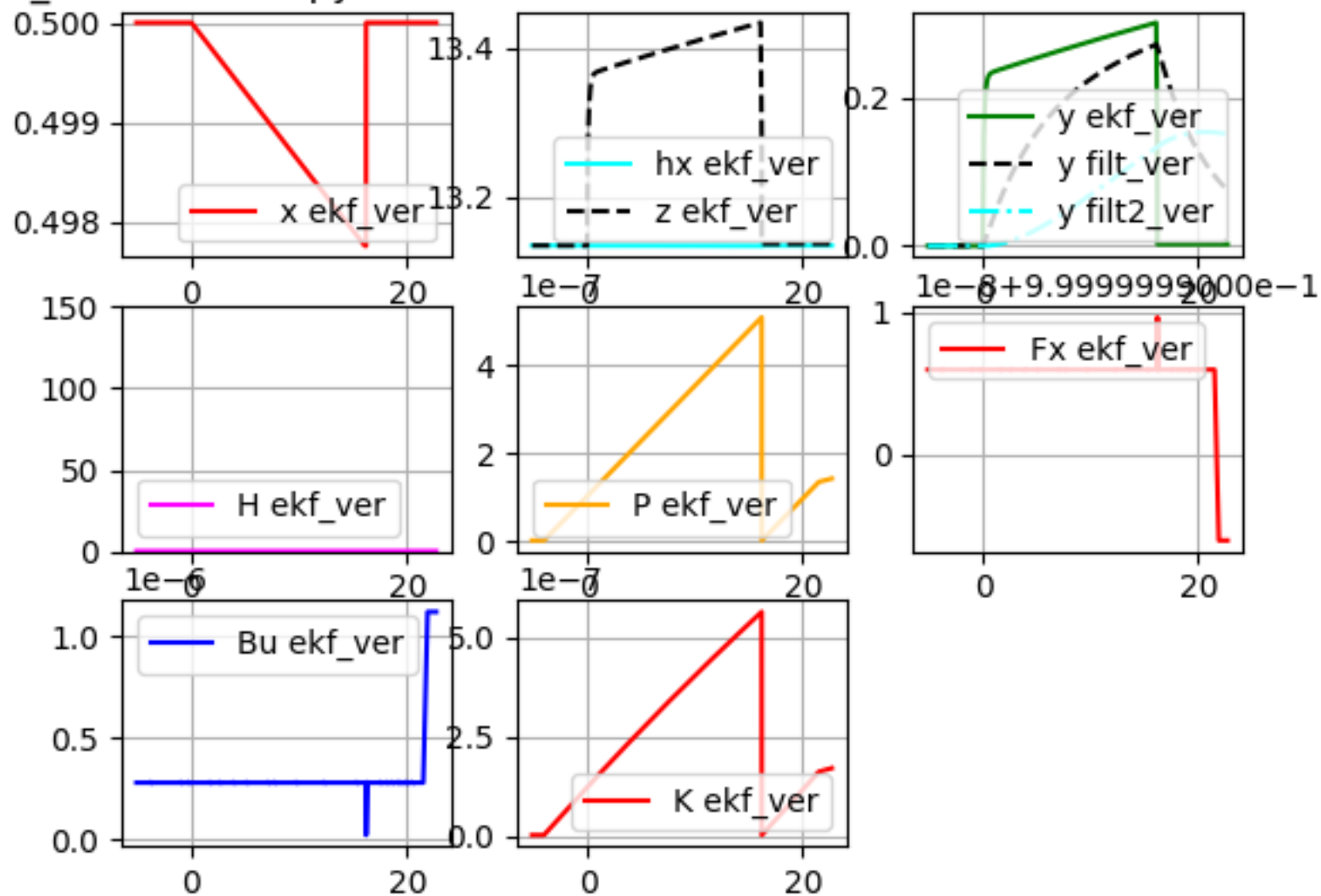
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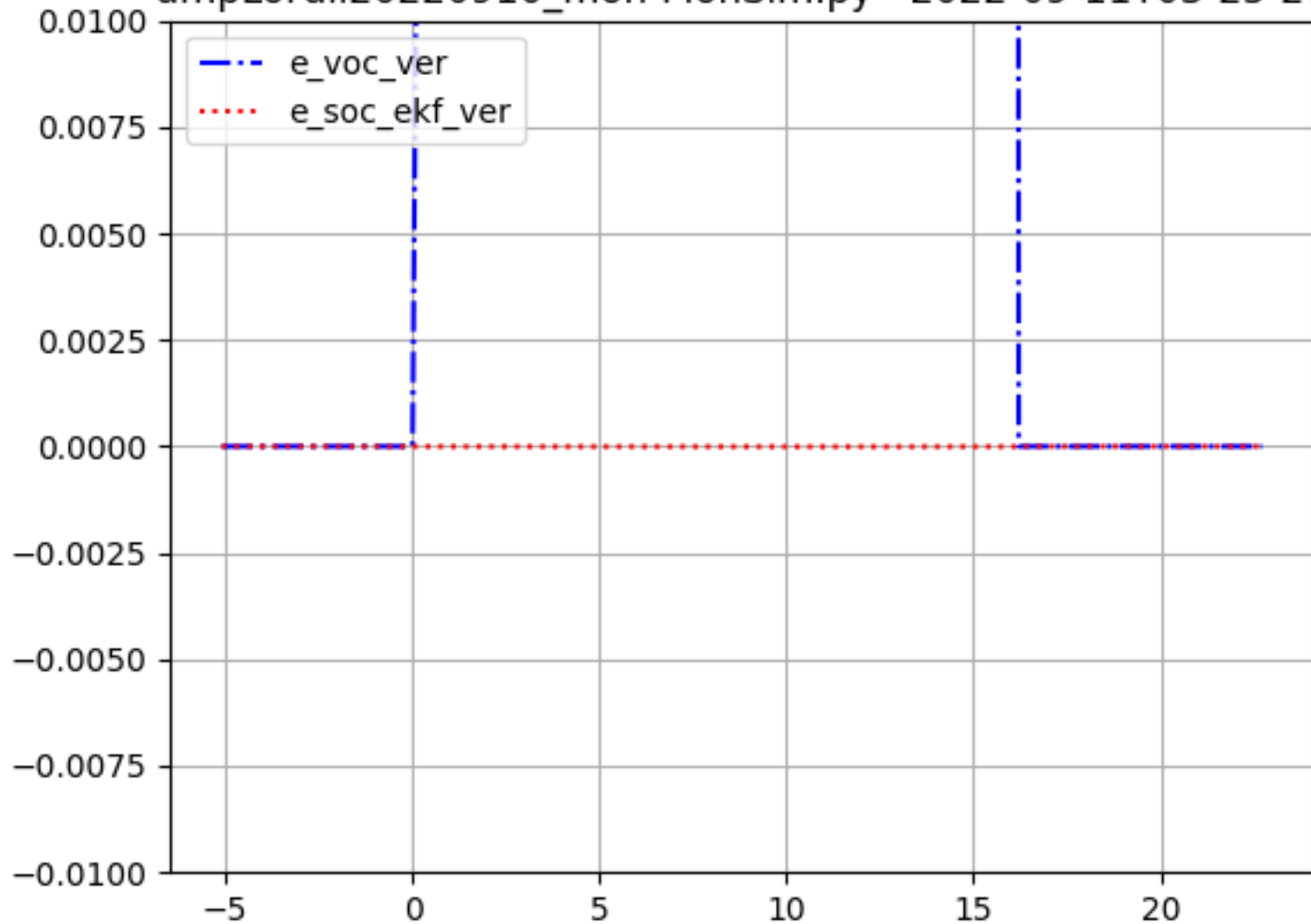
20910_mon-MonSim.py 2022-09-11T03-25-27 MON vs SIM



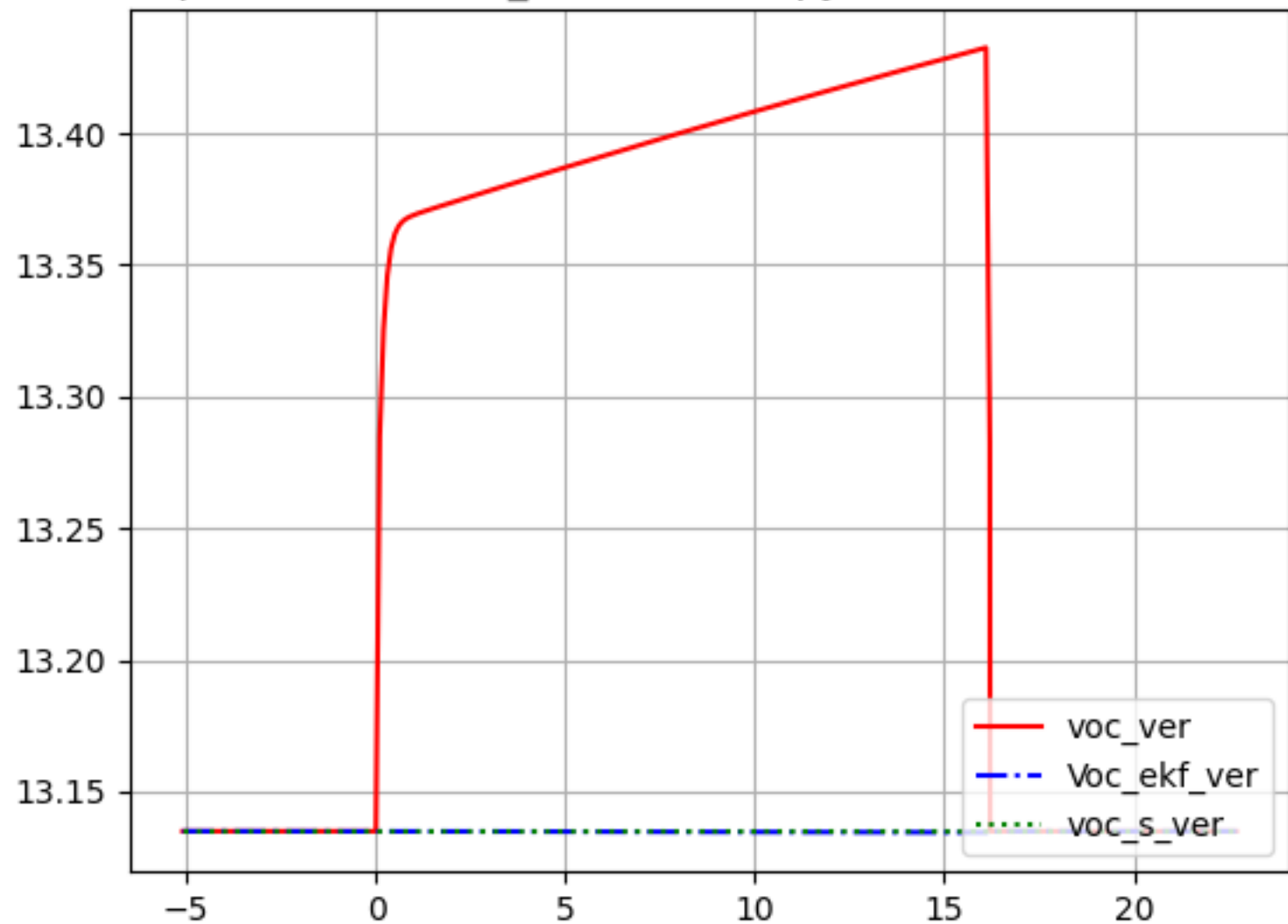
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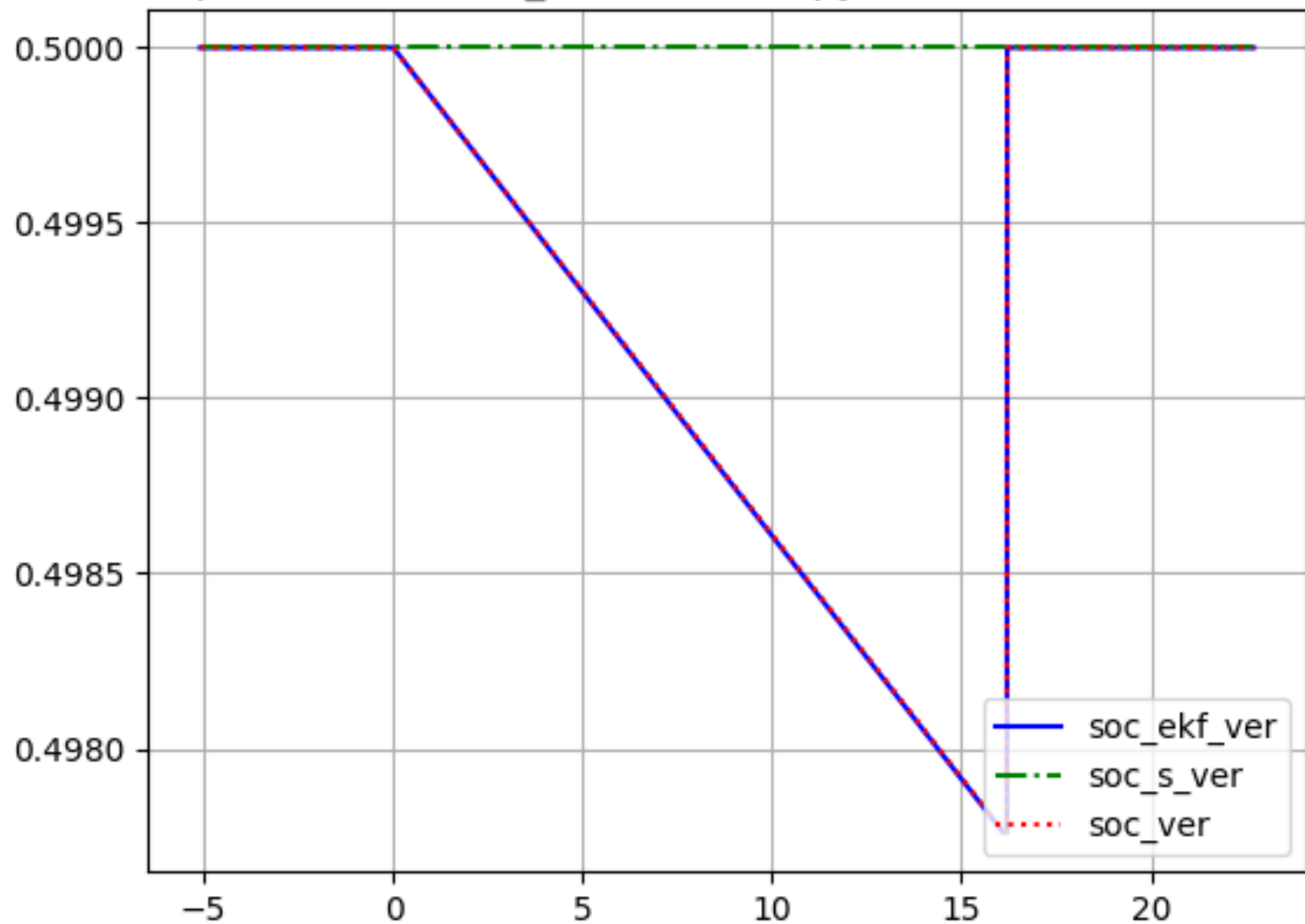
ampLoFail20220910_mon-MonSim.py 2022-09-11T03-25-27



ampLoFail20220910_mon-MonSim.py 2022-09-11T03-25-27



ampLoFail20220910_mon-MonSim.py 2022-09-11T03-25-27



ampLoFail20220910_mon-MonSim.py 2022-09-11T03-25-27

