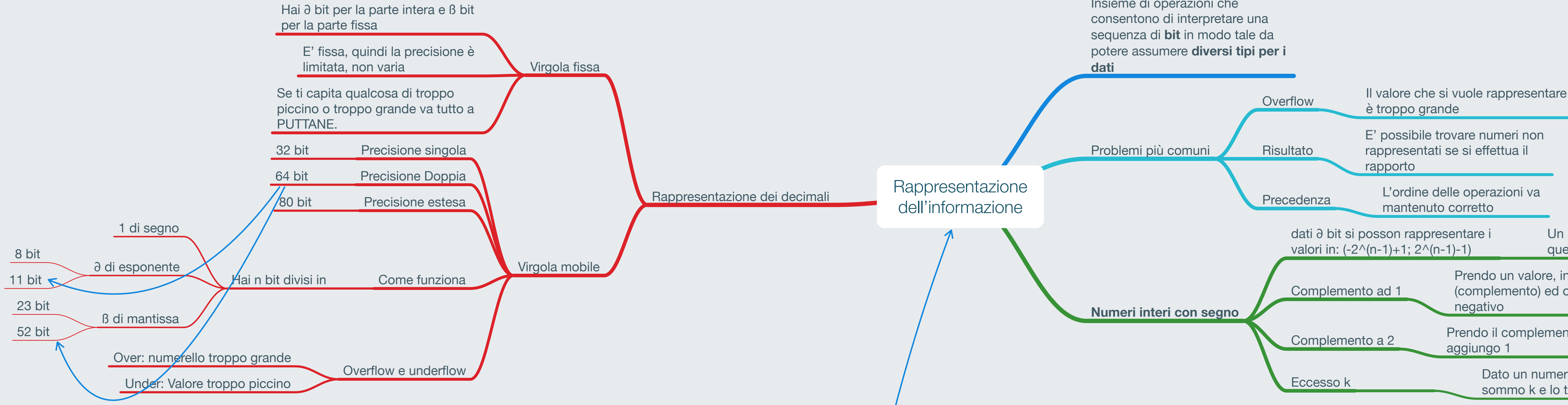
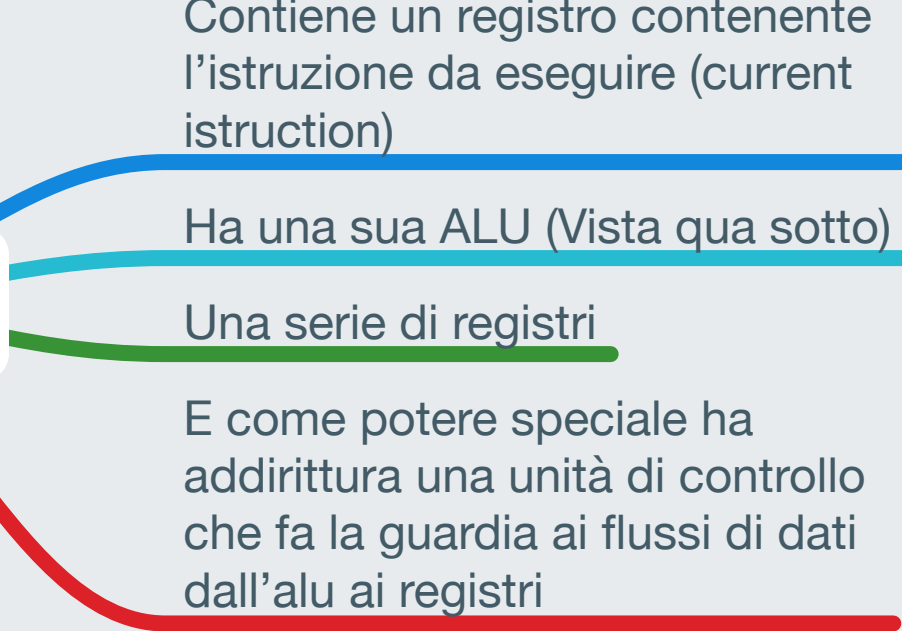


Rappresentazione dell'informazione



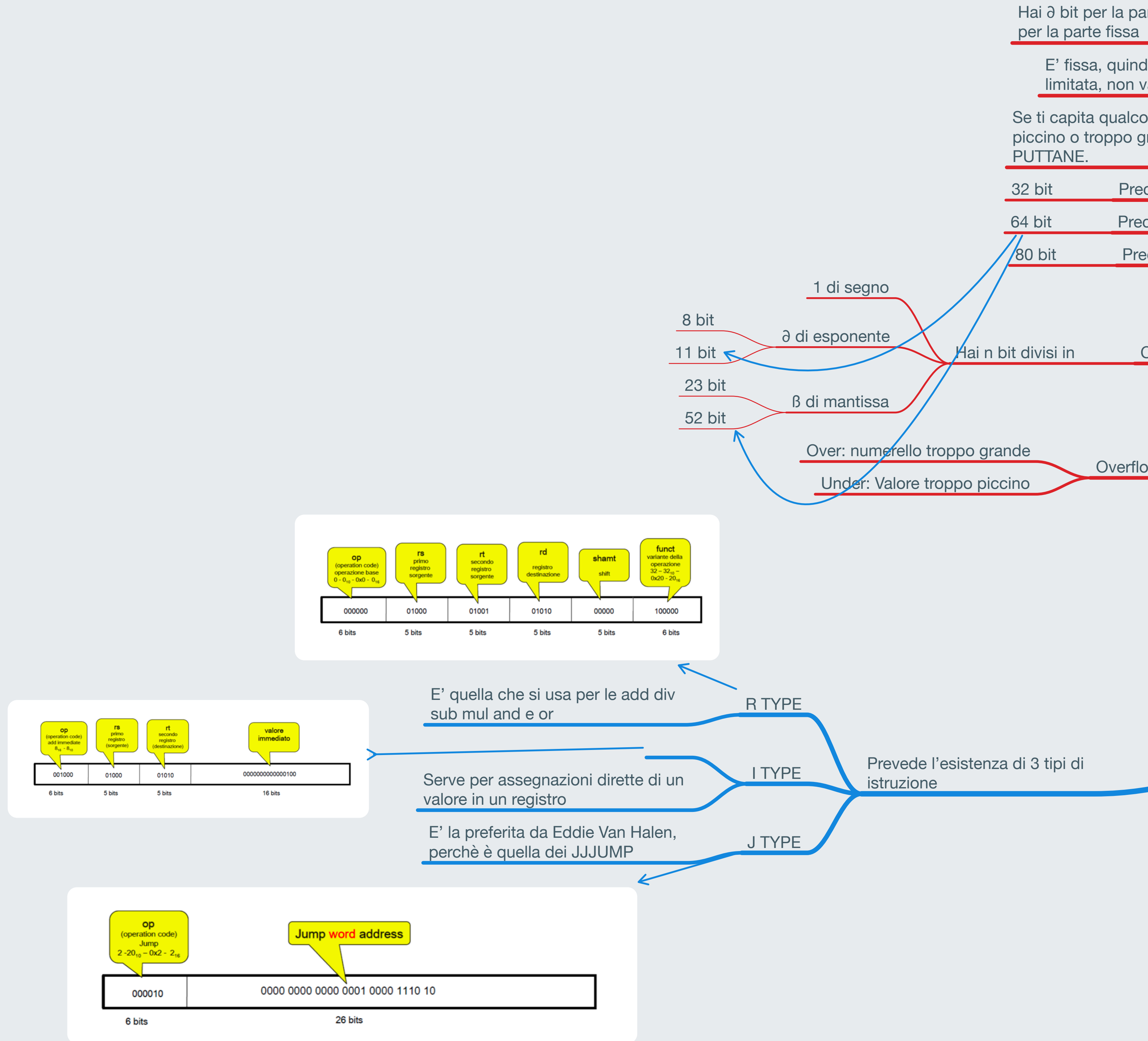
La super CPU



Il magico Hardware

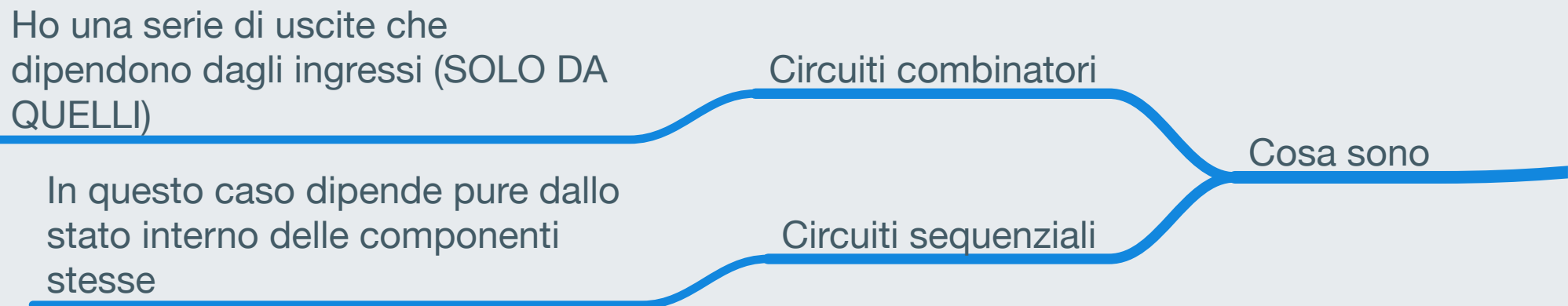


Architettura del MIPS



Architettura degli Elaboratori

Circuiti Combinatori e Sequenziali



Decoder e Multiplexer

