Registermap

LEDG

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	LEDG							
Mode	r0	r0	r0	r0	r0	r0	r0	r0	w							
Reset	-	-	-	-	-	-	-	-	0x00							

LED Green Register
Address: 0x4000

LEDR

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	LEDR									
Mode	r0	r0	r0	r0	r0	r0	w									
Reset	-	-	-	-	-	-	0x000									

LED Red Register
Address: 0x4002

HEX0

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	HEX0						
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	w						
Reset	-	-	-	-	-	-	-	-	-	0x7f						

Seven Segment Digit 0 Register
Address: 0x4010

HEX1

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	HEX1						
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	w						
Reset	-	-	-	-	-	-	-	-	-	0x7f						

Seven Segment Digit 1 Register
Address: 0x4012

HEX2

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	HEX2						
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	w						
Reset	-	-	-	-	-	-	-	-	-	0x7f						

Seven Segment Digit 2 Register
Address: 0x4014

HEX3

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	HEX2						
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	w						
Reset	-	-	-	-	-	-	-	-	-	0x7f						

Seven Segment Digit 3 Register
Address: 0x4016

KEY

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	-	-	-	KEY			
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r			
Reset	-	-	-	-	-	-	-	-	-	-	-	-	-			

Pushbuttons Register
Address: 0x4020

SW

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	SW									
Mode	r0	r0	r0	r0	r0	r0	r									
Reset	-	-	-	-	-	-	-									

Toggle Switch Register
Address: 0x4022

ENDPI0_CONTROL

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	-	-	-	-	ZLP	STALL	FULL
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	w	w	r
Reset	-	-	-	-	-	-	-	-	-	-	-	-	-	0	0	0

Endpoint In 0 Control Register
Address: 0x5000

ENDPI0_DATA

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	DATA							
Mode	r0	r0	r0	r0	r0	r0	r0	r0	w							
Reset	-	-	-	-	-	-	-	-	-							

Endpoint In 0 Data Register
Address: 0x5002

ENDPI1_CONTROL

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	-	-	-	-	ZLP	STALL	FULL
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	w	w	r
Reset	-	-	-	-	-	-	-	-	-	-	-	-	-	0	0	0

Endpoint In 1 Control Register
0x5004

ENDPI1_DATA

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	DATA							
Mode	r0	r0	r0	r0	r0	r0	r0	r0	w							
Reset	-	-	-	-	-	-	-	-	-							

Endpoint In 1 Data Register
0x5006

ENDPO0_CONTROL

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	-	-	-	-	-	-	RDREQ	EMPTY
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	r0	w	r
Reset	-	-	-	-	-	-	-	-	-	-	-	-	-	-	0	0

Endpoint Out 0 Control Register
Address: 0x5040

ENDPO0_DATA

Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Name	-	-	-	-	-	-	-	-	DATA							
Mode	r0	r0	r0	r0	r0	r0	r0	r0	r							
Reset	-	-	-	-	-	-	-	-	-							

Endpoint Out 0 Data Register
Address: 0x5042