EE352L Lab1 worksheet

Date:

Session:

TA:

Mentor:

Task:

Questions:

1. What does MARS stand for?

2. On what platform does MARS run?

3. What does MIPS stand for?

4. What does ISA stand for?

5. What do RISC and CISC stand for?

6. What is the type of architecture of MIPS?

7. How many general purpose registers does MIPS have?

8. What is the size of word in MIPS?

9. What type of instruction does MIPS support?

Assignments: