		Width	Int ALU	Int Multiplier	Memory Port	FP ALU	FP Multiplier	Prediction	Instruction Issue	CPI	Speedup	Cost	Weighted Speedup
Test Suite 1: Varying Numbers of ALUs and Multipliers for In-Order Instruction Execution with a Machine Width of 1													
1		1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
2		1	2	1	1	1	1	taken	in-order	2.2941	1.0000	1450	0.6897
3		1	3	1	1	1	1	taken	in-order	2.2941	1.0000	1550	0.6452
4		1	4	1	1	1	1	taken	in-order	2.2941	1.0000	1650	0.6061
5		1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
6		1	1	2	1	1	1	taken	in-order	2.2941	1.0000	1550	0.6452
7		1	1	3	1	1	1	taken	in-order	2.2941	1.0000	1750	0.5714
8		1	1	4	1	1	1	taken	in-order	2.2941	1.0000	1950	0.5128
9		1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
10		1	1	1	1	2	1	taken	in-order	2.2941	1.0000	1500	0.6667
11		1	1	1	1	3	1	taken	in-order	2.2941	1.0000	1650	0.6061
12		1	1	1	1	4	1	taken	in-order	2.2941	1.0000	1800	0.5556
13		1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
14		1	1	1	1	1	2	taken	in-order	2.2941	1.0000	1600	0.6250
15		1	1	1	1	1	3	taken	in-order	2.2941	1.0000	1850	0.5405
16		1	1	1	1	1	4	taken	in-order	2.2941	1.0000	2100	0.4762
17		1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
18		1	1	1	1	2	2	taken	in-order	2.2941	1.0000	1750	0.5714
19		1	1	1	1	3	3	taken	in-order	2.2941	1.0000	2150	0.4651
20		1	1	1	1	4	4	taken	in-order	2.2941	1.0000	2550	0.3922