

Test Suite 4: Varying Numbers of ALUs and Multipliers for Out-Of-Order Instruction Execution with a Machine Width of 4												
	Width	Int ALU	Int Multiplier	Memory Port	FP ALU	FP Multiplier	Prediction	Instruction Issue	CPI	Speedup	Cost	Weighted Speedup
1	4	1	1	1	1	1	taken	out-of-order	1.6631	1.3794	3485	0.3958
2	4	2	1	1	1	1	taken	out-of-order	1.5252	1.5041	3655	0.4115
3	4	3	1	1	1	1	taken	out-of-order	1.5124	1.5169	3825	0.3966
4	4	4	1	1	1	1	taken	out-of-order	1.5117	1.5176	3995	0.3799
5	4	1	1	1	1	1	taken	out-of-order	1.6631	1.3794	3485	0.3958
6	4	1	2	1	1	1	taken	out-of-order	1.6631	1.3794	3825	0.3606
7	4	1	3	1	1	1	taken	out-of-order	1.6631	1.3794	4165	0.3312
8	4	1	4	1	1	1	taken	out-of-order	1.6631	1.3794	4505	0.3062
9	4	1	1	1	1	1	taken	out-of-order	1.6631	1.3794	3485	0.3958
10	4	1	1	1	2	1	taken	out-of-order	1.6631	1.3794	3740	0.3688
11	4	1	1	1	3	1	taken	out-of-order	1.6631	1.3794	3995	0.3453
12	4	1	1	1	4	1	taken	out-of-order	1.6651	1.3778	4250	0.3242
13	4	1	1	1	1	1	taken	out-of-order	1.6631	1.3794	3485	0.3958
14	4	1	1	1	1	2	taken	out-of-order	1.6631	1.3794	3910	0.3528
15	4	1	1	1	1	3	taken	out-of-order	1.6631	1.3794	4335	0.3182
16	4	1	1	1	1	4	taken	out-of-order	1.6631	1.3794	4760	0.2898