

Test Suite 2: Varying Numbers of ALUs and Multipliers for Out-Of-Order Instruction Execution with a Machine Width of 1												
	Width	Int ALU	Int Multiplier	Memory Port	FP ALU	FP Multiplier	Prediction	Instruction Issue	CPI	Speedup	Cost	Weighted Speedup
1	1	1	1	1	1	1	taken	out-of-order	2.1593	1.0624	2210	0.4807
2	1	2	1	1	1	1	taken	out-of-order	2.1593	1.0624	2380	0.4464
3	1	3	1	1	1	1	taken	out-of-order	2.1593	1.0624	2550	0.4166
4	1	4	1	1	1	1	taken	out-of-order	2.1593	1.0624	2720	0.3906
5	1	1	1	1	1	1	taken	out-of-order	2.1593	1.0624	2210	0.4807
6	1	1	2	1	1	1	taken	out-of-order	2.1593	1.0624	2550	0.4166
7	1	1	3	1	1	1	taken	out-of-order	2.1593	1.0624	2890	0.3676
8	1	1	4	1	1	1	taken	out-of-order	2.1593	1.0624	3230	0.3289
9	1	1	1	1	1	1	taken	out-of-order	2.1593	1.0624	2210	0.4807
10	1	1	1	1	2	1	taken	out-of-order	2.1593	1.0624	2465	0.4310
11	1	1	1	1	3	1	taken	out-of-order	2.1593	1.0624	2720	0.3906
12	1	1	1	1	4	1	taken	out-of-order	2.1593	1.0624	2975	0.3571
13	1	1	1	1	1	1	taken	out-of-order	2.1593	1.0624	2210	0.4807
14	1	1	1	1	1	2	taken	out-of-order	2.1593	1.0624	2635	0.4032
15	1	1	1	1	1	3	taken	out-of-order	2.1593	1.0624	3060	0.3472
16	1	1	1	1	1	4	taken	out-of-order	2.1593	1.0624	3485	0.3049