

Test Suite 5: Varying Machine Width and Numbers of Memory Ports for In-Order Instruction Execution												
	Width	Int ALU	Int Multiplier	Memory Port	FP ALU	FP Multiplier	Prediction	Instruction Issue	CPI	Speedup	Cost	Weighted Speedup
1	1	1	1	1	1	1	taken	in-order	2.2941	1.0000	1350	0.7407
2	1	1	1	2	1	1	taken	in-order	2.2941	1.0000	1650	0.6061
3	1	1	1	3	1	1	taken	in-order	2.2941	1.0000	1950	0.5128
4	1	1	1	4	1	1	taken	in-order	2.2941	1.0000	2250	0.4444
5	2	1	1	1	1	1	taken	in-order	2.1364	1.0738	1600	0.6711
6	2	1	1	2	1	1	taken	in-order	2.1364	1.0738	1900	0.5652
7	2	1	1	3	1	1	taken	in-order	2.1364	1.0738	2200	0.4881
8	2	1	1	4	1	1	taken	in-order	2.1364	1.0738	2500	0.4295
9	4	1	1	1	1	1	taken	in-order	1.9832	1.1568	1850	0.6253
10	4	1	1	2	1	1	taken	in-order	1.9832	1.1568	2150	0.5380
11	4	1	1	3	1	1	taken	in-order	1.9832	1.1568	2450	0.4721
12	4	1	1	4	1	1	taken	in-order	1.9832	1.1568	2750	0.4206