	Width	Int ALU	Int Multiplier	Memory Port	FP ALU	FP Multiplier	Prediction	Instruction Issue	CPI	Speedup	Cost	Weighted Speedup
				Test Suite 3: Varying Numbers of ALUs and Multipliers for In-Order Instruction Execution with a Machine Width of 4								
1	4	1	1	1	1	1	taken	in-order	1.9832	1.1568	2100	0.5508
2	4	2	1	1	1	1	taken	in-order	1.9188	1.1956	2200	0.5435
3	4	3	1	1	1	1	taken	in-order	1.9114	1.2002	2300	0.5218
4	4	4	1	1	1	1	taken	in-order	1.9104	1.2008	2400	0.5004
5	4	1	1	1	1	1	taken	in-order	1.9832	1.1568	2100	0.5508
6	4	1	2	1	1	1	taken	in-order	1.9832	1.1568	2300	0.5029
7	4	1	3	1	1	1	taken	in-order	1.9832	1.1568	2500	0.4627
8	4	1	4	1	1	1	taken	in-order	1.9832	1.1568	2700	0.4284
9	4	1	1	1	1	1	taken	in-order	1.9832	1.1568	2100	0.5508
10	4	1	1	1	2	1	taken	in-order	1.9832	1.1568	2250	0.5141
11	4	1	1	1	3	1	taken	in-order	1.9832	1.1568	2400	0.4820
12	4	1	1	1	4	1	taken	in-order	1.9832	1.1568	2550	0.4536
13	4	1	1	1	1	1	taken	in-order	1.9832	1.1568	2100	0.5508
14	4	1	1	1	1	2	taken	in-order	1.9832	1.1568	2350	0.4922
15	4	1	1	1	1	3	taken	in-order	1.9832	1.1568	2600	0.4449
16	4	1	1	1	1	4	taken	in-order	1.9832	1.1568	2850	0.4059