IMPLEMENTATION OF THE FINITE-DIFFERENCE TIME-DOMAIN METHOD USING GRAPHICS PROCESSING UNITS

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Lively, S. David

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Method Using Graphics Processing Units

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Traditionally, optical circuit design is tested and validated using software which

implement numerical modeling techniques such as Beam Propagation, Finite Element

Analysis and FDTD.

While effective and accurate, FDTD simulations require significant computational

power. Existing installations may distribute the computational requirements across

large clusters of high-powered servers. This approach entails significant expense in

terms of hardware, staffing and software support which may be prohibitive for some

research facilities and private-sector engineering firms.

Application of modern programmable GPGPUs to problems in scientific visual-

ization and computation has facilitated dramatically accelerated development cycles

for a variety of industry segments including large dataset visualization, microproces-

sor design, aerospace and electromagnetic wave propagation in the context of optical

circuit design.

The FDTD algorithm as envisioned by its creators maps well to the massively-

multithreaded data-parallel nature of GPUs. This thesis explores a GPU FDTD

implementation and details performance gains, limitations of the GPU approach,

optimization techniques and potential future enhancements that may provide even

greater benefits from this underutilized and often-overlooked tool.

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INTRODUCTION

FDTD [1] is a proven algorithm, first published in (...) by (yee, et al). It is the underlying mechanism used by many commercial optics simulation packages, as well as open source software such as MIT's Meep.

Given the computationally-intensive nature of FDTD, organizations requiring simulation of large domains or complex circuits must provide significant resources. These may take the form of leased server time or utilization of an on-site high-performance cluster, amongst other options.

In this thesis, we explore an implementation of the Finite-Difference, Time-Domain (FDTD) method of electromagnetic waves simulation as implemented on graphics processing units (GPUs). Initially designed to perform image generation tasks such as those required by games, cinema and related fields, modern versions are well-suited for general computation work. GPUs are now enjoying wide adoption in fields such as machine learning and artificial intelligence, medical research, signals analysis and other areas which require rapid analysis of large datasets.

Even modern consumer-grade GPUs offer thousands or tens of thousands of processing units, while high-end CPUs offer 4-8 cores. While the two are not interchangeable (see: chapter on Device Architecture), some algorithms, such as FDTD, require little or data interdependence, no branching logic (a severe performance impediment on GPUs) and consist of short cycles of simple operations. The power of the GPU lies in performing these simple operations at large scale, with thousands of threads running in parallel.

The following sections detail FDTD. Later sections describe a CPU-based implementation (MIT's Meep simulator), and our GPU-based GoLightly simulator. We verify the GPU solution numerically, and compare performance between CPU- and GPU-based implementations. Finally, we consider future applications and enhancements.

1.1. FDTD Overview

At it's heart, FDTD expresses Maxwell's equations as a discretized set of timedomain equations. These equations describe each electric field component in terms if its orthogonal, coupled magnetic fields, and each magnetic field component as a function of its coupled, orthogonal electric fields.

1.1.1. Wave equation

In time domain, the discretized wave equation for E_z is of the form:

$$\frac{dE_z}{dt} = K * (\frac{dH_x}{dy} + \frac{dH_y}{dx})$$

These time-domain equations describe how a field at a given point in space evolves over time based on the local spatial derivative if its coupled fields. The algorithm updates E and H fields in a "leap frog" manner:

1.1.2. Yee Cell

DEVICE ARCHITECTURE

 $\bf 2.1.~CPU$ independent cores, separate cache, dedicated ALU and registers

2.2. **GPU**

2.2.1. SIMD

SIMD - single ALU for multiple register sets

2.2.2. FDTD in SIMD

why FDTD maps well to GPUs

MEEP

- 3.1. Background
- 3.2. Modeling approach
- 3.3. Performance
- 3.4. Usability

GOLIGHTLY

4.2.2.	GPU
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4.1. goals

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- 5.1. Meep performance
- 5.2. GoLightly performancec
- 5.3. Meep vs GoLightly
- 5.4. Results
- 5.5. Limitations

FUTURE WORK

future work...

REFERENCES

[1] YEE, K. Numerical solution of initial boundary value problems involving maxwell's equations in isotropic media. *IEEE Transactions on Antennas and Propagation* 14, 3 (1966), 302–307.