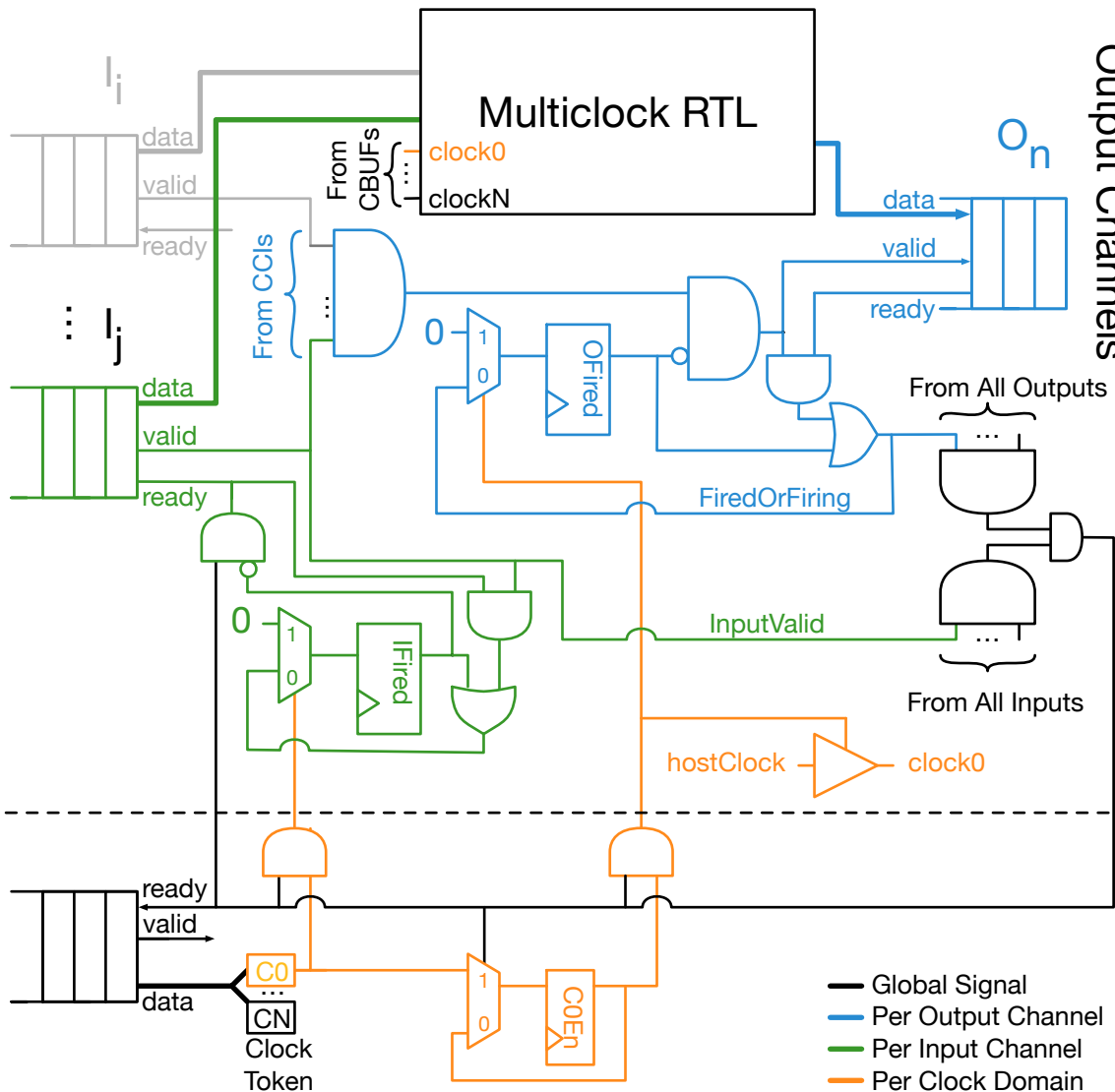


Data Input Channels

Output Channels

Clock Channel



- Global Signal
- Per Output Channel
- Per Input Channel
- Per Clock Domain

Clock Control Path