

# Studying link-time optimizations in programming language development to facilitate the continuum of static and dynamic modules

**David Callanan**

**21444104**

Final Year Project – 2026

B.Sc. Single Honours in Computational Thinking



**Maynooth  
University**  
National University  
of Ireland Maynooth

Department of Computer Science

Maynooth University

Maynooth, Co. Kildare

Ireland

A thesis submitted in partial fulfilment of the requirements for the

B.Sc. Single Honours in Computational Thinking.

**Supervisor: Dr. Phil Maguire**

# Contents

Declaration . . . . .	4
Acknowledgements . . . . .	4
<b>1 Introduction</b>	<b>2</b>
1.1 Overview . . . . .	2
1.2 Motivation . . . . .	2
1.3 Methodology . . . . .	2
1.4 Success Criteria . . . . .	2
<b>2 Technical Background</b>	<b>4</b>
2.1 Literature Review . . . . .	4
2.1.1 Static vs dynamic dispatch . . . . .	4
2.1.2 Static vs dynamic linking . . . . .	4
2.1.3 Module loading in operating system kernels . . . . .	5
2.1.4 Link-time optimizations . . . . .	5
2.1.5 Inlining and IR-level linking . . . . .	5
2.2 Technical Material . . . . .	5
2.2.1 LLVM intermediate representation . . . . .	5
<b>3 The Problem</b>	<b>6</b>
<b>4 The Solution</b>	<b>7</b>
4.1 Individual elements . . . . .	7
4.1.1 Modules treated like classes . . . . .	7

4.1.2	Additional benefit of inlining: interacting with existing languages without indirection. . . . .	7
4.2	Picking of overall solution . . . . .	7
4.3	Design . . . . .	7
4.4	Implementation . . . . .	7
4.4.1	Mutability . . . . .	7
4.4.2	Tooling and Infrastructure . . . . .	8
<b>5</b>	<b>Evaluation</b>	<b>9</b>
<b>6</b>	<b>Conclusion</b>	<b>10</b>
6.0.1	Contribution to state-of-the-art . . . . .	10
<b>7</b>	<b>Parsing Library</b>	<b>11</b>

## Declaration

I hereby certify that this material, which I now submit for assessment on the program of study as part of the B.Sc. Computational Thinking qualification, is entirely my own work and has not been taken from the work of others - save and to the extent that such work has been cited and acknowledged within the text of my work.

I hereby acknowledge and accept that this thesis may be distributed to future final year students, as an example of the standard expected of final year projects.

Signed: \_\_\_\_\_

Date: \_\_\_\_\_

## Acknowledgements

I would like to thank my parents/guardians/siblings/classmate/friends for their ...

## **Abstract**

The logic of computer software is primarily expressed using programming languages, but the balance between versatile and efficient programming languages remains a major challenge. Changes to the execution environment of a program can impact performance, or worse, cause incompatibilities that require major refactoring. Developers often maintain multiple branches of a codebase to keep performance to acceptable levels. One particular difference between execution environments is whether individual modules are statically linked at compile-time or dynamically resolved at runtime. In this research project, we propose a prototype programming language called “Essence C” that better supports the continuum of static and dynamic modules, by allowing the programmer to write code once that is agnostic to the linking strategy. By taking advantage of LLVM’s intermediate representation, it is possible to advance the state-of-the-art of link-time optimizations across module boundaries.

# Chapter 1 – Introduction

## 1.1 Overview

## 1.2 Motivation

## 1.3 Methodology

Thesis structure, and a research strategy.

To address the objectives of this research, a design-science methodology is employed with a focus on continued iteration and evaluation of “Essence C”. This approach was selected because the project is concerned with the implementation of a novel programming language that advances the flexibility and performance of modules in software development, and thus requires a design and implementation process that is informed by ongoing evaluation.

The structure of this thesis is organized to reflect this methodology. Chapter 2 reviews existing literature.

## 1.4 Success Criteria

Success will be evidenced by the following deliverables:

- (1) A functional prototype compiler that implements the designed module system;
- (2) Documentation of the module system and other core language features;
- (3) A simple IDE extension that provides syntax highlighting to the programmer;
- (4) A body of research into the continuum of static and dynamic modules in software development, with kernel development as a case study;

- (5) An implementation of link-time optimization techniques that leverage the designed module system;
- (6) An evaluation of the effectiveness of these optimizations through the benchmarking of sample programs;
- (7) A monorepo containing extensive git history, demonstrating ongoing development progress;
- (8) A final written report detailing the research, design, implementation and evaluation of the project.

## Chapter 2 – Technical Background

### 2.1 Literature Review

#### 2.1.1 Static vs dynamic dispatch

Methods of class instances (or equivalent constructs) dispatch calls either statically or dynamically. When the exact implementation of a method is known at compile-time, static dispatch is typically used, involving a direct assembly call to the method. Since the method is known at compile-time, the compiler can use an immediate addressing, which is most efficient. In contrast, when the exact implementation of a method is not decided until runtime (or if the compiler cannot prove otherwise), dynamic dispatch is used, which involves an indirect call through a vtable or similar structure.

#### 2.1.2 Static vs dynamic linking

There is much overlap in the literature between dispatch and linking, but linking focuses on larger units of code, often referred to as modules or libraries, as opposed to class instances. This concept exists in C where there is no inbuilt notion of static vs dynamic dispatch.

When the exact source code (implementation) of a module is known at compile-time, static linking is typically used, involving the direct inclusion of the module's code into the final executable. Then, the compiler can use immediate addressing to invoke functions or access data from this module. In contrast, when the exact source code of a module is not decided until runtime, dynamic linking is used which typically requires a Procedure Linkage Table (PLT) and Global Offset Table (GOT).

However, various techniques are already employed to optimize dynamic linking:

- (1) Some architectures use the "relaxation" technique to replace indirection at runtime by modifying the assembly instructions.



- (2) PLT Rewriting provided by compilers.
- (3) Bespoke replacement (e.g. linux kernel `static_call`).

However, two primary concerns of interest are still evident:

- (1) There is always some level of pointer indirection that cannot be avoided (nvm);
- (2) Various techniques such as inlining become impossible due to this indirection.

Terrible PIMPLE idiom. Headers of headers.

### 2.1.3 Module loading in operating system kernels

#### 2.1.4 Link-time optimizations

Traditionally, many optimizations were performed at the language level, before the program was compiled to object files. For instance, inlining could not be performed at link-time because there was insufficient information in the object files to reason about the semantics of the program. While dedicated link-time optimizations were introduced, their scope was limited for this reason.

However, LLVM IR sits in between the level of abstraction of source code and object files, and maintains higher-level semantics. By keeping the program in this intermediate representation longer (and treating this as modern object files), recent developments have made link-time optimizations more powerful, allowing for, for instance, inlining across module boundaries.

#### 2.1.5 Inlining and IR-level linking

## 2.2 Technical Material

### 2.2.1 LLVM intermediate representation

## Chapter 3 – The Problem

TODO

# Chapter 4 – The Solution

## 4.1 Individual elements

### 4.1.1 Modules treated like classes

### 4.1.2 Additional benefit of inlining: interacting with existing languages without indirection.

## 4.2 Picking of overall solution

## 4.3 Design

## 4.4 Implementation

### 4.4.1 Mutability

LLVM IR is in SSA form, where registers are immutable. One cannot reuse a register, and must instead create a new register for every mutation. Then, one takes advantage of the phi instruction to pick which register is the correct one at runtime. However, the alloca (stack allocation) model operates under the familiar mindset of mutability, as it works with raw memory addresses. Conveniently, it was observed that the standard mem2reg optimization pass does a fantastic job at converting stack allocations into SSA registers where legal, bypassing the tough development of a custom SSA mutability mechanism. Thus variables are entirely implemented using stack allocations.

## 4.4.2 Tooling and Infrastructure

### Parser Trace Explorer

Parsing issues were frequently encountered during the development cycle, and manual debugging became infeasible as complexity increased. Hence, a *Parser Trace Explorer* was developed, consisting of a JSON trace logger in the compiler frontend, and a separate web-app tool for exploring these logs. A simple SolidStart + Tanstack Router tech stack was selected for this tool due to existing familiarity with this stack, and because of the straightforward requirements of the tool.

Since parsers try a vast number of rules and are inherently recursive, the logs quickly exceed 500,000 lines for small source files. To deal with this, the logs are structured as a tree, and the user of the tool is expected to navigate this tree by selecting the precise rule attempts that the parser is supposed to succeed on, thus hiding irrelevant attempts. A dedicated lexer, in hindsight, would have reduced the exponential growth of the logs, but might not have easily parsed some of the unique language features (such as semicolons acting as both terminators and comments).

# Chapter 5 – Evaluation

TODO

## Chapter 6 – Conclusion

TODO

### 6.0.1 Contribution to state-of-the-art

- inlining - built in language constructs to avoid likes of kernel `static_call` - continuum of dispatch, not just static vs dynamic, but by merging the notion of class instances (and fusing classes and modules), if we detect multiple instances we can switch to an even more dynamic dispatch mechanism. - more efficient inter-language interoperability

# Chapter 7 – Parsing Library

Prior to this project I had already developed a simple parsing library in JavaScript, which allows for:

- (1) Parsing terminal rules using regular expressions.
- (2) Forming non-terminal rules by combining other rules in the following ways, sufficient to develop any complex grammar:
  - (1) “or” making rule optional
  - (2) “join” sequencing rules
  - (3) “multi” repeating rules zero or more times
  - (4) “opt” making rule optional
- (3) Map parsed data to custom structures using the “mapData” function.