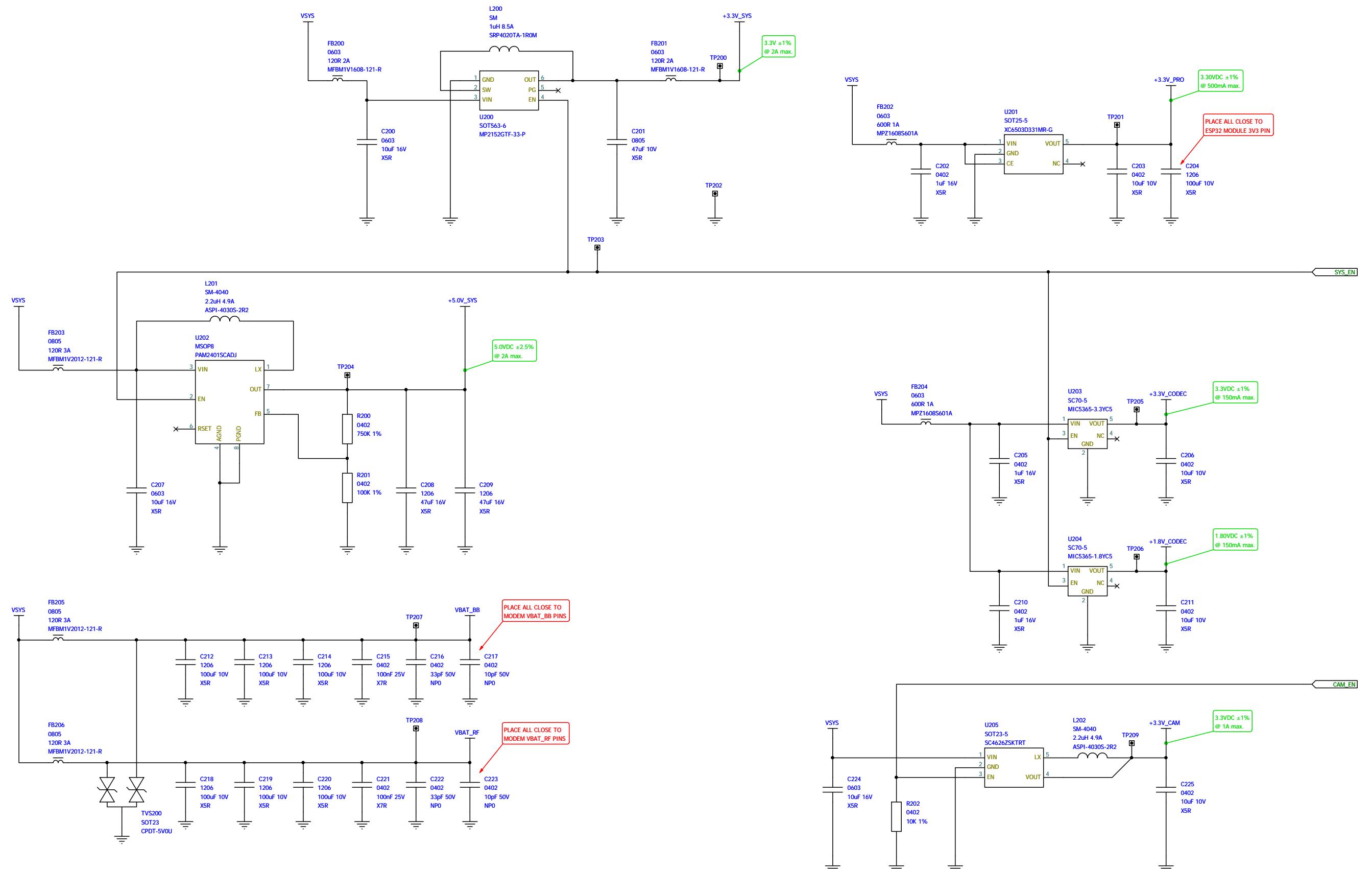


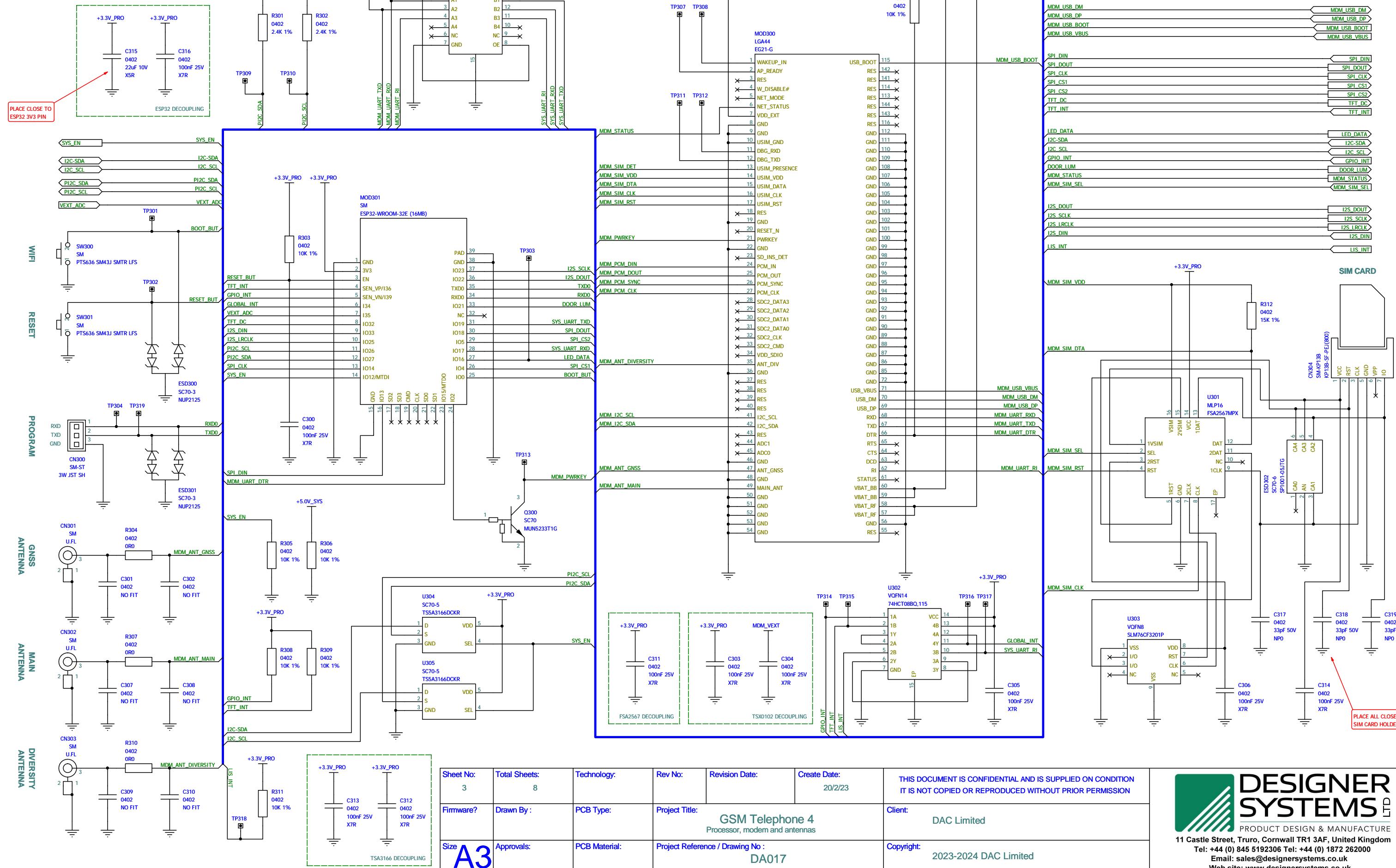
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Firmware?	Drawn By:	PCB Type:	Project Title:	GSM Telephone 4 Input regulation, battery charge and power path	Client:	DAC Limited
Size A3	Approvals:	PCB Material:	Project Reference / Drawing No :	DA017	Copyright:	2023-2024 DAC Limited



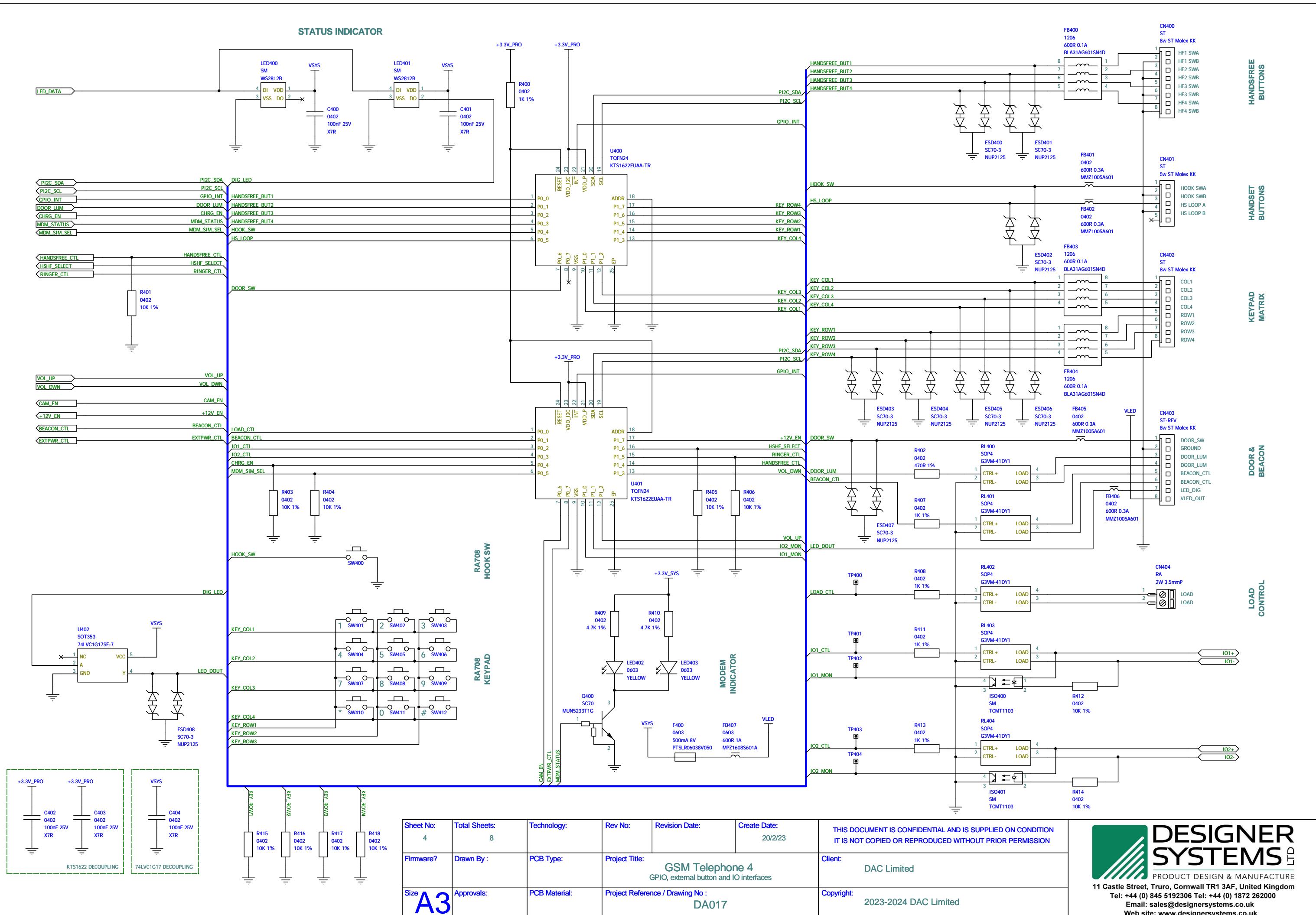
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Notes:

1. Place all decoupling close to device power pins.
 2. Adhere to antenna clearances and stitch all ground layers around antenna traces.
 3. Adhere to all trace routing requirements.



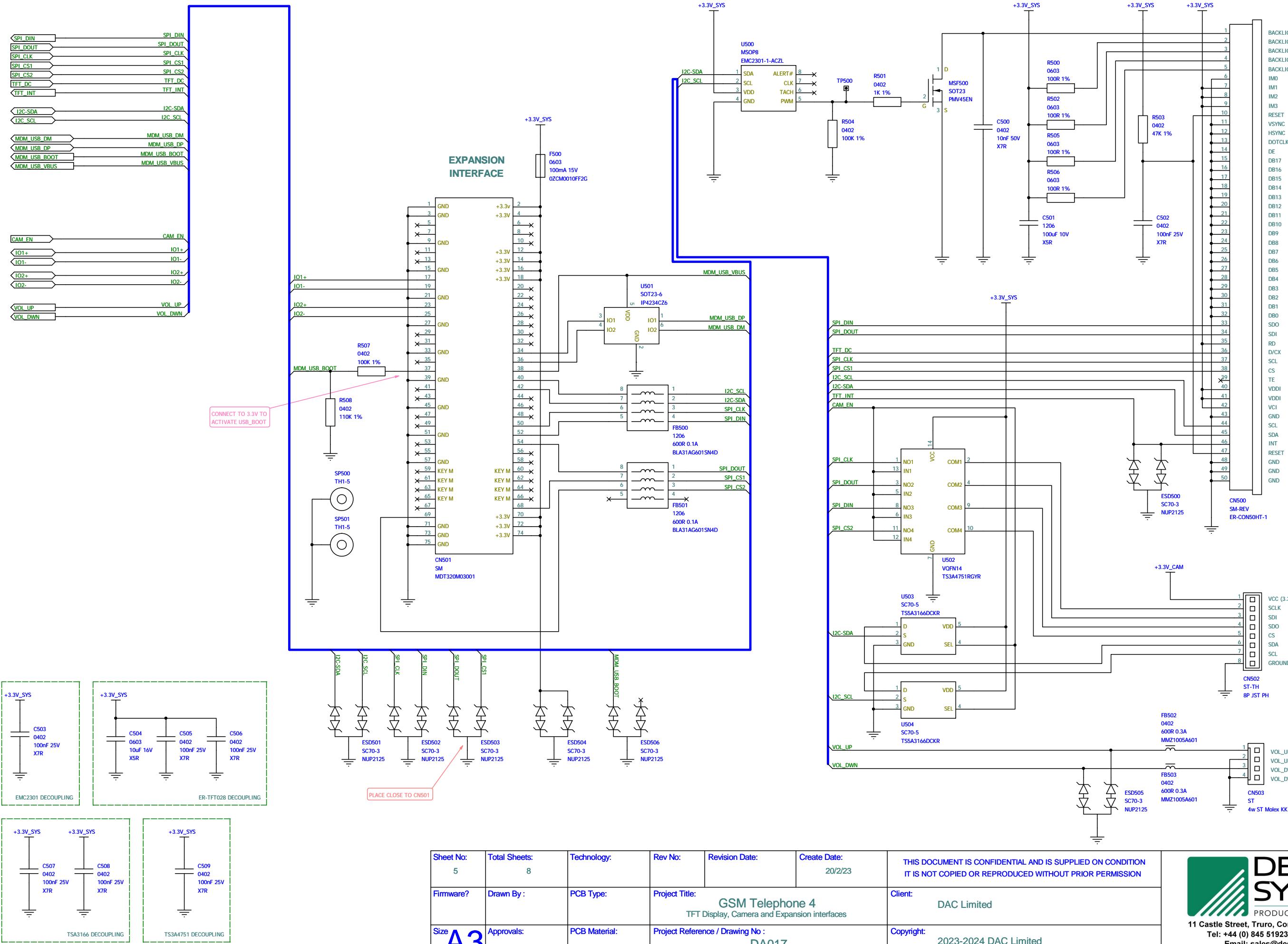
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Size A3	Approvals:	PCB Material:	Project Reference / Drawing No : DA017			Copyright: 2023-2024 DAC Limited



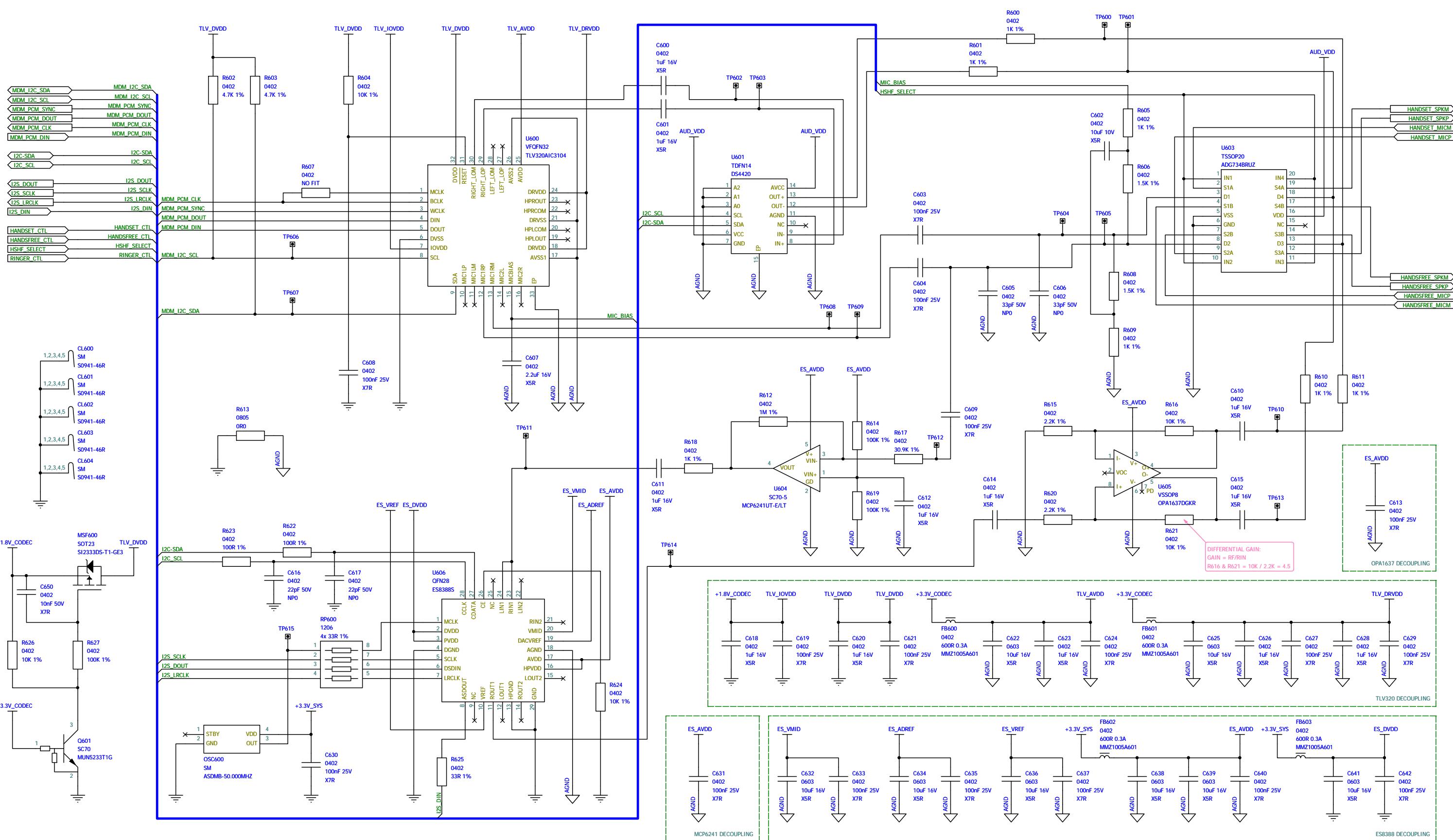
TFT DISPLAY INTERFACE

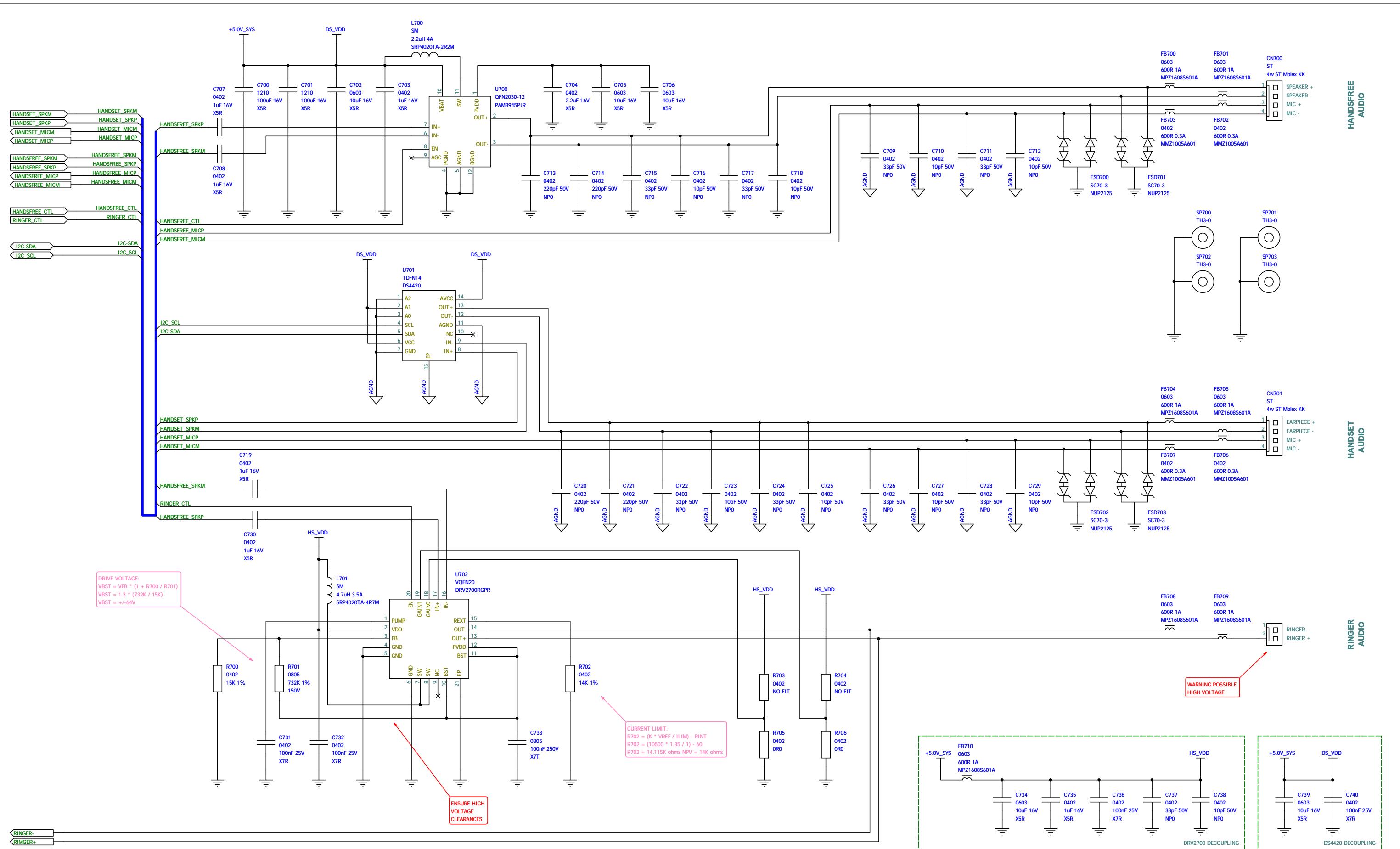
CAMERA INTERFACE

VOLUME INTERFACE

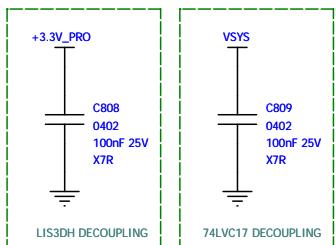
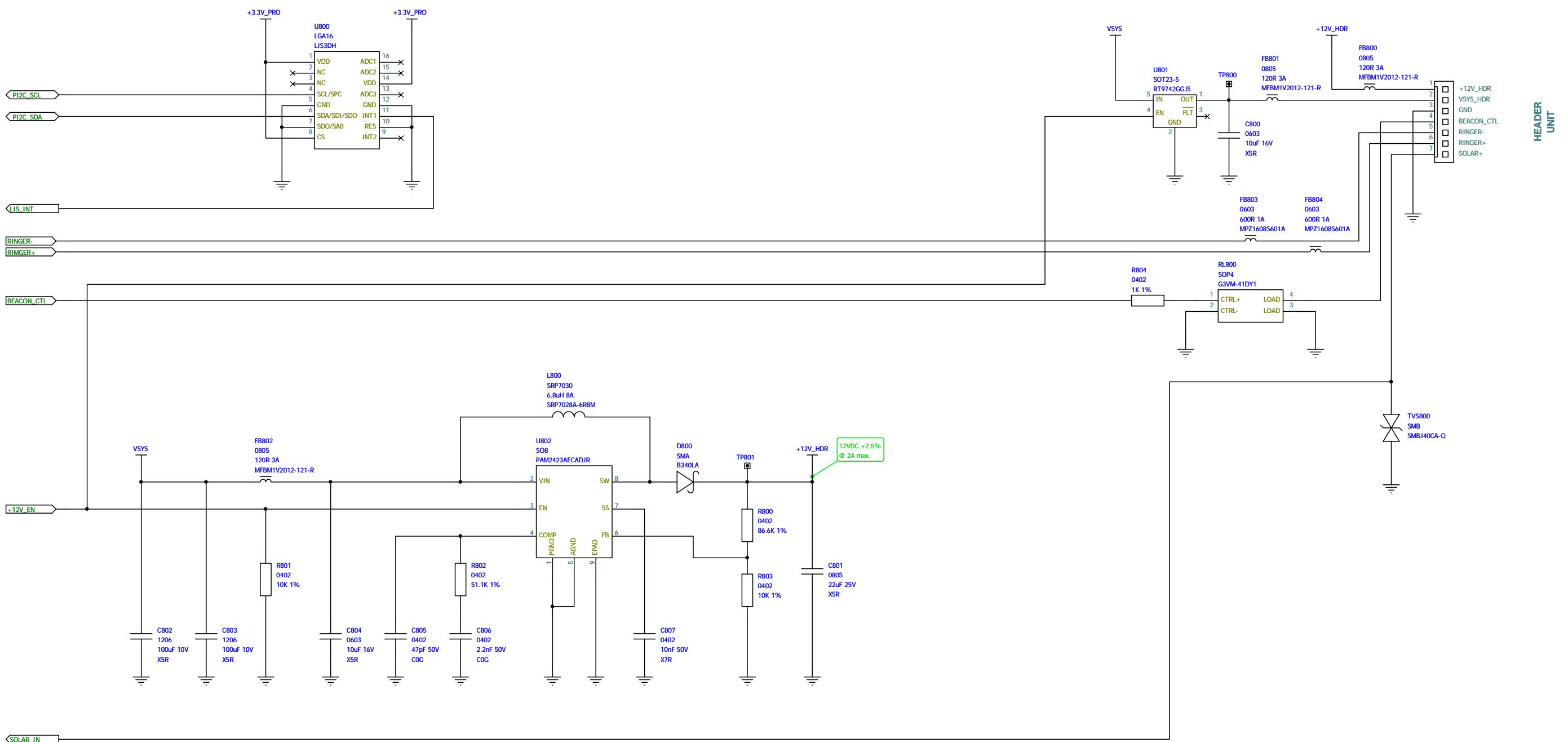


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GSM Telephone 4 revisions

REVISION	DESCRIPTION
DRAFT A	(20/2/23) CREATE VERSION
DRAFT B	(8/3/23) DRAFT VERSION (CONNECTED USIM_GND TO SYSTEM GND ON RECOMMENDATION FROM QUECTEL SO THAT SIM SWITCH CAN BE POWERED FROM NORMAL 3.3V SUPPLY)
DRAFT C	(13/6/23) DRAFT VERSION (ADDED 5V0_SYS ENABLE TO SYS_EN, ADDED SPST ANALOGUE SWITCHES TO I2C LINES TO ISOLATE THE PROCESSOR I2C AWAY FROM THE SYS_EN CONTROLLED CIRCUITRY)
DRAFT D	(15/8/23) DRAFT VERSION (CHANGED U300 LEVEL TRANSLATOR TO SUPPORT MODEM RI SIGNAL TO ALLOW THE GLOBAL INTERRUPT GATE TO WAKE THE PROCESSOR ON INCOMING URC)
ISSUE 1	(22/9/23) RELEASE VERSION
ISSUE 2	(2/2/24) RELEASE VERSION (MOVED CHARGE CONTROLLER I2C INTERFACE TO PROCESSOR SIDE OF ISOLATION TO ALLOW READ DURING POWER DOWN, REMOVED INPUT VOLTAGE PROTECTION AND INTEGRATED 6~30VDC REGULATION AND POTENTIAL DIVIDER FOR VIN_EXT MONITORING, ADDED VOLUME BUTTON HEADER AND CONNECTION TO GPIO FOR IN CALL VOLUME CONTROL, ADDED CAMERA SPECIFIC 3.3V SUPPLY AND ENABLE LINE FOR CONTROL, CHANGED 5W POWER/BATTERY CONNECTOR TO 2W & 3W CTB TERMINALS, CHANGED SIM CARD TO HIROSE PUSH-PUSH VERSION AND ADDED ESD PROTECTION, UPATED 3.3V_SYS SUPPLY TO 2A CAPABILITY,
ISSUE 3	(24/10/24) RELEASE VERSION (ADDED HEADER UNIT PIN HEADER AND +12V BOOST SUPPLY AND VSYS SUPPLY OUTPUTS, CHANGED DOOR/BEACON HEADER TO 8PIN VERSION AND ADDED DIGITAL LED OUTPUT AND VSYS OUTPUT FOR LED BOARD DRIVE, ADDED ACCELEROMETER TO THE DESIGN FOR POLE TILT DETECTION)
ISSUE 3A	(8/1/25) RELEASE VERSION (CHANGED R402 TO 470R FOR BETTER LED BOARD BRIGHTNESS DRIVE, ADDED R311 PULLUP TO LIS_INT LINE)
ISSUE 3B	(10/3/25) RELEASE VERSION (REMOVED EXPANSION SIM LINES AND CONNECTED TO ESIM TO ALLOW SELECTION BETWEEN ONBOARD SIMS, UPDATED HEADER UNIT HEADER TO 7WAY FOR RINGER OUTPUT LINES, ADDITIONAL BEACON_CTL SSR AND SOLAR POSITIVE INPUT TO STEERING DIODE TO EXTERNAL POWER INPUT)
ISSUE 3C	(8/5/25) RELEASE VERSION (REMOVED U602 FROM MICROPHONE PATH AS WAS CAUSING PEER END NOISE DURING CALLS, ADDED ENABLE CONTROL TO EXTERNAL POWER PATH TO ALLOW CONTROL)
ISSUE 4	(11/6/25) RELEASE VERSION (CHANGED R612 TO 1M AND R617 TO 30.9K TO COMPENSATE FOR REMOVAL OF U602 PGA DURING AUDIO TEST, ADDED C315 AND C316 FOR ADDITIONAL ESP32 MODULE DECOUPLING, ADDED Q600, Q601, C650, R626-627 FOR TLV320 CODEC (U600) DELAYED +1.8V TO DVDD DELAY (1MS), ADDED ESD506 TO MDM_USB_BOOT INPUT LINE, ADDED 33PF (C317-319) AND 15K (R312) TO SIM CARD INTERFACE, CHANGED ESD302 ON SIM CARD INTERFACE TO SP1001-05 LOW CAPACITANCE ESD PROTECTOR)

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