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A New Compression Technique for Repetitive Tries

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Abstract

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Chapter 1

Introduction

The problem of compressing large sets of strings, or finite languages, is a fundamental challenge in computer science with applications in areas like bioinformatics, natural language processing, and data indexing. A finite language can be naturally represented by an acyclic deterministic finite automaton (ADFA), commonly known as a trie. In this representation, each string in the language corresponds to a unique path from the root to a final state. Compressing the language is therefore equivalent to compressing its corresponding trie structure.

Traditional compression algorithms often fail to exploit the inherent structural properties of tries. To address this, specialized techniques have been developed. Among the most prominent is the *Extended Burrows-Wheeler Transform (XBWT)* [12], which extends the classical Burrows-Wheeler Transform [4] to labeled trees and can be applied to tries to achieve significant compression by capturing their structural regularities.

However, existing techniques may not be optimal when dealing with tries that exhibit a high degree of repetitiveness. Such is the case for languages containing many strings with shared substrings, leading to tries with large, identical subtrees. Many real-world datasets, such as genomic databases or dictionaries of related terms, generate such highly repetitive structures. This thesis introduces and analyzes a novel compression technique specifically designed to exploit these repetitions. The core idea is to identify and merge identical subtrees by reducing the trie to its minimal deterministic finite automaton (DFA) representation. We implement this method and evaluate its performance against state-of-the-art approaches like XBWT, assessing its effectiveness on various datasets.

1.1 Challenges and Contributions

The primary goal of this thesis is to develop a data structure that both compresses a given finite language and efficiently supports indexing queries, such as navigational and subpath queries (see Definition 6). This challenge involves navigating a fundamental trade-off between compression and indexability, which we explore in detail in Section 2.5. Two straightforward approaches highlight the extremes of this spectrum:

- **Full Compression, Difficult Indexing:** One could minimize the input trie into the smallest possible equivalent DFA using an algorithm like Revuz’s [33]. While this yields optimal compression through DAG compression (see Section 2.1), indexing the resulting general DFA is a notoriously difficult problem. As shown by Equi et al. [11], pattern matching on general DFAs requires super-polynomial time unless the Strong Exponential Time Hypothesis (SETH) fails,

making this approach unsuitable for most indexing purposes.

- **Full Indexability, No Compression:** At the other extreme, the input trie itself can be used as an index. Tries are Wheeler graphs [14], specifically 1-sortable automata (Definition 16), a property that makes them highly amenable to efficient indexing [7]. While this provides excellent query performance through the co-lexicographic ordering of states (see Definition 18), it offers no compression, as even highly repetitive subtrees are stored explicitly.

This thesis proposes a novel algorithm that finds a sweet spot in this trade-off, which we develop throughout Section 2.3. The central idea is to partially minimize the input trie while ensuring the resulting automaton remains efficiently indexable. We achieve this by leveraging the theory of p -sortable graphs (see Section 2.5), developing a method that strategically increases the sortability parameter p just enough to enable significant compression. The motivation for this approach is rooted in the observation that a small increase in p can lead to substantial compression. As noted by Policriti et al. [28], there are cases where increasing p from 1 to 2 allows for an exponential reduction in the automaton's size, a phenomenon we explore in detail in Section 2.5.

Our compression scheme, presented in Section 2.3, works by partitioning the trie's nodes into a predefined number p of chains and then optimizing this partition to merge the maximum number of equivalent states while ensuring p -sortability. To make this optimization problem more concrete, we can frame it as a string partitioning problem. Consider the sequence of nodes in the trie, when read in co-lexicographic order (see Definition 18), as a single long string. The "character" corresponding to each node is its Myhill-Nerode equivalence class (see Theorem 3), which determines if it can be merged with other nodes. The task is to partition this string of nodes into p subsequences such that the number of runs is minimized, where a run is a maximal sequence of consecutive nodes of the same equivalence class. For instance, a subsequence $AAABBA$ contains three runs (AAA , BB , A). Minimizing the number of runs directly corresponds to maximizing the number of merged states, yielding a compact p -sortable automaton.

In Section 2.3, we prove that this optimization is equivalent to the Minimum Weight Perfect Bipartite Matching (MWPBM) problem, allowing us to use efficient, well-studied algorithms to find the optimal solution. The result is a compressed automaton that is p -sortable by construction and thus supports efficient queries using the data structure developed by Cotumaccio et al. [7]. As our experimental results in ?? will show, this method is particularly effective for the highly repetitive datasets common in real-world applications, achieving a balance of compression and indexability that prior methods could not attain.

1.2 Structure of The Thesis

Chapter 2

Preliminary Concepts

2.1 Notation

The following definitions and notation will be used throughout the entire thesis

We first recall the formal definition of a labeled tree.

Definition 1. A **labeled tree** is a rooted structure in which every edge is assigned a label from a finite alphabet Σ . Formally, a labeled tree is a quadruple $T = (V, E, \lambda, r)$, where:

- V is the set of vertices,
- $E \subseteq V \times V$ is the set of directed edges such that (V, E) forms a rooted tree,
- $\lambda : E \rightarrow \Sigma$ is an edge-labeling function,
- $r \in V$ is the root vertex.

In the case of ordered labeled trees, the children of each node are totally ordered; the degree and shape of the tree, as well as the size of the alphabet Σ , are otherwise unconstrained.

We now give the formal definition of a trie.

Definition 2 (Trie). Let Σ be a finite alphabet and $L \subseteq \Sigma^*$ be a finite set of strings. A **trie** for L is a 5-tuple $T = (V, E, \lambda, r, F)$ where:

- V is a finite set of vertices,
- $E \subseteq V \times V$ is a set of edges such that (V, E) forms a rooted tree,
- $\lambda : E \rightarrow \Sigma$ is an edge-labeling function,
- $r \in V$ is the root vertex,
- $F \subseteq V$ is the set of terminal (final) vertices.

The following properties hold:

1. **Determinism:** For every vertex $v \in V$ and every symbol $a \in \Sigma$, there is at most one edge $(v, u) \in E$ such that $\lambda((v, u)) = a$.
2. **String Representation:** For any vertex $v \in V$, let $\text{str}(v)$ be the string obtained by concatenating the labels on the unique path from the root r to v . We set $\text{str}(r) = \varepsilon$ (the empty string).
3. **Language Correspondence:** The language represented by the trie is exactly L , i.e., $L = \{ \text{str}(v) \mid v \in F \}$.

4. **Prefix Property:** The set of all prefixes of words in L coincides with $\{ \text{str}(v) \mid v \in V \}$.

The determinism property ensures that each string prefix identifies a unique path from the root, which makes tries well suited to our compression techniques. A trie can be viewed as a DFA (see Definition 3), allowing the application of automata minimization to compress the tree structure (DAG compression). **DAG compression** is the process of transforming a tree into a directed acyclic graph (DAG) by identifying and merging all isomorphic subtrees. In the context of tries, this is equivalent to finding and merging states with the same right language, which is the standard procedure in DFA minimization. The result is the smallest possible automaton that recognizes the same language.

We now recall the definition of a deterministic finite automaton (DFA), a fundamental computational model.

Definition 3 (Deterministic Finite Automaton). *A deterministic finite automaton (DFA) is a 5-tuple $M = (Q, \Sigma, \delta, q_0, F)$ where:*

- Q is a finite set of states,
- Σ is a finite input alphabet,
- $\delta : Q \times \Sigma \rightarrow Q$ is the transition function,
- $q_0 \in Q$ is the initial state,
- $F \subseteq Q$ is the set of final (accepting) states.

A DFA processes an input string $s \in \Sigma^*$ symbol by symbol, starting from q_0 and following transitions prescribed by δ . The string s is accepted if the automaton halts in a state of F after consuming all symbols; otherwise, it is rejected. The language recognized by a DFA is the set of all strings it accepts.

Also, we define the notation I_q to denote the set of strings reaching state q from the initial state: $I_q = \{ \alpha \in \Sigma^* \mid q = \delta(s, \alpha) \}$

We next define a non-deterministic finite automaton (NFA).

Definition 4 (Non-deterministic Finite Automaton). *A non-deterministic finite automaton is a 5-tuple $(Q, \Sigma, \delta, q_0, F)$ where:*

- Q is a finite set of states,
- Σ is a finite alphabet,
- $\delta : Q \times (\Sigma \cup \{ \varepsilon \}) \rightarrow \mathcal{P}(Q)$ is the transition function (with $\mathcal{P}(Q)$ the power set of Q),
- $q_0 \in Q$ is the initial state,
- $F \subseteq Q$ is the set of accept states.

The principal difference between DFAs and NFAs is that NFAs allow (i) multiple outgoing transitions from a state on the same input symbol and (ii) ε -transitions, whereas DFAs have exactly one outgoing transition per state and input symbol and admit no ε -transitions.

For an automaton $\mathcal{N} = (Q, \Sigma, \delta, q_0, F)$, we extend the transition function to strings as follows, for all $u \in Q$, $a \in \Sigma$, and $\alpha \in \Sigma^*$:

$$\delta(u, \varepsilon) = \{u\}, \quad \delta(u, \alpha a) = \bigcup_{v \in \delta(u, \alpha)} \delta(v, a).$$

Thus, $\delta(q_0, \alpha)$ denotes the set of states reachable from the start state q_0 by reading α . A string α is *accepted* if $\delta(q_0, \alpha) \cap F \neq \emptyset$. The language recognized by \mathcal{N} is denoted by $\mathcal{L}(\mathcal{N})$ and is defined as

$$\mathcal{L}(\mathcal{N}) = \{\alpha \in \Sigma^* \mid \delta(q_0, \alpha) \cap F \neq \emptyset\}.$$

We introduce now the concept of regular language.

Definition 5 (Regular Language). *A language $L \subseteq \Sigma^*$ is called a **regular language** if it is the language recognized by some finite automaton.*

2.2 Labeled Trees and Tries

In this chapter, we will discuss labeled trees, a fundamental data structure used to represent hierarchical data. We will define the concept of a labeled tree, explore its common applications, and introduce the core concepts behind its compression and indexing.

2.2.1 Introduction and Motivation

Before delving into specific compression techniques, it is essential to establish a solid theoretical foundation regarding labeled trees and tries. These structures are fundamental for representing hierarchical data across diverse fields, from bioinformatics to document processing. This chapter provides the necessary background, defining labeled trees, exploring their common applications, and introducing the core concepts behind their compression and indexing.

While labeled trees encompass a broad class of hierarchical structures, this thesis focuses specifically on tries. This focus is motivated by our compression strategy, which relies on identifying and merging identical subtrees. To formally identify these subtrees, we compute the Myhill-Nerode equivalence classes of the trie's states (see Theorem 3), a task that can be accomplished using algorithms for DFA minimization. A fundamental prerequisite for these algorithms is determinism, as they require that from any given state, each symbol corresponds to at most one transition. Tries inherently provide this guarantee, since from any node, there is at most one outgoing edge for each symbol in the alphabet. This deterministic nature is therefore what allows us to apply powerful automata minimization techniques to compress the tree structure.

2.2.2 Applications

Labeled trees are widely used in computer science and data representation due to their hierarchical structure and flexibility in modeling relationships. Prominent applications include:

1. **XML Data Representation:** XML documents are often modeled as labeled trees, where each element is a node labeled by its tag, and hierarchical nesting represents parent-child relationships.
2. **JSON Data Representation:** JSON documents can be viewed as labeled trees, with keys as labels and values as children.
3. **Bioinformatics:** Labeled trees are used to represent phylogenetic trees, genome annotations, and hierarchical clustering.
4. **Compiler Design:** Abstract Syntax Trees (ASTs) for programming languages are labeled trees that capture the structure of code.
5. **File Systems:** The directory structure of file systems can be viewed as a labeled tree.

Efficient representation, navigation, and querying of labeled trees are essential for many applications, motivating the development of specialized data structures and algorithms.

2.2.3 Indexing

The goal of compressing and indexing labeled trees is to design a compressed storage scheme for a labeled tree T with t nodes that allows for efficient navigation operations in T , as well as fast search and retrieval of subtrees or paths within T . To be effective, the compressed representation should minimize the space required to store the tree while supporting a wide range of operations in optimal ($O(1)$) or (near-)optimal time.

We define the following navigation operations on T :

Definition 6 (Tree Operations). *Let T be a labeled tree with node set V , $u \in V$ be a node, and $c \in \Sigma$ be a symbol. We define the following fundamental operations on T :*

- **Navigational queries:** *ask for the parent of u , the i -th child of u , or the label of u . The last two operations might be restricted to the children of u with a specific label c .*
- **Visualization queries:** *retrieve the nodes in the subtree rooted at u (any possible order should be implemented).*
- **Subpath queries:** *Let moreover $P(\alpha)$ be the set of vertices of N which are reached by a path labeled with a given word $\alpha \in \Sigma^*$. Efficient subpath queries that, given a query word $\alpha \in \Sigma^*$, solve:*
 - *Existential queries:* *Determine whether $P(\alpha) \neq \emptyset$, i.e., whether α matches a substring of some string in the language of T .*
 - *Count queries:* *Compute the cardinality of $P(\alpha)$.*
 - *Locate queries:* *Return a representation for all the vertices in $P(\alpha)$.*

A naive solution to index labeled trees is to store the tree as a list of nodes with their labels and parent-child relationships using pointers in $O(t \log t)$. However, this

representation is not space-efficient and does not support fast navigation or query operations.

Many data structures have been proposed to compress and index labeled trees, each with its trade-offs in terms of space usage, query performance, and supported operations. One of the most successful approaches is the Extended Burrows-Wheeler Transform, which extends the classical Burrows-Wheeler Transform (BWT) to handle labeled trees efficiently (Subsection 2.2.5).

2.2.4 Information-Theoretic Lower Bound

Before computing the information-theoretic lower bound for labeled trees, it is essential to define the concept of worst-case entropy, which provides a formal measure of the minimum number of bits required to represent any object from a given set. As detailed by Navarro [30], this is a fundamental concept in data structure design.

Definition 7 (Worst-case entropy). *Let U be a universe of combinatorial objects. The worst-case entropy of U is*

$$H_{wc}(|U|) = \lceil \log_2(|U|) \rceil$$

This definition establishes that the minimum number of bits to uniquely identify any object in a set U is the logarithm of the size of U , rounded up to the nearest integer. We now apply this principle to determine the lower bound for labeled trees.

Lemma 1. *The information-theoretic lower bound for storing a labeled tree T with t nodes over an alphabet Σ is $2t + t \log_2 |\Sigma| - \Theta(\log t)$ bits.*

Proof. The total information required to store a labeled tree can be decomposed into two components: the space needed to encode the tree's structure and the space needed to encode the labels on its nodes.

1. Structural Information (Unlabeled Tree): The number of distinct unlabeled binary trees with t nodes is given by the t -th Catalan number, $C_t = \frac{1}{t+1} \binom{2t}{t}$. Using Stirling's approximation for factorials, the Catalan number can be approximated as:

$$C_t \approx \frac{4^t}{t^{3/2} \sqrt{\pi}}$$

Then, the worst-case entropy (or the information-theoretic minimum number of bits to encode the structure of the tree) is:

$$\log_2 C_t \approx \log_2 \left(\frac{4^t}{t^{3/2} \sqrt{\pi}} \right) = \log_2(4^t) - \log_2(t^{3/2} \sqrt{\pi}) = 2t - \frac{3}{2} \log_2 t - \frac{1}{2} \log_2 \pi$$

The lower-order terms can be expressed using Big Theta notation as $\Theta(\log t)$. Therefore, the space required for the structure is $2t - \Theta(\log t)$ bits.

2. Labeling Information: For a tree with t nodes and an alphabet Σ , each node must be assigned a label. To distinguish between $|\Sigma|$ possible labels, a minimum

of $\log_2 |\Sigma|$ bits is required for each node. Consequently, the total space required to store the labels for all t nodes is:

$$t \log_2 |\Sigma| \text{ bits}$$

Finally, by adding the space required for the structure and the labels, the total information-theoretic lower bound for storing a labeled tree is the sum of the two components:

$$(2t - \Theta(\log t)) + (t \log_2 |\Sigma|) = 2t + t \log_2 |\Sigma| - \Theta(\log t) \text{ bits}$$

This completes the proof. \square

2.2.5 State of The Art

The field of tree indexing and compression has evolved through two main paradigms: succinct data structures that achieve space-optimal representations, and compression techniques that exploit structural repetitions.

In the realm of succinct tree structures, early work by Kosaraju [23] proposed a method to index labeled trees by extending the concept of prefix sorting from strings to labeled trees using trie structures. He introduced the idea of constructing a suffix tree for a reversed trie, enabling subpath queries in $O(|P| \log |\Sigma| + occ)$ time, where occ is the number of occurrences of P in T . However, this approach still required $O(t \log t)$ space (where t is the number of nodes of the tree) and thus was not compressed.

A significant advancement in this direction came with the Extended Burrows-Wheeler Transform (XBWT) [12], a data structure designed for efficient compression and indexing of ordered node-labeled trees. The XBWT works by linearizing a labeled tree into two arrays capturing the structural properties of the tree and its labels. This transformation allows for efficient representation, navigation, and querying of the tree. The key advantage of the XBWT lies in its ability to compress labeled trees while supporting a wide range of operations, such as navigation, visualization and subpath queries (see Definition 6), within (near-)optimal time bounds and entropy-bounded space. The XBWT provides significant improvements in both compression ratio and query performance compared to traditional compression schemes, making it a valuable resource for intensive applications.

Complementing succinct approaches, tree compression has been extensively studied through different paradigms that exploit structural repetitions in distinct ways. One of the classical approaches is *DAG compression*, which represents a tree as a minimal directed acyclic graph (DAG) by identifying and merging identical rooted subtrees. Concretely, whenever two identical subtrees occur, only one copy is stored and all occurrences point to it. The resulting structure can be exponentially smaller than the original tree and can be computed in linear time. DAG compression has been widely used in programming languages, binary decision diagrams, and XML representations [3].

Another line of research extends the well-known LZ77 factorisation from strings to trees. Here, the tree is decomposed into edge-disjoint fragments, each being

either a single node or a copy of a fragment that appeared earlier in a breadth-first traversal. Each fragment is thus defined by pointers to earlier occurrences, much like in the string version of LZ77. This factorisation uniquely determines the tree, and by minimising the number of fragments one obtains a compressed representation. Importantly, such factorizations can be computed in polynomial time (and in linear time for restricted variants), and they yield representations no larger than the smallest tree grammar, thus bridging block compression and grammar-based compression [15].

More recently, top tree compression has been proposed as a method that combines the advantages of subtree sharing and grammar-like approaches. The key idea is to build a hierarchical top tree decomposition, where the input tree is recursively partitioned into clusters that capture connected patterns. These clusters are then merged following a restricted set of operations, producing a binary decomposition tree whose internal repetitions are turned into subtree repeats. Finally, this decomposition is compressed using standard DAG compression, resulting in a so-called top DAG. This approach achieves close-to-optimal worst-case bounds, can be exponentially more succinct than DAG compression, and crucially, supports a wide range of navigational queries (e.g., parent, child, depth, nearest common ancestor) in logarithmic time directly on the compressed representation [3].

Another notable approach is Tree Re-Pair [26], a grammar-based compression technique adapted for tree structures. It extends the principles of the original Re-Pair algorithm [25] to handle the hierarchical nature of trees by identifying and compactly representing frequently occurring patterns. The core idea of the tool is to identify frequently occurring patterns within the tree and represent them more compactly. The process involves the linearization of the tree (e.g., using a specific traversal order) and then the application of the Re-Pair logic. In this way, it finds the most frequent pair of adjacent elements (which could represent nodes, labels, or structural components, depending on the linearization) in the sequence. The pair is then replaced by a new non-terminal symbol, and the corresponding production rule is added to a grammar. All this process is then repeated until no more pairs occur frequently enough or some other stopping criterion is met. The final output is a relatively small grammar (a set of production rules) and a sequence of symbols (including the newly introduced non-terminals) that can be used to reconstruct the original tree. An application of Tree Re-Pair to XML documents can be found in [27].

In summary, DAG compression is efficient but limited to subtree repeats, LZ77 factorisation captures more general repetitions while relating closely to grammar-based methods such as Tree Re-Pair, and top tree compression strikes a balance by exploiting both subtree and pattern repeats while still enabling efficient query support.

This thesis focuses on developing a novel technique specifically tailored for highly repetitive tries. Our approach leverages the structural properties unique to tries and their repetitive patterns. Therefore, we use the XBWT as our primary benchmark for comparison, as it represents a well-established and high-performance baseline specifically designed for trie compression in the field.

2.2.6 Conclusion

This section has established the initial theoretical groundwork for the thesis, beginning with the formal definition of labeled trees and narrowing the focus to tries. We have underscored the deterministic nature of tries as a critical property that makes them suitable for DAG compression techniques. Furthermore, we defined a standard set of navigational and query operations, providing a benchmark for the functionality that an efficient compressed structure must support. By establishing the information-theoretic lower bound for storing labeled trees, we have set a clear goal for compression effectiveness.

The primary objective of this thesis is to develop and analyze a novel approach that leverages DAG compression to reduce the space footprint of tries, particularly those with highly repetitive substructures. Crucially, our goal is not merely to compress but to do so while preserving the trie’s indexability. The subsequent chapters will detail the methods for achieving this, aiming to create a compressed representation that approaches the theoretical space limits while supporting essential query operations efficiently.

In the next section, we present a detailed explanation of the Extended Burrows-Wheeler Transform (XBWT), examining its construction, properties, and how it enables efficient compression and indexing of labeled trees.

2.3 The Extended Burrows-Wheeler Transform

The fundamental definitions, properties, and algorithms related to the Extended Burrows-Wheeler Transform presented in this chapter are based on the work introduced by Ferragina et al. ‘Compressing and Indexing Labeled Trees, with Applications’ [12].

2.3.1 Introduction and Motivation

This chapter explores the Extended Burrows-Wheeler Transform (XBWT), introduced by Ferragina et al. [12], a state of the art technique for labeled tree compression. Understanding the principles and performance of the XBWT is crucial as it will serve as the primary benchmark against which we will evaluate the novel compression scheme proposed in this thesis. By establishing a baseline with a well-regarded method like the XBWT, we can effectively demonstrate the potential advantages and contributions of our new approach, particularly for trees exhibiting high repetitiveness.

In 2005, Ferragina et al. [12] introduced an innovative approach to labeled tree compression by transforming it into a more tractable string compression problem. Their key contribution, the Extended Burrows-Wheeler Transform (XBWT), is a sophisticated data structure that achieves highly efficient compression by combining entropy-compressed edge labels with a succinct representation of the tree topology. This elegant solution not only simplifies the compression process but also maintains the structural relationships essential for tree operations.

The XBWT works by linearizing a labeled tree into two coordinated arrays capturing the structural properties of the tree and storing its labels. This transformation allows for efficient representation, navigation, and querying of the tree. The key advantage of the XBWT lies in its ability to compress labeled trees while supporting a wide range of operations, such as navigation, visualization and subpath queries (see Definition 6), within (near-)optimal time bounds and entropy-bounded space.

One of the primary applications of the XBWT is in compressing and indexing hierarchical data formats, such as XML documents. It provides significant improvements in both compression ratio and query performance compared to traditional tools, making it an invaluable resource for data-intensive applications in fields like bioinformatics, information retrieval, and big data analytics.

This chapter aims to explore the XBWT data structure and its applications in the context of labeled trees. We will start by providing an overview of the theoretical foundations of the XBWT. Finally, we will describe and compare the algorithms for constructing the XBWT and demonstrate its use in compressing and indexing labeled trees.

Key Aspects

The XBWT has several key properties that make it an effective tool for labeled tree compression and indexing:

- **Succinctness:** The XBWT representation of a labeled tree uses space close to the worst-case entropy (Lemma 1), which is $2t - \Theta(\log t) + t \log |\Sigma|$ bits for a tree with t nodes and an alphabet of size $|\Sigma|$.
- **Efficient Querying:** The XBWT supports the navigational queries (Definition 6) in optimal time $O(1)$ if $|\Sigma| = O(\text{polylog}(t))$, otherwise in $O(\log \log^{1+\epsilon} |\Sigma|)$ time. Whereas, given $s \in \Sigma^*$, subpath queries (Definition 6) are supported in $O(|s|)$ if $|\Sigma| = O(\text{polylog}(t))$, otherwise in $O(|s| \log \log^{1+\epsilon} |\Sigma|)$ time.
- **Scalability:** The XBWT is particularly useful for large-scale hierarchical data, such as XML documents or phylogenetic trees, where both compression and fast querying are critical.

2.3.2 Definition

The **Extended Burrows-Wheeler Transform** is a data structure designed to efficiently compress and index *ordered node-labeled trees*. Inspired by the classical Burrows-Wheeler Transform (BWT) [4] for strings, the XBWT extends these principles to hierarchical structures, enabling efficient storage, navigation, and querying of trees. It is particularly effective for trees where each node has a label drawn from an alphabet Σ and the tree structure has an arbitrary shape and degree.

Definition 8. *Let T be a totally ordered node-labeled tree of arbitrary fan-out, depth, and shape, with n internal nodes and l leaves (t nodes in total) and alphabet Σ . Let u be a node in T , we define the following information:*

- *$\text{last}(u)$: a binary value that is 1 if u is the last (rightmost) child of its parent in the total order, and 0 otherwise.*

- $\alpha(u)$: denotes the label of node u concatenated with one bit that is 1 if u is a leaf and 0 otherwise.
- $\pi(u)$: the string obtained by concatenating the labels of the nodes on the UPWARD PATH from u 's parent to the root of T . Formally, we define $\pi(u)$ recursively as:

$$\pi(u) = \begin{cases} \varepsilon & \text{if } u \text{ is the root} \\ \text{label}(\text{parent}(u)) \circ \pi(\text{parent}(u)) & \text{otherwise} \end{cases}$$

where \circ is the concatenation operator and ε is the empty string.

If the leaf-label alphabet Σ_L is disjoint from the internal-node label alphabet Σ_N , the additional indicator bit in $\alpha(u)$ is redundant and may be omitted. In that case, we identify $\alpha(u)$ with the node label, i.e., $\alpha(u) := \text{label}(u)$.

The definition of the XBWT relies on a sequence S , which contains a triplet $(\text{last}(u), \alpha(u), \pi(u))$ for each node u in the tree T .

The construction of S is a two-step process. First, an intermediate vector of triplets is created by traversing the tree T in pre-order and generating a triplet $(\text{last}(u), \alpha(u), \pi(u))$ for each node. Second, it is stably sorted according to the lexicographical order of the ' π ' component to produce the final sequence S (see Table 2.1 for an example).

Theorem 1. *The XBWT of a labeled tree T consists of two arrays, S_{last} and S_{α} . These are constructed from the sequence S of triplets. Specifically, for each i from 1 to t , $S_{\text{last}}[i]$ is the 'last' component of the i -th triplet in S , and $S_{\alpha}[i]$ is the ' α ' component. The total space required is $2t + t \log |\Sigma|$ bits.*

S_{π} (for each i from 1 to t , $S_{\pi}[i]$ is the ' π ' component of the i -th triplet in S), therefore is not needed after the construction of the XBWT. However, in the following discussion, we will still refer to it as it possesses some important properties. See Figure 2.1 for an example of the tree T and its corresponding sequence S .

2.3.3 Properties

The XBWT's effectiveness as an indexing structure stems from a key property of the sequence S . This property, along with its consequences, arises directly from the transform's definition and the sorting process.

Key Property: Grouping by Parent

The fundamental property of the XBWT is that the children of any node u in the tree T form a contiguous block in the sequence S . Let u_1, \dots, u_z be the children of node u in their original order. Their corresponding triplets will appear consecutively in S in that same order.

Example 2.1:

Consider the node u in Figure 2.1. Looking at Table 2.1, we can see that its children form a contiguous block in positions $[5, 6, 7]$ of the sequence S .

This grouping provides several important consequent properties:

Unary Degree Encoding: The subarray S_{last} for the block of children $[u_1, \dots, u_z]$ encodes the degree of u in unary. Specifically, $S_{\text{last}}[u_z] = 1$ and $S_{\text{last}}[u_i] = 0$ for $1 \leq i < z$.

Example 2.2:

Consider the node u in Figure 2.1. Looking at Table 2.1, we can observe that $S_{\text{last}}[5 \dots 7] = \{0, 0, 1\}$, which encodes the degree 3 in unary notation, matching the number of children of node u .

Preservation of Sibling Order: If two nodes u and v have the same label, and the triplet for u precedes the triplet for v in S , then the entire block of children of u will also precede the block of children of v .

Example 2.3:

Consider nodes u and v in Figure 2.1. In Table 2.1, node u appears at index 2 while node v appears at index 4 in the sequence S . Following the preservation of sibling order property, all children of u (occupying positions $[5, 6, 7]$) appear before the child of v (at position 8).

Path-based Indexing: This property extends to entire paths. For any label $c \in \Sigma$, all triplets whose π -components are prefixed by c form a contiguous block in S . If u is the i -th node with label c in S_α , its children's block is located within the larger block of all nodes with paths prefixed by c . This block is delimited by the $(i - 1)$ -th and i -th '1's in the corresponding section of S_{last} .

Example 2.4:

Let's examine nodes u and v in Figure 2.1, both labeled 'B'. In the sequence S shown in Table 2.1, u is the first node with label 'B' (at index 2), and v is the second (at index 4).

The children of all nodes labeled 'B' form a contiguous block in S . In this case, the children of both u and v are located in the range $[5, 8]$. We can distinguish between the children of u and the children of v using the S_{last} array:

- The block of children for u (the first 'B' node) starts at the beginning of the range (index 5) and ends at the position of the first '1' in $S_{\text{last}}[5 \dots 8]$.
- The block of children for v (the second 'B' node) starts after the first '1' and ends at the position of the second '1' in $S_{\text{last}}[5 \dots 8]$.

Other Properties

Additional properties of the XBWT components include:

- The first triplet in S always corresponds to the root of the tree T .
- S_{last} contains n ones (for internal nodes) and l zeros (for leaves).
- S_α is a permutation of the node labels in T .

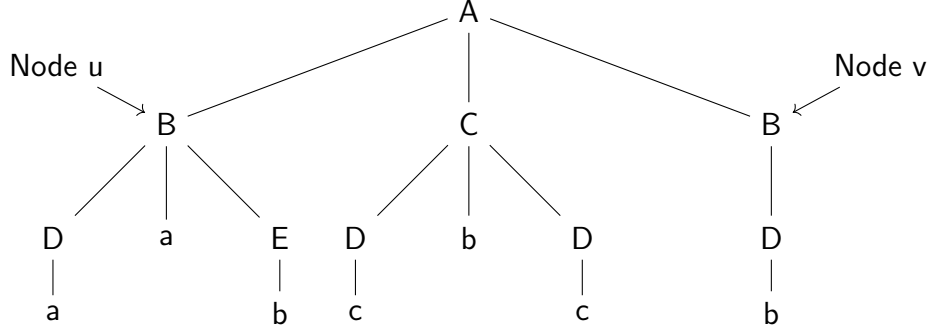


Figure 2.1: A labeled tree T where $\Sigma_N = \{A, B, C, D, E\}$ and $\Sigma_L = \{a, b, c\}$. Notice that $\alpha(u) = \alpha(v) = B$ and $\pi(u) = \pi(v) = A$.

	S_{last}	S_α	S_π
1	0	A	ϵ
2	0	B	A
3	0	C	A
4	1	B	A
5	0	D	BA
6	0	a	BA
7	1	E	BA
8	1	D	BA
9	0	D	CA
10	0	b	CA
11	1	D	CA
12	1	a	DBA
13	1	b	DBA
14	1	c	DCA
15	1	c	DCA
16	1	b	EBA

Table 2.1: The sequence S for the tree shown in Figure 2.1, obtained by stably sorting triplets according to their ' π ' components. In this representation, nodes u and v from the original tree T appear at indices 2 and 4, respectively. The children's block of node u occupies positions 5 through 7, while node v 's single child is located at index 8.

2.3.4 Construction

A naive approach to build the XBWT would be to explicitly construct S through the concretization of π -strings and then sort it using a stable sorting algorithm. However, this approach would require $\Theta(t^2)$ space in the worst case, which is not feasible for deep trees. To overcome this issue, Ferragina et al. [12] proposed a more efficient algorithm that builds S in linear time and $O(t \log t)$ bits of working space.

The linear time algorithm is called **pathSort**, it is based on a generalization of the Skew algorithm for suffix array construction of strings [21]. Let's see briefly how the Skew algorithm works.

Skew Algorithm

The Skew algorithm is an efficient method for constructing the suffix array of a string in linear time. A suffix array is a data structure that lists the starting indices of all the suffixes of a string in lexicographical order, and it is widely used in various string processing algorithms.

Algorithm Overview

Step 0: Construct a Sample. For $k \in \{0, 1, 2\}$, define the index sets

$$B_k = \{i \in [0, n] \mid i \bmod 3 = k\}.$$

Let $C = B_1 \cup B_2$ be the set of sample positions, and let S_C denote the set of sample suffixes.

Step 1: Sort Sample Suffixes. For $k \in \{1, 2\}$, construct the string R_k whose characters are the triples $[t_i t_{i+1} t_{i+2}]$ for $i \in B_k$ in increasing order of i . The last character of R_k is unique because we pad with sentinels so that $t_{n+1} = t_{n+2} = 0$. Let $R = R_1 R_2$ be the concatenation of R_1 and R_2 . Then the nonempty suffixes of R correspond to the sample suffixes in S_C in an order-preserving way: sorting the suffixes of R yields the order of S_C . To sort the suffixes of R , first radix sort the characters of R (the triples) and rename them by their ranks to obtain a string R' . If all characters are distinct, their order directly gives the order of suffixes. Otherwise, sort the suffixes of R' recursively using the same DC3 procedure. Once S_C is sorted, assign ranks to sample suffixes: for $i \in C$, let $\text{rank}(S_i)$ be the rank of S_i within S_C . Additionally, define $\text{rank}(S_{n+1}) = \text{rank}(S_{n+2}) = 0$. For $i \in B_0$, $\text{rank}(S_i)$ is undefined.

Step 2: Sort Nonsample Suffixes. Represent each nonsample suffix S_i with $i \in B_0$ by the pair

$$(t_i, \text{rank}(S_{i+1})).$$

Note that $\text{rank}(S_{i+1})$ is always defined for $i \in B_0$ by the previous step. Then, radix sort these pairs to obtain the order of S_{B_0} .

Step 3: Merge. Merge the two sorted sets S_C and S_{B_0} using a standard comparison-based merging. To compare a suffix $S_i \in S_C$ against a suffix $S_j \in S_{B_0}$, distinguish two cases:

$$\begin{aligned} \text{if } i \in B_1 : S_i \leq S_j &\iff (t_i, \text{rank}(S_{i+1})) \leq_{\text{lex}} (t_j, \text{rank}(S_{j+1})), \\ \text{if } i \in B_2 : S_i \leq S_j &\iff (t_i, t_{i+1}, \text{rank}(S_{i+2})) \leq_{\text{lex}} (t_j, t_{j+1}, \text{rank}(S_{j+2})). \end{aligned}$$

The ranks used above are defined in all cases by Step 1. Each comparison inspects $O(1)$ characters/ranks, and the two-way merge advances each pointer at most once, so the merge runs in linear time overall.

Time Complexity. Excluding the recursive call, all steps are linear-time via radix sorting and a single-pass merge. The recursion operates on a string of length $2n/3$, so the overall time satisfies

$$T(n) = T(2n/3) + O(n) = O(n).$$

PathSort Algorithm

The pseudocode of the pathSort algorithm is shown in Algorithm 1. As we can see, the algorithm is based on the Skew algorithm, but it is adapted to work on labeled trees. Given a value $j \in \{0, 1, 2\}$, the main idea is to recursively sort the upward subpaths of the tree starting at nodes in levels $\not\equiv j \pmod{3}$, then sort the upward subpaths starting at nodes in levels $\equiv j \pmod{3}$ using the result of the previous step, and finally merge the two sets of sorted subpaths by exploiting their lexicographic names. j is chosen in such a way that the number of nodes of the shrunk tree whose level is $\equiv j \pmod{3}$ is at least $t/3$ so that a constant fraction of upward paths are ensured to be dropped at each recursive step. It is important to note that:

1. the height of the new (contracted) tree shrinks by a factor of three, hence the node naming requires the radix sort over triples of names;
2. given the choice of j , the number of nodes of the new (contracted) tree will be at most $2t/3$, thus ensuring that the running time of the algorithm satisfies the recurrence $R(t) = R(2t/3) + \Theta(t) = \Theta(t)$;
3. following an argument similar to [21], the names of the dropped subpaths can be computed in $O(t)$ time from the names of the non-dropped subpaths, by radix sorting.

Algorithm 1 PATHSORT(T)

- 1: Initialize the array of triplets `IntNodes`[1.. t].
 - 2: Visit the internal nodes of T in pre-order. For the i -th visited internal node u , set `IntNodes`[i] $\leftarrow (\alpha(u), \text{level}(u), \text{parent}(u))$.
 - 3: Let $j \in \{0, 1, 2\}$ be such that the number of nodes in `IntNodes` whose level is $\equiv j \pmod{3}$ is at least $t/3$. Sort recursively the upward subpaths starting at nodes in levels $\not\equiv j \pmod{3}$.
 - 4: Sort the upward subpaths starting at nodes in levels $\equiv j \pmod{3}$ using the result of Step 3.
 - 5: Merge the two sets of sorted subpaths by exploiting their lexicographic names.
-

Recursive Step of PathSort

At each recursive step, the algorithm constructs the array `IntNodes`, which stores the triplets $(\alpha(u), \text{level}(u), \text{parent}(u))$ for every internal node u in the given tree T .

Next, the algorithm selects a value j such that the number of nodes in `IntNodes` with depth $\equiv j \pmod{3}$ is at least $t/3$. Based on this choice, two separate arrays are created:

- `IntNodesAtPosJ`, containing nodes at levels $\equiv j \pmod{3}$,
- `IntNodesNotAtPosJ`, containing nodes at levels $\not\equiv j \pmod{3}$

For each node u in `IntNodesNotAtPosJ`, the algorithm extracts the upward path consisting of the first three ancestors of u . These paths are then sorted using radix sort. If the sorted upward paths contain duplicates, the algorithm recursively calls

the PathSort function on a new contracted tree, where nodes are renamed according to their sorted paths. Otherwise, if all upward paths are unique, the nodes in `IntNodesAtPosJ` are sorted and subsequently merged with `IntNodesNotAtPosJ` using lexicographic ordering, following the same merging strategy as in the Skew algorithm.

2.3.5 Inversion

The ability to invert the XBWT is fundamental to its utility as a compression technique. Invertibility guarantees that the original tree can be perfectly reconstructed from its transformed representation (S_{last} and S_α). This ensures that the compression is lossless, meaning no information is lost during the process, which is a critical requirement for most applications.

Property 'Path-based Indexing' (Subsection 2.3.3) ensures that the two arrays S_{last} and S_α of the XBWT can be used to reconstruct the original tree T . The algorithm to invert the XBWT is linear in time and requires $O(t \log t)$ bits of space.

Algorithm 2 operates in three main steps. First, it constructs two auxiliary arrays, F and J , which are crucial for navigating the tree structure within the compressed format.

- **The F array:** This array maps each character $c \in \Sigma$ to the index of the first occurrence in S of a triplet whose π -component is prefixed by c . It essentially marks the starting points of blocks of nodes that share the same initial path label.
- **The J array:** For each entry i in S , $J[i]$ stores the index in S corresponding to the first child of the node represented by $S[i]$. If $S[i]$ represents a leaf, $J[i]$ is set to a sentinel value (e.g., -1).

Example 2.5: F and J arrays

Considering the XBWT in Table 2.1, the F array would map 'A' to index 2 (for node r), 'B' to index 5 (for the children of nodes with label 'B'), and so on. For the J array, let's take the node u at index 2 in S . Its first child is at index 5. Therefore, $J[2]$ would be 5.

Finally, the algorithm employs the array J to simulate a depth-first visit of T , creates its labeled nodes, and properly connects them to their parents.

Algorithm 2 RebuildTree(XBWT[T])

```

1:  $F = \text{BuildF}(\text{XBWT}[T]);$ 
2:  $J = \text{BuildJ}(\text{XBWT}[T], F);$ 
3: Create node  $r$  and set  $Q = \{\langle 1, r \rangle\};$  ▷  $Q$  is a stack
4: while  $Q \neq \emptyset$  do ▷ We still have nodes to create in  $T$ 
5:    $\langle i, u \rangle = \text{pop}(Q);$ 
6:    $j = J[i];$  ▷ Take the block of  $u$ 's children in  $S$ 
7:   if  $j = -1$  then ▷  $u$  is a leaf of  $T$ 
8:     continue;
9:   end if
10:  Find first  $j' \geq j$  such that  $S_{\text{last}}[j'] = 1;$  ▷  $S[j, j']$  are the children of  $u$  in  $T$ 
11:  for  $h = j'$  downto  $j$  do
12:    Create the node  $v$  labeled  $S_\alpha[h];$ 
13:    Attach  $v$  as first child of  $u;$ 
14:     $\text{push}(\langle h, v \rangle, Q);$ 
15:  end for
16: end while
17: return node  $r.$ 

```

Algorithm 3 BuildF(XBWT[T])

```

1: for  $i = 1, \dots, |\Sigma_N|$  do
2:    $C[S_\alpha[i]] \leftarrow C[S_\alpha[i]] + 1;$  ▷ Count the occurrences of node labels
3: end for
4:  $F[1] = 2;$  ▷  $S_\pi[1]$  is the empty string
5: for  $i \in \{1, \dots, |\Sigma_N| - 1\}$  do ▷ Consider just the internal-node labels
6:    $s = 0; j = F[i];$ 
7:   while  $s \neq C[i]$  do ▷ Not all blocks of children have been passed
8:      $j = j + 1;$ 
9:     if  $S_{\text{last}}[j] = 1$  then ▷ One further block of children has passed
10:       $s = s + 1;$ 
11:     end if
12:   end while
13:    $F[i + 1] = j;$ 
14: end for
15: return  $F.$ 

```

Algorithm 4 BuildJ(XBWT[T], F)

```

1: for  $i = 1, \dots, t$  do
2:   if  $S_\alpha[i] \in \Sigma_L$  then
3:      $J[i] = -1;$   $\triangleright S_\alpha[i]$  is a leaf label
4:   else
5:      $z = J[S_\alpha[i]];$ 
6:     while  $S_{\text{last}}[z] \neq 1$  do  $\triangleright$  Reach the last child of  $S_\alpha[i]$ 
7:        $z = z + 1;$ 
8:     end while
9:      $F[S_\alpha[i]] = z + 1;$ 
10:  end if
11: end for
12: return  $J$ .
```

2.3.6 Compressing Labeled Trees

The XBWT[T] exhibits a local homogeneity property on S_α : specifically, the labels $\alpha(u)$ of nodes u whose upward paths $\pi(u)$ share long common prefixes appear in S_α in contiguous (or tightly bounded) clusters. This phenomenon can be formalized via the notion of k -contexts on trees. This property mirrors the strong local homogeneity exhibited by strings under the Burrows-Wheeler Transform [4] when applied to labeled trees.

To illustrate this, let us consider two arbitrary nodes u and v in T , and examine their contexts $\pi(u)$ and $\pi(v)$. Given the sorting of S , the greater the length of the shared prefix between $\pi(u)$ and $\pi(v)$, the closer the corresponding labels $\alpha(u)$ and $\alpha(v)$ will be in the string S_α . These closely spaced labels are expected to be few in number, resulting in S_α exhibiting local homogeneity. As a consequence, we can leverage the advanced algorithmic techniques developed for BWT-based compression methods to achieve efficient compression.

At the end, the XBWT is used for turning the labeled tree compression problem into a string compression problem. To this aim, two string compressors C_α and C_{last} are used to compress the two strings that compose XBWT[T], by exploiting their fine specialties. Of course, many choices are possible for C_α and C_{last} , each having implications on the algorithmic time and compression bounds.

In general, the following theorem holds:

Theorem 2 ([12], Theorem 4). *let C_α be a k -th order string compressor that compresses any string w into $|w|H_k(w) + |w| + o(|w|)$ bits, taking $O(|w|)$ time; and let C_{last} be an algorithm that stores S_{last} without compression. With this simple instantiation, the labeled tree T can be compressed within $tH_k(S_\alpha) + 2t + o(t)$ bits and takes $O(t)$ optimal time.*

Since $H_k(S_\alpha) \leq (\log |\Sigma|) + 1$ (Recall that when a unique alphabet is used to label both internal nodes and leaves, one needs a further bit to distinguish between them. The additional term $+1$ in the bound takes this into account), the above bound is at most $t(\log |\Sigma| + 3) + o(t)$ bits, and can be significantly better than the information-theoretic lower bound and the plain storage of XBWT[T] (both taking $2t + t \log |\Sigma|$

bits), depending on the distribution of the labels among its nodes.

2.3.7 Indexing a Compressed Labeled Tree

In order to implement the efficient operations listed in Subsection 2.2.3 using the compressed arrays S_{last} and S_α of XBWT, we need the chosen compressors C_α and C_{last} to support the following operations:

Given a string $S[1, t]$ over alphabet Σ

- $\text{rank}_c(S, q)$: gives the number of times the symbol $c \in \Sigma$ appears in $S[1, q]$.
- $\text{select}_c(S, i)$: gives the position of the i -th occurrence of the symbol $c \in \Sigma$ in S .

The compressed indexing of $\text{XBWT}[T]$ will be based on three compressed data structures that support rank and select queries over the two strings S_α and S_{last} , and over an auxiliary binary array $A[1, t]$ defined as: $A[1] = 0$, $A[j] = 1$ if and only if the first symbol of $S_\pi[j]$ differs from the first symbol of $S_\pi[j - 1]$. Hence, A contains at most $|\Sigma| + 1$ bits set to 1 out of t positions. It is also easy to see that, through rank and select operations over A , we can succinctly implement the array F employed in Algorithms 2 and 3.

In this section, let $S := (S_{\text{last}}, S_\alpha)$ denote the $\text{XBWT}[T]$ obtained after the construction phase. The following methods are supported by the compressed index:

GetRankedChild(i, k): Returns the position in S of the k -th child of the node at index i . If the child does not exist, it returns -1.

Example 2.6:

In Table 2.2, **GetRankedChild**(2, 2) returns 6.

GetCharRankedChild(i, c, k): Returns the position in S of the k -th child labeled c of the node at index i . If the child does not exist, it returns -1.

Example 2.7:

In Table 2.2, **GetCharRankedChild**(1, B, 2) returns 4.

GetDegree(i): Returns the total number of children of the node at index i in S .

GetCharDegree(i, c): Returns the number of children of the node at index i in S that have the label c .

GetParent(i): Returns the position in S of the parent of the node at index i . If the node is the root (at index 1), it returns -1.

Example 2.8:

In Table 2.2, **GetParent**(8) returns 4.

GetSubtree(i): Retrieves the labels of all nodes in the subtree rooted at the node at index i in S . The labels can be returned in any standard traversal order (e.g., pre-order, in-order, or post-order).

	A	S_{last}	S_{α}	S_{π}
1	0	0	A	ϵ
2	1	0	B	A
3	0	0	C	A
4	0	1	B	A
5	1	0	D	BA
6	0	0	a	BA
7	0	1	E	BA
8	0	1	D	BA
9	1	0	D	CA
10	0	0	b	CA
11	0	1	D	CA
12	1	1	a	DBA
13	0	1	b	DBA
14	0	1	c	DCA
15	0	1	c	DCA
16	1	1	b	EBA

Table 2.2: The sequence S for the tree shown in Figure 2.1, obtained by stably sorting triplets according to their ' π ' components. In this representation, nodes u and v from the original tree T appear at indices 2 and 4, respectively. The children's block of node u occupies positions 5 through 7, while node v 's single child is located at index 8. Also, the auxiliary binary array A is shown.

SubPathSearch(P): For a given labeled path $P = c_1c_2 \cdots c_k$, this function finds the range $S[\text{First} \dots \text{Last}]$ such that all strings in $S_{\pi}[\text{First} \dots \text{Last}]$ are prefixed by the reversed path $P^R = c_k \cdots c_2c_1$.

Example 2.9:

In Table 2.2, **SubPathSearch(BD)** results in the range $[12, 13]$, and **SubPathSearch(AB)** gives the range $[5, 8]$.

It is important to note that their time complexity is dependent on the specific implementation for rank and select over the compressed strings S_{α} and S_{last} .

Let's now see how to implement some of the above methods (from which the others can be derived) using the rank and select operations over the compressed strings S_{α} and S_{last} .

GetChildren(i)

Algorithm 5 exploits directly the properties described before, in particular Property 'Path-based Indexing' (Subsection 2.3.3). The rank operation at line 5 is used to get the number r of nodes labeled c up to position i in S_{α} . Then, the position $F[c]$ is obtained through a select operation on A (line 6). By Property 'Path-based Indexing', the children of $S[i]$ are located at the r -th block of children following position $F[c]$. Lines 8 – 9 identify this block.

Example 2.10:

Let's walk through an example using Table 2.2. Consider the node u at index 2 labeled with B . To find its children:

1. First, we compute $r = 1$ since this is the first occurrence of B in S_α up to position 2.
2. Next, we find $y = F[B] = 5$, which marks the start of the block containing children of all nodes labeled B .
3. Then, we count $z = 1$ ones in S_{last} up to position $y - 1$.
4. Finally, the children block is delimited by the $z + r - 1 = 1\text{st}$ and $z + r = 2\text{nd}$ ones in S_{last} , giving us the range $[5, 7]$.

This range $[5, 7]$ indeed contains the three children of the node at index 2, as we can verify from the tree structure in Figure 2.1.

Algorithm 5 GetChildren(i)

```

1: if  $S_\alpha[i] \in \Sigma_L$  then
2:   return  $-1$                                  $\triangleright S[i]$  is a leaf
3: end if
4:  $c \leftarrow S_\alpha[i]$                              $\triangleright S[i]$  is labeled  $c$ 
5:  $r \leftarrow \text{rank}_c(S_\alpha, i)$ 
6:  $y \leftarrow \text{select}_1(A, c)$                      $\triangleright y = F[c]$ 
7:  $z \leftarrow \text{rank}_1(S_{\text{last}}, y - 1)$ 
8: First  $\leftarrow \text{select}_1(S_{\text{last}}, z + r - 1) + 1$ 
9: Last  $\leftarrow \text{select}_1(S_{\text{last}}, z + r)$ 
10: return (First, Last)
    
```

GetParent(i)

Algorithm 6 is based on Property ‘Path-based Indexing’ (Subsection 2.3.3) and it is the inverse of the GetChildren method. In line 4, the algorithm computes the label c of the parent of $S[i]$ that prefixes the upward path leading to $S[i]$. Then, the parent of $S[i]$ is searched among the nodes labeled c in S_α by exploiting Property ‘Path-based Indexing’ in a reverse manner. Namely, the number k of children-blocks in the range $S[y, i]$ is computed; these are children of nodes labeled c and preceding i in the stable sort of S . Then, the k -th occurrence of c in S_α is selected, which is indeed the parent of $S[i]$.

Example 2.11:

Let’s illustrate how to find a node’s parent using Table 2.2. Consider node v located at index 4 with label B . The process to find its parent involves:

1. Computing $c = \text{rank}_1(A, 4) = 1$, which tells us the parent has label ‘A’ (as A contains exactly one 1 up to position 4).
2. Locating $y = F[A] = 2$, which indicates where the block of children for nodes labeled ‘A’ begins.
3. Calculating $k = \text{rank}_1(S_{\text{last}}, 4 - 1) - \text{rank}_1(S_{\text{last}}, 2 - 1) = 0$, meaning no complete child blocks appear before position 4.
4. Therefore, v ’s parent is the first $((k + 1)\text{-th})$ occurrence of ‘A’ in S_α , corresponding to index 1 (the root of \mathcal{T}).

This example demonstrates how the XBWT structure efficiently encodes parent-child relationships using just the S_{last} and S_α arrays.

Algorithm 6 GetParent(i)

```

1: if  $i = 1$  then
2:   return  $-1$   $\triangleright S[i]$  is the root of  $\mathcal{T}$ 
3: end if
4:  $c \leftarrow \text{rank}_1(A, i)$ 
5:  $y \leftarrow \text{select}_1(A, c)$ 
6:  $k \leftarrow \text{rank}_1(S_{\text{last}}, i - 1) - \text{rank}_1(S_{\text{last}}, y - 1)$ 
7:  $p \leftarrow \text{select}_c(S_\alpha, k + 1)$ 
8: return  $p$ 

```

SubPathSearch(P)

We assume that $P = c_1 c_2 \dots c_k$ algorithm SubPathSearch computes the range $[First, Last]$ in $|P| = l$ phases, each one preserving the following invariant:

- Invariant of Phase i . At the end of the phase, $S_\pi[First]$ is the first entry prefixed by $P[1, i]^R$, and $S_\pi[Last]$ is the last entry prefixed by $P[1, i]^R$, where s^R is the reversal of string s .

At the beginning (i.e., $i = 1$), First and Last are easily determined via the entries $F[c_1]$ and $F[c_1 + 1] - 1$, which point to the first and last entry of S_π prefixed by c_1 (by definition of array F). Since we do not have the F array, we implement these operations via rank and select queries over array A . Let us assume that the invariant holds for Phase $i - 1$, and prove that the i -th iteration of the for-loop in algorithm SubPathSearch preserves the invariant. More precisely, let $S_\pi[First, Last]$ be all entries prefixed by $P[1, i - 1]^R$. So $S[First, Last]$ contains all nodes descending from $P[1, i - 1]$. SubPathSearch determines $S[z_1]$ (respectively $S[z_2]$) as the first (respectively last) node in $S[First, Last]$ that descends from $P[1, i - 1]$ and is labeled c_i , if any. Then it jumps to the first child of $S[z_1]$ and the last child of $S[z_2]$. From Property 2 (item 2) and the correctness of algorithms GetChildren and GetDegree, we infer that the positions of these two children are exactly the first (respectively last) entry in S whose π -component is prefixed by $P[1, i]^R$.

The time complexity of the SubPathSearch algorithm is $O(l)$, where l is the length of the input path P .

Example 2.12:

Consider the tree in Figure 2.1, and let $P = BD$. The algorithm SUBPATH-SEARCH(P) returns the range $[12, 13]$ through the following steps:

1. Initially, $First = F[B] = 5$ and $Last = F[C] - 1 = 8$. The range $S[5, 8]$ contains all nodes descending from paths prefixed by B .
2. For $c_2 = D$:
 - Compute $k_1 = 0$ and $k_2 = 2$
 - This yields $z_1 = 5$ and $z_2 = 8$

- The first child of $S[5]$ is at position 12
- The last (and only) child of $S[8]$ is at position 13

3. Therefore, the algorithm returns the range $[12, 13]$

Note that the number of occurrences of subpath P is 2, as evidenced by the two occurrences of 1 in range $S_{\text{last}}[12, 13]$.

Algorithm 7 SubPathSearch(P)

```

1:  $First \leftarrow F(c_1)$ ;  $Last \leftarrow F(c_1 + 1) - 1$ 
2: if  $First > Last$  then
3:   return “ $P$  is not a subpath of  $T$ ”
4: end if
5: for  $i \leftarrow 2, \dots, k$  do
6:    $k_1 \leftarrow \text{rank}_{c_i}(S_\alpha, First - 1)$ ;  $z_1 \leftarrow \text{select}_{c_i}(S_\alpha, k_1 + 1)$  ▷ first entry in
    $S_\alpha[First, t]$  labeled  $c_i$ 
7:    $k_2 \leftarrow \text{rank}_{c_i}(S_\alpha, Last)$ ;  $z_2 \leftarrow \text{select}_{c_i}(S_\alpha, k_2)$  ▷ last entry in  $S_\alpha[1, Last]$ 
   labeled  $c_i$ 
8:   if  $z_1 > z_2$  then
9:     return “ $P$  is not a subpath of  $T$ ”
10:  end if
11:   $First \leftarrow \text{GetRankedChild}(z_1, 1)$  ▷ get the first child of  $S[z_1]$ 
12:   $Last \leftarrow \text{GetRankedChild}(z_2, \text{GetDegree}(z_2))$  ▷ get the last child of  $S[z_2]$ 
13: end for
14: return  $(First, Last)$ 

```

2.3.8 Implementation

The XBWT data structure has been implemented in C++ using the Succinct Data Structure Library 2.0 (SDSL) for efficient representation and manipulation of compressed data structures. We will develop two algorithms for constructing the XBWT: one efficient linear-time recursive algorithm and one more straightforward iterative algorithm. Also, we will implement the necessary data structures and algorithms for navigating and querying the XBWT, such as parent-child navigation and path-based searches.

The implementation of the XBWT is based on the descriptions provided in the previous sections. Also, it is available on GitHub at the following link: <https://github.com/davide-tonetto-884585/XBWT>.

Implementation Choices

Follows a list of the main choices made during the implementation of the XBWT:

- The implementation is not focused on a specific kind of data, such as XML documents or JSON files, but it is designed to work with any kind of labeled tree.
- The construction method takes as input a labeled tree. It constructs directly a

compressed indexing scheme based on the Extended Burrows-Wheeler Transform of the tree as described in the previous sections.

- The implementation is based on the Succinct Data Structure Library (SDSL) to handle the compressed data structures generated by the XBWT. The SDSL library provides efficient implementations of various compressed data structures and algorithms, which are essential for representing and querying the XBWT efficiently.
- The labels of the alphabet are encoded as integers, starting from 0 to $|\Sigma| - 1$, where $|\Sigma|$ is the cardinality of the alphabet. This encoding respects the order of the labels in the alphabet and allows simplifying and reducing the space needed to store the labels in the compressed data structure. For this reason, the constructor of the XBWT class takes as input a generic labeled tree.

Succinct Data Structures

The implementation of the XBWT relies heavily on succinct data structures to achieve space efficiency while maintaining fast query operations. In particular, we use succinct data structures to compress the two main arrays of the XBWT: S_α and S_{last} . These arrays, which can be quite large for substantial trees, benefit significantly from compression.

The compression is achieved through the Succinct Data Structure Library (SDSL), which provides efficient implementations of various compressed data structures. For S_{last} , which is a binary sequence, we utilize a compressed bit vector that supports fast rank and select operations. For S_α , which contains labels from a potentially large alphabet, we employ a wavelet tree structure that provides both compression and efficient query capabilities.

The SDSL is a C++ library that provides efficient implementations of various compressed data structures and algorithms. It is used in this project to handle the compressed data structures composing the XBWT. The SDSL library provides a wide range of succinct data structures, such as bit vectors, wavelet trees, and compressed suffix arrays, which are essential for representing and querying the XBWT efficiently. The library is available at <https://github.com/simongog/sdsl-lite> [17]. Let's see the implementation details of the SDSL data structures used in the XBWT implementation.

RRR Bit Vector

The RRR bit vector is designed to provide space-efficient representations of bit vectors while supporting efficient rank and select operations. This data structure implements the RRR (Raman, Raman, and Rao) encoding method, which compresses bit vectors by partitioning them into fixed-size blocks and encoding each block based on its population count (the number of 1s) and specific configuration [32].

The space needed for an RRR bit vector of length n with m set bits is $nH_0 + o(n)$ ($\approx \lceil \log \binom{n}{m} \rceil$). The rank support is provided by `sdsl::rank_support_rrr`, adding 80 bits and requiring $O(\log k)$ time for rank queries, where k is the number of set

bits. The select support is provided by `sdsl::select_support_rrr`, adding 64 bits and requiring $O(\log n)$ time for select queries.

This data structure is used to represent S_{last} , a dedicated binary array B_α that stores the additional information associated with each entry of S_α (i.e., $B_\alpha[i] = 1$ if the i -th symbol in S_α corresponds to a leaf and 0 otherwise), and the A array of the XBWT.

Wavelet Tree

The Wavelet tree is designed to efficiently handle sequences over large alphabets, such as integer sequences. It provides a space-efficient representation while supporting fast access, rank, and select operations. The wavelet tree is a balanced binary tree that recursively partitions the alphabet into two equal-sized subsets and encodes the sequence based on the partitioning [18]. The `sdsl::wt_int` uses the RRR bit vectors or other succinct representations for storing the bit vectors in each node of the wavelet tree. This makes the structure space-efficient.

If RRR-compressed bitvectors are used for the internal bitmaps, a wavelet tree over a sequence $S \in \Sigma^n$ (with $|\Sigma| = \sigma$) occupies $nH_0(S) + o(n \log \sigma) + \Theta(\sigma \log n)$ bits of space, where $H_0(S)$ is the zero-order empirical entropy of S , and it supports access, rank, and select queries in $O(\log \sigma)$ time.

This data structure is used to represent the S_α array of the XBWT.

2.3.9 Experiments

Davide T.: Questa sezione andrà riadattata una volta che avremo deciso quali esperimenti fare con l'altro algoritmo

The experiments have been run on a machine with an AMD Ryzen 9 5600Hs CPU with 24 GB of RAM. The results are shown in Table Table 2.3 and Table Table 2.4. The source code for the experiments can be found in the `experiments.cpp` file.

Construction Performance

To evaluate the performance of the implemented algorithms, we conducted a series of experiments on randomly generated trees created using the Python library `networkx`. The trees were generated with sizes ranging from 100 to 900,000 nodes. For each tree, we executed the construction algorithms 10 times, measuring the average execution time for both the linear *PathSort* (P.S.) algorithm and the naive *UpwardStableSort* (N.S.) algorithm used for constructing the XBWT. This approach allowed us to compare their performance across different tree sizes and assess their scalability.

The results are shown in Table Table 2.3. **Alessio:** Spiega subito i risultati.

Alessio: I numeri vanno sempre allineati a destra, così si capisce meglio chi è più grande di chi. Inoltre, anche il numero di cifre dopo la virgola deve essere sempre lo stesso, così la virgola è fissa e si possono leggere meglio i dati. Su questo faccio io, tu fai sulla prossima :)

Nodes	Depth	P.S. Time (s)	N.S. Time (s)
100	22	0.002	0.001
500	45	0.004	0.002
1000	74	0.006	0.003
5000	175	0.028	0.015
10000	288	0.056	0.053
50000	486	0.310	0.350
100000	754	0.690	1.250
500000	2246	4.700	16.460
900000	2658	8.510	34.200

Table 2.3: Performance comparison between PathSort (P.S.) and Naive Sort (N.S.) algorithms.

Space Analysis

To evaluate the space savings achieved through XBWT compression, we conducted experiments on the same set of randomly generated trees used for the construction performance tests. For each tree, we compared the memory usage (in bytes) of three representations: the plain tree, the uncompressed XBWT, and the compressed XBWT.

The plain tree representation consists of the simple balanced parenthesis encoding of the tree structure combined with the edge labels. For example for tree in Figure ??, the plain tree representation would be:

(A(B(D(a))(a)(E(b)))(C(D(c))(b)(D(c)))(B(D(b))))).

By *uncompressed XBWT*, we refer to the XBWT arrays S_{last} and S_{α} (including the additional bit) stored without any compression. Specifically, S_{last} is represented as a plain bitvector (`sds1::bit_vector`), and S_{α} is stored as a wavelet tree (`sds1::wt_int`) with plain bitvectors (`sds1::bit_vector`). In contrast, the *compressed XBWT* representation stores S_{last} and S_A as compressed RRR bitvectors (`sds1::rrr_vector`), and S_{α} as a wavelet tree with RRR bitvectors, as described in the previous chapter.

Table Table 2.4 reports the sizes (in bytes) for each representation of the trees across different sizes. The last column highlights the space savings achieved by the compressed XBWT compared to the plain tree representation, expressed as a percentage. These results illustrate the substantial space reductions achieved through compression, especially as the tree size increases.

Alessio: Oltre ai punti di prima, metti la percentuale anche per UXBWT, magari non come un'altra colonna ma metti tra parentesi. Te lo faccio sulle prime righe per la C.XBWT. Se ti piace, ricorda di spiegare cosa sono i numeri tra parentesi nella descrizione.

Conclusions

From the results shown in Table Table 2.3, we can draw several conclusions about the performance of the PathSort (P.S.) algorithm compared to the Naive Sort (N.S.) algorithm and the space savings achieved by compressing the XBWT.

Firstly, the PathSort algorithm consistently outperforms the Naive Sort algorithm

Nodes	Plain tree (B)	U. XBWT (B)	C. XBWT (B)	Saving (%)
100	390	424	496 (-27.18%)	
500	2390	1112	1136 (52.47%)	
1000	4890	2242	2056	57.96
5000	28890	12911	10400	64
10000	58890	45625	21848	62.90
50000	338890	175146	123216	63.64
100000	688890	349478	259376	62.35
500000	3888890	1850850	1451570	62.67
900000	7088890	3480190	2718570	61.65

Table 2.4: Space analysis of the XBWT. Plain tree is the size in bytes of the tree in the simple balanced parenthesis representation plus the edge labels, U. XBWT is the size in bytes of the tree in the uncompressed XBWT, and C. XBWT is the size in bytes of the tree in the compressed XBWT. The last column shows the space-saving percentage between plain tree and compressed XBWT.

in terms of execution time, especially as the number of nodes increases. For smaller trees, the difference in execution time between the two algorithms is minimal. However, as the number of nodes grows, the PathSort algorithm demonstrates significantly better scalability. For instance, with 900,000 nodes, the PathSort algorithm takes 8.51 seconds, whereas the Naive Sort algorithm takes 34.2 seconds , giving speedup of more than $4\times$.

Secondly, the depth of the tree appears to increase with the number of nodes, which is expected in randomly generated trees. This increase in depth does not seem to adversely affect the performance of the PathSort algorithm as much as it does the Naive Sort algorithm.

For small trees, the compressed XBWT does not always provide immediate savings due to the overhead of succinct data structures. For instance, for 100 nodes, the compressed representation is larger than the plain tree, showing a -27.18% increase in space. However, as the number of nodes increases, the compression becomes more effective, achieving savings of over 60% for large trees.

The space reduction becomes particularly evident for trees with more than 500 nodes. These results confirm that the compressed XBWT provides a scalable and space-efficient alternative for storing and indexing labeled trees. The efficiency gains are particularly beneficial for applications requiring large-scale tree processing, such as bioinformatics and text indexing.

In conclusion, the PathSort algorithm is a more efficient choice for constructing the XBWT, especially for larger trees, and the compression method provides significant space savings, making the overall process more efficient in terms of both time and space.

2.4 Finite State Automata

2.4.1 Introduction and Motivation

Tree compression schemes that effectively exploit repetitive structures require efficient techniques for identifying and representing such repetitions compactly. A powerful approach to this problem is to view a tree as a finite language, where each

path from the root to a leaf represents a word. Such a language can be recognized by a Deterministic Finite Automaton (DFA). More specifically, since trees are inherently acyclic, they can be represented by Acyclic Deterministic Finite Automata (ADFAs).

The problem of finding and compressing identical subtrees is thus equivalent to minimizing the corresponding DFA. DFA minimization ensures that equivalent substructures are merged efficiently, leading to a more compact encoding. The minimized DFA provides a canonical representation of the repetitive structures, which can then be leveraged in our compression pipeline. This theoretical foundation enables us to identify and encode tree patterns systematically, ultimately improving the compression efficiency.

While general-purpose minimization algorithms like Hopcroft's are highly efficient for any DFA, the specific structure of ADFAs allows for even faster, linear-time algorithms. In this context, we focus on Revuz's algorithm, which is specifically designed to minimize ADFAs and is therefore particularly well-suited for compressing tree structures.

This chapter provides the necessary theoretical background on DFA minimization. We will first introduce the concepts of DFAs and their minimization, followed by a detailed look at both Hopcroft's algorithm as a general solution and Revuz's algorithm as a specialized, linear-time solution for acyclic graphs, which is central to our tree compression methodology.

2.4.2 DFA Minimization

The process of automata minimization consists of reducing the number of states in a DFA while preserving its accepted language. The minimization of DFA is crucial for a variety of applications, such as model checking, hardware design, and compilers, as it produces a more effective and compact representation of the automaton, allowing for faster processing and reduced memory usage.

The minimization of DFA is a well-studied problem in automata theory, and there are several algorithms available for this purpose. One of the most popular algorithms for DFA minimization is Hopcroft's algorithm, which was proposed by John Hopcroft in 1971 [20]. Hopcroft's algorithm is an efficient and simple algorithm that can minimize a DFA in $O(n \log n)$ time, where n is the number of states in the DFA.

The algorithm enables computing equivalence classes of nodes, in particular, the Myhill-Nerode equivalence classes [31, 29]. The Myhill-Nerode theorem states that a language is regular if and only if it has a finite number of Myhill-Nerode equivalence classes. This theorem provides a powerful tool for determining the regularity of languages and is a cornerstone of automata theory. Let's formalize the concept of equivalence classes and the Myhill-Nerode theorem.

Definition 9 (Myhill-Nerode Equivalence Relation). *For a language $L \subseteq \Sigma^*$ and any strings $x, y \in \Sigma^*$, we say x is equivalent to y with respect to L (written as $x \approx_L y$) if and only if for all strings $z \in \Sigma^*$:*

$$xz \in L \Leftrightarrow yz \in L$$

That is, strings x and y are equivalent if they have the same behavior with respect to the language L : either they both lead to acceptance or both lead to rejection when any suffix z is appended.

Theorem 3 (Myhill-Nerode theorem [31, 29]). *Let L be a language over an alphabet Σ . Then L is regular if and only if there exists a finite number of Myhill-Nerode equivalence classes for L . Specifically, the number of equivalence classes is equal to the number of states in the minimal DFA recognizing L .*

Throughout this section let $M = (Q, \Sigma, \delta, q_0, F)$ be a DFA. For $q \in Q$ and $a \in \Sigma$, we adopt the shorthand $q.a := \delta(q, a)$. We extend δ to words by the usual recursion:

$$\delta^*(q, \varepsilon) = q, \quad \delta^*(q, wa) = \delta(\delta^*(q, w), a) \quad \text{for } w \in \Sigma^*, a \in \Sigma.$$

For a word $w = w_1w_2 \dots w_n \in \Sigma^*$, we then write $q.w := \delta^*(q, w)$ for the (unique) state reached from q by reading w . A word w is accepted by M iff $\delta^*(q_0, w) \in F$.

Also, Let $M = (Q, \Sigma, \delta, q_0, F)$ be a DFA recognizing L . For states (nodes) $u, v \in Q$, we say that u and v are MN-equivalent iff

$$\forall \alpha \in \Sigma^* : u.\alpha \in F \iff v.\alpha \in F.$$

2.4.3 Revuz' Minimization Algorithm

For our purpose, we will focus on a specific type of finite automaton: an acyclic deterministic finite automaton. An ADFA is a DFA where the transition graph contains no cycles. The acyclic property is key, as it simplifies the minimization process significantly.

In this section, we will discuss an efficient algorithm for minimizing acyclic deterministic finite automata in linear time on the number of edges [33].

Let's begin by providing some definitions needed to understand the algorithm.

Definition 10 (Height function). *For a state s in an automaton, the height $h(s)$ is defined as the length of the longest path starting at s and going to a final state.*

$$h(s) = \max\{|w| : s.w \text{ is final}\}$$

This height function induces a partition Π_i of Q , where Π_i denotes the set of states of height i .

Lets now define the canonical label of each state that will be necessary to identify MN-equivalent states. For $s \in Q$, let $l_1 < \dots < l_k$ be the symbols of the outgoing transitions defined at s (listed in increasing order of Σ). With $b = F$ if $s \in F$ and $b = NF$ otherwise, we set

$$l(s) := (b, l_1, s.l_1, l_2, s.l_2, \dots, l_k, s.l_k).$$

Also, the algorithm uses a function R to map the labels of states to a new signature. This function is defined as follows:

Definition 11 (Signature map R). Let $N[\cdot]$ be the current renaming array that assigns to each state its equivalence class identifier Myhill-Nerode. For a state s labeled

$$l(s) = (b, l_1, s.l_1, l_2, s.l_2, \dots, l_k, s.l_k),$$

where $b \in \{F, NF\}$, $l_i \in \Sigma$ (listed in increasing order), and $nl_i \in Q$, define

$$R(l(s)) = (b, l_1, N[s.l_1], l_2, N[s.l_2], \dots, l_k, N[s.l_k]).$$

It is important to notice that, since the automaton is acyclic, every transition $s \xrightarrow{a} t$ strictly decreases the height: $h(t) < h(s)$. The main loop of Algorithm 8 processes levels in increasing order $i = 0, 1, \dots$, so by the time we handle a state $s \in \Pi_i$, all its targets t lie in $\bigcup_{j < i} \Pi_j$ and have already been assigned a Myhill-Nerode equivalence class.

Algorithm 8 Revuz' Minimization Algorithm for ADFAs

Require: ADFA $M = (Q, \Sigma, \delta, q_0, F)$

Ensure: Minimal DFA $M' = (\{1, \dots, n\}, \Sigma, \delta', N[q_0], F')$ with $F' = \{N[q] \mid q \in F\}$ and $\delta'(N[q], a) = N[\delta(q, a)]$

- 1: Calculate height $h(s)$ for every state s .
 - 2: Create partitions $\Pi_i = \{s \in Q \mid h(s) = i\}$.
 - 3: $N[1, |Q|] = \{1, 2, \dots, |Q|\};$ \triangleright Renaming array
 - 4: $n = 0;$
 - 5: **for** $i := 0$ to $h(q_0)$ **do** $\triangleright q_0$ is the initial state
 - 6: Sort states in Π_i based on $R(l(q)), q \in \Pi_i$.
 - 7: $n = n + 1;$
 - 8: $N[\Pi_i[1]] = n;$
 - 9: **for** $j := 2$ to $|\Pi_i|$ **do**
 - 10: **if** $R(l(\Pi_i[j])) \neq R(l(\Pi_i[j-1]))$
 - 11: $n = n + 1;$
 - 12: **end if**
 - 13: $N[\Pi_i[j]] = n;$
 - 14: **end for**
 - 15: **end for**
-

The algorithm proceeds level by level, from $i = 0$ up to the maximum height, ensuring that states at each level are correctly partitioned into Myhill-Nerode equivalence classes. For each level i , it groups the states in Π_i based on their signatures computed by the function R (see Definition 11). As explained before, when processing level i , the equivalence classes for all states in lower levels ($j < i$) have already been finalized. The signature $R(l(s))$ for a state s depends on its finality and the equivalence classes of its immediate successors. Therefore, two states $s, t \in \Pi_i$ have the same signature if and only if they are MN-equivalent. The algorithm assigns a unique class identifier to each group of states with the same signature.

The whole algorithm can be implemented to run in time $O(m)$ for an acyclic automaton with m edges. Heights may be computed in linear time by a bottom-up traversal. The lists of states of a given height are collected during this traversal.

The signature of a state is easy to compute provided the edges starting in a state have been sorted (by a bucket sort for instance to remain within the linear time constraint). Sorting states by their signature again is done by a lexicographic sort [2].

Example 2.13:

Now we are going to see an example of reduction for a given ADFA. The ADFA is represented in figure Figure 2.2 and, as we can notice, it is also a valid ordered rooted tree with $n = 11$ nodes, $e = 10$ edges, and the following alphabet: $\Sigma = \{0, 1\}$. The node a is the root of the tree and the initial state of the automaton, while the leaf nodes e, g, h, i, l, m are final states. It is important to note that while the algorithm applies to any ADFA, our focus is on those that are also trees, as this is the specific case relevant to our work.

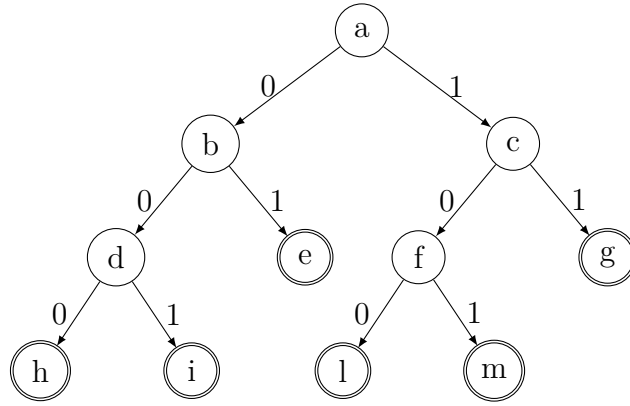


Figure 2.2: Example ADFA to be minimized

Now, let's apply the minimization algorithm step by step:

1. **Height Computation:** First, we compute the height of each state. The height is the length of the longest path to a final state. The final states (e, g, h, i, l, m) have a height of 0. For the other states, the height is calculated as follows:

- $h(d) = 1 + \max(h(h), h(i)) = 1 + 0 = 1$
- $h(f) = 1 + \max(h(l), h(m)) = 1 + 0 = 1$
- $h(b) = 1 + \max(h(d), h(e)) = 1 + \max(1, 0) = 2$
- $h(c) = 1 + \max(h(f), h(g)) = 1 + \max(1, 0) = 2$
- $h(a) = 1 + \max(h(b), h(c)) = 1 + \max(2, 2) = 3$

This gives us the following partitions based on height:

- $\Pi_0 = \{e, g, h, i, l, m\}$
- $\Pi_1 = \{d, f\}$
- $\Pi_2 = \{b, c\}$
- $\Pi_3 = \{a\}$

2. **Processing Π_0 :** All states in Π_0 are final and have no outgoing transitions, so they are all equivalent. We merge them into a single class, let's call it $D = \{e, g, h, i, l, m\}$. After this step, we have a new state D which is final.

3. **Processing Π_1 :** Now we examine the states in Π_1 : d and f . We check their transitions:

- State d : $\delta(d, 0) = h \in D$ and $\delta(d, 1) = i \in D$.
- State f : $\delta(f, 0) = l \in D$ and $\delta(f, 1) = m \in D$.

Since both states transition to the same equivalence class (D) for both symbols 0 and 1, they are equivalent. We merge them into a new class, $C = \{d, f\}$.

4. **Processing Π_2 :** Next, we process the states in Π_2 : b and c .

- State b : $\delta(b, 0) = d \in C$ and $\delta(b, 1) = e \in D$.
- State c : $\delta(c, 0) = f \in C$ and $\delta(c, 1) = g \in D$.

Both states have transitions to class C on symbol 0 and to class D on symbol 1. Therefore, b and c are equivalent. We merge them into a new class, $B = \{b, c\}$.

5. **Processing Π_3 :** Finally, we process Π_3 , which contains only state a . There is nothing to compare it with, so it forms its class, $A = \{a\}$.

After applying the algorithm, we obtain the minimized ADFA represented in figure Figure 2.3. Each node of the original ADFA is represented by a node in the minimized ADFA (equivalence classes). The edges represent transitions between these nodes. The root node A is the initial state of the minimized ADFA, while the node D is the final state.

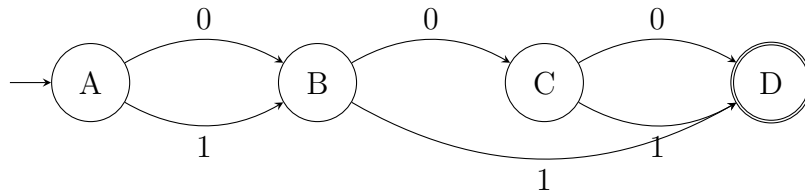


Figure 2.3: Minimized ADFA

The equivalence classes of the nodes are listed in table Table 2.5.

Class	States
A	a
B	b, c
C	d, f
D	e, g, h, i, l, m

Table 2.5: Equivalence classes of the nodes

2.5 Wheeler and p -sortable Graphs

2.5.1 Introduction and Motivation

As established in the introduction, the primary goal of this thesis is to find an effective balance between compressing a finite language and preserving its indexability. The two extremes—full DFA minimization and the raw input trie—are inadequate, as one sacrifices indexing for compression and the other sacrifices compression for indexing. The solution to this problem lies in a specific class of graphs that are structured enough to be indexed efficiently yet flexible enough to allow for significant compression. This chapter introduces the theoretical framework that underpins our approach: Wheeler graphs and their generalization, p -sortable graphs.

A crucial observation is that the input trie representing our language is already a highly structured object. It is a Wheeler graph, a type of graph that admits a special ordering on its nodes and edges, making it exceptionally well-suited for indexing. In formal terms, a trie is a 1-sortable graph. This property explains both its powerful indexing capabilities and its inherent lack of compression.

The concept of p -sortability offers a way to navigate the trade-off. By controllably increasing the sortability parameter p , we can begin to merge MN-equivalent states (see Theorem 3), thereby compressing the graph. The resulting automaton is no longer a simple trie but a more general p -sortable graph that retains strong indexing properties. This chapter will formally define these concepts, which are the foundation of our algorithm for achieving a practical compromise between compression and indexability.

2.5.2 Orders

The core property that makes Wheeler and p -sortable graphs efficiently indexable is the existence of a specific ordering on their states. This ordering provides the necessary structure to navigate the automaton and answer queries quickly, a task that is computationally hard on general graphs. The fundamental ordering used in this context is the *co-lexicographic order* (co-lex), which compares states based on the labels of the paths that reach them. This section formally defines this order and the related concepts that are essential for understanding the structure of indexable automata.

Definition 12 (Co-lexicographic Order on Σ^*). *The co-lex order \preceq is defined as follows. Given two strings $\alpha, \beta \in \Sigma^*$, we say that $\alpha \preceq \beta$ if and only if either:*

- α is a suffix of β , or
- there exist strings $\alpha', \beta', \gamma \in \Sigma^*$ and symbols $a, b \in \Sigma$, such that $\alpha = \alpha'a\gamma$, $\beta = \beta'b\gamma$, and $a \prec b$.

Now, let's define the formal concept of partial order and the width of a partial order.

Definition 13 (Partial Order). *A partial order is a binary relation \leq over a set S that is reflexive, antisymmetric, and transitive. That is, for all $a, b, c \in S$:*

- $a \leq a$ (reflexivity)

- if $a \leq b$ and $b \leq a$, then $a = b$ (antisymmetry)
- if $a \leq b$ and $b \leq c$, then $a \leq c$ (transitivity)

Definition 14 (Antichain). *An antichain of a partially ordered set (S, \leq) is a subset of S where any two distinct elements are incomparable. That is, for any two distinct elements a, b in the antichain, neither $a \leq b$ nor $b \leq a$ holds.*

Definition 15 (Width, [8]). *The width of a partially ordered set is the size of the largest possible antichain.*

By Dilworth's Theorem [8], the width of a partially ordered set (S, \leq) is equal to the cardinality of its largest antichain; this can be equivalently defined as the minimum number of chains needed to partition S , where each chain is a totally ordered subset of S under the relation \leq .

2.5.3 Wheeler Graphs

With the concept of co-lex order established, we can now define the class of graphs that form the starting point of our work. A Wheeler automaton is an automaton where the states can be arranged in a strict, total order.

Definition 16 (Wheeler automaton, [14]). *A finite state automaton $\mathcal{A} = (Q, \Sigma, \delta, q_0, F)$ is a Wheeler automaton if there exists a total order \leq on its set of states Q that satisfies the following axioms:*

1. *The initial state precede all other states in the order.*

For any two transitions $u \in \delta(u', a)$ and $v \in \delta(v', b)$:

2. $a < b \implies u \leq v$,
3. $a = b \wedge u' < v' \implies u \leq v$.

The order \leq is called a Wheeler order.

Consequently, we define the concept of Wheeler language.

Definition 17 (Wheeler language). *A Wheeler language L is a language accepted by a deterministic Wheeler automaton.*

The most important example for Wheeler automaton in this thesis is the trie. Any trie representing a finite language is a Wheeler automaton. The co-lexicographic order of the strings spelling the paths from the root to each node provides the required total ordering of the states. This is why tries are inherently indexable. However, this rigid structure also means they are uncompressed, as every unique path must be stored explicitly, even if it corresponds to a substring that appears many times in the language. Our work begins with this observation: we start with a Wheeler automaton (the trie) and seek to compress it while preserving efficient indexability.

2.5.4 The Co-lex Width of an Automaton

Now that we have the concepts of co-lex order and width, we can combine them to formally define the class of indexable automata that are central to this thesis. The

width of the co-lex partial order on an automaton's states is the critical measure of its structural complexity from an indexing perspective.

The co-lex order can be extended to the set of states of an automaton. The idea of co-lex order on the states of an automaton was first introduced with the notion of Wheeler graphs by Gagie et al. [14] and was later generalized to arbitrary finite automata by Cotumaccio and Prezsa [6], where a partial order replaces the total order. The definition of co-lex order on an automaton is as follows:

Definition 18 ([7]). *Let $N = (Q, \Sigma, \delta, q_0, F)$ be an NFA. A co-lex order on N is a partial order \leq on Q that satisfies the following two axioms:*

1. *For every $u, v \in Q$, if $u < v$, then $\max \lambda(u) < \min \lambda(v)$*
2. *For every $a \in \Sigma$ and $u, v, u', v' \in Q$, if $u \in \delta(u', a)$, $v \in \delta(v', a)$ and $u < v$, then $u' \leq v'$*

Where $\lambda(q)$ denotes the set of labels of transitions entering state q , and $\min \lambda(q)$ and $\max \lambda(q)$ represent the minimum and maximum element of the set, respectively.

The two axioms in Definition 18 allow for pair of states of a finite automaton to be compared. When \leq is total, we say that the co-lex order is a Wheeler order (introduced in [14]).

Consequently, we can introduce the concept of co-lex width of an automaton.

Definition 19 ([7]). *The co-lex width of an NFA N is the minimum width of a co-lex order on N .*

$$\text{width}(N) = \min\{\text{width}(\leq) \mid \leq \text{ is a co-lex order on } N\}$$

The requirement of a Wheeler order is powerful but restrictive. Many automata, especially those resulting from DAG compression, may not satisfy it (see Figure 2.4). This introduces a fundamental trade-off: while DAG compression minimizes an automaton's size, it can destroy the very structure that enables efficient indexing. In fact, it has been shown that indexing general graphs—and thus, highly compressed automata—to support fast string matching is computationally expensive, as showed in [11]. The second axiom of Definition 18 does not always enforce an ordering between any two states, leading to a partial order instead of a total one. This gives rise to the more general notion of a *p-sortable automaton*, where p is the co-lex width of the automaton. Under this definitions, a Wheeler automaton is a **1-sortable automaton**, as a total order has a width of 1 (the largest antichain is a single element).

Example 2.14:

State incomparability can arise in several situations. For example, consider two states u and u' .

- As illustrated in Figure 2.4-(a), if we have two same-labeled transitions where the target states u and v are incomparable, we have no information about the relative order of the source states u' and v' . Consequently, they may also be incomparable.

- Conflicting constraints from different labels can force incomparability, as shown in Figure 2.4-(b). An existing order on the sources of a -transitions (e.g., $u' < v'$) may require $u < v$ to satisfy the Wheeler axioms, while an order on the sources of b -transitions (e.g., $v'' < u''$) may require the opposite, $v < u$. Since both cannot be true, the targets u and v must be incomparable.

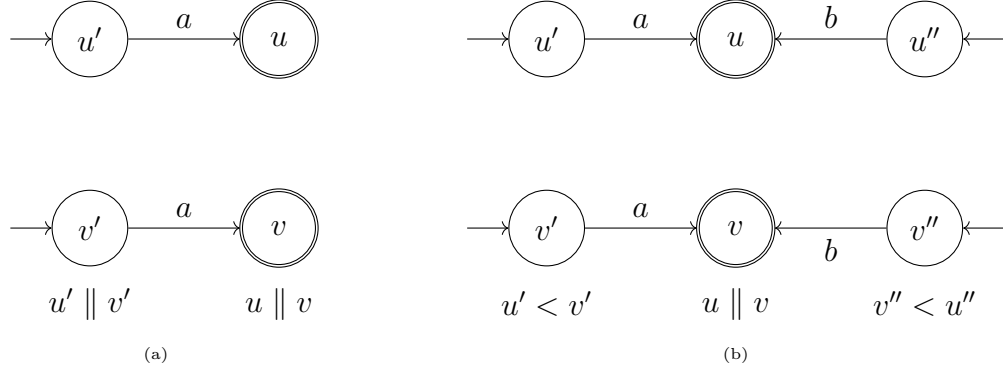


Figure 2.4: Examples of state incomparability in automata.

We now introduce another important result by [28]. Let $D = (Q, \Sigma, \delta, s, F)$ denote the minimal DFA accepting a Wheeler language L , and let $D^w = (Q^w, \Sigma, \delta^w, s^w, F^w)$ denote the minimal Wheeler DFA (WDFA) accepting L . Since D^w is Wheeler, the rational embedding ensures that for any two distinct states $q, q' \in Q^w$, the associated intervals $I_q, I_{q'}$ are disjoint. This property does not generally hold for D , where states may correspond to overlapping sets of prefixes. As a consequence, when transforming D into D^w , certain states of D may need to be *split* into several states in D^w , potentially leading to an exponential blow-up in the number of states.

Example 2.15: [28]

We now provide a simple example of an automaton D with width n that accepts a Wheeler language, yet its minimum equivalent Wheeler DFA, D^w , is exponentially larger. Let D be the automaton depicted in Figure 2.5.

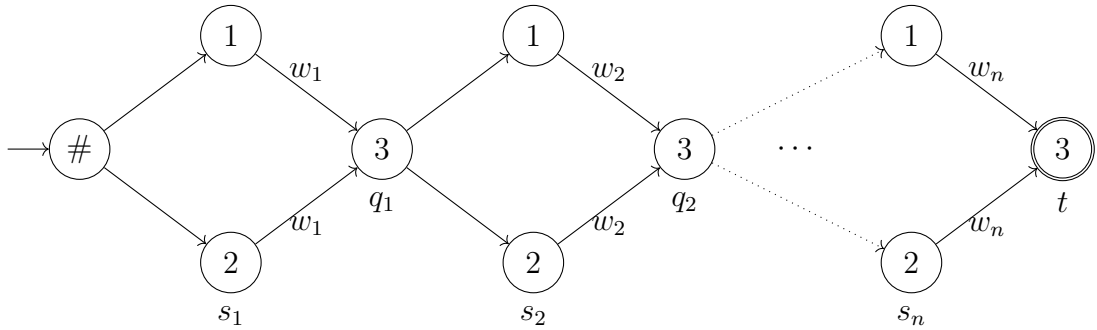


Figure 2.5: A DFA accepting a finite (and thus Wheeler) language, for which the minimal equivalent Wheeler DFA is exponentially larger.

The language $L = \mathcal{L}(D)$, being finite, is a Wheeler language [1]. However, any Wheeler automaton accepting L must have a number of states that is exponential in n . This is because for any pair of distinct strings $\alpha, \gamma \in I_t$, it is possible to find another string β such that the co-lexicographic order of the strings is $\alpha < \beta < \gamma$. Since there are exponentially many such pairwise distinct strings

leading to t , a Wheeler automaton must partition the set I_t into an exponential number of sub-intervals. This forces the state t to be "split" into exponentially many copies, leading to an exponential blow-up in the size of the minimal Wheeler DFA, D_w .

The previous example highlights a crucial trade-off: enforcing the strict ordering of a Wheeler DFA can lead to an exponential increase in the number of states compared to a minimal DFA. Theorem 4 formalizes this observation, showing that the size of a minimal Wheeler DFA cannot be bounded by a polynomial in the size of the minimal DFA.

Theorem 4 ([28], Theorem 29). *Let $L = \mathcal{L}(D) = \mathcal{L}(D^w)$, where L is Wheeler, D is minimal, D^w is minimal Wheeler, and let $f(\cdot, \cdot)$ be such that $|D^w| = \mathcal{O}(f(|D|, \text{width}(D)))$. Then, for any $k, p \in \mathbb{N}$, $f(n, p) \notin \mathcal{O}(n^k + 2^p)$.*

Since this work aims to transform a trie (a 1-sortable automaton) into a more general p -sortable graph (with width $p > 1$) in a way that introduces DAG compression while maintaining efficient indexability, it is motivated by the powerful result of Theorem 4 leading to the fact that even a small increase in sortability (for example from $p = 1$ to $p = 2$) can yield exponential compression. This highlights the potential of exploring the trade-off between sortability and size, which is the central theme of this thesis.

2.5.5 Indexing Finite State Automata

Now we introduce the current state of the art in indexing finite state automata. In 2023, Cotumaccio et al. [6] introduced a compressed data structure for automata whose performance and space complexity are directly tied to the automaton's colex width, p . This structure generalizes the famous Burros-Wheeler transform [4] and supports subpath queries (Definition 6) on a query word α of length m in $\mathcal{O}(mp^2 \log(p|\Sigma|))$ time. The space required is $\log(|\Sigma|) + \log p + 2$ bits per edge for DFAs and $\log(|\Sigma|) + 2 \log p + 2$ bits per edge for NFAs. This highlights a direct trade-off: both query time and space per edge depend on the width parameter p , which governs the automaton's compressibility.

To highlight the importance of this data structure, we recall that the final output of our compression pipeline is a p -sortable DAG compressed automaton with a controlled colex width p . This allows us to leverage these advanced indexing capabilities on the compressed automata produced by our method.

2.6 Min-Weight Perfect Bipartite Matching

2.6.1 Introduction and Motivation

The fundamental goal of our compression scheme is to transform an input trie into a compressed, p -sortable automaton by partitioning its nodes into p chains in an optimal way.

To make this optimization problem more concrete, we can frame it as a string partitioning problem. Imagine the sequence of nodes in the trie, when read in co-

lexicographic order, as a single long string. The "character" corresponding to each node is its Myhill-Nerode equivalence class, which determines if it can be merged with other nodes. The task is to partition this string of nodes into p subsequences such that the number of runs is minimized (consecutive nodes of the same equivalence class are merged into a single "run"). For instance, a subsequence $AAABBA$ contains three runs. Minimizing the number of runs directly corresponds to maximizing the number of merged states, yielding a compact p -sortable automaton.

In this sections, we will provide the necessary background on the Minimum Weight Perfect Bipartite Matching (MWPBM) problem, a fundamental challenge in combinatorial optimization. We will then demonstrate in Section 2.3 a formal reduction from our partitioning problem to MWPBM. This reduction is the key to our method, as it allows us to model our problem as a bipartite graph and leverage well-known, efficient algorithms to find the optimal solution for our compression task.

2.6.2 Bipartite Graphs

Definition 20. A graph $G = (V, E)$ is called *bipartite* if its vertex set V can be partitioned into two disjoint subsets $V = V_1 \cup V_2$ such that every edge in E has the form (v_1, v_2) where $v_1 \in V_1$ and $v_2 \in V_2$.

In other words, the vertices of the graph can be divided into two separate groups such that all edges connect a vertex from the first group to a vertex from the second group. An example of a bipartite graph is shown in Figure 2.6.

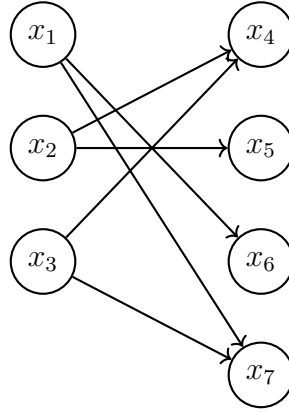


Figure 2.6: Example of a bipartite graph $G = (V, E)$ where $V_1 = \{x_1, x_2, x_3\}$, $V_2 = \{x_4, x_5, x_6, x_7\}$ and $E = \{(x_1, x_4), (x_1, x_5), (x_1, x_7), (x_2, x_4), (x_2, x_5), (x_2, x_6), (x_3, x_4), (x_3, x_7)\}$.

Definition 21 (Weighted Bipartite Graph). A *weighted bipartite graph* is a bipartite graph $G = (V, E, w)$, where w is a weight function $w : E \rightarrow \mathbb{R}$ that assigns a real-valued weight to each edge.

In a weighted bipartite graph, each edge has a numerical value, or "weight", associated with it.

2.6.3 Problem Definition

Given a bipartite graph $G = (V, E)$ (Definition 20), let's define the concept of a matching.

Definition 22 (Matching). *Given a bipartite graph $G = (V, E)$, a matching $M \subseteq E$ is a collection of edges such that every vertex of V is incident to at most one edge of M .*

In other words, a matching is a set of edges such that no two edges share a common vertex. If a vertex v has no edge of M incident to it, then v is said to be exposed (or unmatched). A matching is **perfect** if no vertex is exposed; in other words, a matching is perfect if its cardinality is equal to $|V_1| = |V_2|$ [16].

Example 2.16:

In Figure 2.7, we illustrate three distinct scenarios. Subfigure (a) depicts a set of edges that does not constitute a valid matching, as vertex u_1 is incident to more than one edge, namely (u_1, v_1) and (u_1, v_2) , violating the definition of a matching. Subfigure (b) presents a valid, yet non-perfect matching; here, vertices u_3 and v_3 are exposed, meaning they are not incident to any edge in the matching. Finally, subfigure (c) shows a perfect matching, where every vertex in the graph is incident to exactly one edge in the matching, satisfying the condition $|M| = |V_1| = |V_2| = 3$.

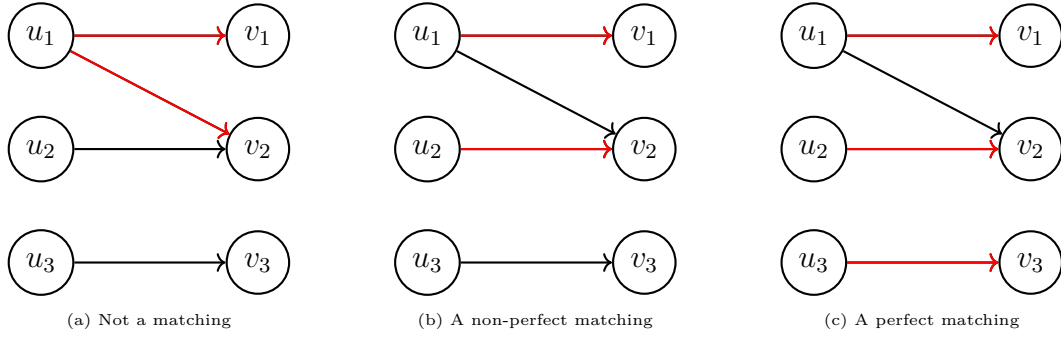


Figure 2.7: Examples of a non-matching (a), a non-perfect matching (b), and a perfect matching (c) in a bipartite graph. The edges in the set M are highlighted in red.

The problem of finding a minimum weight perfect matching in a weighted bipartite graph (Definition 21) is a well-known problem in combinatorial optimization. The problem can be formulated as follows:

Definition 23 (Minimum weight perfect matching in weighted bipartite graphs). *Given a weighted bipartite graph $G = (V, E, w)$, find a perfect matching M such that the sum of the weights of the edges in M is minimized.*

The weight of a matching is the sum of the weights of the edges in the matching. We define the weight of a matching M as follows:

$$w(M) = \sum_{e \in M} w(e) \quad (2.1)$$

Example 2.17:

Consider the weighted bipartite graph in Figure 2.8. The goal is to find a perfect matching with the minimum possible total weight. Both subfigures show a valid perfect matching; however, only one of them has the minimum weight.

- Subfigure (a) shows the perfect matching $M_a = \{(u_1, v_2), (u_2, v_1), (u_3, v_3)\}$. Its total weight is $w(M_a) = w(u_1, v_2) + w(u_2, v_1) + w(u_3, v_3) = 2 + 1 + 1 = 4$.

This is a valid perfect matching, but it is not optimal.

- Subfigure (b) shows the perfect matching $M_b = \{(u_1, v_1), (u_2, v_2), (u_3, v_3)\}$. Its total weight is $w(M_b) = w(u_1, v_1) + w(u_2, v_2) + w(u_3, v_3) = 1 + 1 + 1 = 3$.

Since $w(M_b) < w(M_a)$, the matching in (b) is a minimum weight perfect matching for this graph, while the matching in (a) is a non-minimum perfect matching.

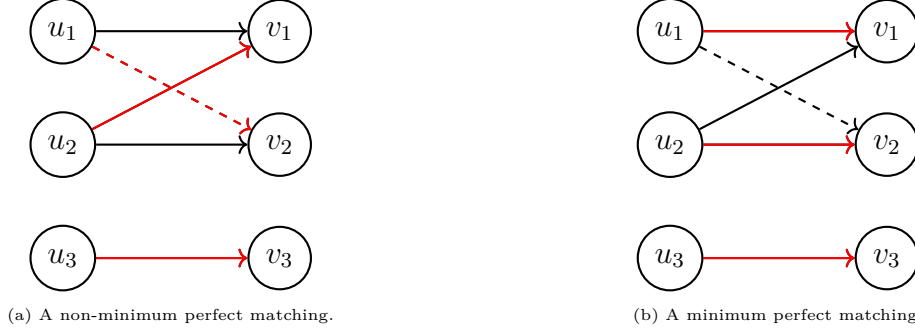


Figure 2.8: Example of a non-minimum perfect matching (a) and minimum perfect matching (b) in a weighted bipartite graph. Dashed edges have weight 2 while solid edges 1. The edges in a matching are highlighted in red.

2.6.4 Hall's Marriage Theorem

Hall's Marriage Theorem [19] provides a necessary and sufficient condition for the existence of a matching that saturates an entire partition of a bipartite graph. Specifically, it allows us to determine if there is a matching that includes every vertex from the smaller of the two partitions. In the special case where the partitions are of equal size ($|V_1| = |V_2|$), the theorem gives a condition for the existence of a perfect matching.

This theorem will be crucial in proving the correctness of our reduction from the tree partitioning problem to the minimum weight perfect bipartite matching problem. Specifically, we will use it to show that our constructed bipartite graph always admits a perfect matching, ensuring that our reduction is valid.

Definition 24 (Neighborhood). *For a subset of vertices $W \subseteq V_1$, the **neighborhood** of W , denoted by $N(W)$, is the subset of all vertices in V_2 that are adjacent to at least one vertex in W .*

$$N(W) = \{v \in V_2 \mid \exists u \in W \wedge \{u, v\} \in E\}$$

Theorem 5 (Hall's Marriage Theorem [19]). *Let $G = (V, E)$ be a bipartite graph with $V = V_1 \cup V_2$. There exists a matching M in G that covers V_1 if and only if for every subset $W \subseteq V_1$, the following condition holds:*

$$|N(W)| \geq |W|$$

This condition is known as **Hall's condition**.

In simpler terms, a matching that covers all vertices in V_1 exists if and only if every group of vertices chosen from V_1 collectively has at least as many neighbors in V_2 as there are vertices in the chosen group.

2.6.5 State of the Art

There are several algorithms to solve the problem of finding a minimum weight perfect matching in a bipartite graph. The first algorithm to solve this problem was proposed by Kuhn in 1955 [24]. The algorithm is based on the Hungarian method, which is a combinatorial optimization algorithm that solves the MWPBM problem in polynomial time. In the original paper the complexity of the algorithm was $O(n^4)$ where n is the number of nodes in the bipartite graph. Later Dinic and Kronrod [9] showed that the algorithm can be implemented in $O(n^3)$ time.

The Hungarian method is a powerful algorithm; however, it is not very intuitive and can be difficult to implement. In recent years, several other algorithms have been proposed to solve this problem. In 1970, Edmonds and Karp [10] proposed an algorithm that solves the problem in $O(nm + n^2 \log n)$ time, where m is the number of edges. In 1989 Gabow and Tarjan [13] proposed an algorithm that solves the problem in $O(\sqrt{nm} \log(nW))$ time, where W denote the highest edge weight in the graph; costs are assumed to be integral. The algorithms work by scaling. Lastly, in 2009, Sankowski and Piotr [34] introduced a randomized algorithm that solves the problem in $O(Wn^\omega)$ time, where ω is the exponent of matrix multiplication, and W is the highest edge weight in the graph.

In 2022, Chen, Li, et al. [5] proposed a nearly linear time algorithm for the Minimum Cost Flow (MCF) problem, running in $O(m'^{1+o(1)})$ on a network with m' edges. This is highly relevant as the MWPBM problem can be reduced to MCF. Specifically, an MWPBM instance on a bipartite graph with n vertices and m edges can be transformed into an MCF problem on a network with $m' = n^2 + m$ edges.

Davide T.: [move in experiment chapter \(?\)](#)

2.6.6 Implementation

To run experiments for the proposed compression scheme, we used a C++ implementation by Vladimir Kolmogorov of the minimum cost perfect matching algorithm described in [22]. The implementation is available at <https://pub.ista.ac.at/~vnk/software.html>.

In brief, paper [22] presents Blossom V, a practical implementation of Edmonds' blossom algorithm for computing minimum-cost perfect matchings in undirected weighted graphs. While the theoretical worst-case bounds for the blossom family have steadily improved since Edmonds' original $O(n^2m)$ algorithm, Blossom V is designed for strong empirical performance rather than new asymptotic guarantees. It combines two ingredients that had proven effective separately in prior work: the variable δ (variable dual updates) strategy popularized by Blossom IV, and systematic use of priority queues to efficiently select minimum-slack edges.

Blossom V targets general (not necessarily bipartite) graphs, and thus directly applies to our bipartite instances as a special case. In our experiments we use the publicly available Blossom V implementation as a black-box solver to compute minimum-cost perfect matchings for the graphs generated by our compression pipeline.

Chapter 3

Tree Compression Scheme

As introduced in the first chapter, the primary goal of this thesis is to develop a novel tree compression scheme that effectively leverages repetitive structures within the input tree. The proposed algorithm is designed to identify and compactly represent these recurring patterns, thereby improving compression performance, particularly for highly repetitive trees. This chapter provides an overview of the proposed compression scheme.

3.1 Compression Scheme Pipeline

Let T be an ordered tree of arbitrary fan-out, depth, and shape. T consists of n internal nodes and ℓ leaves, for a total of $t = n + \ell$ nodes. Every node of T is labeled with a symbol drawn from an alphabet Σ . We assume that Σ is the set of labels effectively used in the nodes of T and that these labels are encoded with the integers in the range $[1, |\Sigma|]$. Then we define the array π where, for each node u , $\pi(u)$ is the string obtained by concatenating the labels on the **upward path** from the parent of u to the root of the tree (the root has an empty π component).

The following pipeline is used to compress the tree T :

1. Initially, the array π is computed for T by traversing the tree in a pre-order fashion. Then the nodes are stably sorted by the lexicographic order of their π strings. To sort the nodes, the **Path Sort** algorithm introduced in Subsection 2.3.4 is used, enabling the sorting of nodes in linear time and $O(t \log t)$ space. This sorting step is the one used in the XBWT transform described in Section 2.3.
2. Then, using the Algorithm 8 for minimizing acyclic deterministic finite automata (ADFAs) described in Subsection 2.4.3, the nodes are partitioned into equivalence classes where two nodes are equivalent if they have the same subtree rooted at them.
3. Given a width p , the previously sorted nodes are then divided into p chains to minimize the run-length encoding of each chain (considering the equivalence classes). To do so, we reduce this problem — which we call *Chains-Division problem* — to the Minimum Perfect Bipartite Matching problem (see Section 2.6), which can be solved in polynomial time as described in Section Subsection 2.6.5.
4. Lastly, by collapsing consecutive equivalent nodes within each chain (i.e., nodes in the same equivalence class that appear one after the other in the chain), we create either a DFA or an NFA (for further details, see Section 3.3). This automaton can then be indexed using the scheme from [7] (refer to ??).

3.2 Reducing the Chains-Division Problem to the Assignment Problem

In this section, we will show how we can reduce the problem of finding the optimal partition of the nodes of a labeled tree T given their equivalence classes into p chains to the Minimum Weight Perfect Bipartite Matching problem (see Definition 23). We define \mathcal{C} as the set of equivalence classes of the nodes of T , and t as the number of nodes of the tree. This reduction will allow us to solve the problem in polynomial time, as shown in the previous chapter.

In particular, we show that, given a tree T and the number of chains p , we can construct a bipartite graph $G = (V, E)$ in which a perfect matching (Definition 22) always exists. In turn, a perfect matching with minimum weight enables us to retrieve the optimal partition of the nodes in T into p chains, such that the run-length encoding of each chain is minimized.

Then, we will show how to optimize the reduction by introducing some constraints that will allow us to reduce the number of edges in the bipartite graph, and we will also show how to move from the Minimum Weight Perfect Bipartite Matching problem to the more studied Maximum Weight Perfect Bipartite Matching problem without losing generality.

3.2.1 Chains-Division Problem Definition

It is essential to begin by defining the problem we aim to solve.

Definition 25 (Chains-Division Problem). *Given a labeled tree T , the equivalence classes \mathcal{C} , the stable order of the nodes in T according to the upward path π as defined in Definition 8, and an integer parameter $p \in [2, t]$, find the optimal partition of the nodes of T into p chains such that the run-length encoding of each chain is minimized.*

Let's give a formal definition of run-length encoding.

Definition 26 (Run length encoding). *Given a sequence $S = \{s_1, s_2, \dots, s_n\}$, the run length encoding of S is the sequence $R = \{r_1, r_2, \dots, r_m\}$ where r_i is the number of times the element s_i is repeated in S .*

It allows us to represent the sequence S in a more compact way.

Example 3.1:

Let $S = \{A, A, B, B, B, C, C, A, A\}$. The run-length encoding of S would be $R = \{(A, 2), (B, 3), (C, 2), (A, 2)\}$. The length of the RLE, which is the value we want to minimize, is $|R| = 4$.

So, we aim to divide the nodes of the tree into p chains such that the run-length encoding of the chains is minimized, meaning we want to reduce the number of distinct equivalence classes in each chain. Follows the definition of a chain.

Definition 27 (Chains). *Given a tree T , a chain C is a sequence of nodes $C = \{c_1, c_2, \dots, c_m\}$ such that $C \subseteq V$. Additionally, each node of T belongs to exactly*

one chain, and the nodes in the chain are ordered according to the upward path π (as defined in Definition 8) of each node c_i .

Note that, following the XBWT definition, two sibling nodes are comparable. The stable sorting algorithm respects the original sibling order, meaning the node that appears first among its siblings in a pre-order traversal will also come first in the sorted sequence.

Example 3.2: Chains-Division Problem

Consider a tree T with 7 nodes having the following equivalence classes: $E = \{A, B, A, C, A, B, B\}$, where the nodes are ordered according to their upward paths. Let's say we want to divide these nodes into $p = 2$ chains.

Non-optimal division: If we divide the nodes into chains $C_1 = \{A, B, A, C\}$ and $C_2 = \{A, B, B\}$, the run-length encoding would be:

- C_1 : $(A, 1), (B, 1), (A, 1), (C, 1)$ - requiring 4 pairs
- C_2 : $(A, 1), (B, 2)$ - requiring 2 pairs

Total RLE cost: $4 + 2 = 6$

Optimal division: A better division would be $C_1 = \{A, A, A\}$ and $C_2 = \{B, C, B, B\}$, with run-length encoding:

- C_1 : $(A, 3)$ - requiring 1 pair
- C_2 : $(B, 1), (C, 1), (B, 2)$ - requiring 3 pairs

Total RLE cost: $1 + 3 = 4$

This example demonstrates how grouping nodes of the same equivalence class in chains minimizes the total run-length encoding cost. The optimal solution can be found by reducing this problem to the MWPBM problem as described in this chapter.

3.2.2 Bipartite Graph Construction

Now, we will show how to construct a bipartite graph that allows us to solve the CHAINS-DIVISION problem.

Definition 28 (Bipartite graph construction). *Let T be a tree with t nodes, and p the number of chains we want to partition the nodes into. Let \mathcal{C} be the set of equivalence classes of the nodes of T . We can construct a bipartite graph $G = (V, E)$ such that vertices are divided in two disjoint sets $V = V_1 \cup V_2$ in the following way:*

- V_1 contains $t + p$ nodes composed by p source nodes s_1, s_2, \dots, s_p (referred to collectively as \mathcal{S}) followed by the t elements (referred to collectively as \mathcal{T}_1) of \mathcal{C} . The nodes in V_1 follow a strict ordering $s_1 \prec s_2 \prec \dots \prec s_p \prec u_1 \prec u_2 \prec \dots \prec u_t$, where u_i are the tree nodes ordered according to the upward path π as defined in Definition 8.
- V_2 contains $t + p$ nodes composed by the t elements (referred to collectively as \mathcal{T}_2) of \mathcal{C} followed by p destination nodes d_1, d_2, \dots, d_p (referred to collectively as \mathcal{D}). The nodes in V_2 follow a strict ordering $v_1 \prec v_2 \prec \dots \prec v_t \prec d_1 \prec d_2 \prec$

$\dots \prec d_p$, where v_i are the tree nodes ordered according to the upward path π as defined in Definition 8.

Then the edges of the graph G are constructed in the following way:

1. The \mathcal{S} nodes are connected to the first p nodes with distinct equivalence class in V_2 , with weight 1.
2. Let $u_i \in \mathcal{T}_1$. We define $\mathcal{C}(u_i)$ as the equivalence class of node u_i . For each node u_i , we construct the following edges:
 - For the first p nodes $v_j \in \mathcal{T}_2$ such that $j > i$ and $\mathcal{C}(v_j) \neq \mathcal{C}(u_i)$, we add an edge (u_i, v_j) with weight 1. If there are fewer than p nodes in V_2 with distinct equivalence classes, we stop earlier.
 - Let $v_k \in \mathcal{T}_2$ be the first node in the ordering such that $k > i$ and $\mathcal{C}(v_k) = \mathcal{C}(u_i)$, we add an edge (u_i, v_k) with weight 0. If such a node does not exist, we add p edges (u_i, d_j) with weight 0 for each $j = 1, 2, \dots, p$, where $d_j \in \mathcal{D}$.

Notice that it is important to consider the order of the nodes of the two sets V_1 and V_2 as stated in the definition, because we will need to connect the source nodes to the destination nodes in a way that will allow us to find the optimal partition of the nodes of the tree. An example of the node structure is shown in Example 3.3.

Notice also that when we talk about the same \mathcal{T}_1 node placed in \mathcal{T}_2 , we are referring to the corresponding node in \mathcal{T}_2 that derives from the same node in the original tree T since the nodes of the tree are ordered in both sets \mathcal{T}_1 and \mathcal{T}_2 . In Figures 3.2, 3.3 and 3.5, the node's correspondence is achieved by putting the two nodes at the same level.

Example 3.3: Vertices

This example illustrates the structure of the bipartite graph vertices for the tree shown in Figure 3.1. The nodes in this tree are labeled by their equivalence classes obtained from the minimization of the corresponding ADFA in Example 2.13. Our goal is to partition the tree's nodes into $p = 2$ chains. The nodes of the tree are ordered according to Algorithm 1, the order is the following:

$$a \prec b \prec d \prec h \prec l \prec f \prec c \prec e \prec i \prec m \prec g$$

Figure 3.2 shows the corresponding bipartite graph. The graph is composed of:

- Two source nodes (s_1, s_2) on the left, representing the start of each chain.
- Two sets of tree nodes, representing the partitions \mathcal{T}_1 (left column) and \mathcal{T}_2 (right column). These nodes are ordered based on the `pathSort` algorithm. The labels (A, B, C, D) correspond to the equivalence classes from the original tree.
- Two destination nodes (d_1, d_2) on the right, representing the end of each chain.

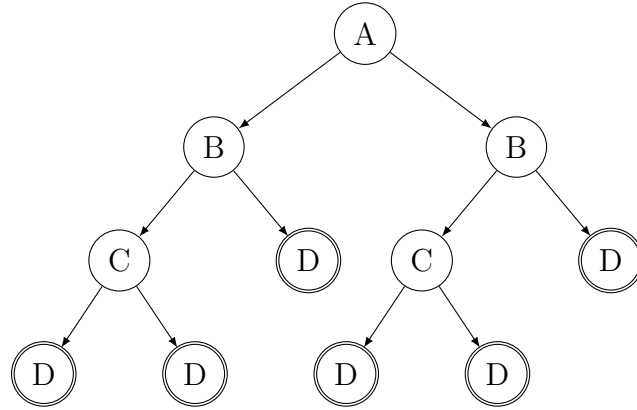


Figure 3.1: Tree ADFA of Figure 2.2. Each node is labeled with its equivalence class.

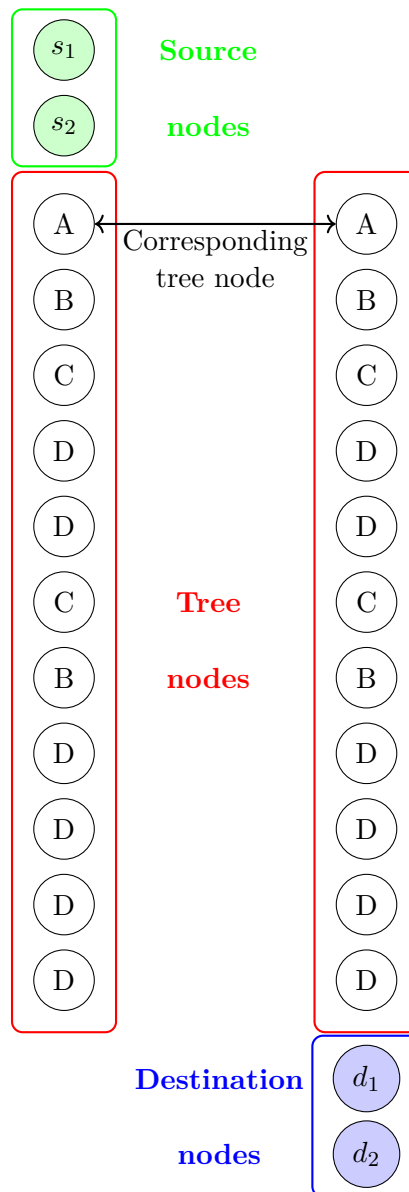


Figure 3.2: Corresponding bipartite graph structure for the tree in Figure 3.1 with $p = 2$. The nodes are ordered from top to bottom using Algorithm 1.

Example 3.4: Edges

Let's see a small example for each case. Consider $p = 2$. In Figure 3.3-(a), there is an example for the sources' edges. As stated before, for each source, p edges with weight 1 are created and connected to the first p nodes with distinct equivalence classes in \mathcal{T}_2 .

In Figure 3.3-(b), there is an example for the tree nodes' edges. For each node in \mathcal{T}_1 , edges with weight 1 are created and connected to the first p nodes with distinct equivalence class in \mathcal{T}_2 after the corresponding node in \mathcal{T}_2 (coming after the node itself in the ordering), and edges with weight 0 are created and connected to the first node with the same class in \mathcal{T}_2 after the corresponding node in \mathcal{T}_2 . As we can see from the image, we consider the first node in \mathcal{T}_1 labelled A that is connected to the node labelled B with weight 1, and to the node labelled C with weight 1, and to the second node labelled A in \mathcal{T}_2 with weight 0.

Lastly, in Figure 3.3-(c) there is an example for the destination nodes' edges. We start by considering the node in \mathcal{T}_1 that is labeled A , which is connected to a node labeled B with weight 1. Then, since there is no node with the same class in \mathcal{T}_2 , we connect it to the destination nodes d_1 and d_2 with weight 0. The same is done for the second node in \mathcal{T}_1 that is labelled B since no nodes are coming after it in the order; it is connected to the destination nodes d_1 and d_2 with weight 0.

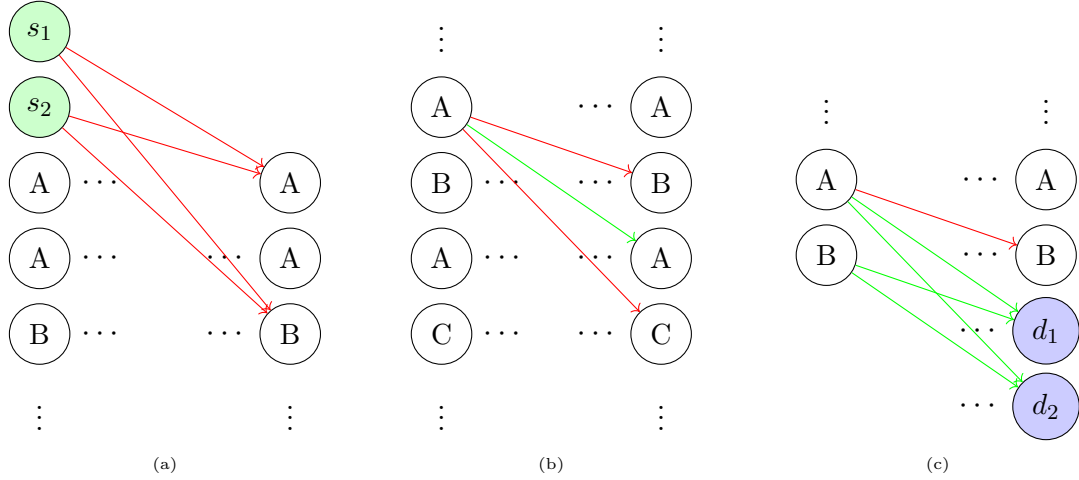


Figure 3.3: Examples of the connection construction in the bipartite graph for $p = 2$, showing the cases for source nodes \mathcal{S} (a), internal tree nodes \mathcal{T}_1 and \mathcal{T}_2 (b), and destination nodes \mathcal{D} (c). Red arrows indicate edges with weight 1, while green arrows indicate edges with weight 0.

Before we present the proof of the correctness of the reduction, let us state the following theorem regarding the number of edges in the bipartite graph resulting from Definition 28. This theorem is essential for understanding the complexity of the final algorithm employed to solve the MWPBM problem and so, the CHAIN-DIVISION problem.

Theorem 6 (Bipartite graph properties). *The bipartite graph G constructed as stated in Definition 28 has $2t + 2p$ nodes and $O(t(p + 1) + p^2 + tp)$ edges.*

Proof. The $O(t(p + 1))$ edges come from the tree nodes, the $O(p^2)$ edges come from the sources since each source node is connected to p nodes, and the $O(tp)$ edges come from the destination nodes since in the worst case we have t distinct equivalence

classes, which means that all the nodes are connected to the destination nodes. \square

3.2.3 Proof of Correctness

In this section, we present the proof of the correctness of the reduction introduced in the previous sections. Let us start by stating the following lemmas.

Lemma 2. *The optimal solution of an instance \mathcal{I} of the CHAINS-DIVISION problem for a tree T is always greater than or equal to $|\mathcal{C}|$.*

Proof. To minimize the run-length encoding of the chains, we note that the minimum cost of a chain is 1. Consequently, the optimal cost of the CHAINS-DIVISION problem for the tree T is always greater than or equal to the cardinality of the set of equivalence classes \mathcal{C} . This is because if we partition them into $p = |\mathcal{C}|$ chains, the cost will be equal to $|\mathcal{C}|$, since each chain contains only nodes belonging to the same class. Conversely, if we partition them into $p < |\mathcal{C}|$ chains, the cost will be greater than or equal to $|\mathcal{C}|$ since we will need to include at least two nodes from different classes within a single chain. \square

Claim 1. *The solutions for the CHAINS-DIVISION problem for the instances where the number p of chains is greater than $|\mathcal{C}|$ are not better than the solutions for the instances where $p \leq |\mathcal{C}|$.*

Proof. The proof builds upon Lemma 2. If we use a number of chains $p > |\mathcal{C}|$, we would have at least $p - |\mathcal{C}|$ empty chains, since there are only $|\mathcal{C}|$ non-empty equivalence classes of nodes to partition. As the minimum cost for any chain is 1, these empty chains contribute to the total cost. An optimal arrangement would involve $|\mathcal{C}|$ chains, each containing nodes from a single equivalence class, costing $|\mathcal{C}|$, and $p - |\mathcal{C}|$ empty chains, each costing 1. The total cost would be $|\mathcal{C}| + (p - |\mathcal{C}|) = p$. Since $p > |\mathcal{C}|$, this cost is greater than the optimal cost of $|\mathcal{C}|$ achievable with $p = |\mathcal{C}|$ chains. Therefore, any solution with $p > |\mathcal{C}|$ is suboptimal. \square

Therefore, for the proof of the reduction, we will only consider instances of the problem where $p \leq |\mathcal{C}|$, as they do not present a trivial solution.

Lemma 3. *Given a bipartite graph G constructed as stated in Definition 28, for each node $u_i \in \mathcal{T}_1$ it is impossible for u_i to be connected to a node $v_j \in \mathcal{T}_2$, such that $j \leq i$ in the order of the nodes.*

Proof. The proof comes from the construction of G (Definition 28) where the nodes of \mathcal{T}_1 are always connected to the nodes of \mathcal{T}_2 coming after them. \square

Lemma 4. *In the bipartite graph G constructed as per Definition 28, for every node $u \in \mathcal{T}_1$ $|N(\{u\})| \geq 1$.*

In other words, every node in V_1 is connected to at least another node in V_2 .

Proof. Let $u_i \in \mathcal{T}_1$ be an arbitrary node. We analyze the construction of its outgoing edges based on Definition 28. There are two mutually exclusive cases for u_i :

1. There exists at least one node $v_k \in \mathcal{T}_2$ with $k > i$ that has the same equivalence class as u_i , i.e., $\mathcal{C}(v_k) = \mathcal{C}(u_i)$. In this case, the construction specifies that an edge is added between u_i and the first such node v_k . This guarantees u_i has at least one neighbor.
2. There are no nodes $v_k \in \mathcal{T}_2$ with $k > i$ that share the same equivalence class as u_i . This occurs when u_i is the last node of its equivalence class in the specified ordering. In this scenario, the construction adds p edges from u_i to each of the destination nodes $d_j \in \mathcal{D}$. Since $p \geq 2$, u_i is connected to at least two nodes.

In either case, any node $u_i \in \mathcal{T}_1$ is guaranteed to have at least one outgoing edge. Therefore, its neighborhood is non-empty. \square

Lemma 5. *For any pair of distinct nodes $u_i, u_j \in \mathcal{T}_1$ such that $u_i \prec u_j$, $N(\{u_i\}) \not\subseteq N(\{u_j\})$.*

Proof. By Lemma 4, the set of neighbors $N(\{u_i\})$ and $N(\{u_j\})$ are non-empty. We will show that $N(\{u_i\}) \not\subseteq N(\{u_j\})$.

By construction, u_i is connected to v_{i+1} . This holds whether v_{i+1} is the next node in the same equivalence class or one of the first p nodes in a different class. Thus, $v_{i+1} \in N(\{u_i\})$.

From Lemma 3, any neighbor v_k of u_j must have $k > j$. Since $i < j$, we have $i + 1 \leq j$. This means v_{i+1} cannot be a neighbor of u_j , as $i + 1 < k$. Therefore, $v_{i+1} \in N(\{u_i\})$ but $v_{i+1} \notin N(\{u_j\})$, which proves that $N(\{u_i\}) \not\subseteq N(\{u_j\})$. \square

Lemma 6. *For any pair of distinct nodes $u_i, u_j \in \mathcal{T}_1$ such that $u_i \prec u_j$ and u_j is not the last node of its equivalence class in the order, then $N(\{u_j\}) \not\subseteq N(\{u_i\})$.*

Proof. Since u_j is not the last node of its equivalence class, by construction, it must be connected to the first node $v_k \in \mathcal{T}_2$ with $k > j$ such that $\mathcal{C}(v_k) = \mathcal{C}(u_j)$. Therefore, $v_k \in N(\{u_j\})$.

By construction, it follows that u_i cannot be connected to v_k . This is due to the existence of another node $v_l \in \mathcal{T}_2$ with $i < l \leq j$, such that $\mathcal{C}(v_l) = \mathcal{C}(u_k)$. Consequently, we conclude that $v_k \notin N(\{u_i\})$, as the edges of u_i connect to nodes in \mathcal{T}_2 , all of which belong to distinct classes.

In summary, we have identified a node v_k that is included in $N(u_j)$ but excluded from $N(u_i)$, thus proving that $N(u_j) \not\subseteq N(u_i)$. \square

Lemma 7. *For every possible instance of the CHAINS-DIVISION problem, a perfect matching exists in the bipartite graph G constructed as specified in Definition 28.*

Proof. The proof comes from the construction of the bipartite graph G and from Theorem 5. We are going to prove that G satisfies Hall's condition (see Theorem 5) and so, since by construction $|V_1| = |V_2|$, a perfect matching for G exists.

To verify Hall's condition, we need to prove that for any subset $W \subseteq V_1$ we have that $|N(W)| \geq |W|$, where $N(W)$ is the neighborhood of W (Definition 24). We have the following cases:

1. $W \subseteq \mathcal{S}$: Let W be a subset of \mathcal{S} of size k . By construction, every source node $s_i \in \mathcal{S}$ is connected to the same set of p nodes in V_2 , which are the first p nodes with distinct equivalence classes in the ordering. Therefore, for any non-empty $W \subseteq \mathcal{S}$, the neighborhood $N(W)$ consists of exactly these p nodes, so $|N(W)| = p$. From Lemma 2 and Claim 1, we only consider instances where $p \leq |\mathcal{C}|$, ensuring that at least p such nodes exist. Since $|\mathcal{S}| = p$, we have $|W| = k \leq p$. Thus, $|N(W)| \geq |W|$.
2. $W \subseteq \mathcal{T}_1$: Let $W = \{u_{i_1}, \dots, u_{i_k}\} \subseteq \mathcal{T}_1$ with $i_1 < i_2 < \dots < i_k$. We analyze two subcases:

Case A: No node in W is the last of its class. By Lemmas 5 and 6, for any two nodes $u_a, u_b \in W$ with $a < b$, we have both $N(\{u_a\}) \not\subseteq N(\{u_b\})$ and $N(\{u_b\}) \not\subseteq N(\{u_a\})$. This implies that each node in W contributes at least one unique neighbor to the total neighborhood $N(W)$. Therefore, $|N(W)| \geq |W|$.

Case B: At least one node in W is the last of its class. Let $u_j \in W$ be a node that is the last of its equivalence class. By construction, u_j is connected to all p destination nodes \mathcal{D} , which means $\mathcal{D} \subseteq N(W)$ and thus $|N(W)| \geq p$.

For such a node u_j , its neighborhood $N(\{u_j\})$ may be a subset of $N(\{u_a\})$ for some $u_a \in W$ with $a < j$, since Lemma 6 does not apply. However, the reverse is not true, as Lemma 5 guarantees $N(\{u_a\}) \not\subseteq N(\{u_j\})$. This asymmetry ensures that $N(\{u_a\})$ always contributes at least one neighbor not present in $N(\{u_j\})$. This property, combined with the fact that all nodes that are last of their class are connected to the p destination nodes, is sufficient to guarantee that $|N(W)| \geq |W|$ since $p \geq 2$.

In both cases, Hall's condition $|N(W)| \geq |W|$ is satisfied for any $W \subseteq \mathcal{T}_1$.

3. $W = W_S \cup W_U$, where $W_S \subseteq \mathcal{S}, W_U \subseteq \mathcal{T}_1$: The neighborhood of W_S consists of p nodes, as established in case 1 ($W \subseteq \mathcal{S}$). By Lemma 3, the neighbors of any node in W_U appear later in the node ordering than the node itself. Since all nodes in \mathcal{T}_1 are ordered after the source nodes, the neighbors of W_U are distinct from the neighbors of W_S . Specifically, $N(W_S)$ consists of the first p nodes with distinct equivalence classes.

We now analyze two subcases for nodes in W_U : If W_U contains a node $u_i \in \mathcal{T}_1$ that is not the last of its equivalence class, then there exists at least one node $v_j \in \mathcal{T}_2$ with $i < j$ and $\mathcal{C}(u_i) = \mathcal{C}(v_j)$ that is connected to u_i but not to any source node. This contributes additional neighbors to $N(W)$, ensuring $|N(W)| \geq |W|$. If W_U contains a node $u_i \in \mathcal{T}_1$ that is the last of its equivalence class, then by construction, u_i is connected to all destination nodes. This guarantees $|N(W)| \geq |W|$.

In both subcases, Hall's condition $|N(W)| \geq |W|$ is satisfied.

□

We can now prove the correctness of the reduction. Consider a perfect matching M in G . Therefore, $|V_1| = |V_2|$ and M is perfect, every node in V_1 is matched to exactly one node in V_2 , and vice versa. The matching M consists of $t + p$ edges. Due to

the construction of G (Definition 28) and Lemma 3 (a node $u_i \in \mathcal{T}_1$ only connect to a node $v_j \in \mathcal{T}_2$ with $i < j$ or to destination nodes \mathcal{D}), the matching M naturally decomposes into p paths starting from the source nodes s_1, \dots, s_p and ending at the destination nodes d_1, \dots, d_p . Each path traverses a sequence of nodes corresponding to the nodes of the original tree T . Specifically, a path starting at s_i will match it to a node $u_a \in \mathcal{T}_2$. Then, the corresponding node of $u_a \in \mathcal{T}_1$ can be matched to a node $u_b \in \mathcal{T}_2$ (where $b > a$). This continues until a node $u_x \in \mathcal{T}_1$ is matched to a destination node $d_k \in \mathcal{D}$. This forms a sequence $s_i \rightarrow u_a \rightarrow u_b \rightarrow \dots \rightarrow u_x \rightarrow d_k$. Following this technique, we will retrieve all the optimal chains from the solution of the MWPBM problem.

Example 3.5:

Consider the example in Figure 3.4. It shows a bipartite graph constructed from a tree (not shown) and a perfect matching within it. The solid arrows represent the edges of the perfect matching, where an edge (u, v) signifies that node v follows node u in a path. The dashed arrows link the segments of the paths by connecting a node's representation in V_2 to its corresponding representation in V_1 .

The matching partitions the nodes into two distinct paths, differentiated by color:

- **Path 1 (red):** Starting from source s_1 , the matching edge (s_1, A) leads to the first node, A . The dashed arrow from this node in V_2 points to the same node A in V_1 , which is then matched with another A in V_2 . Following the next dashed arrow to the final A in V_1 , we see it is matched with the destination d_1 . This sequence traces the path $s_1 \rightarrow A \rightarrow A \rightarrow d_1$ leading to the chain $C_{red} = \{A, A\}$.
- **Path 2 (blue):** Starting from source s_2 , the matching edge (s_2, B) leads to node B . The dashed arrow connects to the next B in V_1 , which is matched with destination d_2 , tracing the path $s_2 \rightarrow B \rightarrow d_2$ leading to the chain $C_{blue} = \{B\}$.

This demonstrates how a perfect matching in the bipartite graph yields a valid partition of the original tree's nodes into paths from sources to destinations.

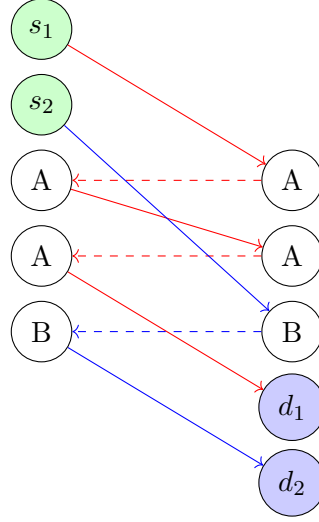


Figure 3.4: An example of a perfect matching (solid lines) in the constructed bipartite graph. The matching defines a partition into two paths (red and blue), which are traced by following the solid and dashed arrows.

Theorem 7. *An optimal solution of an instance \mathcal{I} with $p \leq |\mathcal{C}|$ of the CHAINS-DIVISION problem (Definition 25) is equivalent to an optimal solution of the MW-PBM problem (Definition 23) for the instance $r(\mathcal{I})$ where $r : \mathcal{I}_{\text{CHAINS-DIVISION}} \rightarrow \mathcal{I}_{\text{MW-PBM}}$ is the reduction function that maps an instance of the CHAINS-DIVISION problem to an instance of the MWPBM problem for a bipartite graph G constructed as stated in Definition 28.*

Proof. Let $\mathcal{I} = (T, \mathcal{C}, p)$ be an instance of the CHAINS-DIVISION problem, where T is a tree with t nodes, \mathcal{C} is the set of equivalence classes, and p is the target number of chains. We assume $p \leq |\mathcal{C}|$, per Claim 1. Let $G = r(\mathcal{I})$ be the bipartite graph constructed according to Definition 28. We will demonstrate a bijection between the set of valid chain partitions of T and the set of perfect matchings in G , such that the cost of a partition equals the weight of its corresponding matching.

First, we establish the existence of a perfect matching. By construction, the graph G is bipartite with partitions V_1 and V_2 such that $|V_1| = |V_2| = t + p$. Lemma 7 ensures that a perfect matching exists in G .

Let $\mathcal{P} = \{C_1, \dots, C_p\}$ be a valid partition of the nodes of T into p chains. We can construct a perfect matching $M_{\mathcal{P}}$ in G as follows: For each chain $C_k = [u_1, \dots, u_{m_k}]$, we construct a path in G : match s_k to $u_1 \in \mathcal{T}_2$. Then match $u_i \in \mathcal{T}_1$ to $u_{i+1} \in \mathcal{T}_2$ for $i = 1, \dots, m_k - 1$. Finally, match $u_{m_k} \in \mathcal{T}_1$ to one of the available destination nodes $d_j \in \mathcal{D}$. Since we have p chains and p source/destination nodes, and every tree node is in exactly one chain, this process uses all $t + p$ nodes in V_1 and V_2 , forming a perfect matching. The weight of this matching is given by:

$$\begin{aligned} W(M_{\mathcal{P}}) &= \sum_{(u,v) \in M_{\mathcal{P}}} w(u,v) \\ &= p + |\{(u_i, u_j) \in M_{\mathcal{P}} \mid u_i \in \mathcal{T}_1, u_j \in \mathcal{T}_2, \mathcal{C}(u_i) \neq \mathcal{C}(u_j)\}| \end{aligned}$$

where p represents the contribution from the source nodes s_i , as each source node must be connected with weight 1 to start a chain. A class change occurs exactly when a path in the matching uses a weight-1 edge between tree nodes. Therefore,

$W(M)$ is exactly equal to the RLE cost of the partition defined by the matching $M_{\mathcal{P}}$.

Conversely, let M be a perfect matching in G . The structure of G ensures that M consists of p disjoint paths starting from source nodes $\{s_1, \dots, s_p\}$ and ending at destination nodes $\{d_1, \dots, d_p\}$. Each path defines an ordered chain of nodes from T . By Lemma 3, the node order within these chains is consistent with the original node ordering π . Thus, M maps to a valid partition of T . The cost of this partition is equal to $W(M)$.

Since there is a cost-preserving bijection between the set of all valid partitions and the set of all perfect matchings, an optimal solution to one problem corresponds to an optimal solution to the other. Therefore, finding a minimum weight perfect matching in G is equivalent to solving the CHAINS-DIVISION problem for T . \square

Example 3.6:

Consider the example in Figure 3.5 where we have the bipartite graph for the tree in Figure 3.1 and $p = 2$. In Figure 3.5-(a) we have the resulting bipartite graph, and in Figure 3.5-(b) we have one of the possible minimum perfect matchings for the graph in (a) having weight 5. At the end we can see that the optimal partition of the nodes of the tree T is $C_1 = \{A, C, C, B\}$ and $C_2 = \{B, D, D, D, D, D, D\}$ with a total cost of 5, this can be obtained starting from the sources and by following the edges of the nodes, jumping to the corresponding node in V_1 and following the edges again until we reach the destination nodes.

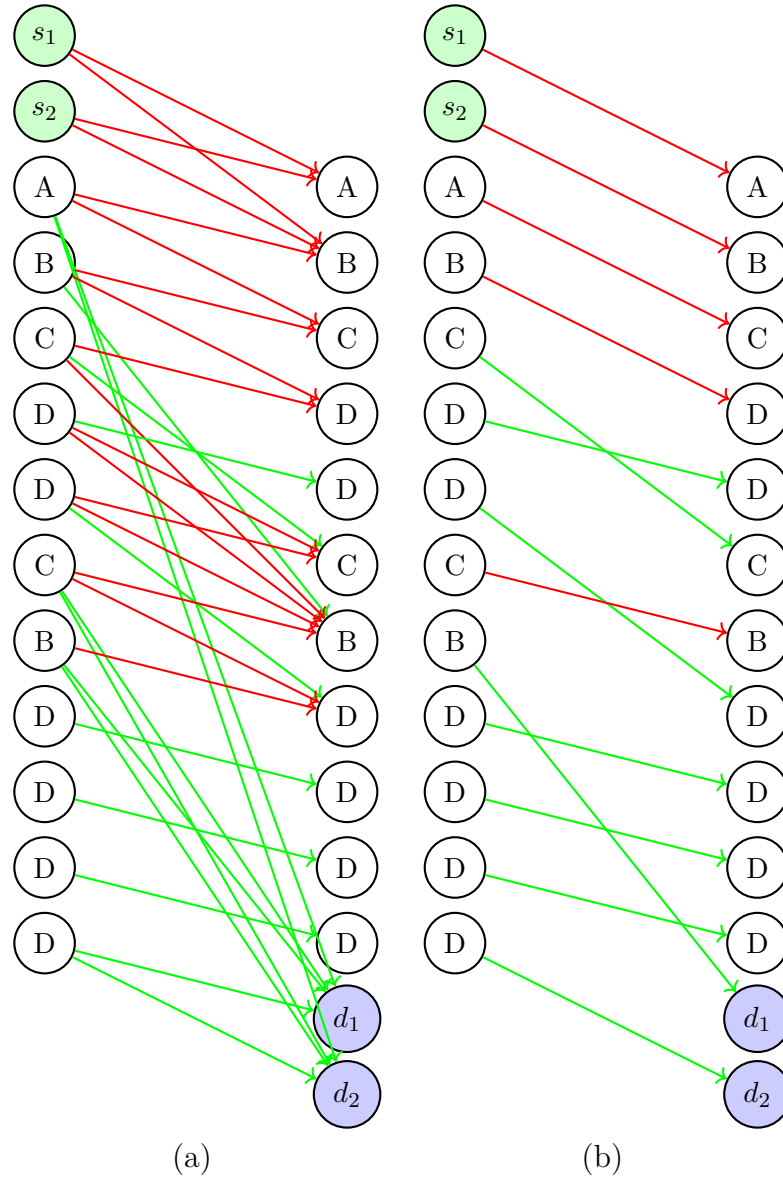


Figure 3.5: Example of a reduction for the tree in Figure 3.1. In (a), we have the resulting bipartite graph constructed. In (b), we have the resulting perfect matching for the graph in (a). Green edges weigh 0, while red edges weigh 1.

3.2.4 Heuristics and Improvements

Some changes can be made to the reduction in order to optimize it and to reduce the number of edges in the bipartite graph. Here are some of the improvements that can be made.

Lemma 8 (Sources' edges optimization). *The sources' edges can be optimized by connecting each source only to the smallest node in \mathcal{T}_2 that is not connected to any other source.*

Proof. Since the source nodes are needed to distinguish the chains as starting points, we need that each source is connected to exactly one node in \mathcal{T}_2 . Having the sources connected to the first p nodes with distinct equivalence classes in \mathcal{T}_2 is not necessary since it allows us just to invert the chains starting from each source, and so it is redundant.

Moreover, Hall's condition $|N(W)| \geq |W|$ still holds for any subset $W \subseteq V_1$ in the optimized graph. This follows trivially from the fact that case 1 of Lemma 7 still guarantees that $|N(W)| \geq |W|$ for any subset $W \subseteq \mathcal{S}$. \square

Example 3.7:

Consider the bipartite graph shown in Figure 3.6. The red solid arrows represent edges with weight 1, while the red dashed arrows represent edges that can be optimized away according to Lemma 8.

In this example, we can observe the application of the optimization rule:

- The first tree node A has a solid edge to the first destination node A and a dashed edge to destination node B to be removed. This since source s_1 is already connected to node A .
- Similarly, the second source node s_2 has a dashed edge to the first destination node A , which can be removed because s_1 is already connected to node A with the same weight.

After applying the optimization, only the solid edges remain, resulting in a reduced bipartite graph that maintains the same optimal matching cost while having fewer edges to consider during the matching algorithm.

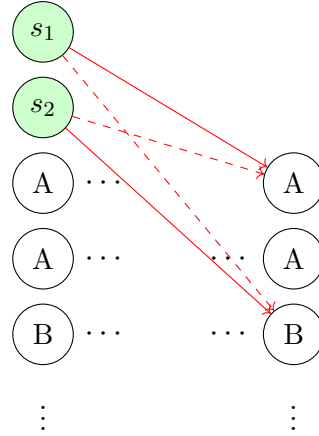


Figure 3.6: Bipartite graph after applying Sources' edges optimization. Dashed edges from the original graph have been removed according to Lemma 8, reducing the number of edges while preserving optimality. Red edges have weight 1.

This will reduce the number of edges coming from the sources from p^2 to p .

Lemma 9 (Tree nodes' edges optimization 1). *The tree nodes' edges can be optimized by removing the edges of the nodes in \mathcal{T}_1 that are connected to nodes in \mathcal{T}_2 already linked to a source node in V_1 .*

Proof. This optimization follows directly from Lemma 8. From Definition 22 we know that a matching $M \in E$ is a collection of edges such that every vertex of V is incident to at most one edge of M . In other words, a matching is a set of edges such that no two edges share a common vertex. Given that, in all the solutions to the problem, all sources will be connected to exactly one node in \mathcal{T}_2 . Therefore, we can remove the edges of the nodes in \mathcal{T}_1 that are connected to nodes in \mathcal{T}_2 already linked to a source node in V_1 since they will not be part of the final matching. \square

Example 3.8:

Consider the bipartite graph shown in Figure 3.7. The red arrows represent edges with weight 1, while green arrows represent edges with weight 0. Dashed edges represent edges that can be optimized away according to Lemma 9.

In particular, in Figure 3.7, we can observe that the dashed edge from the first node A in \mathcal{T}_1 can be optimized away since it is connected to B in \mathcal{T}_2 , which is already linked to a source node in V_1 .

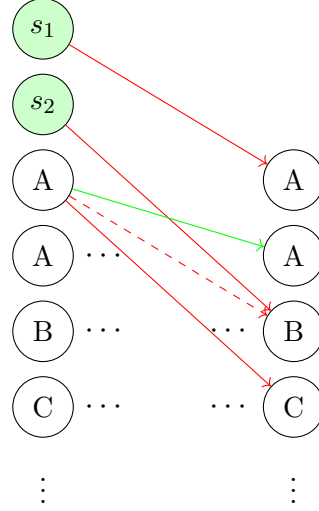


Figure 3.7: Bipartite graph after applying tree nodes' edges optimization 1. Dashed edges from the original graph have been removed according to Lemma 9, reducing the number of edges while preserving optimality. Red edges have weight 1, while green edges have weight 0.

This will reduce the number of edges by a factor of $p - 1$.

Lemma 10 (Tree nodes' edges optimization 2). *Edges of nodes in \mathcal{T}_1 can be optimized by removing the edges with weight 1 starting from a node $u \in \mathcal{T}_1$ to a node $v \in \mathcal{T}_2$ if the node u has another edge with weight 0 connected to a node $z \in \mathcal{T}_2$ such that $z \prec v$ in the ordering of the nodes.*

Proof. Let M be any perfect matching. Suppose M contains an edge (u, v) that satisfies the conditions of the lemma, i.e., $u \in \mathcal{T}_1$, $v \in \mathcal{T}_2$, $w(u, v) = 1$, and there exists an edge (u, z) with $w(u, z) = 0$ for some $z \in \mathcal{T}_2$ with $z \prec v$.

We will show that this edge (u, v) is not necessary for an optimal solution. Since M is a perfect matching, z must be matched with some node $u' \in \mathcal{T}_1$, so $(u', z) \in M$. Note that $u' \neq u$ and that $u' \prec z$ for Lemma 3.

Consider an alternative matching $M' = (M \setminus \{(u, v), (u', z)\}) \cup \{(u, z), (u'', v)\}$. This is a valid perfect matching where u is matched with z , and $u'' \in \mathcal{T}_1$ is matched with v . By construction and from Lemma 3 we know that $u \prec u' \preceq u'' \prec v$.

The weight of this new matching is $W(M') = W(M) - w(u, v) - w(u', z) + w(u, z) + w(u'', v)$. By substituting the known weights $w(u, v) = 1$ and $w(u, z) = 0$, we get: $W(M') = W(M) - 1 - w(u', z) + 0 + w(u'', v) = W(M) + (w(u'', v) - w(u', z)) - 1$.

The construction of the graph ensures that for any node $u'' \in \mathcal{T}_1$, the cost of connecting to v is either the same or one greater than connecting to a preceding node z . That is, $w(u'', v) - w(u', z) \in \{-1, 1\}$. This property arises from the problem

reduction, where moving to a subsequent node in the ordering can at most increment the cost by one.

Therefore, $w(u'', v) - w(u', z) \leq 1$, which implies $W(M') \leq W(M)$. Thus, the edge (u, v) can be removed from the graph without affecting the weight of the optimal solution.

Finally, we need to prove that Lemma 7 still holds. The proof follows from showing that the two fundamental neighborhood properties remain valid after edge removal:

1. Lemma 5 remains valid because it relies on the fact that each node $u_i \in \mathcal{T}_1$ is connected to $v_{i+1} \in \mathcal{T}_2$. The edge (u, v) we remove cannot be this critical edge (u_i, v_{i+1}) since, by our optimization condition, there exists a node $z \prec v$ connected to u with weight 0; the edge (u_i, v_{i+1}) is always the first valid connection for u_i in the ordering. Therefore, v cannot be v_{i+1} for the node u .
2. Lemma 6 is preserved because it concerns edges between nodes of the same equivalence class. Our optimization only removes an edge when there exists a better alternative to a preceding node, meaning that the same-class connectivity pattern is unaffected by this edge removal.
3. The edge removal does not affect source and destination nodes' connections, as we only remove tree node edges.

Since both Lemmas 5 and 6 remain valid and the source and destination connectivity is preserved, all conditions required by Lemma 7 continue to hold, ensuring the existence of a perfect matching in the optimized graph.

Alessio: La questione è che: se da un nodo con valore 1 voglio connettermi ad un nodo con valore 3, ma prima c'è un altro nodo con valore 1, tanto vale pasó che comunque ci sarà un collegamento tra quello nuovo e li 3 (da dimostrare). In questo modo, prendendo quel nodo, il costo della mia catena aumenta di 0, quindi non peggiora, e il coto di un'altra catena potrebbe non aumentare, quindi togliamo una scelta ovviamente errata. **Davide T.:** ho provato a renderla più generale. così mi sembra funzionare □

Example 3.9:

Consider the bipartite graph in Figure 3.8. The red arrows represent edges with weight 1, while green arrows represent edges with weight 0. Dashed edges represent edges that can be optimized away according to Lemma 10.

In this case, we can observe the application of the optimization rule: The edge (A, C) is removed from the graph, as it is not necessary for an optimal solution. This is because the node A has another edge with weight 0 connected to a node B with $B \prec C$.

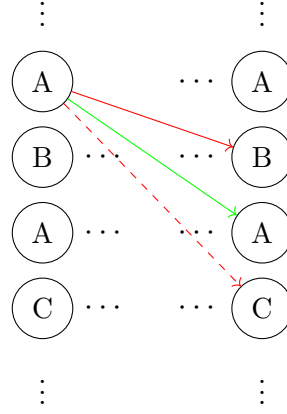


Figure 3.8: Bipartite graph after applying tree nodes' edges optimization 2. Dashed edges from the original graph have been removed according to Lemma 10, reducing the number of edges while preserving optimality. Red edges have weight 1, while green edges have weight 0.

3.2.5 Moving to Maximum weight perfect bipartite matching

In this section, we will discuss how to slightly modify the reduction process to move from a minimum weight perfect bipartite matching problem to a maximum weight perfect bipartite matching problem. This will be helpful in solving the problem more efficiently by using some known algorithms to solve the maximum weight perfect bipartite matching problem.

Theorem 8. *An optimal solution of an instance \mathcal{I} with $p \leq |E|$ of the CHAINS-DIVISION problem is equivalent to an optimal solution of the Maximum Weight Perfect Bipartite Matching for the instance $r(\mathcal{I})$ where $r : \mathcal{I}_{\text{CHAINS-DIVISION}} \rightarrow \mathcal{I}_{\text{MW PBM}}$ is the reduction function that maps an instance of the CHAINS-DIVISION problem to an instance of the Maximum Weight Perfect Bipartite Matching problem constructed as stated in Definition 28 but with inverted weights (weight 0 becomes 1 and weight 1 becomes 0).*

Proof. Let M be a perfect matching in the bipartite graph G constructed as stated in Definition 28. Let $w(M)$ be the sum of the weights of the edges in the matching M . From the previous theorem, we know that the optimal solution of the CHAINS-DIVISION problem is equivalent to finding a perfect matching M in G that minimizes $w(M)$.

Let G' be a bipartite graph constructed as G but with inverted weights (weight 0 becomes 1 and weight 1 becomes 0). Let M' be a perfect matching in G' and let $w'(M')$ be the sum of the weights of the edges in the matching M' . Let k be the number of edges in the matching.

We can see that for any matching M in G :

$$w'(M) = k - w(M)$$

This means that maximizing $w'(M)$ is equivalent to minimizing $w(M)$. Therefore, finding the maximum weight perfect matching in G' is equivalent to finding the

minimum weight perfect matching in G , which in turn is equivalent to finding the optimal solution of the CHAINS-DIVISION problem. \square

3.3 Collapsing Nodes in Chains

The next crucial step of our compression scheme is to reduce the space required by each chain by collapsing equivalent nodes. Specifically, any sequence of consecutive nodes within the same chain that belong to the same equivalence class is merged into a single representative node. This new node preserves the connectivity of the original structure by inheriting all distinct outgoing and ingoing edges from the nodes it replaces. In the following subsections we will introduce the concept of non-deterministic finite automaton and define how nodes are collapsed in a chain. Then, we prove that this transformation is language-preserving (see Lemma 13).

3.3.1 How to Collapse Nodes

Now, we define the concept of collapsing consecutive equivalent nodes in a chain.

Definition 29 (Collapsing consecutive equivalent nodes). *Let V be the set of nodes of the tree, let Σ be the alphabet, and let $E \subseteq V \times \Sigma \times V$ be the set of labeled edges. Let $\mathcal{P} = \{C_1, C_2, \dots, C_m\}$ be the set of all chains partitioning V .*

For each chain $C_i = (u_1^{(i)}, u_2^{(i)}, \dots, u_{n_i}^{(i)}) \in \mathcal{P}$, partition it into maximal consecutive blocks $B_1^{(i)}, \dots, B_{k_i}^{(i)}$, where each block $B_t^{(i)} = (u_j^{(i)}, \dots, u_\ell^{(i)})$ satisfies $\mathcal{C}(u_r^{(i)}) = \mathcal{C}(u_s^{(i)})$ for all $r, s \in \{j, \dots, \ell\}$, and the block is maximal (cannot be extended).

The collapsed chain is $C'_i = (v_1^{(i)}, \dots, v_{k_i}^{(i)})$, where each block $B_t^{(i)}$ is replaced by a single node $v_t^{(i)}$.

Define the global collapse map $\Phi : V \rightarrow V'$ where $V' = \bigcup_{i=1}^m C'_i$, such that $\Phi(u) = v_t^{(i)}$ if $u \in B_t^{(i)}$ for some chain C_i and block $B_t^{(i)}$.

Then:

- *Two consecutive nodes $u_j^{(i)}, u_{j+1}^{(i)} \in C_i$ are collapsed into the same node if and only if $\mathcal{C}(u_j^{(i)}) = \mathcal{C}(u_{j+1}^{(i)})$.*
- *The edge set after collapsing all chains is*

$$E' = \{ (\Phi(x), a, \Phi(y)) : (x, a, y) \in E \},$$

where parallel duplicates are removed (i.e., E' is treated as a set).

Example 3.10:

Consider Example 3.6 where we obtained the chains $C_1 = \{A, C, C, B\}$ and $C_2 = \{B, D, D, D, D, D, D\}$ for the tree ADFA in Figure 2.2 by applying the reduction from CHAIN-DIVISION to MWPBM. The nodes inside each chain are the following:

- $C_1 = \{a, d, f, c\}$
- $C_2 = \{b, h, l, e, i, m, g\}$

Applying the collapsing operation from Definition 29:

- For $C_1 = (a, d, f, c)$ with classes (A, C, C, B) :
 - Block $B_1 = \{a\}$ (class A) \rightarrow collapsed node v_1 . The node a is the initial state. It has two outgoing edges: $a \xrightarrow{0} b$ and $a \xrightarrow{1} c$. Since b and c collapse to w_1 and v_3 respectively, we obtain $v_1 \xrightarrow{0} w_1$ and $v_1 \xrightarrow{1} v_3$.
 - Block $B_2 = \{d, f\}$ (class C) \rightarrow collapsed node v_2 . The outgoing edges of d and f are:

$$d \xrightarrow{0} h, d \xrightarrow{1} i, \quad f \xrightarrow{0} l, f \xrightarrow{1} m.$$

After collapsing, we obtain:

$$v_2 \xrightarrow{0} w_2, v_2 \xrightarrow{1} w_2, \quad v_2 \xrightarrow{0} w_2, v_2 \xrightarrow{1} w_2.$$

Since we have two identical edges we can keep only one of each.

- Block $B_3 = \{c\}$ (class B) \rightarrow collapsed node v_3 . It has two outgoing edges: $c \xrightarrow{0} f$ and $c \xrightarrow{1} g$. Since f and g collapse to v_2 and w_2 respectively, we obtain $v_3 \xrightarrow{0} v_2$ and $v_3 \xrightarrow{1} w_2$.

Result: $C'_1 = (v_1, v_2, v_3)$ with classes (A, C, B) . Here, v_1 is the initial state.

- For $C_2 = (b, h, l, e, i, m, g)$ with classes (B, D, D, D, D, D, D) :
 - Block $B_1 = \{b\}$ (class B) \rightarrow collapsed node w_1 . It has two outgoing edges: $b \xrightarrow{0} d$ and $b \xrightarrow{1} e$. As d, e collapse to v_2 and w_2 respectively, we obtain $w_1 \xrightarrow{0} v_2$ and $w_1 \xrightarrow{1} w_2$.
 - Block $B_2 = \{h, l, e, i, m, g\}$ (all class D) \rightarrow collapsed node w_2 . The node w_2 collects all incoming edges formerly targeting any of b, h, l, e, i, m, g , and it is accepting.

Result: $C'_2 = (w_1, w_2)$ with classes (B, D) , and w_2 is the unique accepting state for this example.

The collapsed chains preserve all distinct outgoing and incoming edges through the collapse map Φ , significantly reducing the space complexity from 11 nodes to 5 nodes total. The resulting NFA is shown in Figure 3.9.

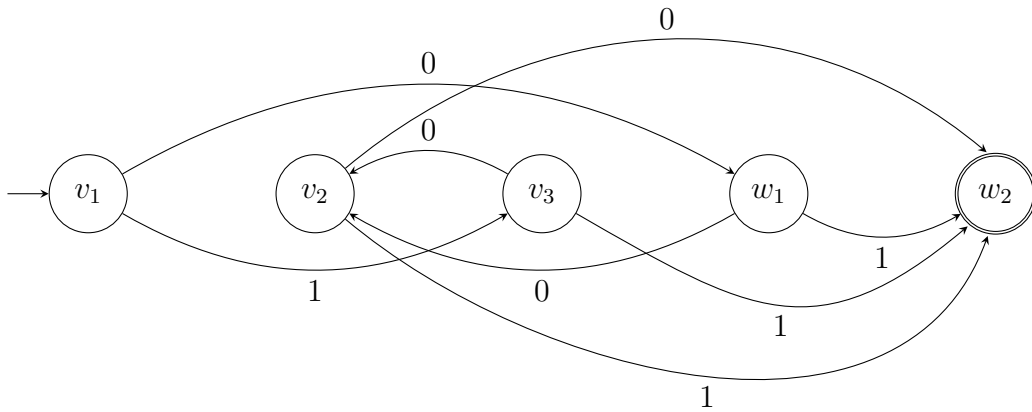


Figure 3.9: NFA obtained after collapsing equivalent nodes in chains C_1 and C_2 .

3.3.2 Language Equivalence

Now, we need to prove that the language recognized by the NFA obtained after collapsing equivalent nodes in chains following Definition 29 is equivalent to the language of the original tree ADFA. We define the language of an NFA or ADFA F as $L(F)$.

Lemma 11 (Root forms a singleton class under Revuz). *In a tree ADFA minimized by Revuz's algorithm (Subsection 2.4.3), the root forms a singleton equivalence class.*

Proof. Let T be a tree ADFA, let r be its root, and let $D = h(r)$ be its height as in Definition 10. For every other node $u \neq r$ at depth $k \geq 1$, we have $h(u) \leq D - k < D$. Hence r is the unique state in the level Π_D . Since Revuz's algorithm partitions by height and refines within each level, r cannot be merged with any other state and thus forms its own equivalence class. \square

Lemma 12 (All leaves fall into the same class under Revuz). *In a tree ADFA minimized by Revuz's algorithm, all leaves belong to the same equivalence class.*

Proof. Every leaf ℓ is final and has no outgoing transitions, so $h(\ell) = 0$. In the base step on level Π_0 , the label of a state depends only on its finality and on transitions to already distinguished classes. All leaves share the same label (final, no outgoing transitions), hence they are merged into a single equivalence class in Π_0 . \square

Lemma 13. *Let T be a tree ADFA with alphabet Σ . Let C_1, C_2, \dots, C_m be the chains partitioning T as defined in Definition 25. Let N be the NFA obtained after collapsing equivalent nodes in chains C_1, C_2, \dots, C_m as defined in Definition 29. Then, $L(N) = L(T)$.*

Proof. We prove the equality $L(N) = L(T)$ by showing both inclusions $L(T) \subseteq L(N)$ and $L(N) \subseteq L(T)$.

$(L(T) \subseteq L(N))$: Let $w \in L(T)$. Then there exists an accepting path in T from the root to some accepting state:

$$q_0 \xrightarrow{a_1} q_1 \xrightarrow{a_2} q_2 \xrightarrow{a_3} \dots \xrightarrow{a_n} q_n$$

where q_0 is the root, q_n is accepting, and $w = a_1 a_2 \dots a_n$.

By Definition 29, each node q_i in the original tree is mapped to a collapsed node $\Phi(q_i)$ in N . Since the collapse map preserves all edges (by the definition of E'), there exists a corresponding path in N :

$$\Phi(q_0) \xrightarrow{a_1} \Phi(q_1) \xrightarrow{a_2} \Phi(q_2) \xrightarrow{a_3} \dots \xrightarrow{a_n} \Phi(q_n)$$

Since q_0 is the root of T , by Lemma 11 its image $\Phi(q_0)$ is the unique initial state of N . Since q_n is accepting in T , by Lemma 12 its image $\Phi(q_n)$ lies in the unique leaf class, which is marked accepting in N . Hence, $w \in L(N)$.

$(L(N) \subseteq L(T))$: Let $w \in L(N)$. Then there exists an accepting path in N :

$$v_0 \xrightarrow{a_1} v_1 \xrightarrow{a_2} v_2 \xrightarrow{a_3} \dots \xrightarrow{a_n} v_n$$

where v_0 is the initial state, v_n is accepting, and $w = a_1a_2 \dots a_n$.

Each collapsed node v_i corresponds to some block $B_t^{(j)}$ in the original tree. By Definition 29, every edge (v_{i-1}, a_i, v_i) in N corresponds to at least one edge (u, a_i, u') in the original tree T , where $\Phi(u) = v_{i-1}$ and $\Phi(u') = v_i$.

Since the chains preserve the ordering from the original tree structure, we can construct a valid path in T by selecting appropriate representatives from each collapsed block. Specifically, we can choose nodes u_0, u_1, \dots, u_n in T such that $\Phi(u_i) = v_i$ and $(u_{i-1}, a_i, u_i) \in E$ for all $i = 1, \dots, n$.

Since v_0 corresponds to the root block and v_n is accepting, for Lemmas 11 and 12 we have u_0 as the root and u_n as an accepting state in T respectively. Therefore, $w \in L(T)$.

Thus, $L(N) = L(T)$. □

Collapsing the nodes as in Definition 29 preserves the language of the original tree ADFA and the resulting chains inherit a total order. This enables the application of the NFA indexing scheme of Cotumaccio et al. [7], which we present in the next chapter.

Theorem 9 (Myhill–Nerode). *Let $L \subseteq \Sigma^*$ and define the indistinguishability relation*

$$x \sim_L y \iff \forall z \in \Sigma^*, xz \in L \iff yz \in L.$$

Then:

1. \sim_L is an equivalence relation on Σ^* .
2. L is regular iff \sim_L has finitely many equivalence classes.
3. The states of the minimal DFA for L are in one-to-one correspondence with the equivalence classes of \sim_L .
4. L is exactly the union of those equivalence classes that intersect L :

$$L = \bigcup \{[x]_{\sim_L} \mid x \in L\}.$$

Lemma 14. *Let $M = (Q, \Sigma, \delta, q_0, F)$ be a DFA recognizing $L \subseteq \Sigma^*$. If two states $p, q \in Q$ correspond to the same Myhill–Nerode class for L (i.e., for all $w \in \Sigma^*$ we have $\delta(p, w) \in F \iff \delta(q, w) \in F$), then merging p and q into a single state yields an automaton (possibly nondeterministic) that still recognizes exactly L .*

Proof. By the Myhill–Nerode theorem, every state of M corresponds to a unique equivalence class of \sim_L , and L is exactly the union of those classes that intersect L . If p and q belong to the same equivalence class, then for every continuation $z \in \Sigma^*$ we have

$$\delta(p, z) \in F \iff \delta(q, z) \in F.$$

Thus replacing p with q (or vice versa) in any path does not affect whether the run ends in an accepting state. Therefore merging p and q does not alter the set of accepted strings, i.e. the recognized language remains L . □

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