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Op amps taking the "raw" voltage off the sense resistors, and buffering it for the ADCs. Also protects the ADC inputs. Op amp output voltage is limited to VDDA, and they're cheap/easy to replace if damaged.

These resistors should be chosen so that VDDA minus R times the maximum expected phototransistor current remains well above Vcesat at all times. Ideally near 1V. Otherwise the phototransistors will start behaving very nonlinearly as the current flow changes.

