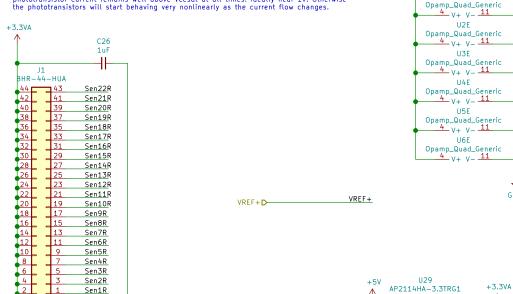


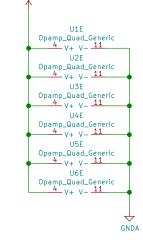
These resistors should be chosen so that VDDA minus R times the maximum expected phototransistor current remains well above Vcesat at all times. Ideally near 1V. Otherwise the phototransistors will start behaving very nonlinearly as the current flow changes.



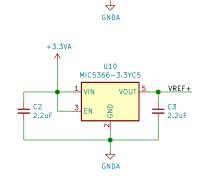
Sen1R

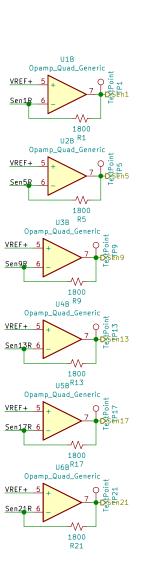
C27 1uF

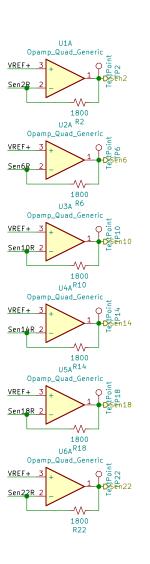
GNDA

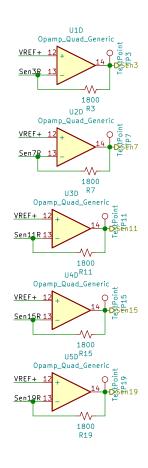


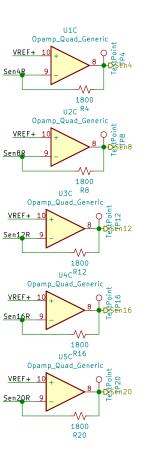
+3.3VA











Sheet: /Analog Input/ File: Analog Input.sch Title: Size: A3 Date: KiCad E.D.A. kicad (5.1.7)