APPENDIX D REGISTER INDEX (IN ALPHABETICAL ORDER)

Symbol	Name	Page(s)
BRG0,1	Baud rate generator register 0 and 1	58, 59, 222
DAR0H	Destination address pointer 0 (high-order)	60, 169
DAR1H	Destination address pointer 1 (high-order)	60, 169
DAR0L	Destination address pointer 0 (low-order)	60, 169
DAR1L	Destination address pointer 1 (low-order)	60, 169
DAR0M	Destination address pointer 0 (middle-order)	60, 169
DAR1M	Destination address pointer 1 (middle-order)	60, 169
DIC0, 1	DMA interrupt request control registers 0 and 1	60, 166
DMAC0, 1	DMA control registers 0 and 1	60, 165
DMAM0, 1	DMA mode registers 0 and 1	60, 163
EMS0-2	External interrupt macro service control registers 0 to 2	58, 97
EXIC0-2	External interrupt request control registers 0 to 2	58, 97
FLAG	User flag register	51, 60
IDB	Internal data area base register	57, 60
INTM	External interrupt mode register	58, 96
IRQS	Interrupt source register	58, 92
ISPR	Interrupt priority register	58, 94
MD0, 1	Modulo/timer registers 0 and 1	59, 199
MSC	Macro service counter	86
MSP	Macro service pointer	86
MSS	Macro service segment	86
P0-2	Ports 0 to 2	58, 181
PC	Program counter	47
PM0-2	Port 0 to 2 mode registers	58, 185
PMC0-2	Port 0 to 2 mode control registers	58, 184
PMT	Port T mode register	58, 190
PRC	Processor control register	60, 196
PSW	Program status word	47
PT	Port T	58, 189
RFM	Refresh mode register	60, 140
RxB0, 1	Receive buffer registers 0 and 1	58, 59, 211
SAR0H	Source address pointer 0 (high-order)	60, 167
SAR1H	Source address pointer 1 (high-order)	60, 167

Symbol	Name	Page(s)
SAR0L	Source address pointer 0 (low-order)	60, 167
SAR1L	Source address pointer 1 (low-order)	60, 167
SAR0M	Source address pointer 0 (middle-order)	60, 167
SAR1M	Source address pointer 1 (middle-order)	60, 167
SCC0, 1	Serial control registers 0 and 1	58, 59, 224
SCHR	Search character	86
SCM0, 1	Serial mode registers 0 and 1	58, 59, 219
SCS0, 1	Serial status registers 0 and 1	58, 59, 226
SEIC0, 1	Serial error interrupt request control registers 0 and 1	58, 59, 229
SFRP	Special function register pointer	86
SRIC0, 1	Serial reception interrupt request control registers 0 and 1	58, 59, 229
SRMS0, 1	Serial reception macro service control registers 0 and 1	58, 59, 230
STBC	Standby control register	60, 231
STIC0, 1	Serial transmission interrupt request control registers 0 and 1	58, 59, 229
STMS0, 1	Serial transmission macro service control registers 0 and 1	58, 59, 230
TBIC	Time base interrupt request control register	60, 209
TC0H	Terminal counter 0 (high-order)	60, 171
TC1H	Terminal counter 1 (high-order)	60, 171
TC0L	Terminal counter 0 (low-order)	60, 171
TC1L	Terminal counter 1 (low-order)	60, 171
TM0, 1	Timer registers 0 and 1	59, 197
TMC0, 1	Timer control registers 0 and 1	59, 199
TMIC0-2	Timer unit interrupt request control registers 0 to 2	59, 206
TMMS0-2	Timer unit macro service control registers 0 to 2	59, 206
TxB0, 1	Transmit buffer registers 0 and 1	58, 59, 211
WTC	Wait control register	60, 121

V25⁺ and V35⁺

NEC

NEC Electronics Inc.

CORPORATE HEADQUARTERS

475 Ellis Street
P.O. Box 7241
Mountain View, CA 94039-7241
TEL 415-960-6000

For literature, call toll-free 7 a.m. to 6 p.m. Pacific time: **1-800-366-9782**
or FAX your request to: **1-800-729-9288**

IEU-1427