

# Lecture 24 - PWM signals with Output Compare & Timers

Thursday, April 30, 2020 12:00 PM

Objectives:	<ul style="list-style-type: none"><li>- Learn how PWM signals can be generated using the output compare modules</li><li>- Learn how the OC modules are configured</li><li>- Learn how Servo motor position is controlled using a PWM signal</li><li>- Learn how timers can be used to generate PWM signals</li></ul>
-------------	--

## OC SFRs

- OCxCON - Control Register
- OCxR - Compare Register
- OCxRS - Secondary Compare Register

## Controlling Multiple Servo Motors With Two Timers

