Lecture 11 - Timers and Interrupts

Tuesday, March 3, 2020   10:56 AM

Objective:	- Learn how timers work and how they are configured
	- Begin discussion on MIPS interrupts and the PIC Interrupt Controller

TYPE A - TIMERS #1 - 16 bit ONLY

TYPE B - TIMERS 2..5 - 16 bit or CAN BE 32

32 bit COMBINATIONS 2 & 3 OR 4 & 5

INTERRUPT PROCESSING

MAIN
PROGRAM

SAVE
STATE

EVENT →

INT
SERVICE
ROUTINE

ISR

RESTORE
STATE

RESUME EXECUTION