base junction, which is subject to change with variations in DC bias voltage. Suffice to say that the voltage gain of a common-base amplifier can be very high.

• The ratio of a transistor's collector current to emitter current is called α . The α value for any transistor is always less than unity, or in other words, less than 1.

4.8 The cascode amplifier

While the C-B (common-base) amplifier is known for wider bandwidth than the C-E (common-emitter) configuration, the low input impedance (10s of Ω) of C-B is a limitation for many applications. The solution is to precede the C-B stage by a low gain C-E stage which has moderately high input impedance (k\Omegas). See Figure 4.61. The stages are in a cascode configuration, stacked in series, as opposed to cascaded for a standard amplifier chain. See "Capacitor coupled three stage common-emitter amplifier" (page 255) for a cascade example. The cascode amplifier configuration has both wide bandwidth and a moderately high input impedance.

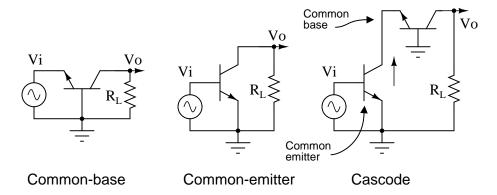


Figure 4.61: The cascode amplifier is combined common-emitter and common-base. This is an AC circuit equivalent with batteries and capacitors replaced by short circuits.

The key to understanding the wide bandwidth of the cascode configuration is the *Miller effect*. The (page 279) is the multiplication of the bandwidth robbing collector-base capacitance by voltage gain A_v . This C-B capacitance is smaller than the E-B capacitance. Thus, one would think that the C-B capacitance would have little effect. However, in the C-E configuration, the collector output signal is out of phase with the input at the base. The collector signal capacitively coupled back opposes the base signal. Moreover, the collector feedback is $(1-A_v)$ times larger than the base signal. Keep in mind that A_v is a negative number for the inverting C-E amplifier. Thus, the small C-B capacitance appears $(1+A|_v|)$ times larger than its actual value. This capacitive gain reducing feedback increases with frequency, reducing the high frequency response of a C-E amplifier.

The approximate voltage gain of the C-E amplifier in Figure 4.62 is $-R_L/r_{EE}$. The emitter current is set to 1.0 mA by biasing. R_{EE} = 26mV/ I_E = 26mV/1.0ma = 26 Ω . Thus, A_v = $-R_L/R_{EE}$ = -4700/26 = -181. The pn2222 datasheet list C_{cbo} = 8 pF.[5] The miller capacitance is $C_{cbo}(1-A_v)$. Gain A_v = -181, negative since it is inverting gain. C_{miller} = $C_{cbo}(1-A_v)$ = 8pF(1-(-181)=1456pF

The way to reduce the common-emitter gain is to reduce the load resistance. The gain of a C-E amplifier is approximately R_C/R_E . The internal emitter resistance r_{EE} at 1mA emitter current is 26Ω . For details on the 26Ω , see "Derivation of R_{EE} ", see (page 243). The collector load R_C is the resistance of the emitter of the C-B stage loading the C-E stage, 26Ω again. CE gain amplifier gain is approximately $A_v = R_C/R_E = 26/26 = 1$. This Miller capacitance is $C_{miller} = C_{cbo}(1-A_v) = 8 pF(1-(-1)=16 pF)$. We now have a moderately high input impedance C-E stage without suffering the Miller effect, but no C-E dB voltage gain. The C-B stage provides a high voltage gain, $A_V = -181$. Current gain of cascode is β of the C-E stage, 1 for the C-B, β overall. Thus, the cascode has moderately high input impedance of the C-E, good gain, and good bandwidth of the C-B.

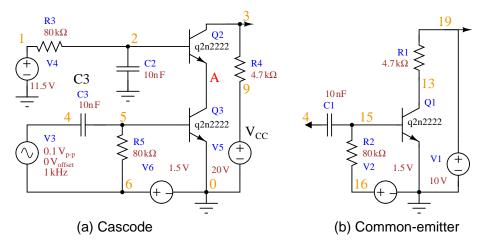


Figure 4.62: SPICE: Cascode and common-emitter for comparison.

The SPICE version of both a cascode amplifier, and for comparison, a common-emitter amplifier is shown in Figure 4.62. The netlist is in Table 4.6. The AC source V3 drives both amplifiers via node 4. The bias resistors for this circuit are calculated in an example problem (page 248).

The waveforms in Figure 4.63 show the operation of the cascode stage. The input signal is displayed multiplied by 10 so that it may be shown with the outputs. Note that both the Cascode, Common-emitter, and Va (intermediate point) outputs are inverted from the input. Both the Cascode and Common emitter have large amplitude outputs. The Va point has a DC level of about 10V, about half way between 20V and ground. The signal is larger than can be accounted for by a C-E gain of 1, It is three times larger than expected.

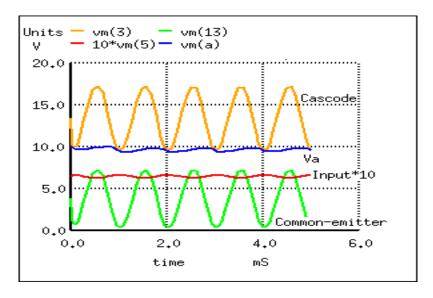


Figure 4.63: SPICE waveforms. Note that Input is multiplied by 10 for visibility.

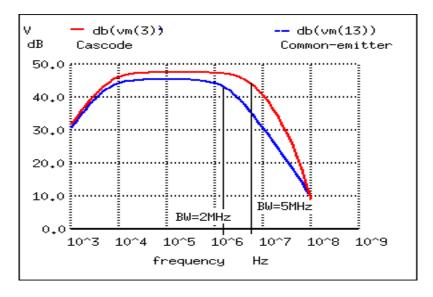


Figure 4.64: Cascode vs common-emitter banwidth.

Table 4.6: SPICE netlist for printing AC input and output voltages.

```
*SPICE circuit <03502.eps> from XCircuit v3.20
V1 19 0 10
Q1 13 15 0 q2n2222
Q2 3 2 A q2n2222
R1 19 13 4.7k
V2 16 0 1.5
C1 4 15 10n
R2 15 16 80k
Q3 A 5 0 q2n2222
V3 4 6 SIN(0 0.1 1k) ac 1
R3 1 2 80k
R4 3 9 4.7k
C2 2 0 10n
C3 4 5 10n
R5 5 6 80k
V4 1 0 11.5
V5 9 0 20
V6 6 0 1.5
.model q2n2222 npn (is=19f bf=150
+ vaf=100 ikf=0.18 ise=50p ne=2.5 br=7.5
+ var=6.4 ikr=12m isc=8.7p nc=1.2 rb=50
+ re=0.4 rc=0.3 cje=26p tf=0.5n
+ cjc=11p tr=7n xtb=1.5 kf=0.032f af=1)
.tran 1u 5m
.AC DEC 10 1k 100Meg
.end
```

Figure 4.64 shows the frequency response to both the cascode and common-emitter amplifiers. The SPICE statements responsible for the AC analysis, extracted from the listing:

```
V3 4 6 SIN(0 0.1 1k) ac 1 .AC DEC 10 1k 100Meg
```

Note the "ac 1" is necessary at the end of the V3 statement. The cascode has marginally better mid-band gain. However, we are primarily looking for the bandwidth measured at the -3dB points, down from the midband gain for each amplifier. This is shown by the vertical solid lines in Figure 4.64. It is also possible to print the data of interest from nutmeg to the screen, the SPICE graphical viewer (command, first line):

```
nutmeg 6 -> print frequency db(vm(3)) db(vm(13))
Index
        frequency
                       db(vm(3))
                                   db(vm(13))
2.2
                        47.54
                                    45.41
        0.158MHz
                        46.95
33
        1.995MHz
                                    42.06
37
                        44.63
                                    36.17
        5.012MHz
```

Index 22 gives the midband dB gain for Cascode vm(3)=47.5dB and Common-emitter vm(13)=45.4dB. Out of many printed lines, Index 33 was the closest to being 3dB down from 45.4dB at 42.0dB for the Common-emitter circuit. The corresponding Index 33 frequency is approximately 2Mhz, the common-emitter bandwidth. Index 37 vm(3)=44.6db is approximately 3db down from 47.5db. The corresponding Index37 frequency is 5Mhz, the cascode bandwidth. Thus, the cascode amplifier has a wider bandwidth. We are not concerned with the low frequency degradation of gain. It is due to the capacitors, which could be remedied with larger ones.

The 5MHz bandwith of our cascode example, while better than the common-emitter example, is not exemplary for an RF (radio frequency) amplifier. A pair of RF or microwave transistors with lower interelectrode capacitances should be used for higher bandwidth. Before the invention of the RF dual gate MOSFET, the BJT cascode amplifier could have been found in UHF (ultra high frequency) TV tuners.

- REVIEW
- A *cascode* amplifier consists of a common-emitter stage loaded by the emitter of a common-base stage.
- The heavily loaded C-E stage has a low gain of 1, overcoming the Miller effect
- A cascode amplifier has a high gain, moderately high input impedance, a high output impedance, and a high bandwidth.

4.9 Biasing techniques

In the common-emitter section of this chapter, we saw a SPICE analysis where the output waveform resembled a half-wave rectified shape: only half of the input waveform was reproduced, with the other half being completely cut off. Since our purpose at that time was to reproduce the entire waveshape, this constituted a problem. The solution to this problem was