



UNM LEARN



David Kirby

[Course Home](#)[Quizzes & Tests](#)**Review Test Submission: Quix 4 - Clocks, Ports & Timers**

Review Test Submission: Quix 4 - Clocks, Ports & Timers

User	David Kirby
Course	ECE-344L-003 (Spring 2020)
Test	Quix 4 - Clocks, Ports & Timers
Started	4/16/20 8:25 PM
Submitted	4/16/20 8:27 PM
Due Date	4/16/20 10:00 PM
Status	Completed
Attempt Score	100 out of 100 points
Time Elapsed	2 minutes
Results Displayed	Submitted Answers, Correct Answers

Question 1

10 out of 10 points

How many bits wide is a single timer in the PIC32?

Selected Answer: 16-bits

Correct Answer: 16-bits

Question 2

10 out of 10 points

What is a timer pre-scaler?

Selected Answer: A circuit to reduce the frequency of a clock by dividing said signal


Correct Answer:

A circuit to reduce the frequency of a clock by dividing said signal

Question 3

10 out of 10 points

Which register holds the data written to port I/O pins?


Selected Answers:  LAT_x

Correct Answers:  LAT_x

Question 4

10 out of 10 points

Which register controls the data direction for an I/O pin?

Selected Answers:  TRIS_x


Correct Answers:  TRIS_x

Question 5

10 out of 10 points

Which register allows I/O pins to be accessed?


Selected Answers:  PORT_x


Correct Answers:  PORT_x

Question 6

10 out of 10 points

After reset, are port pins set to output as a default?


Selected Answer:  No


Correct Answer:  No

Question 7

10 out of 10 points

Which of the following is NOT an SFR for a timer?


Selected Answer:  ODC_x


Correct Answer:  ODC_x

Question 8

10 out of 10 points

Which of the following is a characteristic of a Type B timer?


Selected Answer:  Can be combined with another timer to form a 32-bit timer


Correct Answer:  Can be combined with another timer to form a 32-bit timer

Question 9

10 out of 10 points

What purpose does *#pragma config* serve when configuring system clocks?


Selected Answers:  Allows the use of keyword templates to set options


Correct Answers:  Allows the use of keyword templates to set options

Question 10

10 out of 10 points

How is the clock configured in the PIC32 microcontroller?

Selected Answers:  By setting the appropriate bits in the Special Function Registers (SFRs)

Correct Answers:  By setting the appropriate bits in the Special Function Registers (SFRs)

Thursday, April 16, 2020 8:27:59 PM MDT

← OK