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M David Kirby 1 ▼

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Review Test Submission: Quiz 5 - Interrupts & Communications

User	David Kirby
Course	ECE-344L-003 (Spring 2020)
Test	Quiz 5 - Interrupts & Communications
Started	4/21/20 8:31 PM
Submitted	4/21/20 8:37 PM
Due Date	4/21/20 10:00 PM
Status	Completed
Attempt Score	100 out of 100 points
Time Elapsed	6 minutes
Results Displayed	All Answers, Submitted Answers, Correct Answers

Question 1

5 out of 5 points

An ISR can return a value upon completion.

Selected Answer: ☒ False

Answers: ☐ True

☒ False

Question 2

7 out of 7 points

Which of the following is NOT a characteristic of an I2C interface?

Selected Answer: ☒ Full Duplex

Answers: ☒ Full Duplex

☐ Two signal wires

☐ Multiple master operation

Synchronous communication

Question 3

10 out of 10 points

Which of the following does NOT occur during an interrupt?

Selected Answer: ☒ The CPU clock is halted

Answers: The pre-interrupt CPU state is saved
 A function is called
 The CPU state is restored and program continues
 ☒ The CPU clock is halted

Question 4

5 out of 5 points

Arguments cannot be passed to an ISR.

Selected Answer: ☒ True

Answers: ☒ True
 False

Question 5

7 out of 7 points

What type of communication scheme allows data to be received or transmitted at any one time?

Selected Answer: ☒ Half Duplex

Answers: Full Duplex
 ☒ Half Duplex
 UART
 SPI

Question 6

7 out of 7 points

Which of the following is NOT a characteristic of a UART interface?

Selected Answer: ☒ Synchronous communication

Answers: Full Duplex

Minimum two signal wires

Multiple protocol support

☒ Synchronous communication

Question 7

5 out of 5 points

Is ISR execution synchronous with respect to the main program?

Selected Answer: ☒ No

Answers: Yes

☒ No

Question 8

7 out of 7 points

What type of communication scheme allows data to be received and transmitted at the same time?

Selected Answer: ☒ Full Duplex

Answers: ☒ Full Duplex

Half Duplex

I2C

Shared medium

Question 9

7 out of 7 points

Which of the following is NOT a characteristic of a SPI interface?

Selected Answer: ☒ Two signal wires

Answers: Full Duplex

☒ Two signal wires

Devices can be connected in parallel
Synchronous communication

Question 10

10 out of 10 points

Which type of communication link relies on a start signal to sample data?

Selected Answer: ☒ Asynchronous

Answers: ☒ Asynchronous

Synchronous

SPI

I2C

Question 11

10 out of 10 points

Which type of communication link designates a master clock to sample data?

Selected Answer: ☒ Synchronous

Answers: Asynchronous

☒ Synchronous

UART

RS-232

Question 12

10 out of 10 points

In the PIC32 microcontroller, how many different sources can cause an interrupt?

Selected Answer: ☒ 96

Answers: 16


32

64

 96**Question 13**



5 out of 5 points

Interrupts will not occur unless they are enabled with the Interrupt Controller.

Selected Answer:  TrueAnswers:  True
False**Question 14**

5 out of 5 points

An ISR can be called by another function, regardless of whether an interrupt has occurred or not.

Selected Answer:  FalseAnswers: True
 False

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← OK