Lab 4 - Timer Calculations

Friday, March 27, 2020 10:56 AM

Part I

Timer Frequencies:

2Hz 1Hx

.25Hz

Timer calculations

Pbclk = 10Mhz

Use timer 1 - 16 bits

Max count = 65535

Max divide value 256

PBCLK/256 = 39,062 Hz Tin= 25.6 uSec

.5Sec/25.6uSec/count = 19531 count

Part II

PBCLK/256 = 39,062 Hz Tin=25.6uSec 10Sec/(25.6uSec/count)=390,625 counts

Need a 32 bit timer - we will use Timers 2,3 Use a different divider value Use a divisor of 64 Tin = 6.4uSec 10Sec/(6.4uSec/count) = 1,562,500 counts