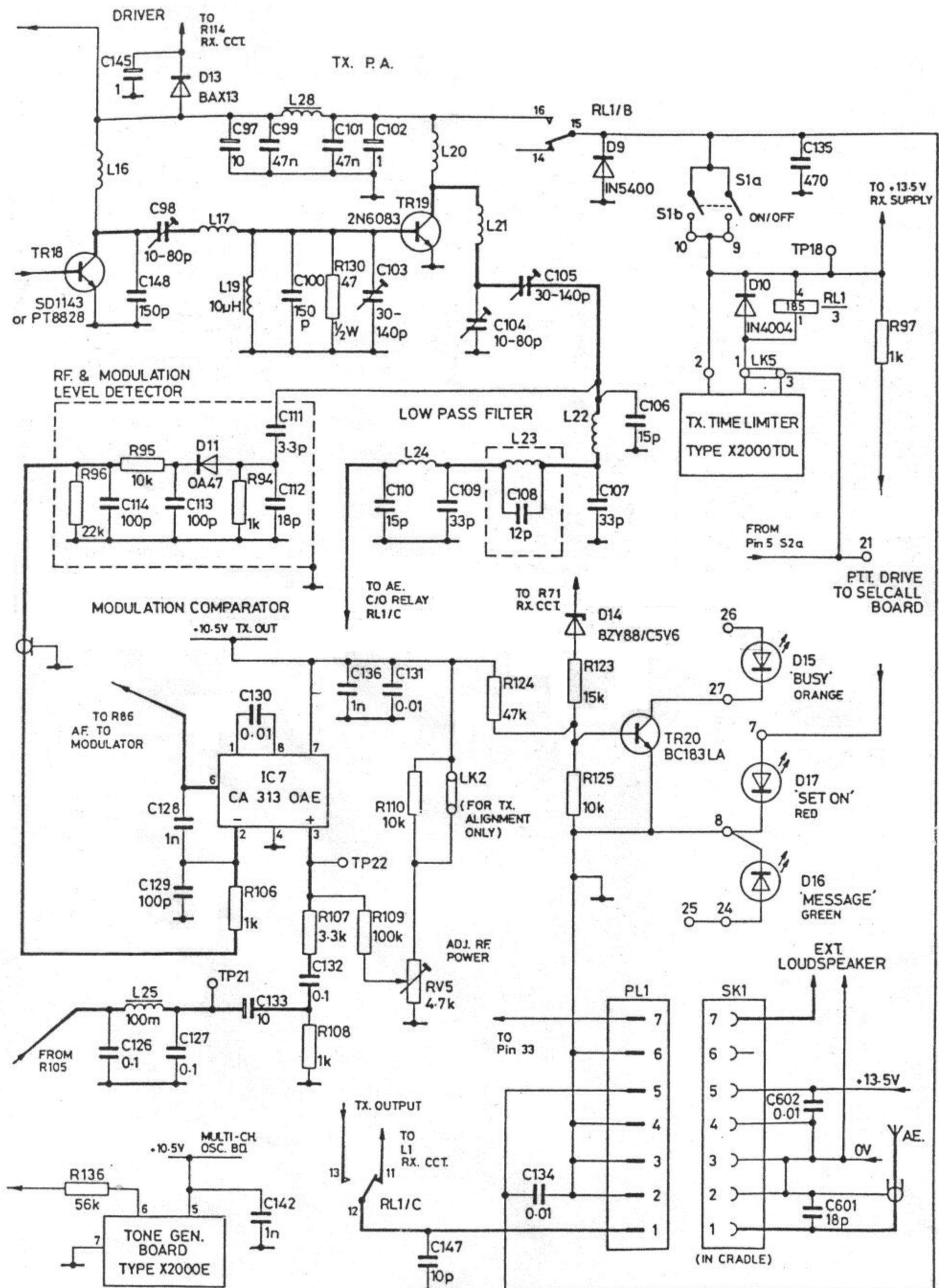
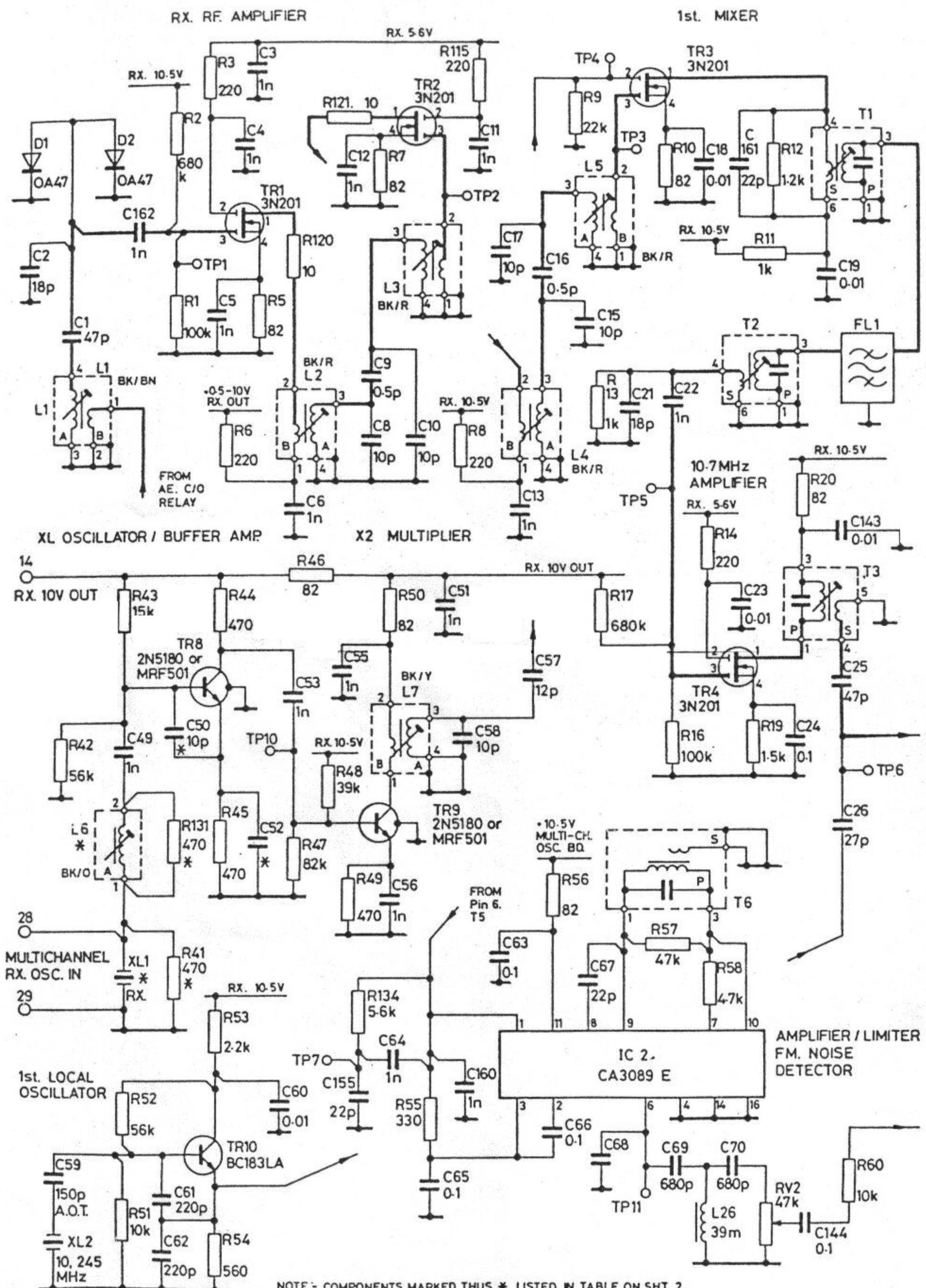


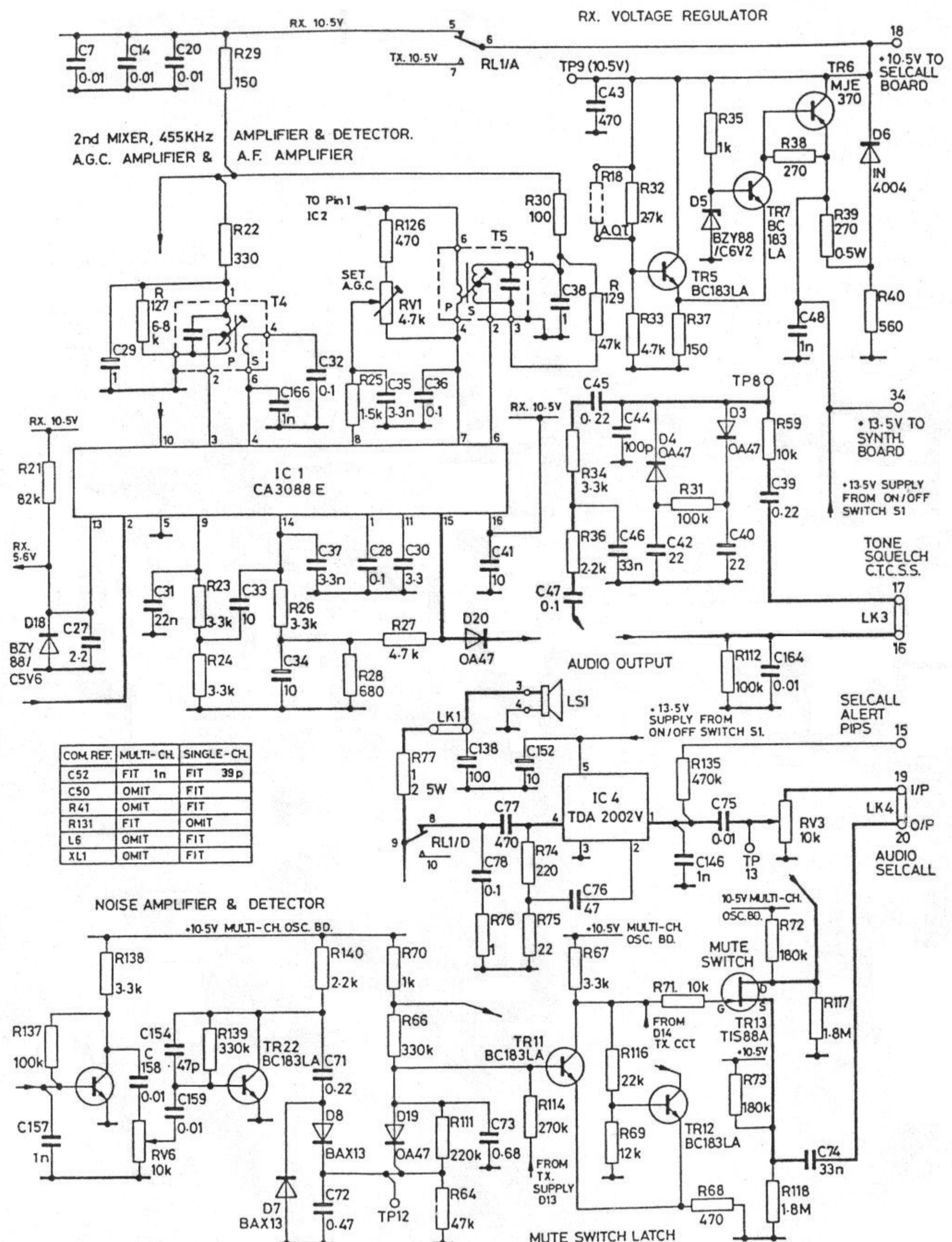
TRANSMITTER CIRCUIT DIAGRAM Fig 17

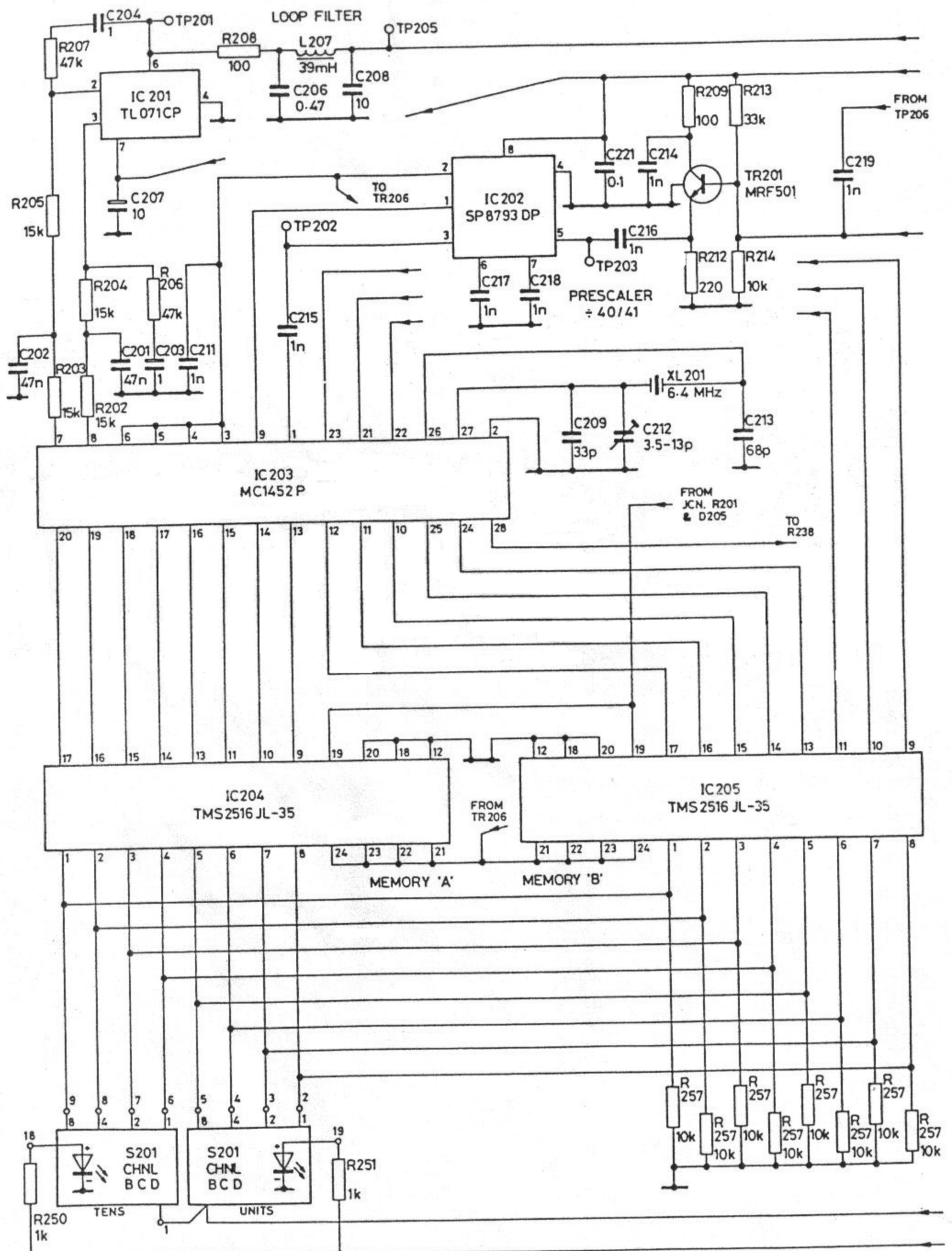


TRANSMITTER CIRCUIT DIAGRAM Fig 17

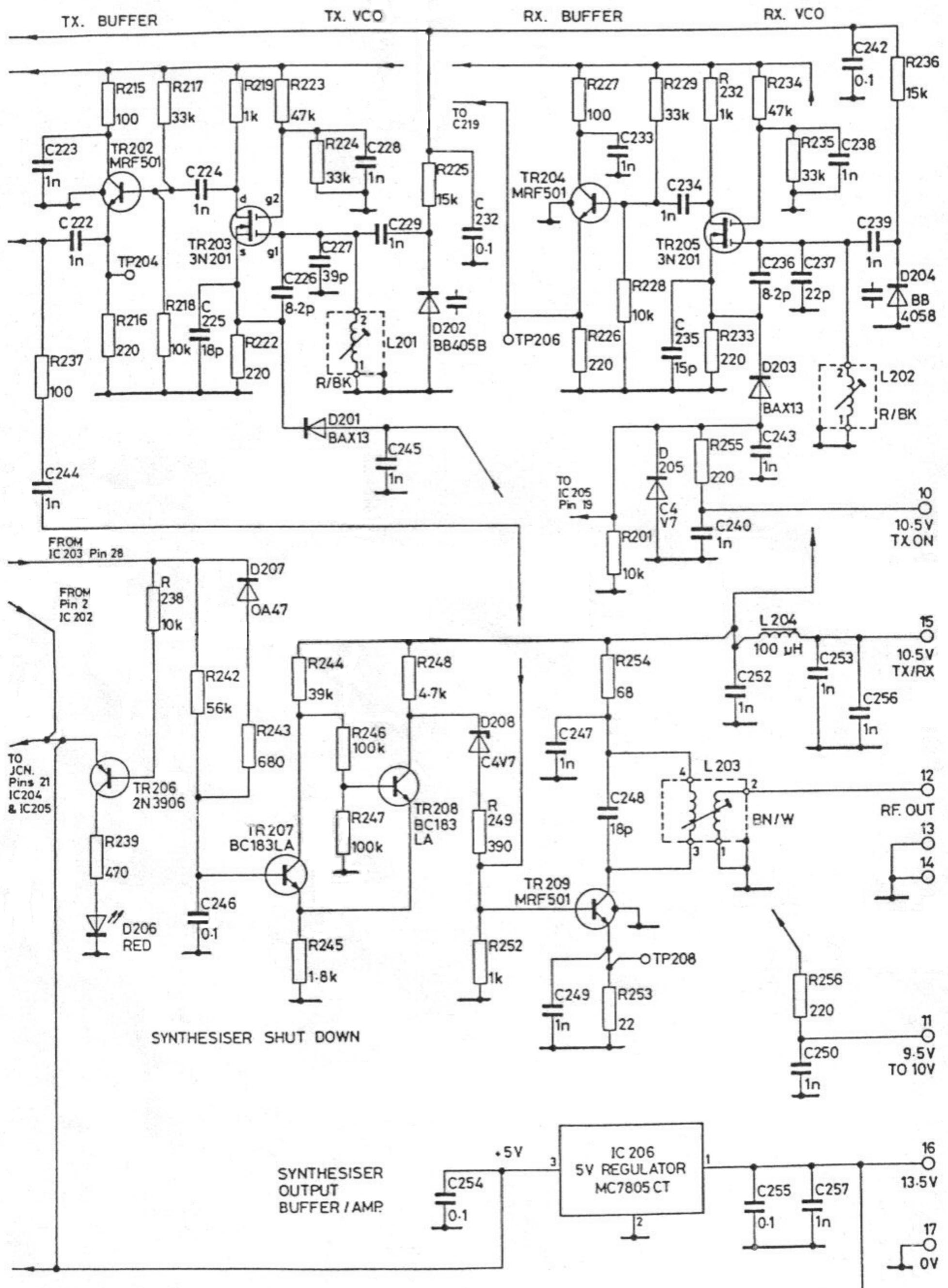


RECEIVER CIRCUIT DIAGRAM Fig 18





SYNTHESISER CIRCUIT DIAGRAM Fig 22



SYNTHESISER CIRCUIT DIAGRAM Fig 22