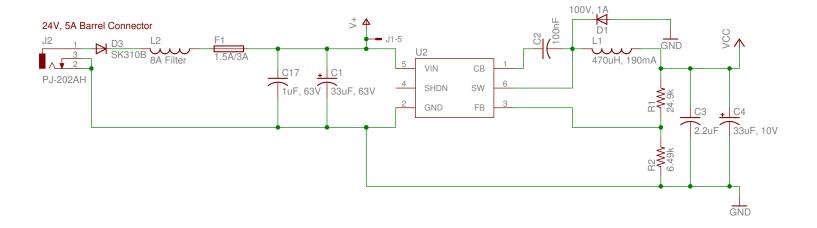
# 5-48VDC Input, 3.7VDC Output Regulator

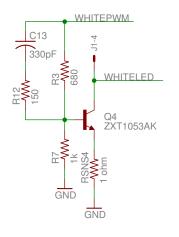


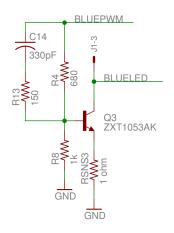
L1 - SRR6603-471ML D1 - DFLS1100-7 D3 - SK310B L2 - 74279226101 F1 - 0ZCD0150FF2C C1 - EEHZA1J220XP C4 - 10SVP33M

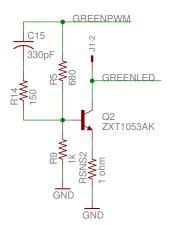
SaikoLED TeensyLED Breakout Board Brian Neltner, SaikoLED LLC, neltnerb@saikoled.com		
TITLE: TeensyLED	VERSION: X2	
Document Number: 15041400	REV: 15052000	
Date: 5/20/15 9:35 PM	Sheet: 1/4	

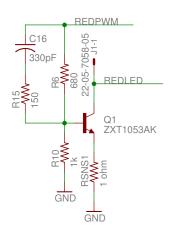
## 4x 700mA Current Sinks

RCTL Topology allows fast response with peak current limit of 25mA for compatibility with MK20DX256VLH7, but with <2mA steady-state current.

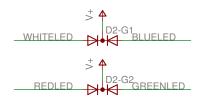






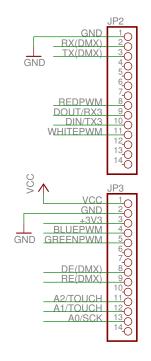


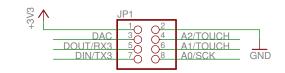
Flyback diodes prevent reverse bias on LEDs due to wire inductance when the current sink turns off.

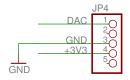


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TITLE: TeensyLED	VERSION: X2	
Document Number: 15041400	REV: 15052000	
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Teensy 3.1 Socket



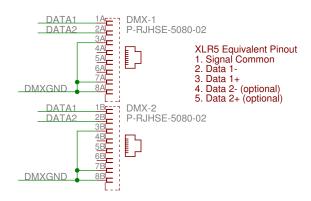


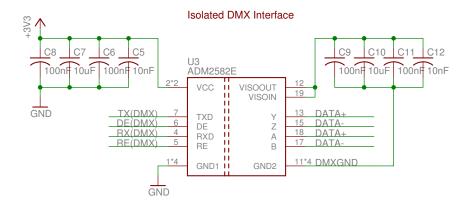


SaikoLED TeensyLED Breakout Board Brian Neltner, SaikoLED LLC, neltnerb@saikoled.com		
TITLE: TeensyLED	VERSION: X2	
Document Number: 15041400	REV: 15052000	
Date: 5/20/15 9:35 PM	Sheet: 3/4	

### **DMX** Interface

#### **RJ45 DMX Connector**





### DMX Signal Polarity Jumper

	J3		
DATA1	10	$\Omega^2$	DATA2
DATA+	3	$\bigcirc$ 4	DATA-
DATA2	5	$\sim$ 6	DATA1
		0	

SaikoLED TeensyLED Breakout Board Brian Neltner, SaikoLED LLC, neltnerb@saikoled.com		
TITLE: TeensyLED	VERSION: X2	
Document Number: 15041400	REV: 15052000	
Date: 5/20/15 9:35 PM	Sheet: 4/4	