

Interface Pin No 3 Wire SPI 4 Wire SPI Sym 1 GND **GND GND** Backup options for OLED reset line CFAL12832D-B* OLED Display 3-wire SPI mode. 2 D2 NC NC GPIO control on CS or alt or driven from VDD with an RC delay $> 110\,\mathrm{ms}.$ 3 D1 SDA SDA Note: top side ZIF connector has pins reversed from FPC. 4 D0 CLK SCLK 5 D/C# NC D/C DIP_SWITCH_X04 6 RES# RESET RESET MOSID <u>VDD_3V3_F</u> OLED_SCK 9 SCKD 7 CS# **GND GND** RES_ALTD 8 **GND** BS1 **GND** OLED_BS1 R401 BS0 Vcc **GND** OLED_BSO 4 R402 R401 VDD_3V3**▷**-OLED_RES 10 Vdd Vcc** Vcc** VDD_3V3_F C401 dnp-4.7uF Vcc** Vcc** 11 Vbat 20mA approx max 12 12 GND **GND GND** P401 OLED_CS GND +3.3v Supply voltage Ground Supply ground U401 Notes: Tie D2 and D1 together **

Microcontroller Control lines defined by layout / code Okay to Tie Vdd and Vbat together

I2C

GND

SDA*

SDA*

SCL

Vcc

RESET

GND

Vcc

GND

Vcc**

Vcc**

GND

128x32 OLED Display CFAL12832D-B

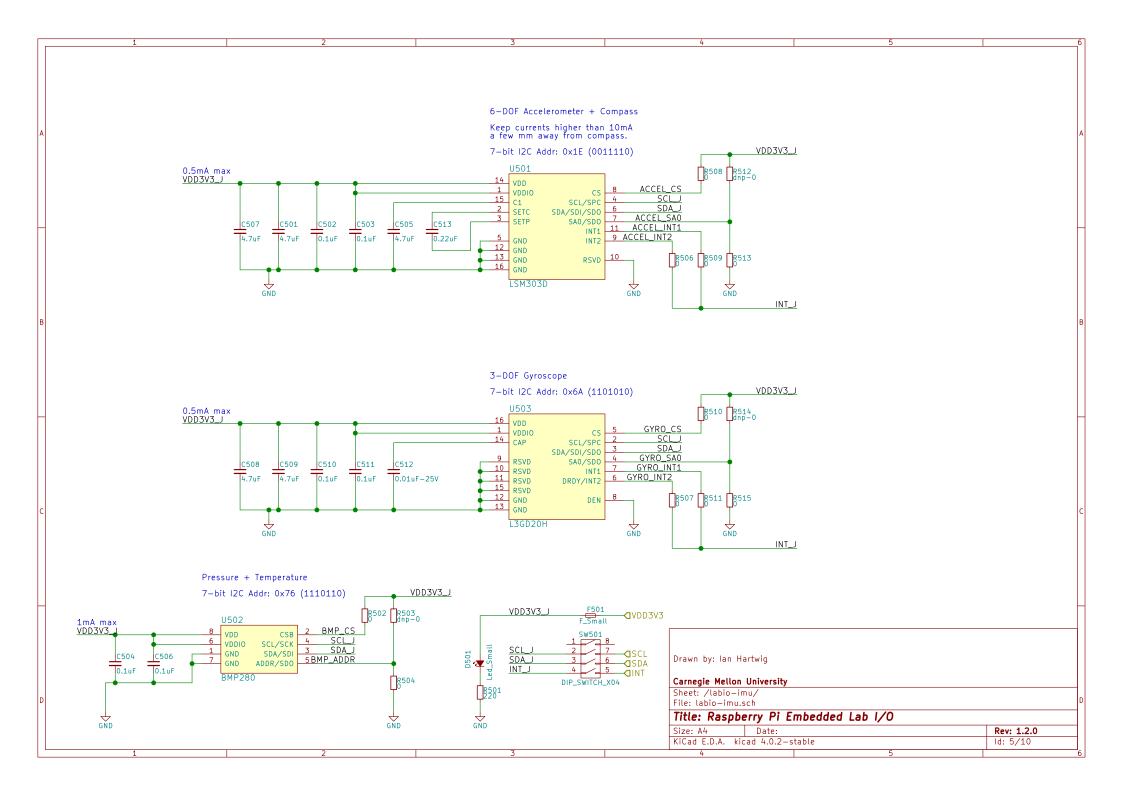
Drawn by: Ian Hartwig

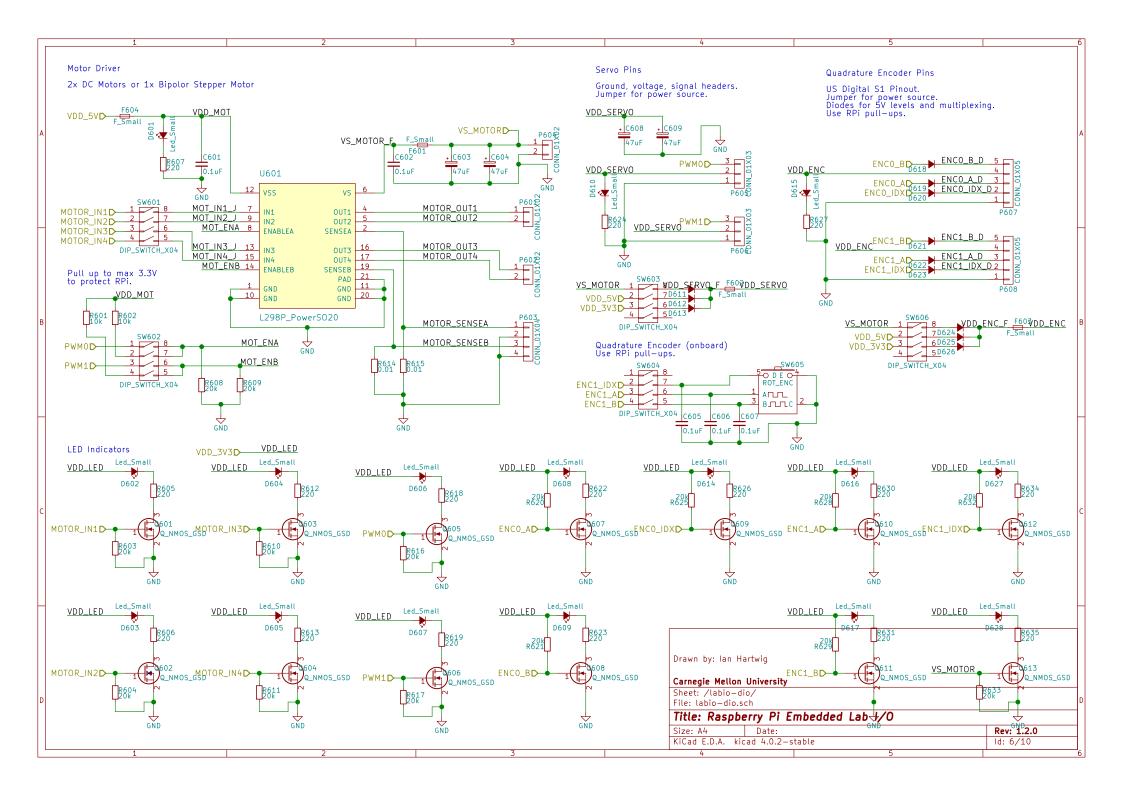
Carnegie Mellon University

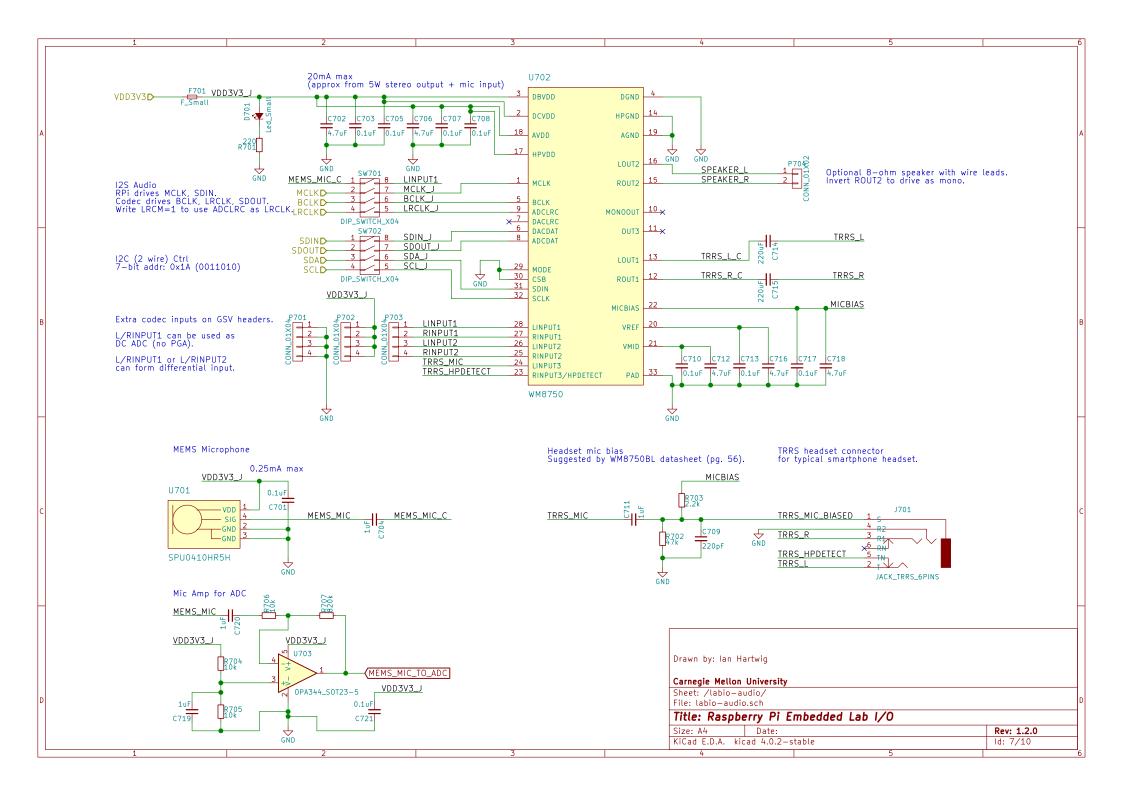
Sheet: /labio-display/ File: labio-display.sch

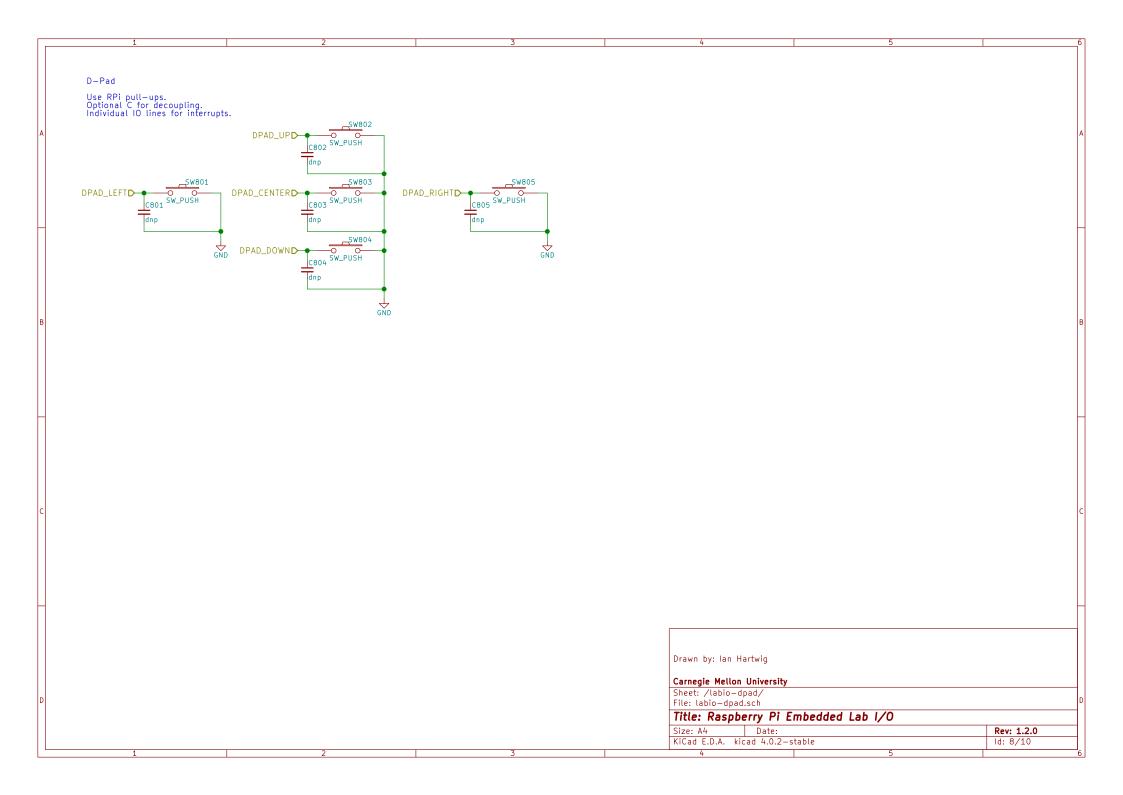
Title: Raspberry Pi Embedded Lab I/O

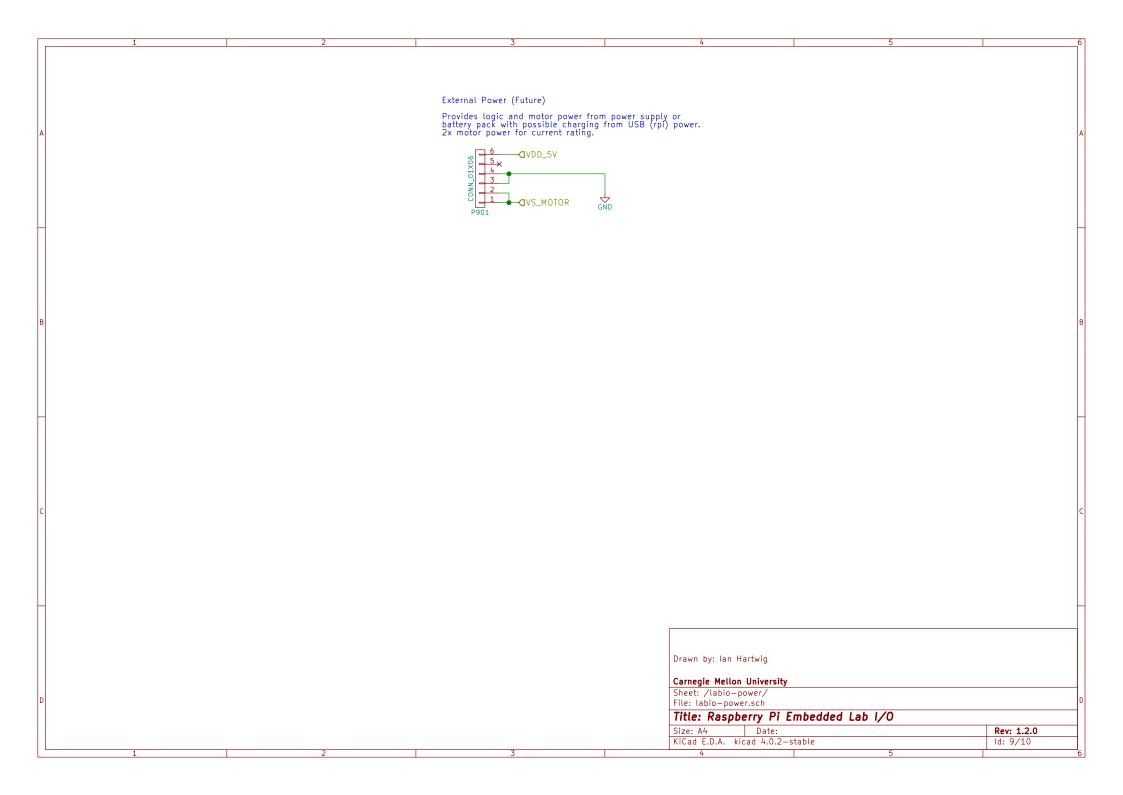
Size: A4 Date: Rev: 1.2.0 KiCad E.D.A. kicad 4.0.2-stable ld: 4/10











RPi-like Mounting Holes

M3 for M3/no 4/M2.5 hardware.

H1001 H1003 H1004 H1005 H1007 H1008 H1009 \bigcirc_{1}^{1} \bigcirc_{1}^{1} \bigcirc_{1}^{1} \bigcirc_{1}^{1} \bigcirc_{1}^{1} \bigcirc_{1}^{1} \bigcirc_{1}^{1}

hole hole hole hole hole hole

Heatsink Mounting

25x25mm with 30x30mm M3 holes

H1002 H1006

<u>1</u>× <u>1</u>×

hole hole

WiSE Lab WiSE Lab

Carnegie Mellon Carnegie Mellon

Drawn by: Ian Hartwig

Carnegie Mellon University

Sheet: /labio-mech/ File: labio-mech.sch

Title: Raspberry Pi Embedded Lab I/O

 Size: A4
 Date:
 Rev: 1.2.0

 KiCad E.D.A. kicad 4.0.2-stable
 Id: 10/10