

TITLE: bkt-esp32

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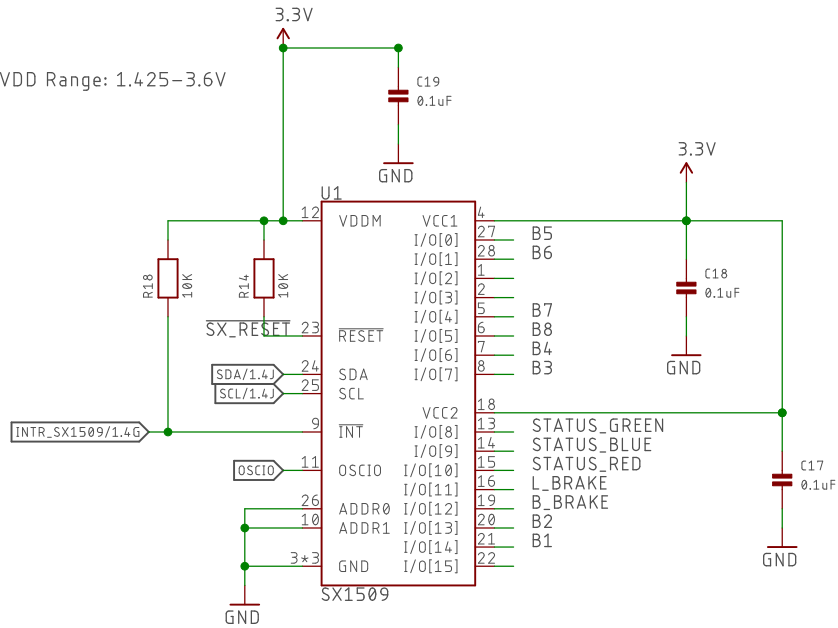
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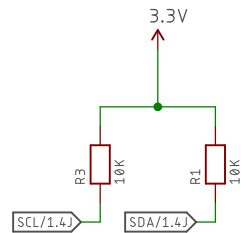
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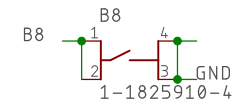
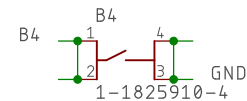
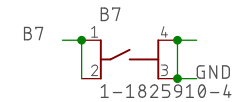
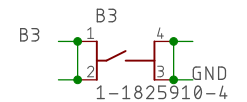
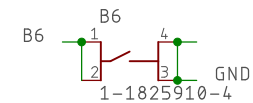
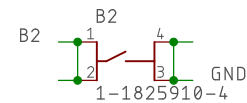
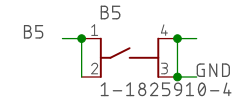
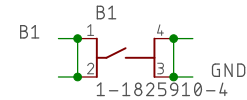
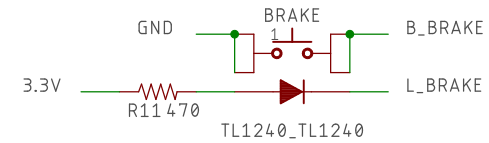
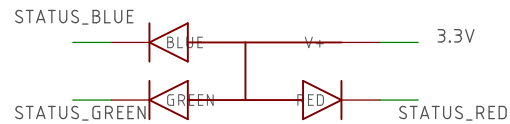
VDD Range: 1.425–3.6V



I2C Pull-up Resistors



SJ1 enables/disables 10k pull-up resistors on the SDA and SCL lines. (Default: closed)



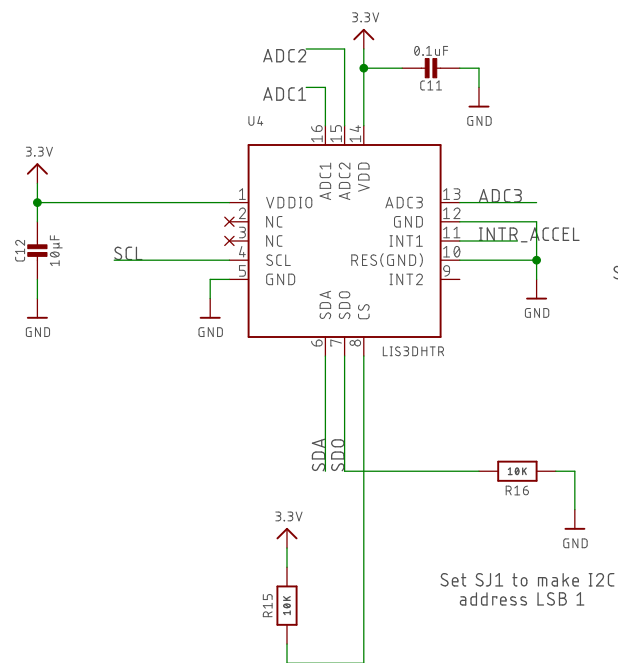
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* CS needs to be set off board to select SPI or I2C
CS High = I2C, Low = SPI
Defaults to I2C as-is

I2C

SCL (Pin 4) = Serial Clock
SDA (Pin 6) = Serial Data
SD0 (Pin 7) = SA0/Address LSB

SD0 pulled low by default to set I2C address LSB to 0

SPI

SCL (Pin 4) = Serial Clock
SDA (Pin 6) = Data In (MOSI)
SD0 (Pin 7) = Data Out (MISO)

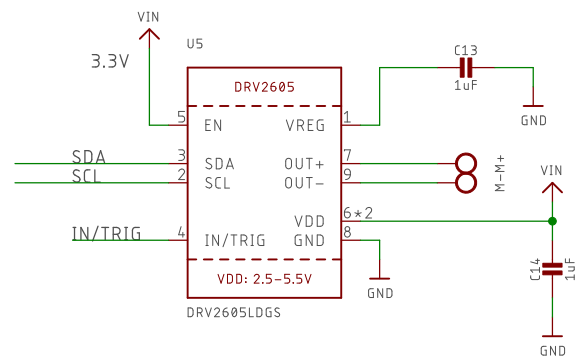
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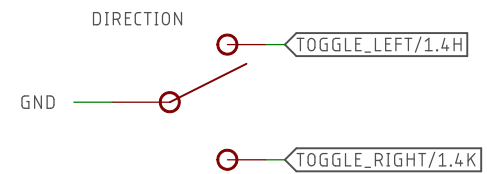
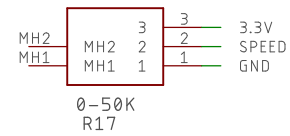
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Speed Control



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