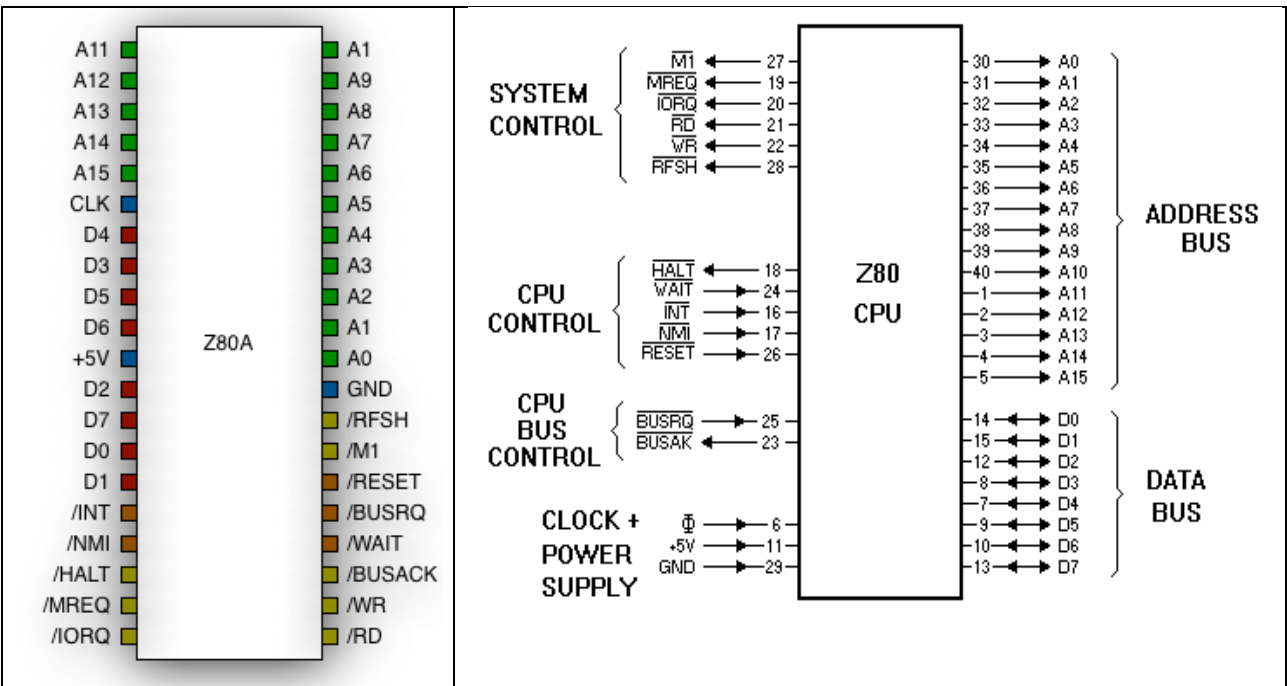
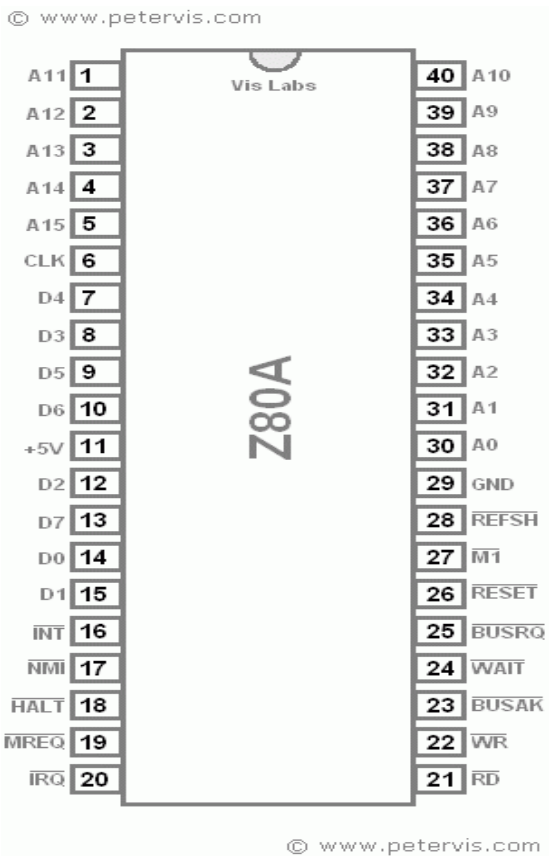



# Processador Z80

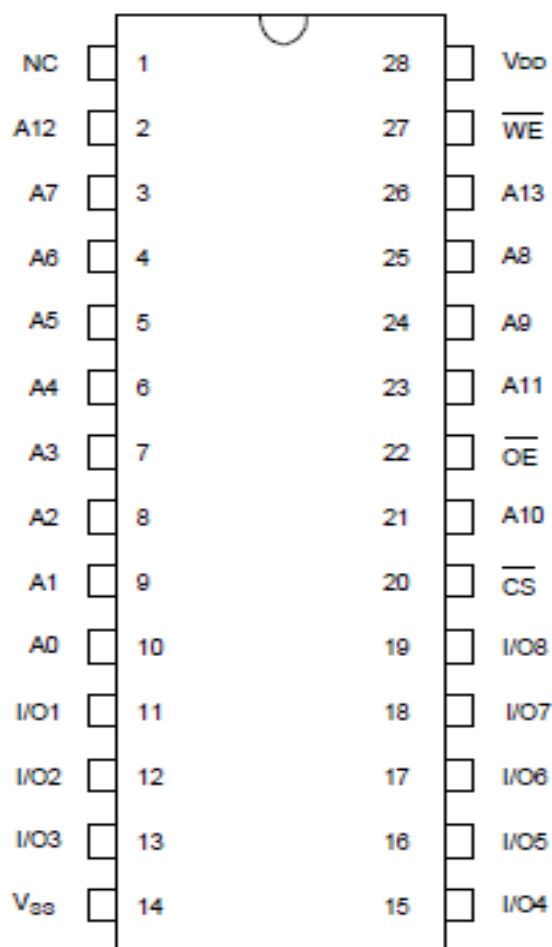


## Memória RAM (Memória de Dados - MD)

**W24129A**

**Winbond**  
Electronics Corp.

**16K × 8 HIGH-SPEED CMOS STATIC RAM**



### PIN DESCRIPTION

SYMBOL	DESCRIPTION
A0-A13	Address Inputs
I/O1-I/O8	Data Inputs/Outputs
$\overline{\text{CS}}$	Chip Select Input
$\overline{\text{WE}}$	Write Enable Input
$\overline{\text{OE}}$	Output Enable Input
VDD	Power Supply
VSS	Ground

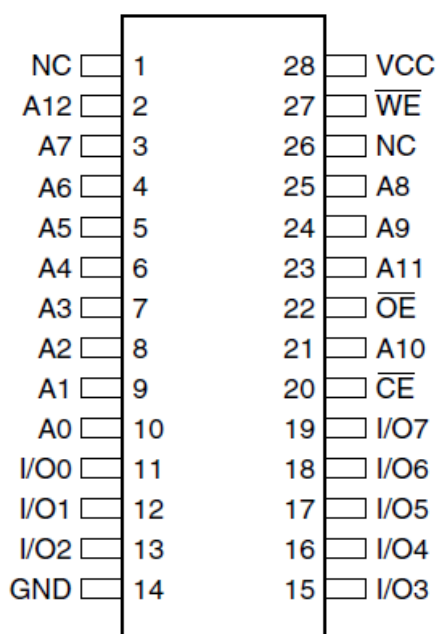
# Memória EEPROM (Memória de Programa - MP)



**AT28C64B**

64K (8K x 8)  
Parallel  
EEPROM with  
Page Write and  
Software Data  
Protection

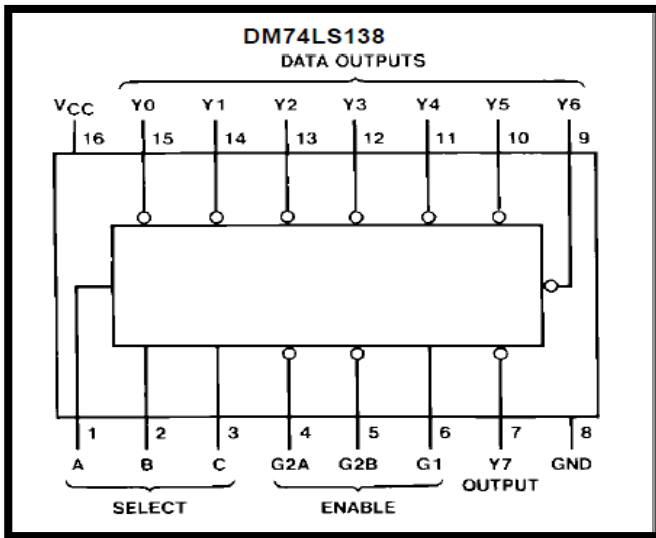
## 2.1 28-lead PDIP, 28-lead SOIC Top View



## 2. Pin Configurations

Pin Name	Function
A0 - A12	Addresses
CE	Chip Enable
OE	Output Enable
WE	Write Enable
I/O0 - I/O7	Data Inputs/Outputs
NC	No Connect
DC	Don't Connect

## Decodificador 3x8 – Modelo 74138

[illegible]

H - High, L - Low, X - Don't Care

