mux_4_to_1:1 ibuf lut3 lut2 muxf5 x(3:0) ibuf x_1_IBUF obuf 0 y_OBUF en_IBUF Mmux_y_6_f5 Mmux_y_8 en1 ibuf ibuf lut3 sel(1:0) sel_0_IBUF x_0_IBUF 0 Mmux_y_7 ibuf ibuf sel_1_IBUF x_3_IBUF ibuf x_2_IBUF

mux_4_to_1