INSTRUCTION\_REGISTERR

INSTRUCTION DECODER

**INSTRUCTION UNIT**

CSR

ADDER

**D  
A  
T  
A  
  
B  
U  
S  
  
x\_resul  
  
m\_res  
  
W\_res**

GPIO

bram

timer

uart

Slave0

Slave(n)

**A  
L  
U**

REGISTER\_FILE

**LOAD\_STORE\_UNIT**

**A  
L  
U**

BYPASS AND INTERLOCK

MULTIPLIER

SHIFTER

LOGICAL

W I S H B O N E