

# INTRODUCTION TO COMPUTER ARCHITECTURE

---

Prof. Sebastian Eslava M.Sc. Ph.D.

[jseslavag@unal.edu.co](mailto:jseslavag@unal.edu.co)

Universidad Nacional de Colombia

Facultad de Ingeniería

Departamento de Ingeniería Eléctrica y  
Electrónica

# What is the course about?

- About computer organization and architecture
- Computers are ubiquitous.
- Question: What is a computer?
  - In general, a computer is “a machine that can solve problems for people by carrying out instructions given to it.”
    - PC, Desktop, Notebook, Workstation, Mainframes, Supercomputer



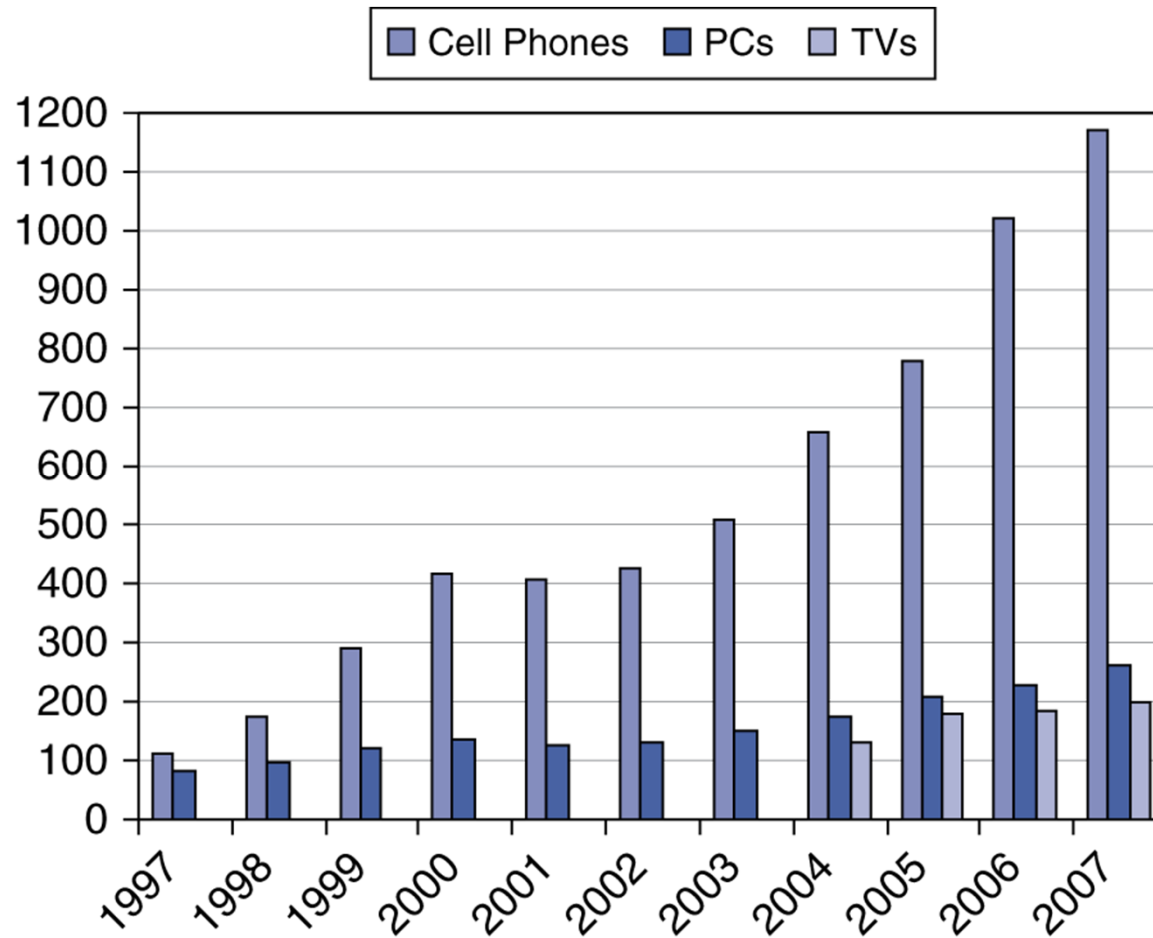
# Classes of Computing Applications and their characteristics

|                              | Type                       | Price (\$) | Example application             |
|------------------------------|----------------------------|------------|---------------------------------|
| <b>3. Embedded computers</b> | Disposable computer        | 0.5        | Greeting cards                  |
|                              | Microcontroller            | 5          | Watches, cars, appliances       |
|                              | Game computer              | 50         | Home video games                |
| <b>1. Desktop computers</b>  | Personal computer          | 500        | Desktop or notebook computer    |
| <b>2. Servers</b>            | Server                     | 5K         | Network server                  |
|                              | Collection of Workstations | 50–500K    | Departmental minisupercomputer  |
|                              | Mainframe                  | 5M         | Batch data processing in a bank |

# The Computer Revolution

- Progress in computer technology
  - Underpinned by Moore's Law
- Makes novel applications feasible
  - Computers in automobiles
  - Cell phones
  - Human genome project
  - World Wide Web
  - Search Engines
- Computers are pervasive

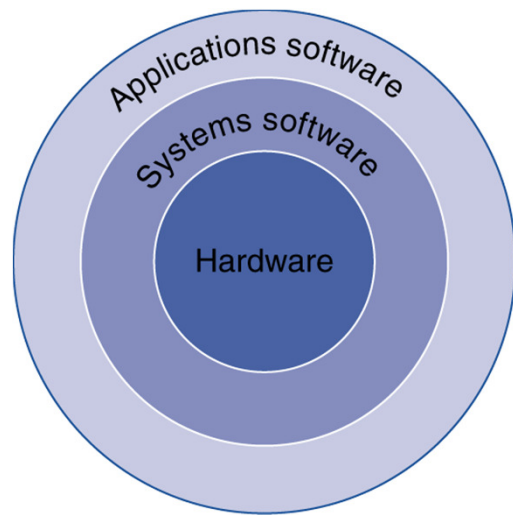
# The Processor Market



# What You Will Learn

- How programs are translated into the machine language
  - And how the hardware executes them
- The hardware/software interface
- What determines program performance
  - And how it can be improved
- How hardware designers improve performance
- What is parallel processing

# Below Your Program



- Application software
  - Written in high-level language
- System software
  - Compiler: translates HLL code to machine code
  - Operating System: service code
    - Handling input/output
    - Managing memory and storage
    - Scheduling tasks & sharing resources
- Hardware
  - Processor, memory, I/O controllers

# Levels of Program Code

- High-level language
  - Level of abstraction closer to problem domain
  - Provides for productivity and portability
- Assembly language
  - Textual representation of instructions
- Hardware representation
  - Binary digits (bits)
  - Encoded instructions and data

High-level  
language  
program  
(in C)

```
swap(int v[], int k)
{int temp;
  temp = v[k];
  v[k] = v[k+1];
  v[k+1] = temp;
}
```

Compiler

Assembly  
language  
program  
(for MIPS)

```
swap:
    muli $2, $5, 4
    add  $2, $4, $2
    lw   $15, 0($2)
    lw   $16, 4($2)
    sw   $16, 0($2)
    sw   $15, 4($2)
    jr   $31
```

Assembler

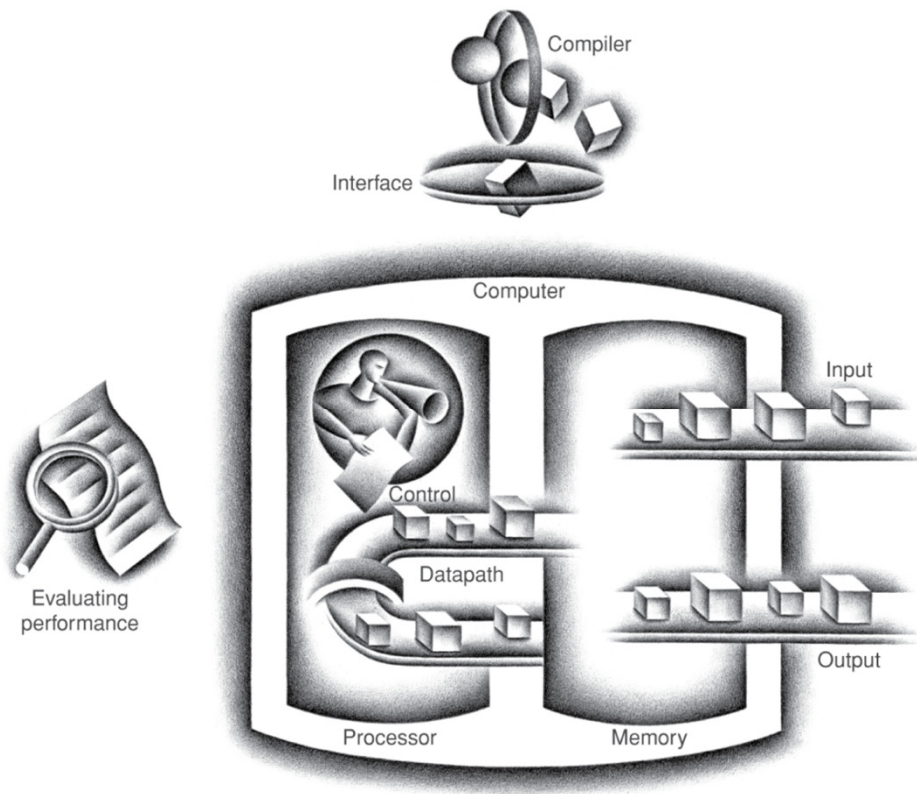
Binary machine  
language  
program  
(for MIPS)

```
000000001010000100000000000011000
000000000000110000001100000100001
100011000110001000000000000000000
1000110011110010000000000000000100
101011001111001000000000000000000
1010110001100010000000000000000100
00000011111000000000000000001000
```



# Components of a Computer

## The BIG Picture



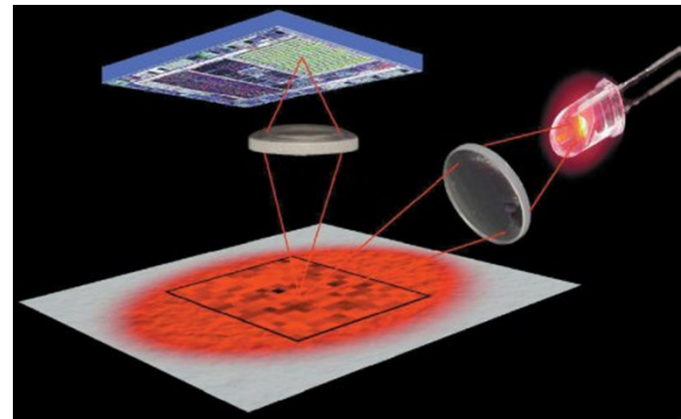
- Same components for all kinds of computer
  - Desktop, server, embedded
- Input/output includes
  - User-interface devices
    - Display, keyboard, mouse
  - Storage devices
    - Hard disk, CD/DVD, flash
  - Network adapters
    - For communicating with other computers

# Anatomy of a Computer



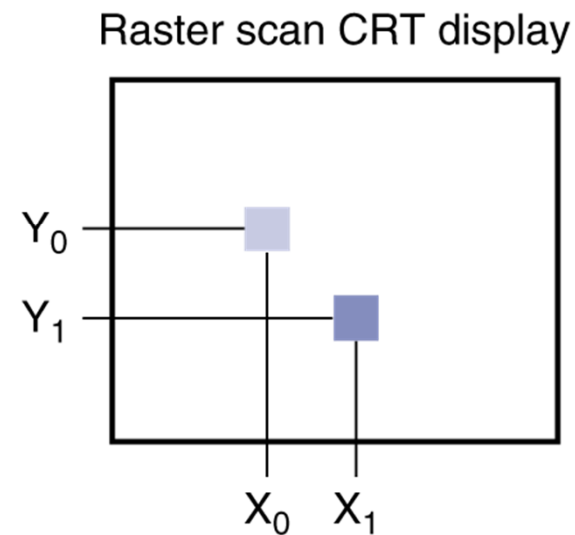
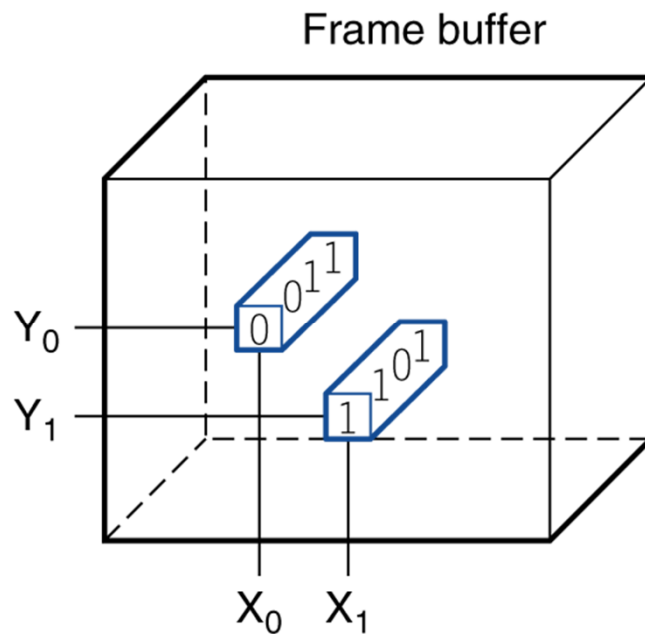
# Anatomy of a Mouse

- Optical mouse
  - LED illuminates desktop
  - Small low-res camera
  - Basic image processor
    - Looks for x, y movement
  - Buttons & wheel
- Supersedes roller-ball mechanical mouse

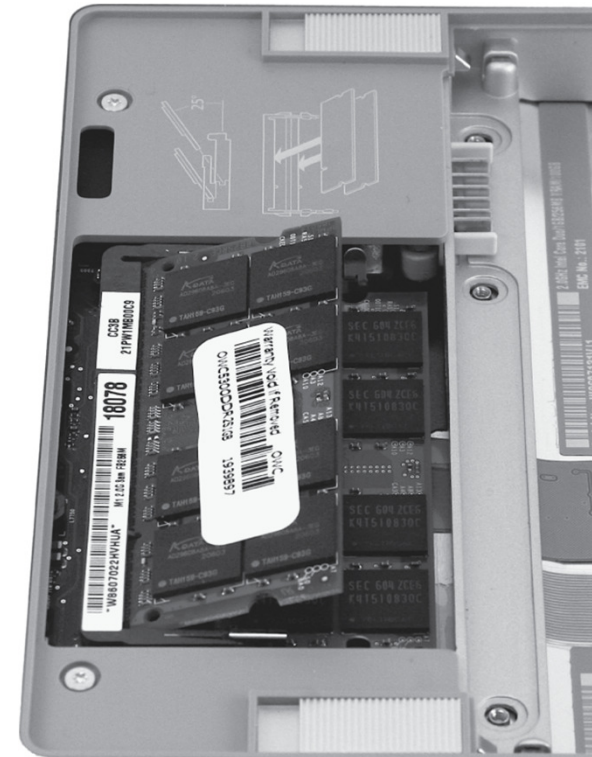
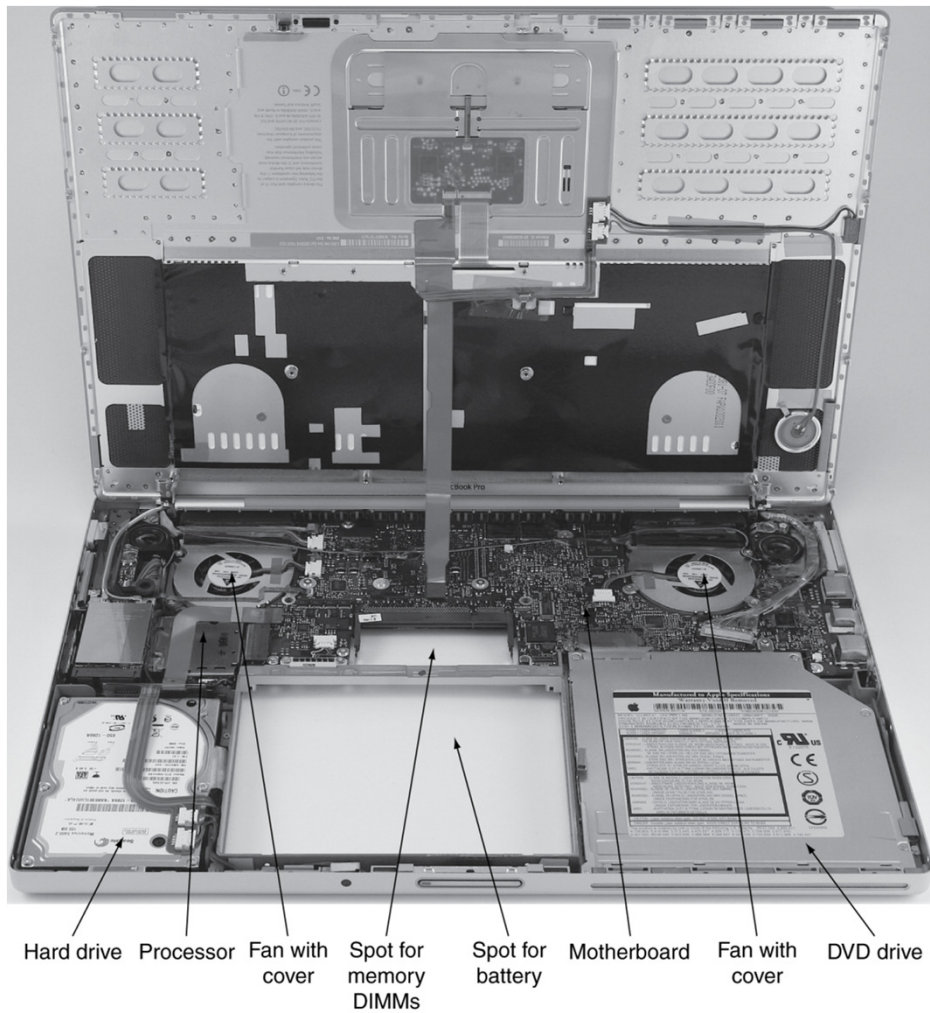


# Through the Looking Glass

- LCD screen: picture elements (pixels)
  - Mirrors content of frame buffer memory

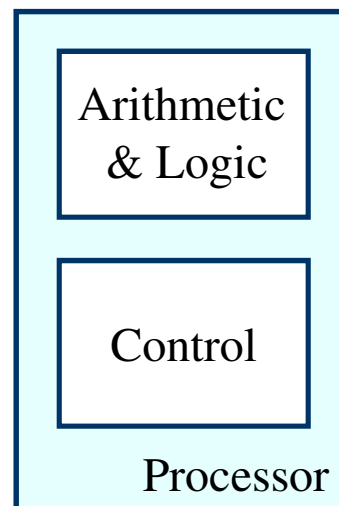


# Opening the Box



# Inside the Processor (CPU)

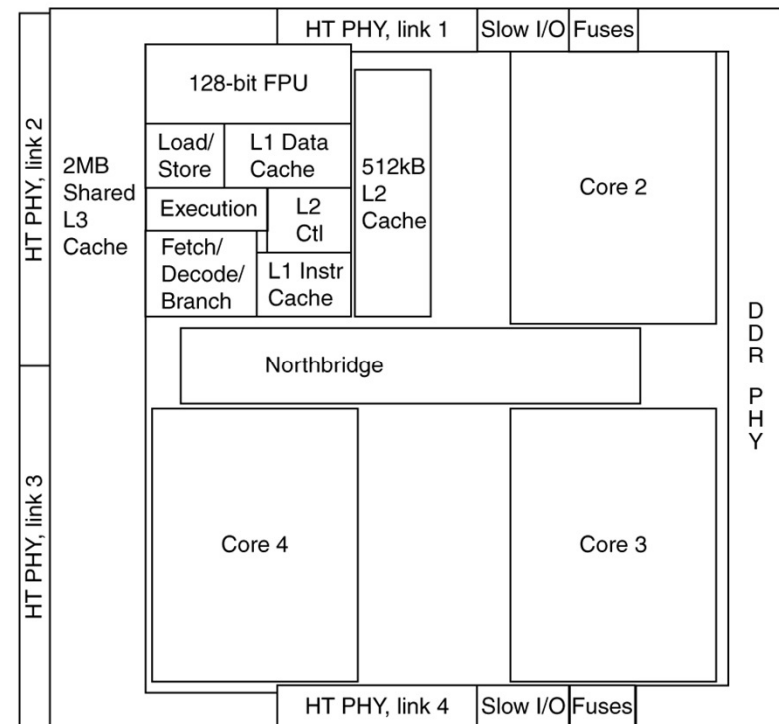
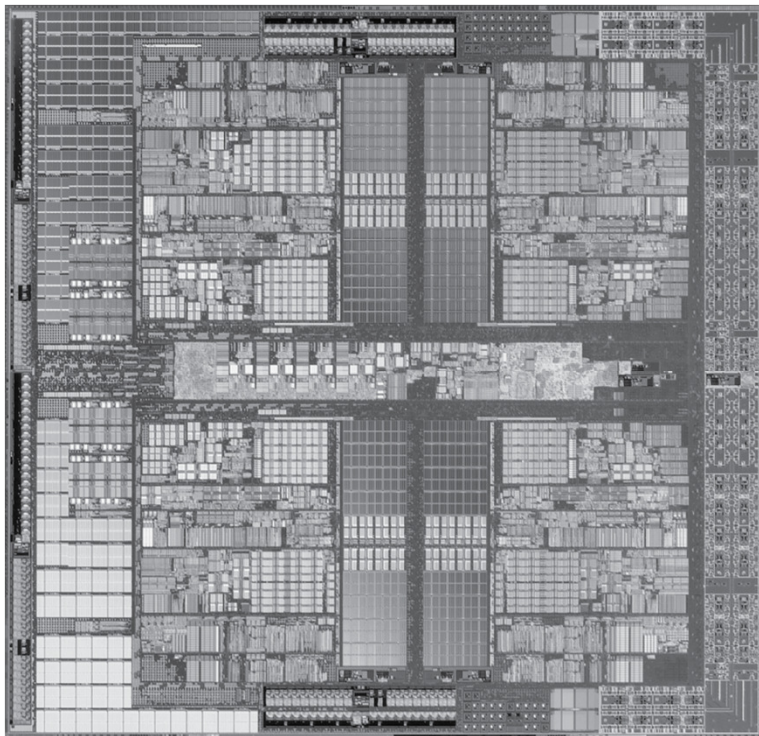
- Datapath: performs operations on data
- Control: sequences datapath, memory, ...
- Cache memory
  - Small fast SRAM memory for immediate access to data





# Inside the Processor

- AMD Barcelona: 4 processor cores



# Abstractions

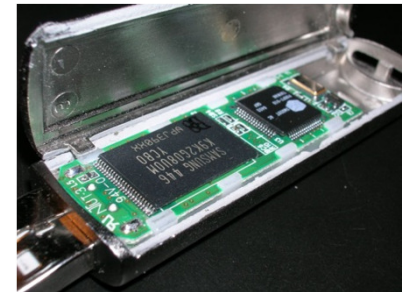
## The BIG Picture

- Abstraction helps us deal with complexity
  - Hide lower-level detail
- Instruction set architecture (ISA)
  - The hardware/software interface
- Application binary interface
  - The ISA plus system software interface
- Implementation
  - The details underlying and interface



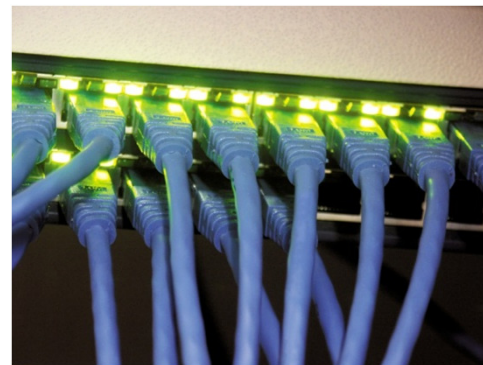
# A Safe Place for Data

- Volatile main memory
  - Loses instructions and data when power off
- Non-volatile secondary memory
  - Magnetic disk
  - Flash memory
  - Optical disk (CDROM, DVD)



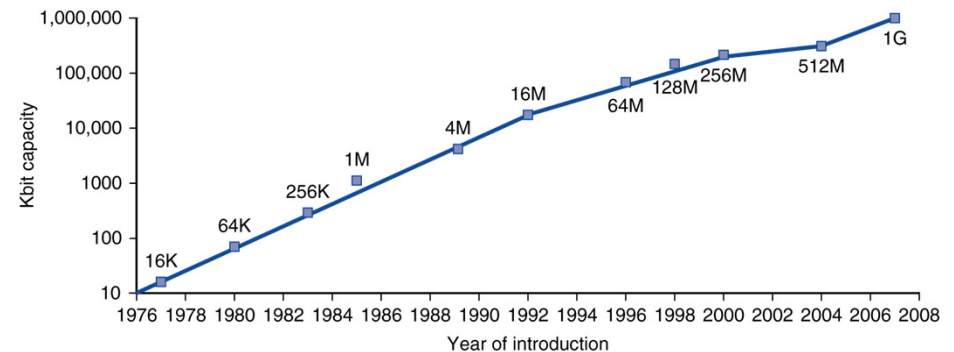
# Networks

- Communication and resource sharing
- Local area network (LAN): Ethernet
  - Within a building
- Wide area network (WAN: the Internet
- Wireless network: WiFi, Bluetooth



# Technology Trends

- Electronics technology continues to evolve
  - Increased capacity and performance
  - Reduced cost



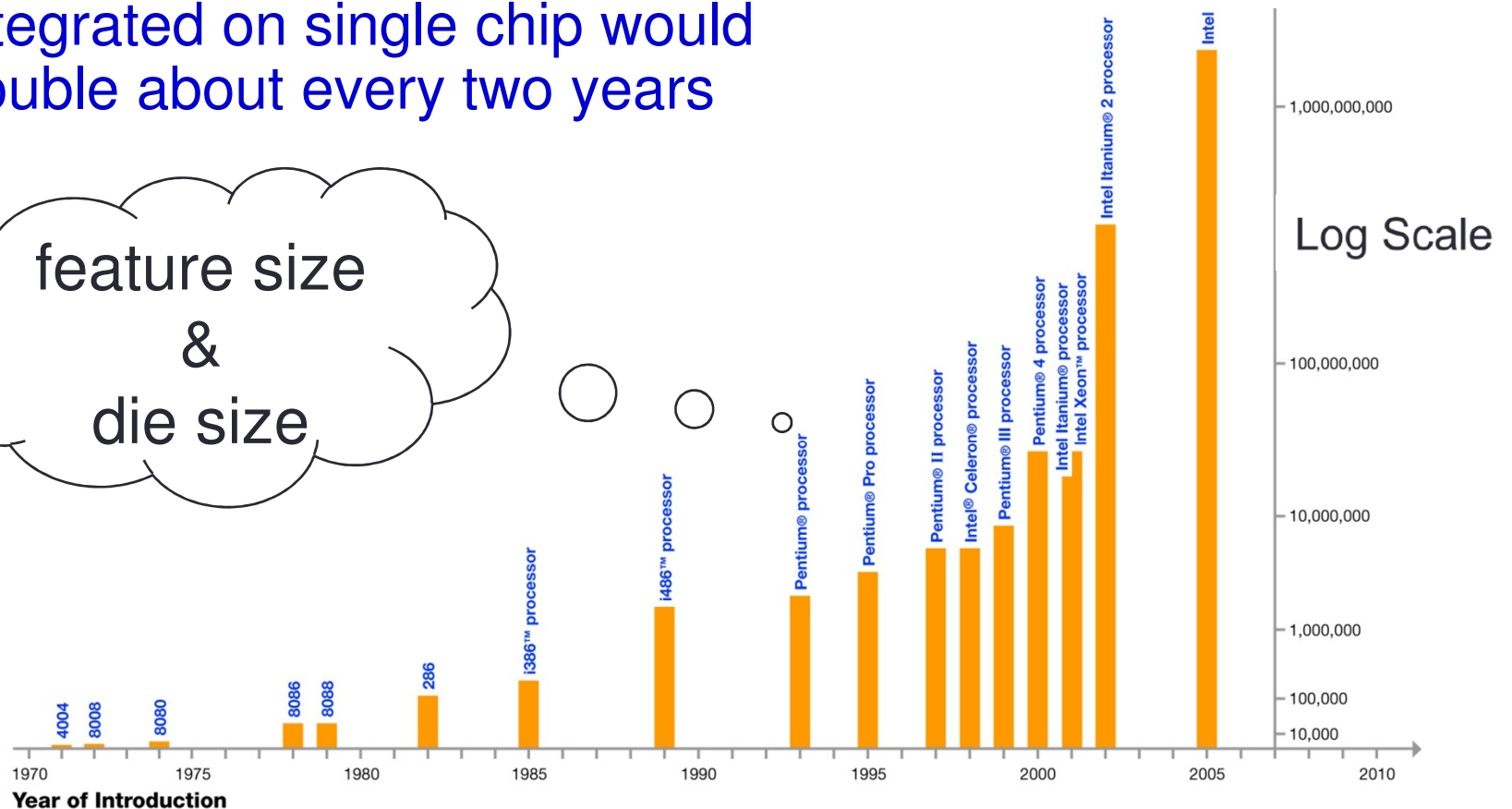
DRAM capacity

| Year | Technology                 | Relative performance/cost |
|------|----------------------------|---------------------------|
| 1951 | Vacuum tube                | 1                         |
| 1965 | Transistor                 | 35                        |
| 1975 | Integrated circuit (IC)    | 900                       |
| 1995 | Very large scale IC (VLSI) | 2,400,000                 |
| 2005 | Ultra large scale IC       | 6,200,000,000             |

# Moore's Law

- In 1965, Intel's Gordon Moore predicted that the number of transistors that can be integrated on single chip would double about every two years

feature size  
&  
die size



\*Note: Vertical scale of chart not proportional to actual Transistor count.

Courtesy, Intel ®

# Technology Scaling Road Map (ITRS)

| Year                | 2004 | 2006 | 2008 | 2010 | 2012 |
|---------------------|------|------|------|------|------|
| Feature size (nm)   | 90   | 65   | 45   | 32   | 22   |
| Intg. Capacity (BT) | 2    | 4    | 6    | 16   | 32   |

- Fun facts about 45nm transistors
  - 30 million can fit on the head of a pin
  - You could fit more than 2,000 across the width of a human hair
  - If car prices had fallen at the same rate as the price of a single transistor has since 1968, a new car today would cost about 1 cent

# Another Example of Moore's Law Impact

DRAM capacity growth over 3 decades

