Davis 109006201 https://github.com/davizjc/Embedded-System-Lab-EXAM.git
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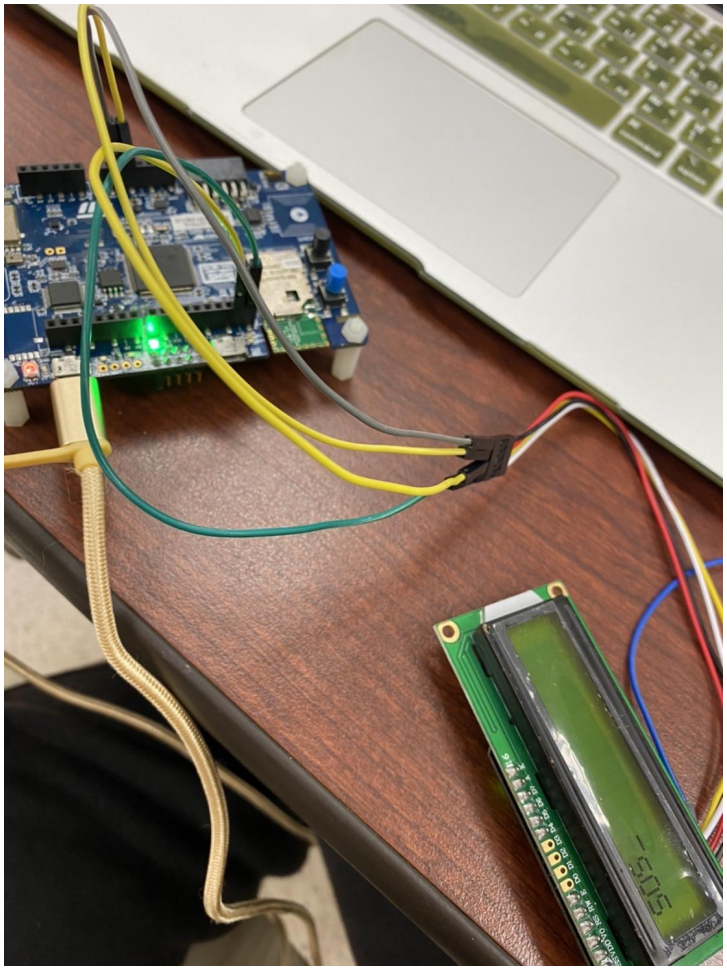
Exam1

1. Exam1-p1

- 

2. Exam1-p2

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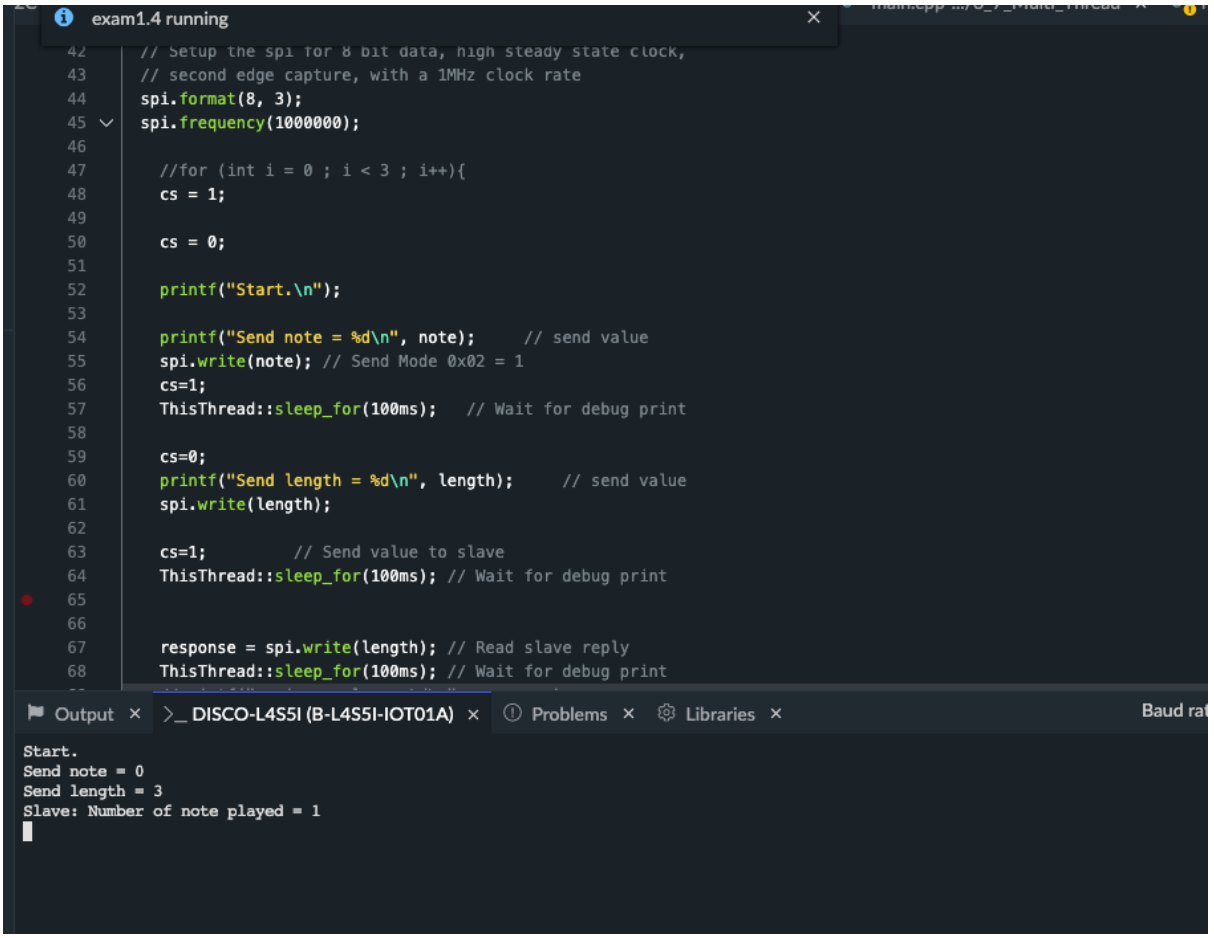
3. Exam1 - p3

4. exam 1 - p4


exam1.4 running
// Setup the spi for 8 bit data, high steady state clock,
// second edge capture, with a 1MHz clock rate
spi.format(8, 3);
spi.frequency(1000000);
//for (int i = 0 ; i < 3 ; i++){
cs = 1;
cs = 0;
printf("Start.\n");
printf("Send note = %d\n", note); // send value
spi.write(note); // Send Mode 0x02 = 1
cs=1;
ThisThread::sleep_for(100ms); // Wait for debug print
cs=0;
printf("Send length = %d\n", length); // send value
spi.write(length);
cs=1; // Send value to slave
ThisThread::sleep_for(100ms); // Wait for debug print
response = spi.write(length); // Read slave reply
ThisThread::sleep_for(100ms); // Wait for debug print
Output
DISCO-L4S5I (B-L4S5I-IOT01A)
Problems
Libraries
Start.
Send note = 0
Send length = 3
Slave: Number of note played = 1