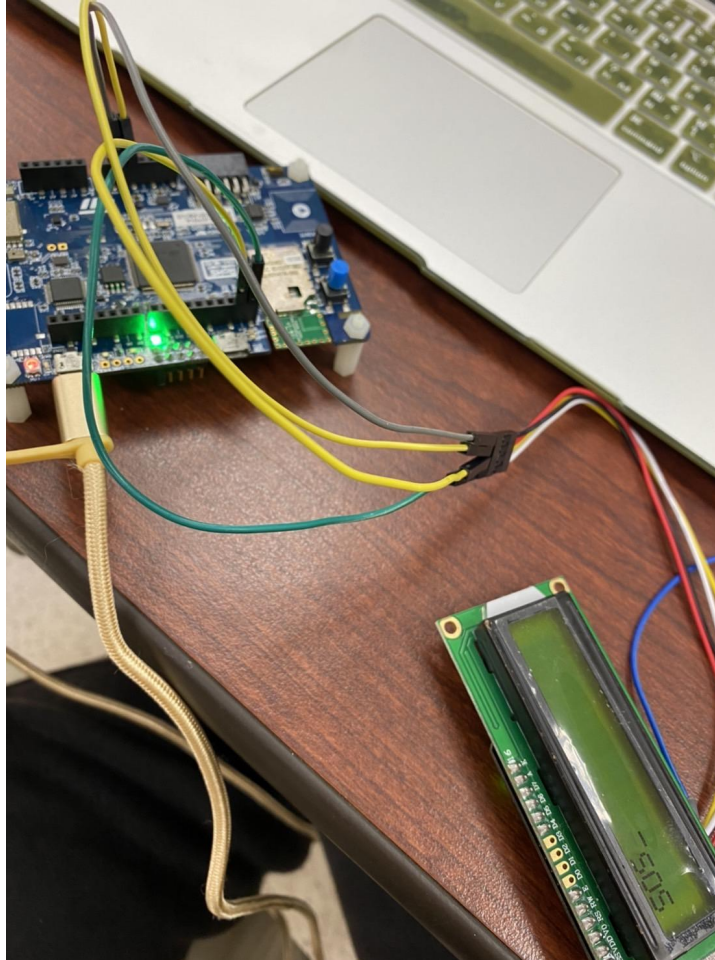


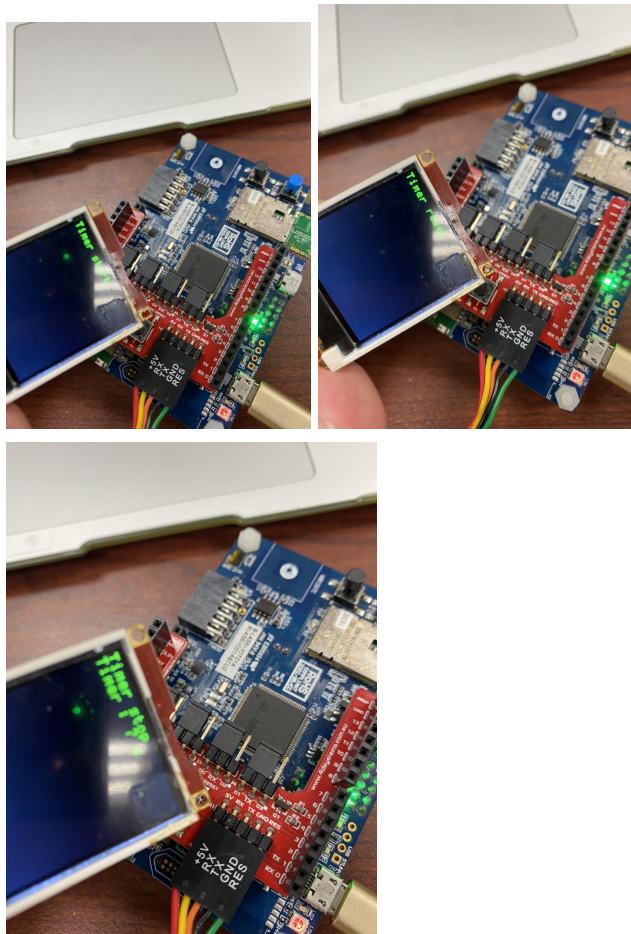
Davis 109006201 <https://github.com/davizjc/Embedded-System-Lab-EXAM.git>
周志偉 git@github.com:davizjc/Embedded-System-Lab-EXAM.git

Exam1

1. Exam1-p1

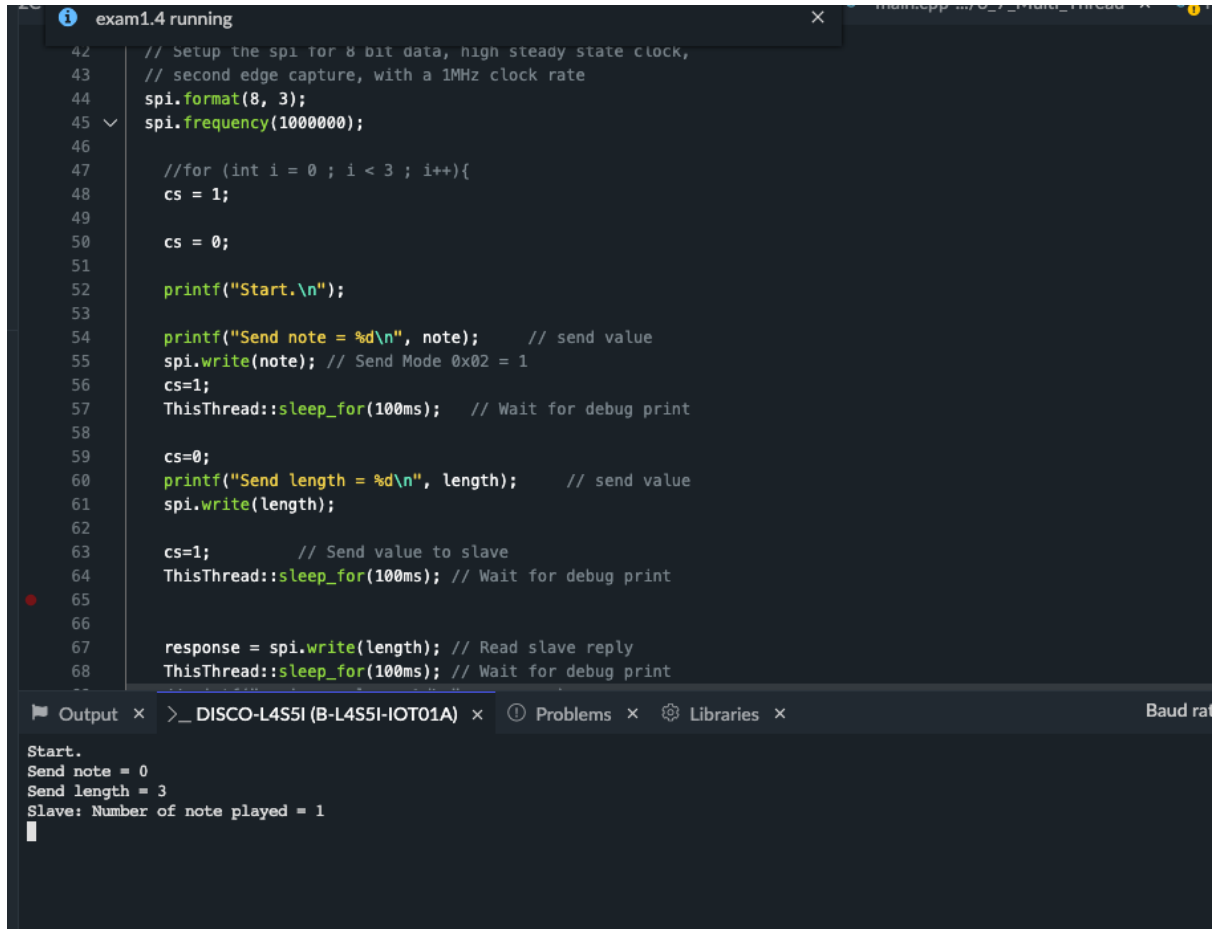


2. Exam1-p2



3. Exam1 - p3

4. exam 1 - p4



The screenshot shows an IDE with a C program for SPI communication. The code is in a file named 'exam1.4 running'. It configures an SPI interface for 8-bit data and 1MHz clock rate. It then sends a note value and a length value to a slave device. The output window shows the execution results.

```
42 // Setup the spi for 8 bit data, high steady state clock,  
43 // second edge capture, with a 1MHz clock rate  
44 spi.format(8, 3);  
45 spi.frequency(1000000);  
46  
47 //for (int i = 0 ; i < 3 ; i++){  
48 cs = 1;  
49  
50 cs = 0;  
51  
52 printf("Start.\n");  
53  
54 printf("Send note = %d\n", note); // send value  
55 spi.write(note); // Send Mode 0x02 = 1  
56 cs=1;  
57 ThisThread::sleep_for(100ms); // Wait for debug print  
58  
59 cs=0;  
60 printf("Send length = %d\n", length); // send value  
61 spi.write(length);  
62  
63 cs=1; // Send value to slave  
64 ThisThread::sleep_for(100ms); // Wait for debug print  
65  
66  
67 response = spi.write(length); // Read slave reply  
68 ThisThread::sleep_for(100ms); // Wait for debug print
```

Output:

```
Start.  
Send note = 0  
Send length = 3  
Slave: Number of note played = 1
```