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# Design and Implementation of a Digital PID Controller for DC-DC Buck Converter

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**Abstract**—This paper presents the design, simulation and real-time implementation of a digital PID controller for DC-DC buck converter. The developed controller is capable to drive the output voltage of the buck converter to track a desired voltage reference regardless of input voltage or load variations. The parameters of the digital PID controller are calculated based on the transfer function of the buck converter and Matlab PIDTOOL. An Arduino Uno is used to implement the developed controller. Numerical simulation and experimental validation are presented to demonstrate the effectiveness of the developed controller in terms of overshoot limitations and accuracy.

**Index Terms**—Arduino Uno, DC-DC buck converter, Digital PID controller.

## I. INTRODUCTION

DC-DC converters perform two functions: change the output voltage level (increase or decrease) and provide a constant load voltage. There are different topologies of DC-DC converters, among them, we can cite the buck converter which is used often to step-down voltage from its input to its load. Many industrial applications use this kind of converters (battery chargers, computers, medical devices...) due to their high efficiency, low cost, reduced size. However, the control of these converters are still a difficult task because such a system exhibits a nonlinear model, and disturbances and uncertainties [1]- [8].

Recently, many control techniques for DC-DC buck converter have been proposed such as linearization control [9], [10], backstepping control [11], adaptive control [12], sliding mode [13], fuzzy logic control [14], [15] and fuzzy Takagi-Sugeno models based control [8]. Among all these techniques, the PID control still the most commonly used technique due to its easy practical implementation and low cost [16]- [19].

The main contribution of this paper is the design, simulation and real-time implementation of a new PID controller for a buck converter using an Arduino Uno and Matlab/Simulink environment. Firstly, the mathematical model of the buck converter is presented. Secondly, the continuous-time PID

controller and its digital form are developed. Next, simulation and experimental tests are presented to show the effectiveness of the developed controller in terms of time responses, overshoot limitations and accuracy. Finally, the paper is ended by conclusion.

## II. MODELLING OF DC-DC BUCK CONVERTER

The DC-DC buck converter circuit is shown in Fig.1 which consists of a DC power supply  $V_i$ , MOSFET transistor, output capacitor  $C$ , inductor  $L$ , ultrafast diode  $D$ , and resistor load  $R$ .

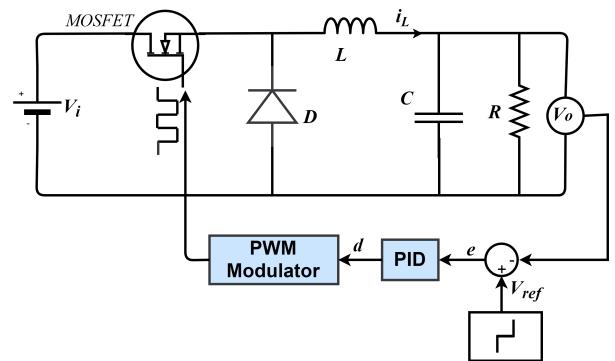


Fig. 1. Overall control scheme.

The transfer function of a DC-DC buck converter is given by [20]:

$$G(s) = \frac{V_o}{u} = V_{in} \left( \frac{R}{R + R_L} \right) \left( \frac{\frac{s}{\omega_{ZERO}} + 1}{\frac{\omega_0^2}{Q\omega_0} + 1} \right) \quad (1)$$

where

$$\Omega(s) = \frac{s^2}{\omega_0^2} + \frac{s}{Q\omega_0} + 1$$

TABLE I  
PARAMETERS OF THE BUCK CONVERTER

Parameters	Value
Output capacitor $C$	270 $\mu F$
MOSFET resistance $R_m$	0.1 $\Omega$
Output capacitor resistance $R_c$	0.18 $\Omega$
Inductor $L$	600 $\mu H$
Winding resistance of inductor $R_L$	0.1 $\Omega$
Resistance load $R$	30 $\Omega$
Switching frequency $f_s$	31.38 KHz

$$\omega_0 = \frac{1}{\sqrt{LC} \frac{R+R_c}{R+R_L}}, \quad \omega_{ZERO} = \frac{1}{CR_c}$$

$$Q = \frac{1}{\omega_0 \left( \frac{L}{R+R_L} + \frac{RR_L C}{R+R_L} + R_c C \right)}$$

The considered buck converter components are shown in Table I.

### III. DIGITAL PID STRUCTURE

The famous temporal expression of a pid controller is given by:

$$u(t) = K_p e(t) + K_i \int_0^t e(\tau) d\tau + K_d \frac{de(t)}{dt} \quad (2)$$

where  $u(t)$  denotes the control signal and  $e(t)$  denotes the error.  $K_d$  represents the derivative gain,  $K_p$  represents proportional gain and  $K_i$  represents the integral gain.

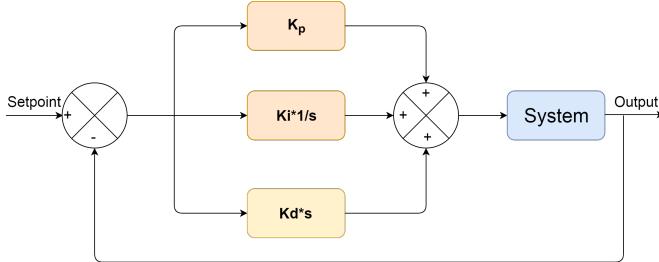


Fig. 2. Parallel PID controller form diagram

The discrete-time PID-controller form can be given by:

$$u_k = K_p e_k + K_i \sum_{n=1}^k e_n + K_d (e_k - e_{k-1}) \quad (3)$$

The implementation of discrete-time PID-controller is provided by using the following known velocity form:

$$u_k = u_{k-1} + K_p (e_k - e_{k-1}) + K_i e_k + K_d (e_k - 2e_{k-1} + e_{k-2}) \quad (4)$$

## IV. NUMERICAL SIMULATION AND EXPERIMENTAL VALIDATION

### A. Numerical simulation

In order to show the validity and effectiveness of the developed controller, numerical simulation tests are established using the the overall scheme presented in Figure 3 for a step signal  $V_{ref} = 6V$  where the input voltage is set to be  $V_{ref} = 12V$ . Figure 5(a) to Figure 5(d) show, respectively, the responses of the output voltage, error, duty cycle and PWM signal. It is clear from these simulation results that the voltage load follow perfectly its reference voltage with a very short time response (0.15s) and without overshoot.

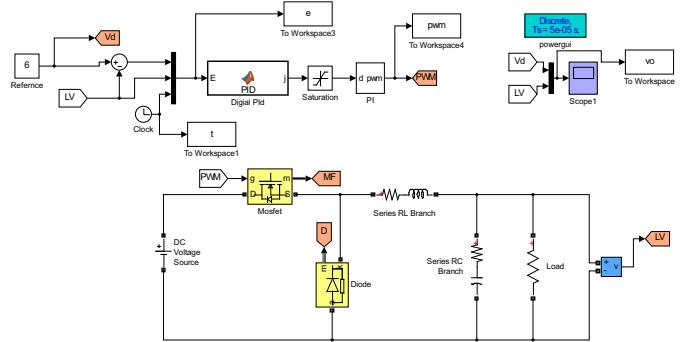


Fig. 3. Simulink model control

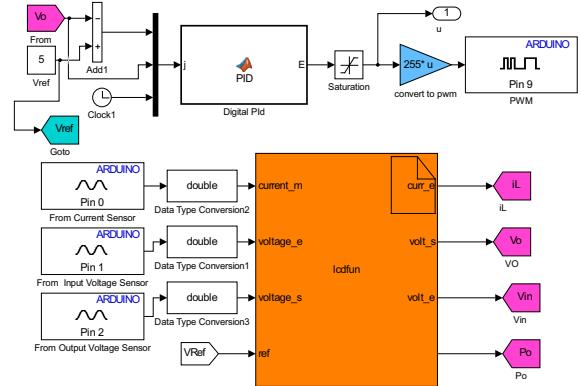


Fig. 4. Simulink model for hardware implementation.

## V. EXPERIMENTAL RESULTS

In order to verify the simulation results, experimental tests are carried out using the Matlab/Simulink environment and an Arduino Uno. As shown in Figure 4, a spatial Simulink arduino package is employed to interface the Matlab software with the hardware. The test bench consists of DC-DC buck converter, a current sensor, a voltage current sensor, a computer equipped with MATLAB/Simulink platform, DC power supply, PC USB oscilloscope and an Arduino uno, as shown in Figure 6.

The experiment is performed with an input voltage  $V_{in} = 10V$ . Figure 7 show the experimental responses of the duty

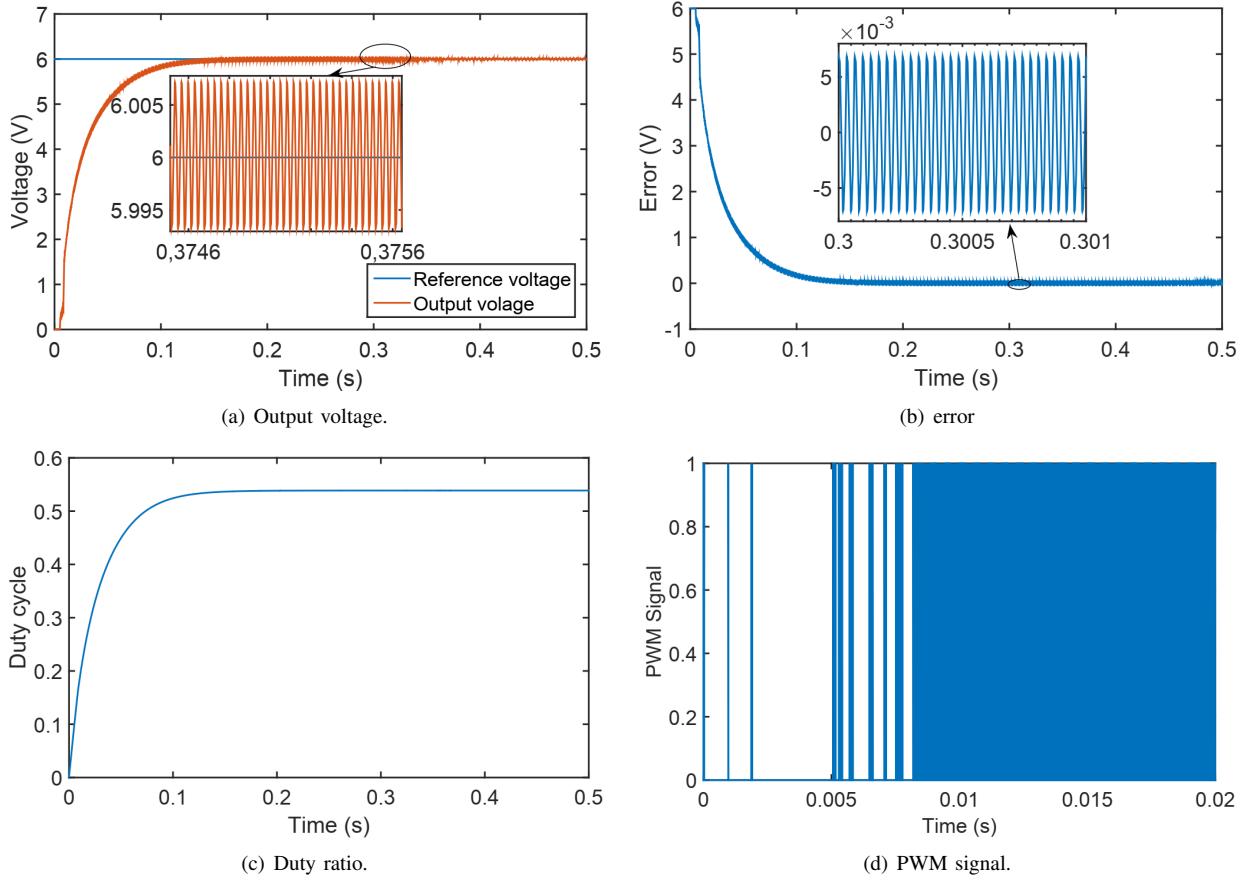


Fig. 5. Simulation results for voltage reference  $V_{ref} = 6V$ .

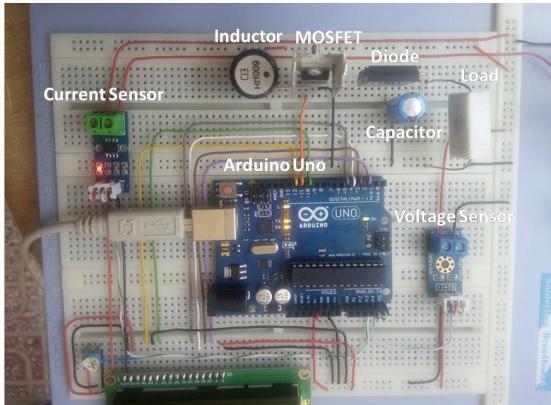


Fig. 6. Hardware setup.

cycle and output voltage for reference voltage  $V_{ref} = 5V$  and Figure 8 show the experimental response of output voltage for multi-step voltage. It is clear from these experimental results that the voltage load tracks perfectly its reference voltage with a very short time response and without overshoot.

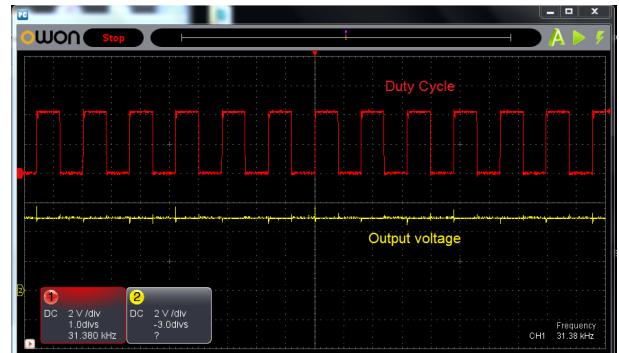


Fig. 7. Experimental waveforms for desired voltage  $V_{ref} = 5V$ .

## VI. CONCLUSION

In this work, the design and real-time implementation of a PID controller for buck converter using an Arduino uno and Matlab/Simulink environment is proposed. The developed controller is qualified to drive the output voltage to follow a reference voltage signal. The presented numerical simulation tests and experimental results show the effectiveness of the developed controller in terms of overshoot limitations and accuracy.

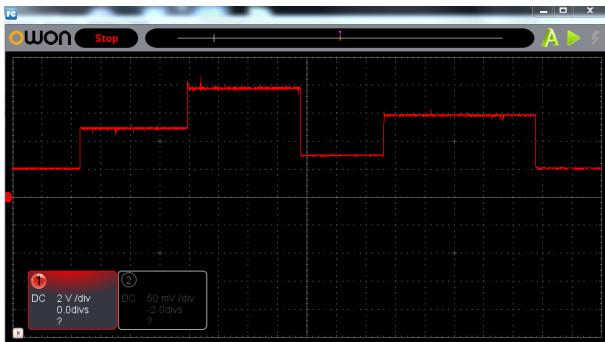


Fig. 8. Experimental waveforms for multi-step reference voltage  $V_{ref} = 12V$ .

## REFERENCES

- [1] FORSYTH, A. J. and MOLLOW, S. V. Modelling and control of DC-DC converters. *Power engineering journal*, vol. 12, no 5, pp. 229-236, 1998.
- [2] BOUCHAMA, Z., HARMAS, M. N., and ZEHAR, K. Finite time nonlinear control for dc-dc converters. *Soft Computing & Electrical Engineering*, vol. 1, no 1, pp. 36-452, 2019.
- [3] H. Mamur and R. Ahiska, "Application of a DC-DC boost converter with maximum power point tracking for low power thermoelectric generators," *Energy conversion and management*, vol. 197, pp. 265–272, 2015.
- [4] MEHTA, Axaykumar and NAIK, Brijesh. Output Feedback Second-Order Sliding Mode Controller for DC-DC Buck Converter. In : Sliding Mode Controllers for Power Electronic Converters. Springer, Singapore, pp. 79-92, 2019
- [5] OUNNAS, D., RAMDANI, M., CHENIKHER, S., and BOUKTIR, T., An Efficient Maximum Power Point Tracking Controller for Photovoltaic Systems Using Takagi-Sugeno Fuzzy Models. *Arabian Journal for Science and Engineering*, , vol. 42, no. 12, pp. 4971-4982, 2017
- [6] GUIZA, Dhaouadi, SOUFI, Youcef, OUNNAS, Djamel, et al. Design and Implementation of Takagi-Sugeno Fuzzy Tracking Control for a DC-DC Buck Converter. *Advances in Electrical and Electronic Engineering*, vol. 17, no 3, pp. 234-243, 2019.
- [7] OUNNAS, D., RAMDANI, M., CHENIKHER, S., et al. An Efficient Maximum Power Point Tracking Controller for Photovoltaic Systems Using Takagi-Sugeno Fuzzy Models. *Arabian Journal for Science and Engineering*, vol. 42, no 12, p. 4971-4982, 2017.
- [8] DJAMEL, OUNNAS, DHAOUADI, GUIZA, YOUSSEF, SOUFI, and MAAMRI, MAHMOUD. Hardware Implementation of Digital PID Controller for DC-DC Boost Converter. In : 2019 4th International Conference on Power Electronics and their Applications (ICPEA). IEEE, 2019. pp. 1-4.
- [9] KONDRATIEV, I., SANTI, E., DOUGAL, R., et al. Synergetic control for DC-DC buck converters with constant power load. In : 2004 IEEE 35th Annual Power Electronics Specialists Conference (IEEE Cat. No. 04CH37551). IEEE, p. 3758-3764, 2004.
- [10] VIDAL IDIARTE, Enric, RESTREPO, Carlos, EL AROUDI, Abdellali, et al. Digital Control of a Buck Converter Based on Input Output Linearization. An Interpretation Using Discrete Time Sliding Control Theory. *Energies*, vol. 12, no 14, p. 2738, 2019.
- [11] NIZAMI, Tousif Khan, CHAKRAVARTY, Arghya, and MAHANTA, Chitralekha. Analysis and experimental investigation into a finite time current observer based adaptive backstepping control of buck converters. *Journal of the Franklin Institute*, vol. 355, no 12, pp. 4996-5017, 2018
- [12] WU, Jiarong et LU, Yimin. Feedback Linearization Adaptive Control for a Buck Converter with Constant Power Loads. In : 2018 IEEE International Power Electronics and Application Conference and Exposition (PEAC). IEEE, 2018. p. 1-6.
- [13] PANDEY, Sanjeev Kumar, PATIL, Sanjaykumar L., GINOYA, Divyesh, et al. Robust control of mismatched buck DC-DC converters by PWM-based sliding mode control schemes. *Control Engineering Practice*, vol. 84, pp. 183-193, 2019.
- [14] BEHIH, K., BENMAHAMMED, K., BOUCHAMA, Z., et al. Real-Time Investigation of an Adaptive Fuzzy Synergetic Controller for a DC-DC Buck Converter. *Engineering, Technology & Applied Science Research*, , vol. 9, no 6, pp. 4984-4989, 2019.
- [15] SWAMY, Kadali Narasimha, PRITHVI, P. Hemanth, et CHAKRAVARTHI, NS Kalyan. Design Buck Converter Using Fuzzy Logic Controller. 2019.
- [16] SATPATHY, Sambit, GHOSH, Sushmita, DAS, Sanchali, et al. Study of Dynamic Response of DC-DC Buck Converter Based on PID Controller. *International Journal of Computational Intelligence & IoT*, vol. 2, no 4, 2018.
- [17] ZHU, Xi-tong, SUN, Fang-ya, ZHOU, Nuo, et al. Analysis of Voltage Mode Buck Converter Based on PID Control Characteristic. 2018.
- [18] BEVRANI, Hassan, HIYAMA, Takashi, et BEVRANI, Hossein. Robust PID based power system stabiliser: design and real-time implementation. *International Journal of Electrical Power & Energy Systems*, vol. 33, no 2, p. 179-188, 2011.
- [19] SALEEM, Omer et RIZWAN, Mohsin. Performance optimization of LQR-based PID controller for DC-DC buck converter via iterative-learning-tuning of state-weighting matrix. *International Journal of Numerical Modelling: Electronic Networks, Devices and Fields*, vol. 32, no 3, pp. e2572, 2019.
- [20] WANG, Jiajia, ZHEN, Shaowei, HE, Yajuan, et al. Digital PID Based on Pseudo Type III Compensation for DC-DC Converter. In : 2018 IEEE Asia Pacific Conference on Circuits and Systems (APCCAS). IEEE, 2018. p. 94-97.