```
nb_kernel_ElecEw_VdwCSTab_GeomW3P1_VF_avx_256_single
                               nb_kernel_ElecEw_VdwCSTab_GeomW3W3_VF_avx_256_single
                                nb_kernel_ElecEw_VdwCSTab_GeomW4P1_VF_avx_256_single
                                nb_kernel_ElecEw_VdwCSTab_GeomW4W4_VF_avx_256_single
                                  nb_kernel_ElecEw_VdwLJ_GeomP1P1_VF_avx_256_single
                                  nb kernel ElecEw VdwLJ GeomW3P1 VF avx 256 single
                                  nb_kernel_ElecEw_VdwLJ_GeomW3W3_VF_avx_256_single
                                  nb_kernel_ElecEw_VdwLJ_GeomW4P1_VF_avx_256_single
                                 nb kernel ElecEw VdwLJ GeomW4W4 VF avx 256 single
                                 nb_kernel_ElecEw_VdwNone_GeomP1P1_VF_avx_256_single
                                nb_kernel_ElecEw_VdwNone_GeomW3P1_VF_avx_256_single
                                nb kernel ElecEw VdwNone GeomW3W3 VF avx 256 single
                                 nb_kernel_ElecEw_VdwNone_GeomW4P1_VF_avx_256_single
                                nb_kernel_ElecEw_VdwNone_GeomW4W4_VF_avx_256_single
                                nb_kernel_ElecEwSh_VdwLJSh_GeomP1P1_VF_avx_256_single
                                nb_kernel_ElecEwSh_VdwLJSh_GeomW3P1_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwLJSh_GeomW3W3_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwLJSh_GeomW4P1_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwLJSh_GeomW4W4_VF_avx_256_single
                                nb_kernel_ElecEwSh_VdwNone_GeomP1P1_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwNone_GeomW3P1_VF_avx_256_single
gmx_mm256_pmecorrV_ps
                               nb_kernel_ElecEwSh_VdwNone_GeomW3W3_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwNone_GeomW4P1_VF_avx_256_single
                               nb_kernel_ElecEwSh_VdwNone_GeomW4W4_VF_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomP1P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwLJSw_GeomP1P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW3P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwLJSw_GeomW3P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW3W3_VF_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW3W3_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW4P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwLJSw_GeomW4P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW4W4_VF_avx_256_single
                               nb_kernel_ElecEwSw_VdwLJSw_GeomW4W4_F_avx_256_single
                                nb_kernel_ElecEwSw_VdwNone_GeomP1P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwNone_GeomP1P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwNone_GeomW3P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwNone_GeomW3P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwNone_GeomW3W3_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwNone_GeomW3W3_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwNone_GeomW4P1_VF_avx_256_single
                                nb_kernel_ElecEwSw_VdwNone_GeomW4P1_F_avx_256_single
                               nb_kernel_ElecEwSw_VdwNone_GeomW4W4_VF_avx_256_single
```

nb\_kernel\_ElecEwSw\_VdwNone\_GeomW4W4\_F\_avx\_256\_single

nb\_kernel\_ElecEw\_VdwCSTab\_GeomP1P1\_VF\_avx\_256\_single