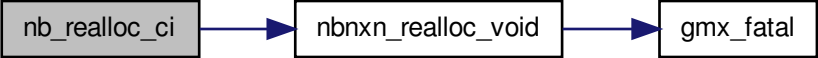


nb_realloc_ci



```
graph LR; A[nb_realloc_ci] --> B[nbnxn_realloc_void]; B --> C[gmx_fatal];
```

nbnxn_realloc_void

gmx_fatal