

src/mdlib/nbnxn_kernels/nbnxn_kernel_gpu_ref.h

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graph BT; A[src/mdlib/nbnxn_kernels/nbnxn_kernel_gpu_ref.c] --> C[src/mdlib/nbnxn_kernels/nbnxn_kernel_gpu_ref.h]; B[src/mdlib/sim_util.c] --> C;
```

src/mdlib/nbnxn_kernels/nbnxn_kernel_gpu_ref.c

src/mdlib/sim_util.c