

Project Digest #001

January, 2020

All the latest from FPGA world







## January 2020 report

Before you start, don't forget to subscribe 2 . We collect and share for you all new what happens in the FPGA world: Tutorials, Articles, Events, Announcements, Videos, Webinars, Workshops, etc. 😊

Learn more about the ideological component of the project

PS: if you want to place information about your company or event or training or service or products or you have questions, please, write us an e-mail: gate-array.com@yandex.ru

## Changes on the gate-array.com project

- 1. We just started (15.01.2020), so there are not many project changes. We working on the design, so if you see some artifacts or to be sure that you see a last version press Ctrl+F5 at yours browser or clear the browser history. If you have a suggestion how we can improve our project, please inform us.
- 2. At present we have 2 social networks with 3 accounts: one at twitter @gatearraydotcom and Telegram: feed news at @gatearraydotcomnews and chat for discussions @gatearraydotcom
- 3. We have a Russian language mirror www.fpga-systems.ru





## New tutorials, published at gate-array.com

l Concept of Clocking and Asynchronous Design [Articles]

<u>AbdullahRefaey</u>

16.01.2020, 09:35

Thanks to AbdullahRefaey for this article.







83	Article: Rad-Tolerant Quad-Core DIY Processor for FPGAs	max	31.01.2020, 19:17
82	Tutorial: Issue 6: BPS Integration with Vivado and Vivado HLS	KeisN13	31.01.2020, 16:12
81	Tutorial: Beamforming Acceleration	KeisN13	31.01.2020, 14:50
79	Tutorial: Pynq – BNN for Fabric Texture Analysis on the EDGE – Part 1, 2, 3	KeisN13	31.01.2020, 14:27
78	Event: Xilinx XDF 2019 materials is now available for everyone: FREE	KeisN13	31.01.2020, 09:29
77	Article: Get CDC protocols right with an automated formal-to- simulation flow	KeisN13	31.01.2020, 09:09
76	Article: Accelerate your UVM adoption and usage with an IDE	KeisN13	31.01.2020, 08:47
75	Article: What the FAQ Are Programmable Logic Devices?	max	30.01.2020, 21:55
74	Article: FOS: A Modular FPGA Operating System For Dynamic Workloads	KeisN13	30.01.2020, 07:08
73	Article: Introduction to FPGA   Structure, Components, Applications	KeisN13	30.01.2020, 06:57
72	Article: Automotive cybersecurity begins with secure ASIC, FPGA and SoC hardware	KeisN13	30.01.2020, 06:39
71	Announce: Pentek Announces New Quartz RFSoC Board Aligned with the Technical Sta	KeisN13	30.01.2020, 06:35
68	Tutorial: Ultra96 Global Shutter Camera and Edge Detection	KeisN13	30.01.2020, 06:21
67	Tutorial: Vitis Emulation	KeisN13	30.01.2020, 06:08
66	Article: First-Time FPGA Success Requires Exhaustive Examination of Clock Domain	KeisN13	30.01.2020, 05:58
65	Article: MCU Designer's Intro to FPGA Software	max	29.01.2020, 23:17
64	Announce: Flex Logix EFLX 1K Core Design Kits Available Now for TSMC 40nm ULP/LP	KeisN13	29.01.2020, 17:58
63	Breif: Where Is The EFPGA Market And Ecosystem Headed?	KeisN13	29.01.2020, 17:53
62	Announce: M100PFS - First Industry SoC-FPGA Solution based on Multicore RISC-V	KeisN13	29.01.2020, 16:26
61	Tutorial: Integrating optimized RTL Kernels into Accelerated Applications using	KeisN13	29.01.2020, 12:43











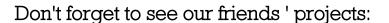


PS: Thanks a lot max for your help with news feed. Hope that in next report will be more news from more users.











https://t.me/gatearraydotcomnews - The news from FPGA world



https://t.me/QuokkaFPGA - Quokka FPGA toolkit.

HLS from C# to RTL



https://t.me/pdpllml - Domain Specific Computing for machine learning

https://t.me/fpgasic - FPG/A/SIC tips and tricks





Inline Group CTC – official Xilinx distributor <u>www.plis.ru</u> and training center <u>www.plis2.ru</u> in Russia

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Don't forget to leave a few comments on the news and ask a question to the author of the articles. We hope for your <u>feedback</u> on improving the project.

Please, fill free to share this report with your colleges and friends.

Best regards, gate-array.com team