EE 371 Autumn 2016 - Lab 2

William Li, Dawn Liang, Jun Park October 29, 2016

| We certify that the work in this report is our own, and that any work that is not ours is cited. | | |
|--|------------|-----------|
| William Li | Dawn Liang | Jun Park |
| | | |
| Signature | Signature | Signature |
| | | |
| Date | Date | Date |

Contents

| 1 | Abstract | |
|---|-------------------------------|--|
| 2 | Introduction | |
| 3 | Discussion | |
| | 3.1 Design | |
| | 3.1.1 Design Specification | |
| | 3.1.2 Design Procedure | |
| | 3.1.3 System Description | |
| | 3.1.4 Software Implementation | |
| | 3.1.5 Hardware Implementation | |
| | 3.2 Test | |
| | 3.2.1 Test Plan | |
| | 3.2.2 Test Specification | |
| | 3.2.3 Test Cases | |
| 4 | Results | |
| | 4.1 Analysis of Errors | |
| 5 | Summary & Conclusion | |
| 6 | Appendix | |
| U | 6.1 Lock Control System | |
| | 6.2 C Programming | |
| | 0.4 C110gramming | |

1 ABSTRACT

This lab focuses on designing a simple

2 INTRODUCTION

3 DISCUSSION

- 3.1 Design
- 3.1.1 Design Specification
- 3.1.2 Design Procedure
- 3.1.3 System Description
- 3.1.4 Software Implementation
- 3.1.5 Hardware Implementation
- **3.2** Test
- 3.2.1 Test Plan
- 3.2.2 Test Specification
- 3.2.3 Test Cases

4 RESULTS

4.1 Analysis of Errors

5 SUMMARY & CONCLUSION

- **6 APPENDIX**
- 6.1 Lock Control System
- **6.2** C Programming