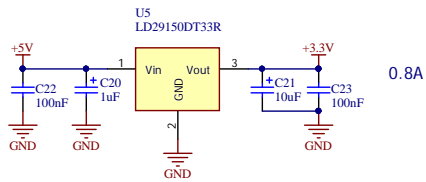
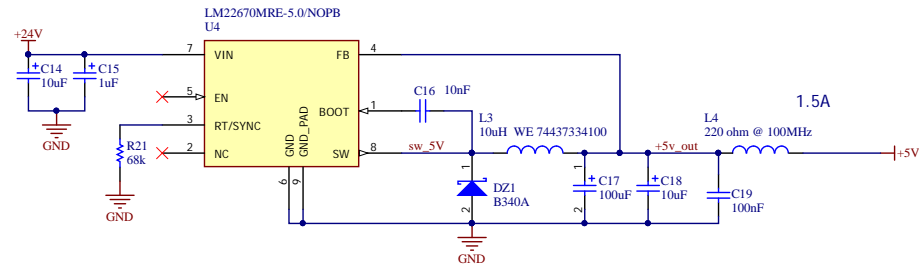
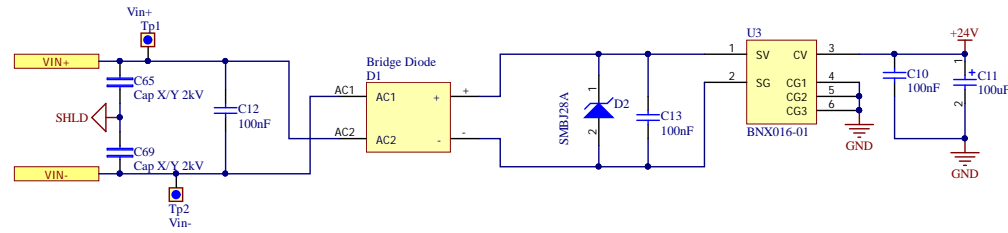
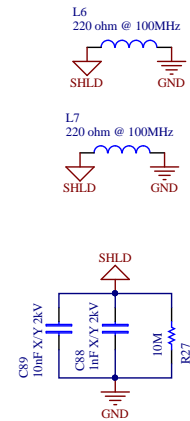
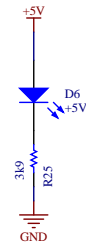
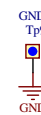
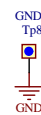
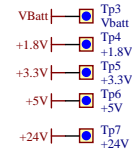
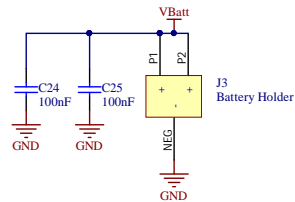
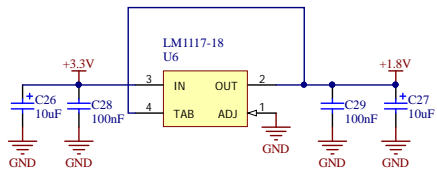


Aggiungere commom mode filter.



0.8A



A

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C

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D

PE23_TCLK4_MCI_DA3_L18	43	PE23_TCLK4_MCI_DA3_L18
PA27_LCDHSYNC_PWMLO_SPI1_NPCS2_H4	44	PA27_LCDHSYNC_PWMLO_SPI1_NPCS2_H4
PA24_LCDPWM_PCK0_H5	45	PA24_LCDPWM_PCK0_H5
PA29_LCDDEN_PWMML1_J5	46	PA29_LCDDEN_PWMML1_J5
PA12_LCDDAT12_G1_RX0_G5	48	PA12_LCDDAT12_G1_RX0_G5
PA16_LCDDAT16_NTRST_H6	49	PA16_LCDDAT16_NTRST_H6
PA10_LCDDAT10_G1_RXDV_F5	50	PA10_LCDDAT10_G1_RXDV_F5
PA7_LCDDAT7_E5	51	PA7_LCDDAT7_E5
PA1_LCDDAT1_F6	52	PA1_LCDDAT1_F6
PA14_LCDDAT14_G1_TX0_E4	53	PA14_LCDDAT14_G1_TX0_E4
PA2_LCDDAT2_G1_TXCK_E6	54	PA2_LCDDAT2_G1_TXCK_E6
PA11_LCDDAT11_G1_RXER_D5	55	PA11_LCDDAT11_G1_RXER_D5
PA4_LCDDAT4_G1_TXEN_D6	56	PA4_LCDDAT4_G1_TXEN_D6
PA26_LCDVSYNC_PWMH0_SPI1_NPCS1_E3	57	PA26_LCDVSYNC_PWMH0_SPI1_NPCS1_E3
PA3_LCDDAT3_G1_RXCK_C6	58	PA3_LCDDAT3_G1_RXCK_C6
PA13_LCDDAT13_G1_RX1_C5	59	PA13_LCDDAT13_G1_RX1_C5
PA5_LCDDAT5_G1_TXER_B6	60	PA5_LCDDAT5_G1_TXER_B6
PA15_LCDDAT15_G1_TX1_B5	61	PA15_LCDDAT15_G1_TX1_B5
PA0_LCDDAT0_TMS_A7	62	PA0_LCDDAT0_TMS_A7
PA6_LCDDAT6_G1_CRS_A6	63	PA6_LCDDAT6_G1_CRS_A6
GND	64	GND
GND	65	GND

PA8_LCDDAT8_TCK_A5	66	PA8_LCDDAT8_TCK_A5
PA21_LCDDAT21_G1_TX3_B4	67	PA21_LCDDAT21_G1_TX3_B4
PA23_LCDDAT23_G1_MDIO_A4	68	PA23_LCDDAT23_G1_MDIO_A4
PA19_LCDDAT19_G1_RX3_C4	69	PA19_LCDDAT19_G1_RX3_C4
PA20_LCDDAT20_G1_TX2_A3	70	PA20_LCDDAT20_G1_TX2_A3
PA22_LCDDAT22_G1_MDC_B3	71	PA22_LCDDAT22_G1_MDC_B3
PA17_LCDDAT17_D4	72	PA17_LCDDAT17_D4
PA9_LCDDAT9_G1_COL_F4	73	PA9_LCDDAT9_G1_COL_F4
PA25_LCDDISP_TDO_F3	74	PA25_LCDDISP_TDO_F3
PA28_LCDPCK_PWMH1_SPI1_NPCS3_G3	75	PA28_LCDPCK_PWMH1_SPI1_NPCS3_G3
PA18_LCDDAT18_G1_RX2_G4	76	PA18_LCDDAT18_G1_RX2_G4
ETH_LEDSPED	77	ETH_LEDSPED
ETH_LEDACLINK	78	ETH_LEDACLINK
ETH_RXN	79	ETH_RXN
ETH_RXP	80	ETH_RXP
ETH_TXN	81	ETH_TXN
ETH_TXP	82	ETH_TXP
NC	83	NC
NC	84	NC

PUSH_BTN_R1	PD23_R10
PUSH_BTN_R2	PD21_P10
PUSH_BTN_R3	PD31_SPI0_NPCS2_PCK1_P5
PUSH_BTN_R4	PC20_ISI_D1_TF1_V11
PUSH_BTN_R5	PD22_U11
PUSH_BTN_R6	PD25_R11
PUSH_BTN_R7	PD24_U10
PUSH_BTN_R8	PD19_P11

PUSH_BTN	PC31_AD4_PWMH1_T13
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PUSH_BTN_LED0	PC23_ISI_D4_RD1_U16
PUSH_BTN_LED1	PC24_ISI_D5_RK1_PCK1_T16

RELE_OUT	PB15_G0_TX3_SPI2_NPCS2_PWMLO_E2
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PE28_RTS4_L17	42	PE28_RTS4_L17
PE21_TIOA4_MCI_DA1_M18	41	PE21_TIOA4_MCI_DA1_M18
PE27_TIOB4_MCI_DA2_M19	40	PE27_TIOB4_MCI_DA2_M19
PE15_SCLK_TIOA0_M19	39	PE15_SCLK_TIOA0_M19
PE17_TXD3_TCLK0_M19	38	PE17_TXD3_TCLK0_M19
PE26_SCK4_IQ0_M17	37	PE26_SCK4_IQ0_M17
PE11_TCLK2_R18	36	PE11_TCLK2_R18
PE18_TIOA5_MCI1_CK_P19	35	PE18_TIOA5_MCI1_CK_P19
PE13_TIOB1_PWM2_P18	34	PE13_TIOB1_PWM2_P18
PE24_RTS3_L19	33	PE24_RTS3_L19
PE9_TIOA2_L18	32	PE9_TIOA2_L18
PE7_TIOB3_PWM1_U19	31	PE7_TIOB3_PWM1_U19
PE6_TIOA3_V19	30	PE6_TIOA3_V19
PE14_TCLK1_PWMH3_N17	29	PE14_TCLK1_PWMH3_N17
PE0_MCI0_CDB_CTS4_W19_INT	28	PE0_MCI0_CDB_CTS4_W19_INT
PE12_TIOA1_PWMH2_P17	27	PE12_TIOA1_PWMH2_P17
PE2_MCI0_DBI_T17	26	PE2_MCI0_DBI_T17
PC25_ISI_D6_TWD3_URXD1_V17	25	PC25_ISI_D6_TWD3_URXD1_V17
PE8_TCLK3_PWMH3_L19_INT	24	PE8_TCLK3_PWMH3_L19_INT
PE1_MCI0_DBI_T17	23	PE1_MCI0_DBI_T17
NC	22	NC
NC	21	NC
PE5_CTS3_R17	20	PE5_CTS3_R17
PE4_MCI0_DBI_T18_INT	19	PE4_MCI0_DBI_T18_INT
PE19_TIOB5_MCI1_CDA_M16	18	PE19_TIOB5_MCI1_CDA_M16
PE27_TXD4_M16	17	PE27_TXD4_M16
PC29_AD2_SPI0_NPCS3_PWMH0_R13	16	PC29_AD2_SPI0_NPCS3_PWMH0_R13
PE16_RXD3_TIOB0_M15	15	PE16_RXD3_TIOB0_M15
PE10_RXD3_TIOB0_M15	14	PE10_RXD3_TIOB0_M15
PE3_MCI0_DBI_T16_INT	13	PE3_MCI0_DBI_T16_INT
PC26_ISI_D7_TWCK3_UTXD1_R16	12	PC26_ISI_D7_TWCK3_UTXD1_R16
PC23_ISI_D4_RD1_U16	11	PC23_ISI_D4_RD1_U16
PC24_ISI_D5_RK1_PCK1_T16	10	PC24_ISI_D5_RK1_PCK1_T16
PC21_ISI_D2_TD1_U15	9	PC21_ISI_D2_TD1_U15
PC19_ISI_D3_TWD1_U15	8	PC19_ISI_D3_TWD1_U15
PC31_AD4_PWMH1_T13	7	PC31_AD4_PWMH1_T13
PD25_R11	6	PD25_R11
PD22_U11	5	PD22_U11
PC20_ISI_D1_TF1_V11	4	PC20_ISI_D1_TF1_V11
PC3_SPI0_NPCS0_PWMH3_ISI_D11_T9	3	PC3_SPI0_NPCS0_PWMH3_ISI_D11_T9
PC30_AD3_PWMH0_T12	2	PC30_AD3_PWMH0_T12

U1
DevelBoard
Develer srl
DDB256R512F-R4

PD19_P11	168	PD19_P11
PD24_U10	167	PD24_U10
PD23_R10	166	PD23_R10
PD21_P10	165	PD21_P10
PE10_TIOB2_N14	164	PE10_TIOB2_N14
3V3	163	3V3
3V3	162	3V3
PC22_ISI_D3_RF1_T15	161	PC22_ISI_D3_RF1_T15
PD27_T14	160	PD27_T14
PC1_SPI0_MOSI_PWMML2_ISI_D9_P8	159	PC1_SPI0_MOSI_PWMML2_ISI_D9_P8
PD20_T10	158	PD20_T10
PB26_PCK0_RK0_PWMH0_M5	157	PB26_PCK0_RK0_PWMH0_M5
PD31_SPI0_NPCS2_PCK1_P5	156	PD31_SPI0_NPCS2_PCK1_P5
PD15_RTS1_SPI2_SPCK_N4	155	PD15_RTS1_SPI2_SPCK_N4
PIOBU1	154	PIOBU1
PC0_SPI0_MISO_PWMH2_ISI_D8	153	PC0_SPI0_MISO_PWMH2_ISI_D8
BOOT_RECOVERY	152	BOOT_RECOVERY
PC2_SPI0_SPCK_PWMH3_ISI_D10_V5	151	PC2_SPI0_SPCK_PWMH3_ISI_D10_V5
ADREF_EXT	150	ADREF_EXT
RESET	149	RESET
GND	148	GND
GND	147	GND
GND	146	GND
HHSDMC_V16	145	HHSDMC_V16
HHSDPB_W16	144	HHSDPB_W16
HHSDBB_W15	143	HHSDBB_W15
HHSDBB_V15	142	HHSDBB_V15
HHSDBA_DHSDB_W14	141	HHSDBA_DHSDB_W14
HHSDBA_DHSDB_V14	140	HHSDBA_DHSDB_V14
PA30_TWDO_D3	139	PA30_TWDO_D3
PA31_TWCKO_J4	138	PA31_TWCKO_J4
PC4_SPI0_NPCS1_MCI0_CK_PCK1_W5_INT	137	PC4_SPI0_NPCS1_MCI0_CK_PCK1_W5_INT
PB29_TWD2_RDO_PWMML1_M3	136	PB29_TWD2_RDO_PWMML1_M3
PC27_AD0_SPI0_NPCS1_PWMLO_U12	135	PC27_AD0_SPI0_NPCS1_PWMLO_U12
PIOBU0_U3	134	PIOBU0_U3
PC28_AD1_SPI0_NPCS2_PWMML1_T11	133	PC28_AD1_SPI0_NPCS2_PWMML1_T11
PD26_U13	132	PD26_U13
WKUP_V3	131	WKUP_V3
JTAGSEL_V2	130	JTAGSEL_V2
PD14_CTS1_P2	129	PD14_CTS1_P2
PB30_TWCK2_RFO_M4	128	PB30_TWCK2_RFO_M4
PD17_TXD1_SPI2_NPCS0_R3	127	PD17_TXD1_SPI2_NPCS0_R3
PD16_RXD1_R2	126	PD16_RXD1_R2

PD20_T10	22	PD20_T10
PC22_ISI_D3_RF1_T15	22	PC22_ISI_D3_RF1_T15
PD27_T14	22	PD27_T14
PB26_PCK0_RK0_PWMH0_M5	22	PB26_PCK0_RK0_PWMH0_M5
PC0_SPI0_MISO_PWMH2_ISI_D8	22	PC0_SPI0_MISO_PWMH2_ISI_D8
PC1_SPI0_MOSI_PWMML2_ISI_D9_P8	22	PC1_SPI0_MOSI_PWMML2_ISI_D9_P8
PC2_SPI0_SPCK_PWMH3_ISI_D10_V5	22	PC2_SPI0_SPCK_PWMH3_ISI_D10_V5
PC3_SPI0_NPCS0_PWMML3_ISI_D11_T9	22	PC3_SPI0_NPCS0_PWMML3_ISI_D11_T9
PB5_G0_COL_TXD2_PCK2_H2	22	PB5_G0_COL_TXD2_PCK2_H2
PB4_G0_CRS_RXD2_ISI_HSYNC_J3	22	PB4_G0_CRS_RXD2_ISI_HSYNC_J3

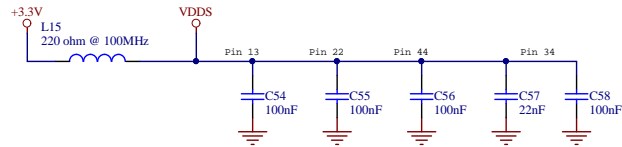
CC1310_RESET	
CC1310_PIN0	
CC1310_PIN1	
CC1310_MISO	
CC1310_MOSI	
CC1310_SCLK	
CC1310_CS	
EINK_RTS	
EINK_BUSY	
EINK_DC	
EINK_MOSI	
EINK_SCLK	
EINK_CS	



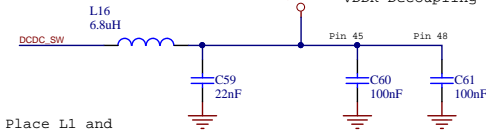
NC	85	NC
NC	86	NC
NC	87	NC
NC	88	NC
NC	89	NC
NC	90	NC
NC	91	NC
NC	92	NC
PB1_G0_RXCK_SCK2_ISI_PCK_A2	93	PB1_G0_RXCK_SCK2_ISI_PCK_A2
PB3_G0_TXER_CTS2_ISI_VSYNC_C2	94	PB3_G0_TXER_CTS2_ISI_VSYNC_C2
PB19_SHT_MOSI_C1	95	PB19_SHT_MOSI_C1
PB18_SHT_MISO_C2	96	PB18_SHT_MISO_C2
PB14_G0_TX2_SPI2_NPCS1_PWMH0_A1	97	PB14_G0_TX2_SPI2_NPCS1_PWMH0_A1
PB24_DRXD_TDI_E1	98	PB24_DRXD_TDI_E1
PB15_G0_TX3_SPI2_NPCS2_PWMLO_E2	99	PB15_G0_TX3_SPI2_NPCS2_PWMLO_E2
PB10_G0_RX2_PCK2_PWMML1_F1	100	PB10_G0_RX2_PCK2_PWMML1_F1
PB22_SHT_MOSI_G1	101	PB22_SHT_MOSI_G1
PB5_G0_COL_TXD2_PCK2_H2	102	PB5_G0_COL_TXD2_PCK2_H2
PB23_SHT_MISO_G1	103	PB23_SHT_MISO_G1
PB25_SHT_MISO_G1	104	PB25_SHT_MISO_G1
PB27_SHT_MISO_G1	105	PB27_SHT_MISO_G1
NC	106	NC
NC	107	NC
PB4_G0_CRS_RXD2_ISI_HSYNC_J3	108	PB4_G0_CRS_RXD2_ISI_HSYNC_J3
PB11_G0_RX3_RTS2_PWMH1_K4	109	PB11_G0_RX3_RTS2_PWMH1_K4
PB28_SPI2_NPCS3_TDO_PWMH1_K1	110	PB28_SPI2_NPCS3_TDO_PWMH1_K1
PB18_SHT_MISO_K2	111	PB18_SHT_MISO_K2
PB21_SHT_MISO_K3	112	PB21_SHT_MISO_K3
PB27_SPI1_NPCS3_TKO_PWMLO_L2	113	PB27_SPI1_NPCS3_TKO_PWMLO_L2
PB8_PCK0_M1	114	PB8_PCK0_M1
PD9_FIQ_M2	115	PD9_FIQ_M2
PE1_ADTRG_L4	116	PE1_ADTRG_L4
PD12_RXD0_N1	117	PD12_RXD0_N1
PD10_CTS0_N2	118	PD10_CTS0_N2
PD11_RTS0_SPI2_MISO_N3	119	PD11_RTS0_SPI2_MISO_N3
PD10_CTS0_N2	120	PD10_CTS0_N2
PD28_SCK0_R1	121	PD28_SCK0_R1
PD28_SCK0_R1	122	PD28_SCK0_R1
PE29_DIBP_URXD0_TWD1_V1	123	PE29_DIBP_URXD0_TWD1_V1
SHDN_T2	124	SHDN_T2
PE30_DIBN_UTXD0_TWCK1_U2	125	PE30_DIBN_UTXD0_TWCK1_U2
VBATT	126	VBATT

CC1310 RF

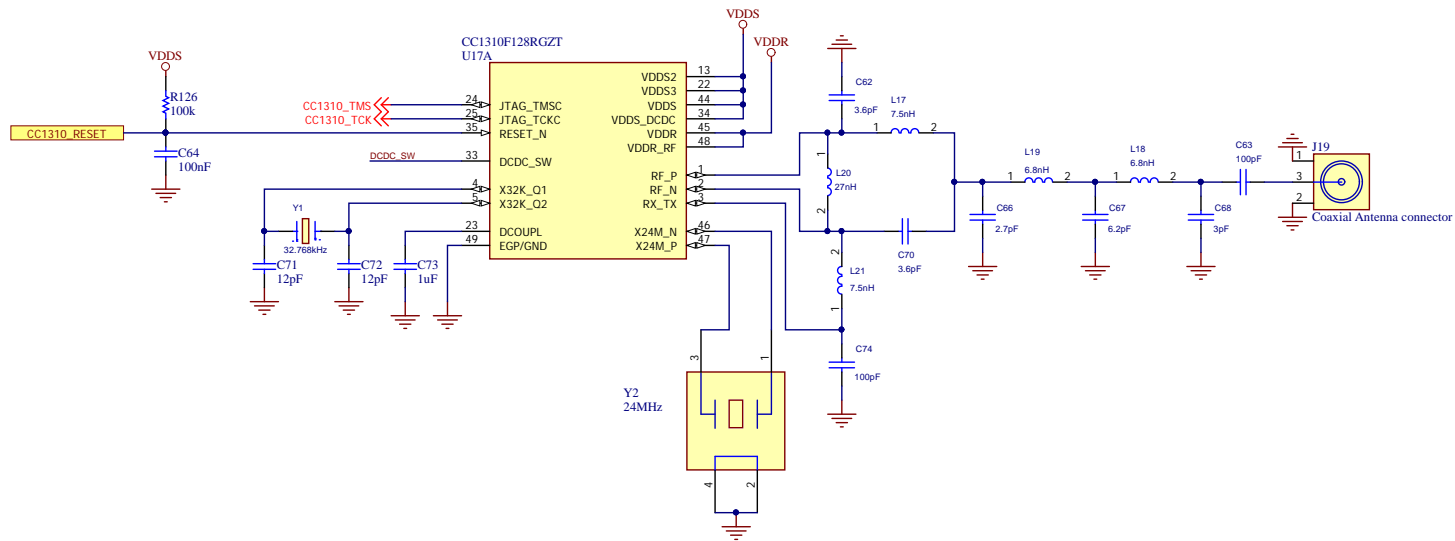
VDDS Decoupling Capacitors



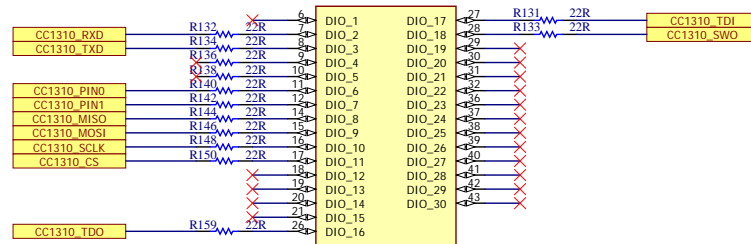
VDDR Decoupling Capacitors

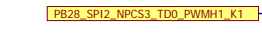


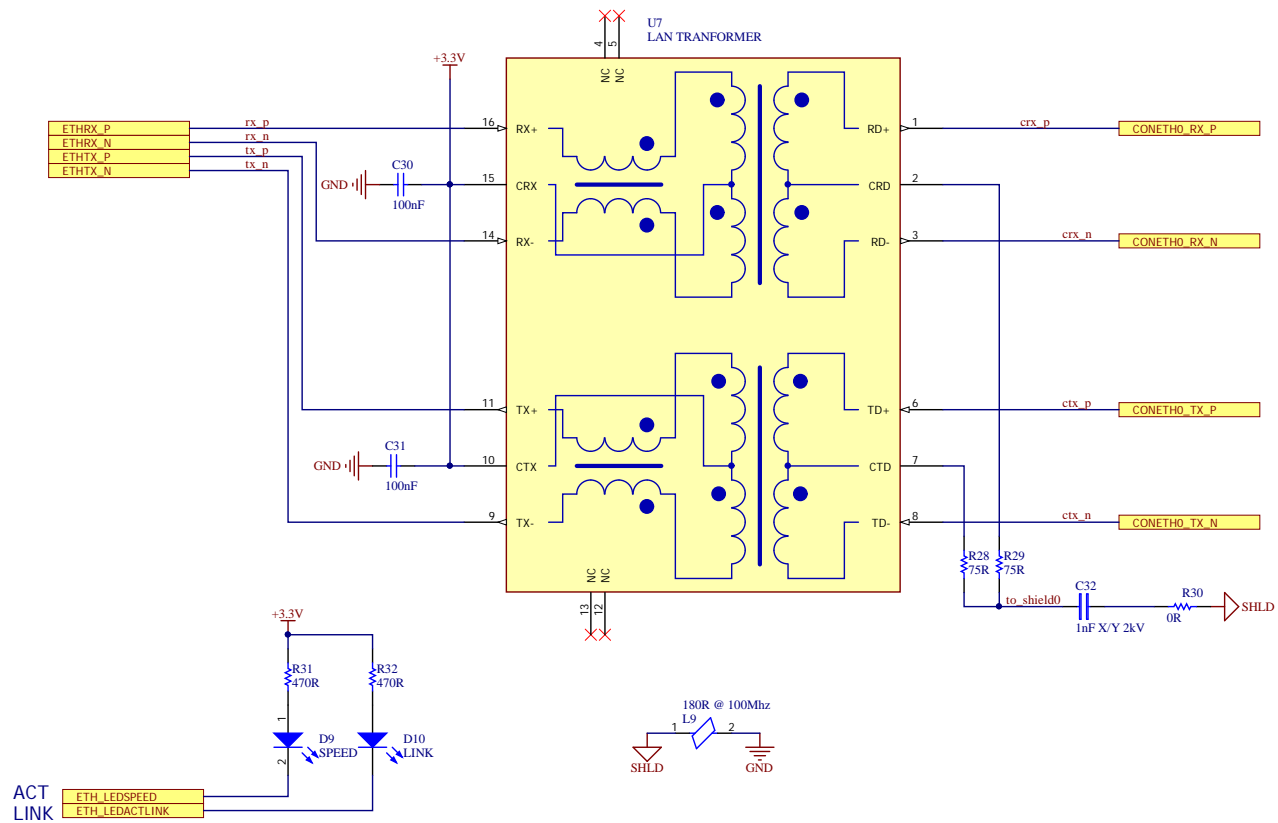
Place L1 and C8 close to pin 33



CC1310F128RGZT U17B







A

B

C

D

A

B

C

D

