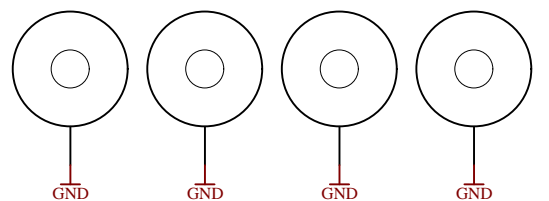
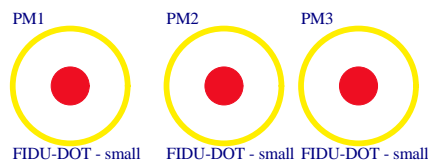


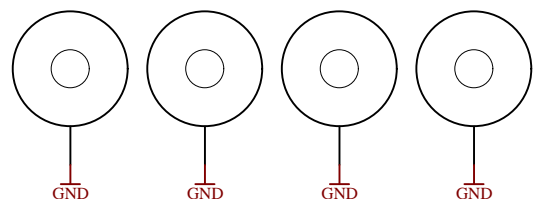
U_FMC
FMC.SchDoc

U_POWER_3V3
POWER_3V3.SchDoc

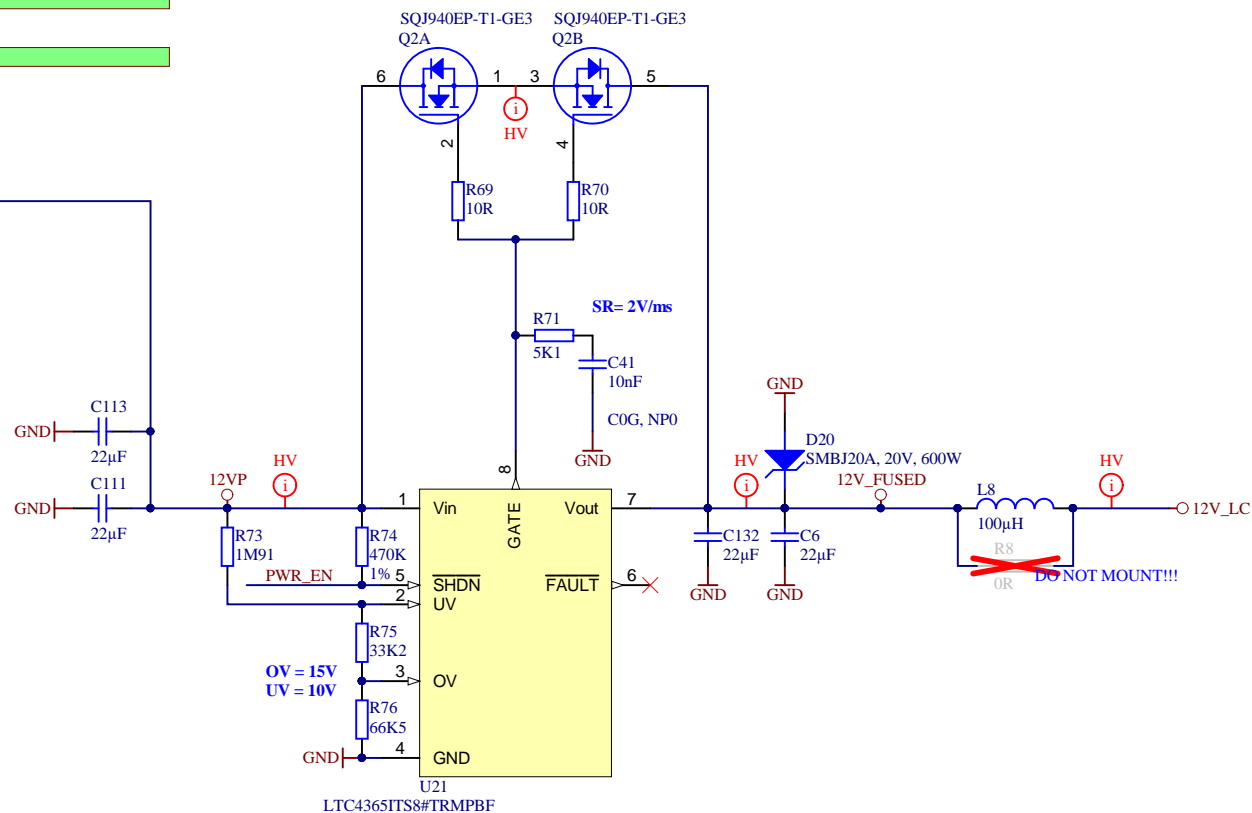
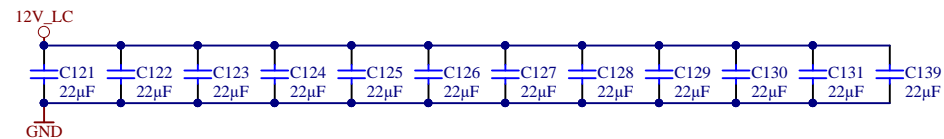
U_CPLD
CPLD.SchDoc



Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm



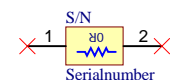
Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm



MECH1

TE Address Overlay

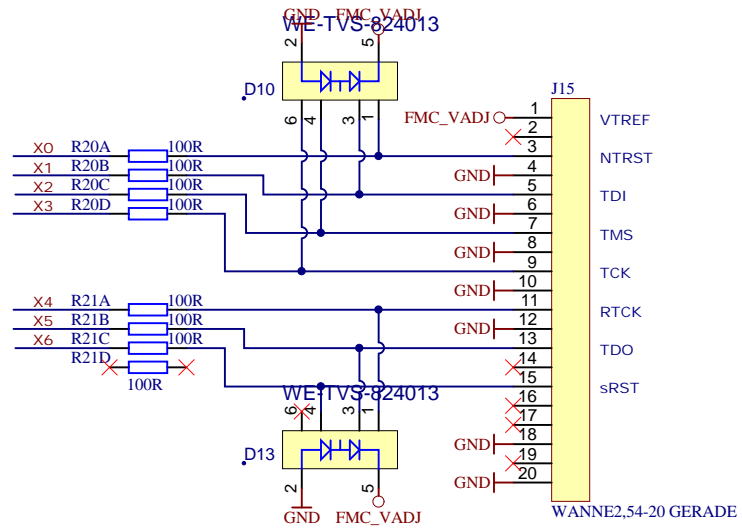
LOGO ADDRESS




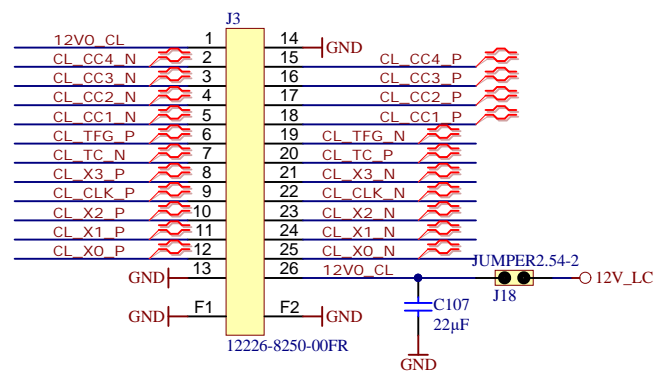
Serial
Serial
Serialnumber 6.3 x 6.3mm




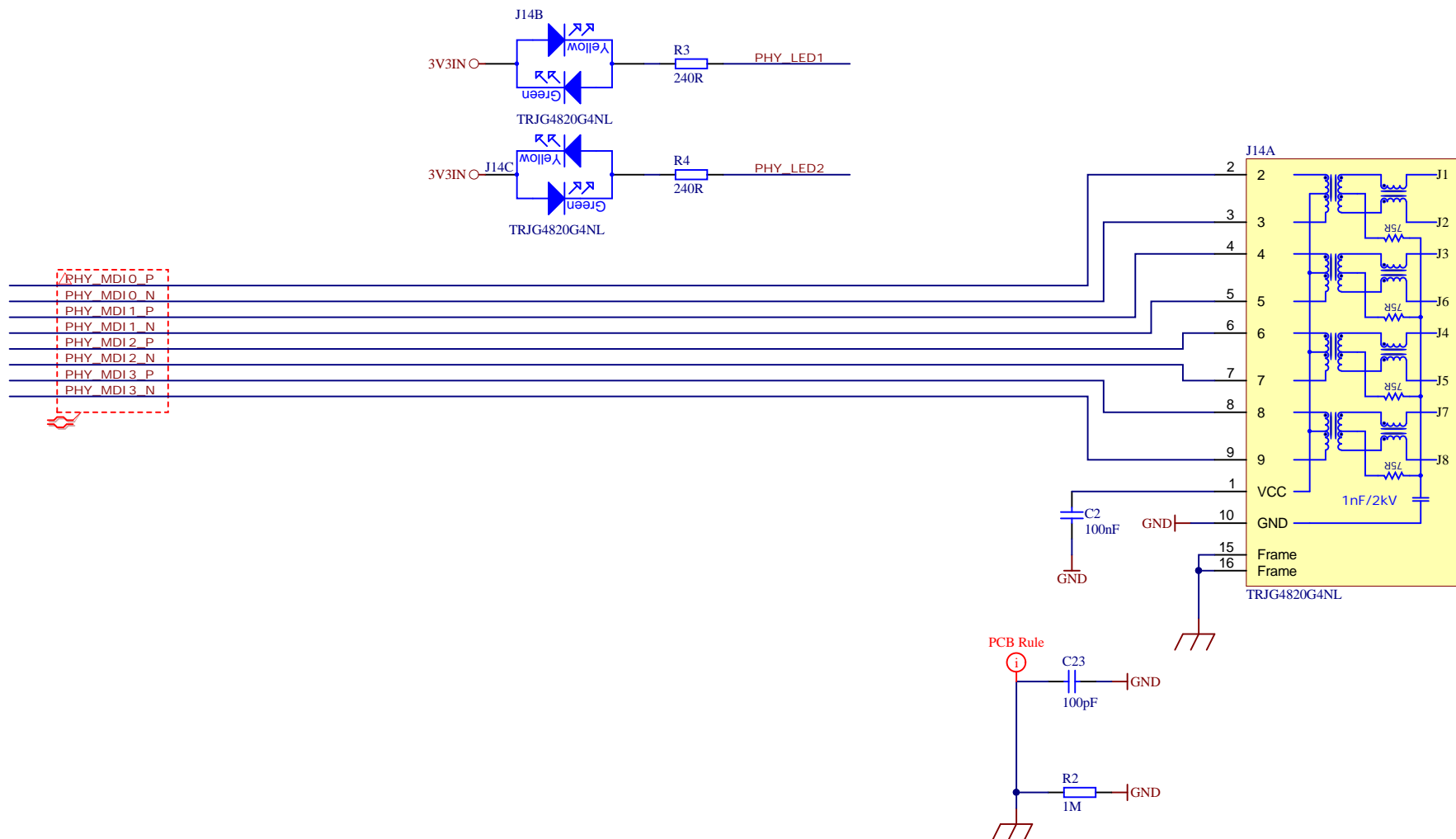
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A4	Number: TE0701 default	Rev. 06	
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 1	of 19
Filename: TE0701.SchDoc			




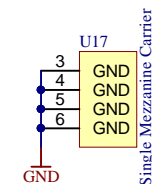
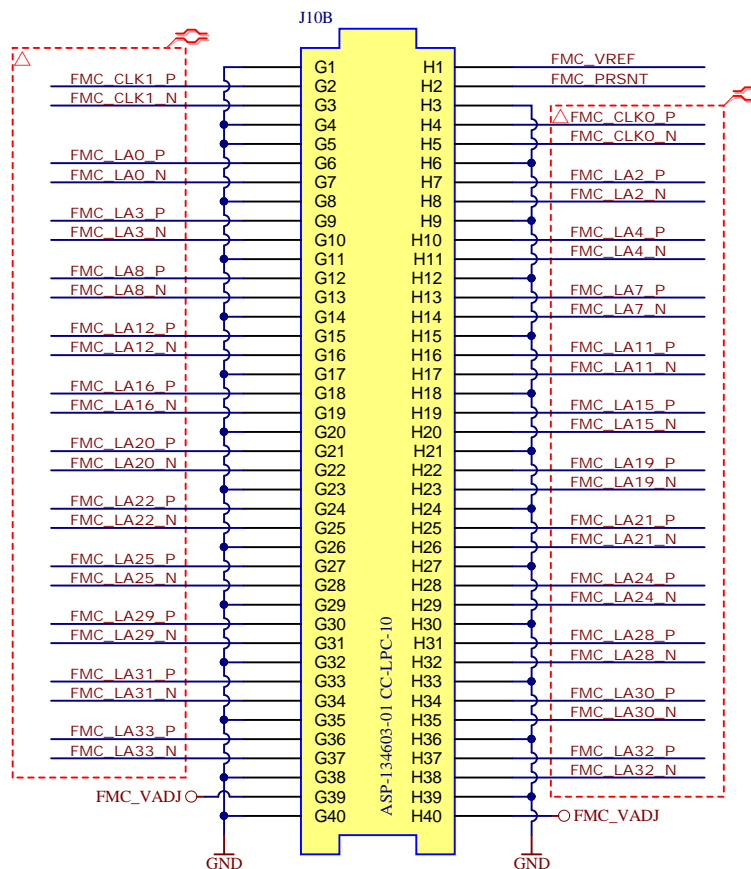
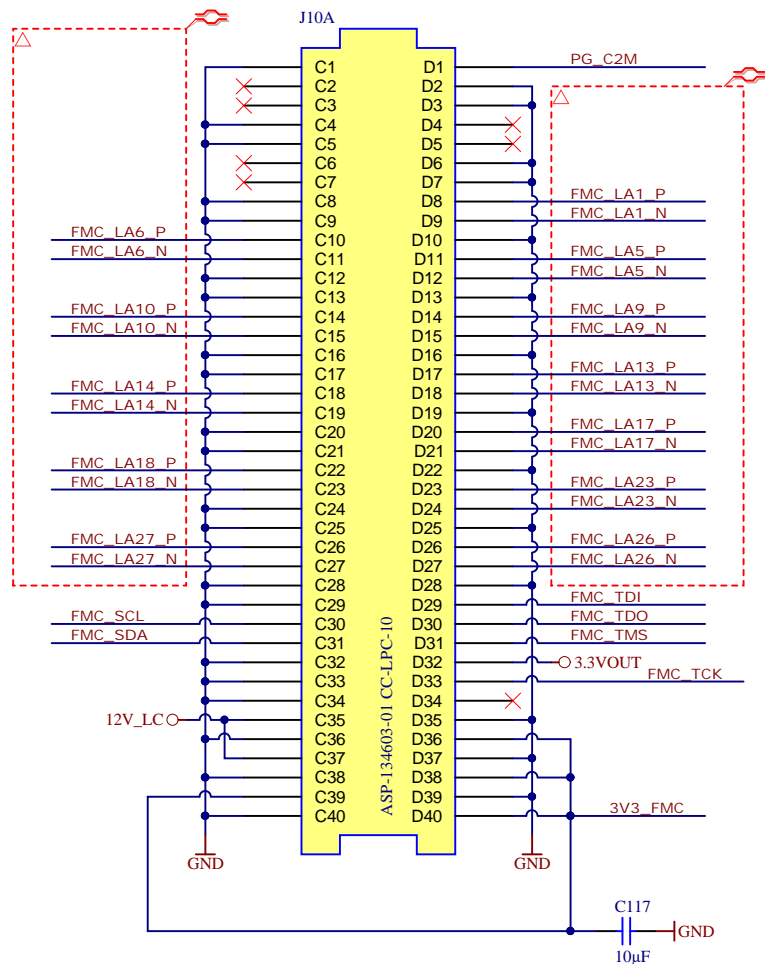
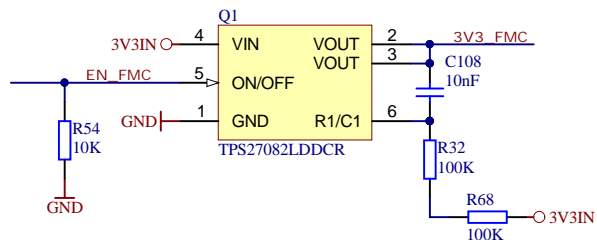
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		A4	Number: TE0701 default
Date: 2016-10-14	Copyright: Trenz Electronic GmbH		Rev. 06
Filename: ARM_JTAG.SchDoc		Page 2 of 19	



	Title: TE0701 - CameraLink			
	A4	Number: TE0701 default		Rev. 06
	Date: 2016-10-14	Copyright: Trenz Electronic GmbH		Page 3 of 19
	Filename: CameraLink.SchDoc			



	Title: TE0701 - Ethernet		
	A4	Nummer: TE0701 default	Rev. 06
	Datum: 2016-10-14	Zeichner: Trenz Electronic GmbH	Blatt 5 von 19
	Filename: Ethernet.SchDoc		



Title: TE0701 - FMC		
A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 6 of 19
Filename: FMC.SchDoc		

1

2

3

4

A

A

B

B

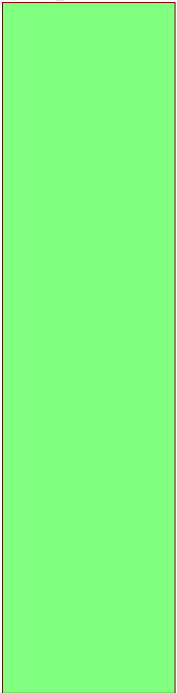
C

C

D

D

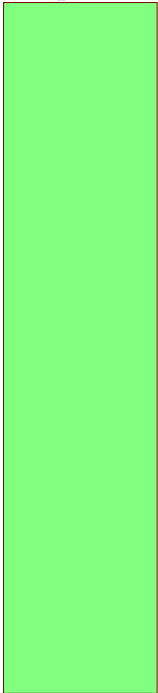
U_TE0720_JB1
TE0720_JB1.SchDoc




U_TE0720_JB3
TE0720_JB3.SchDoc



U_TE0720_JB2
TE0720_JB2.SchDoc





Title: TE0701 - FPGA B2B Connectors			
A4	Number: TE0701 default		Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH		Page 7 of 19
Filename: FPGA_B2B_Connectors.SchDoc			

1

2

3

4

1

2

3

4

A

A

B

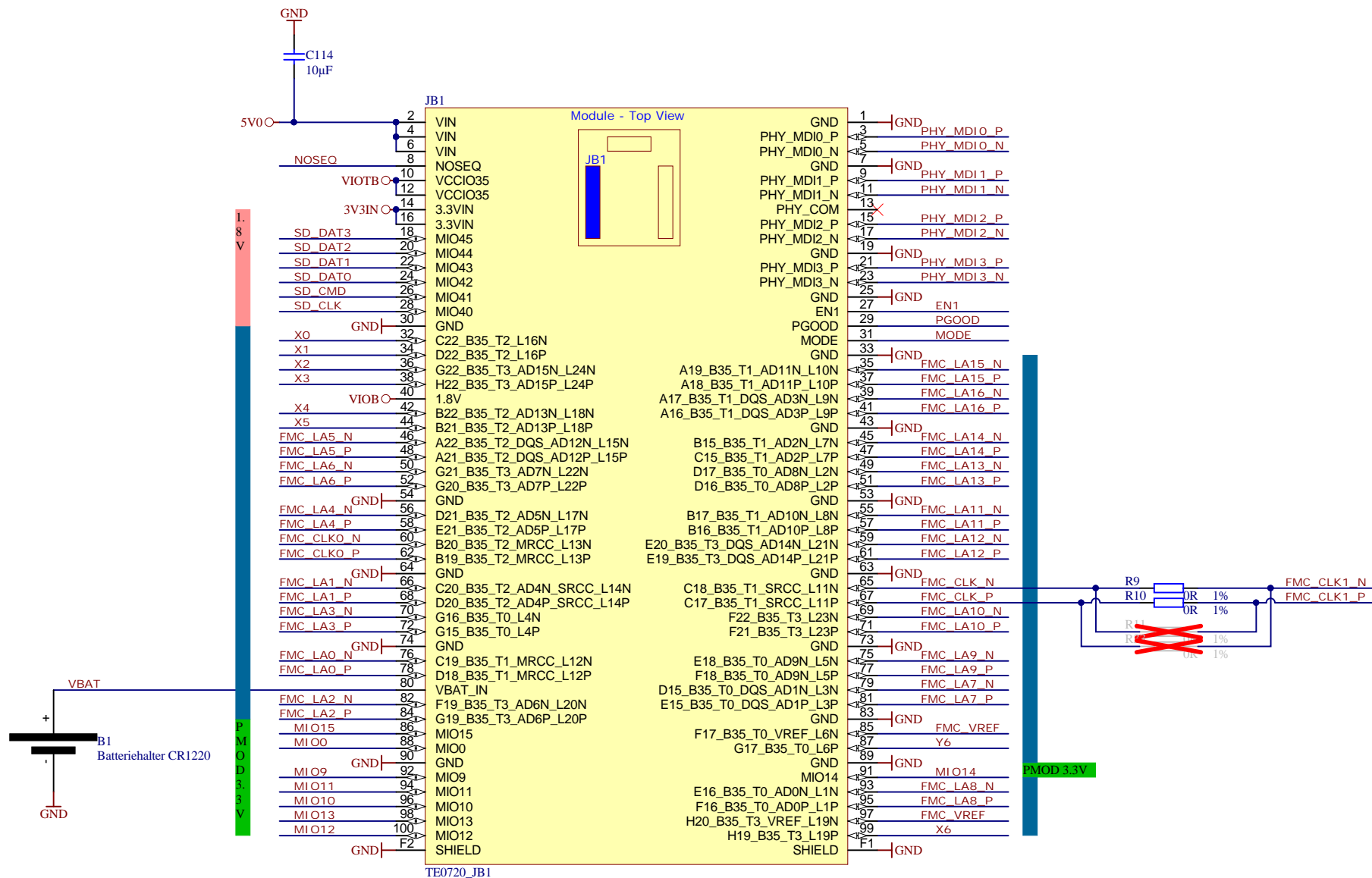
B

C

C

D

D



Title: TE0701 - FPGA B2B Connectors			
A4	Number: TE0701 default	Rev. 06	
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 8 of 19	
Filename: TE0720_JB1.SchDoc			

1

2

3

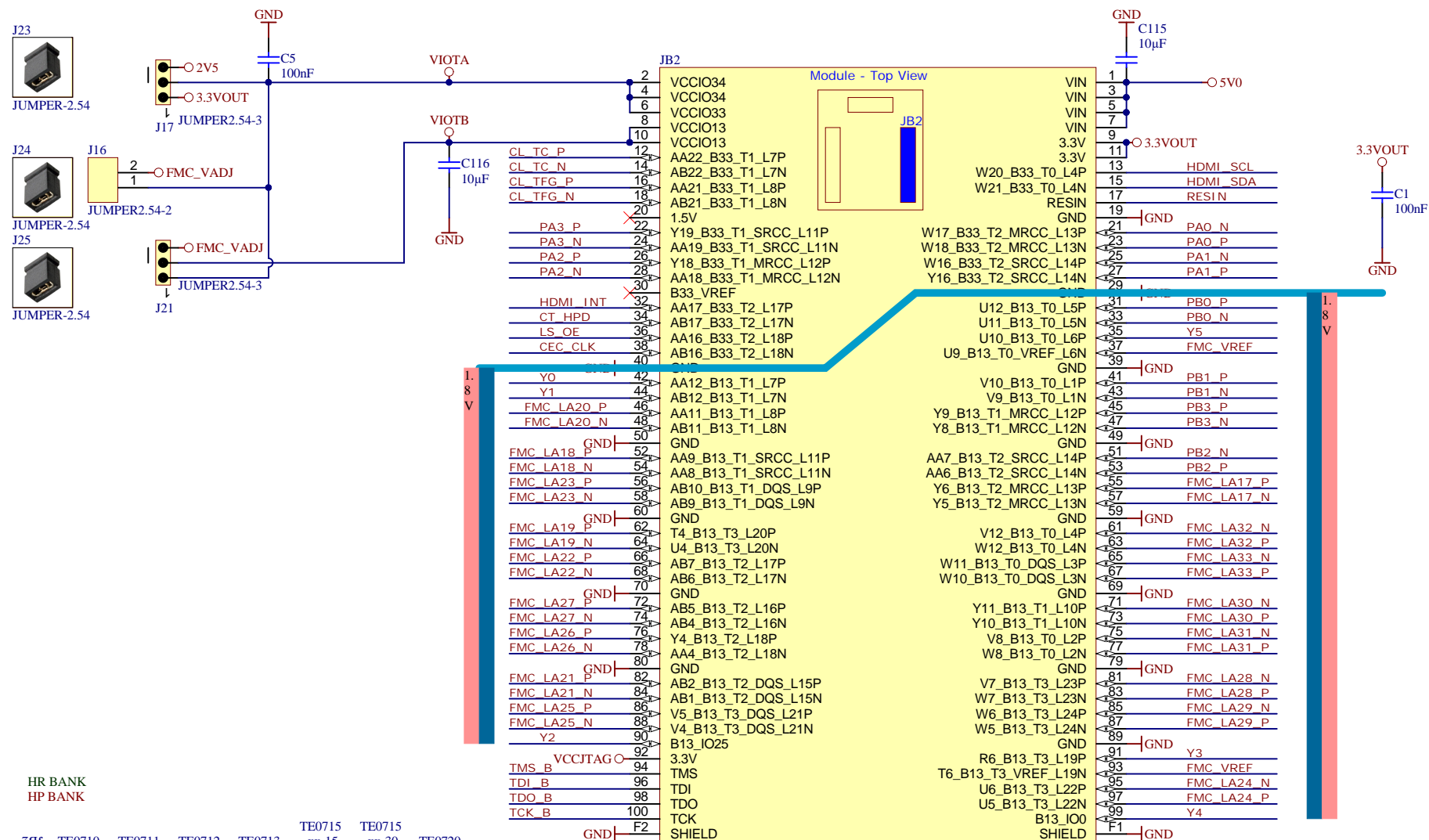
4

1

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4



HR BANK
HP BANK

TE0715 -xx-15	TE0715 -xx-30	TE0720	VIOTA
2 NC	NC	B34	VIOTA
4 NC	NC	B34	VIOTA
6 NC	NC	B34	VIOTA
8 B34	B35	B15	VIOTB
10 B34	B35	B15	VIOTB
12 B34	B35	B15	VIOTB



Title: TE0701 - FPGA B2B Connectors		
A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 9 of 19
Filename: TE0720_JB2.SchDoc		

1

2

3

4

A

B

C

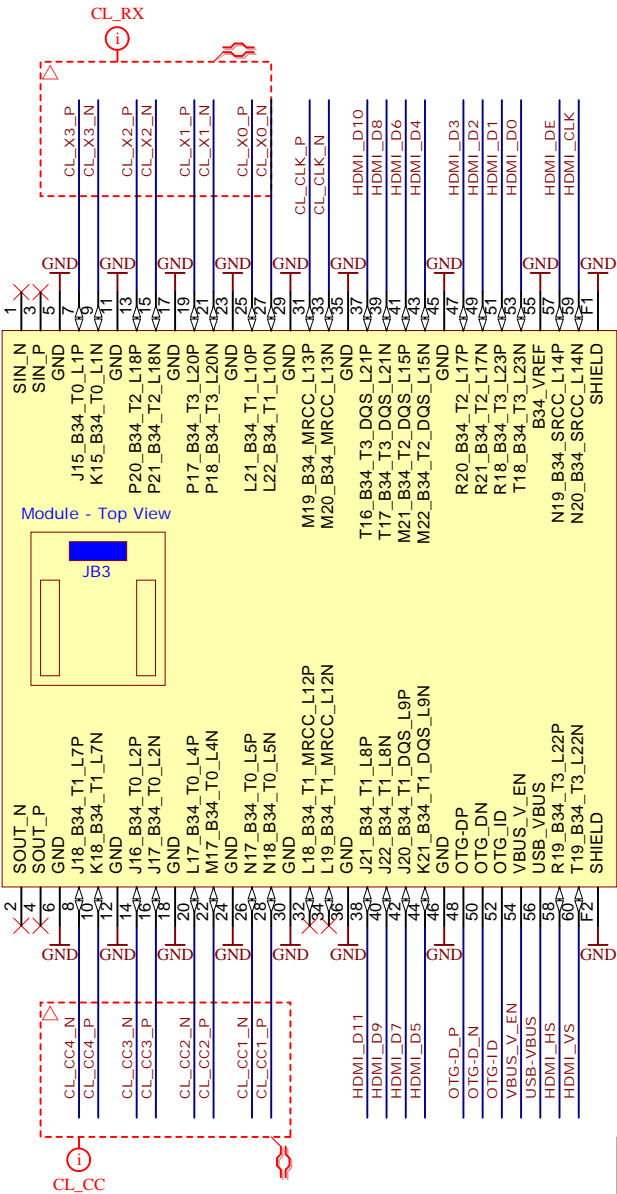
D

A

B

C

D



Title: TE0701 - FPGA B2B Connectors		
A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 10 of 19
Filename: TE0720_JB3.SchDoc		

1

2

3

4

U1A

Video

HDMI

VSYNC

HSYNC

CLK

DE

D0

D1

D2

D3

D4

D5

D6

D7

D8

D9

D10

D11

D12

D13

D14

D15

D16

D17

D18

D19

D20

D21

D22

D23

D24

D25

D26

D27

D28

D29

D30

D31

D32

D33

D34

D35

GND

ADV7511KSTZ

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

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VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

SCL_A

SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

HPD_B

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

CT_HP

VCC5V

HPD

CEC

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

SDA

INT

HPD_A

CEC_A

CEC_CLK

DDCSCL

DDCSDA

HEAC+

HEAC-

SCL

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SDA_A

HPD_A

LS_OE

CEC_B

SCL_B

SDA_B

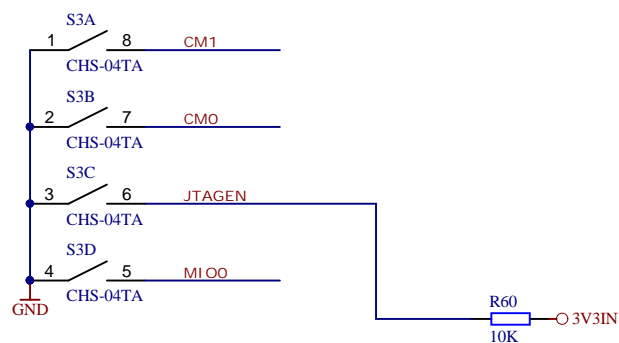
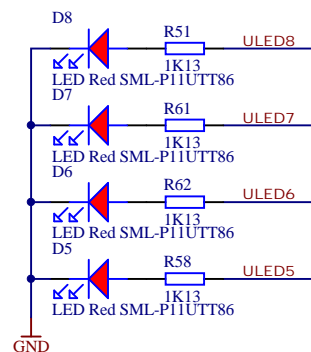
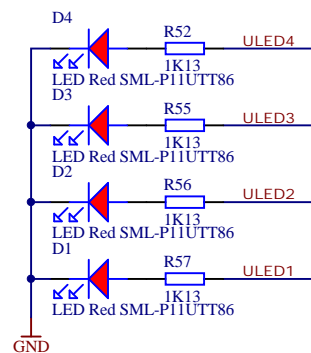
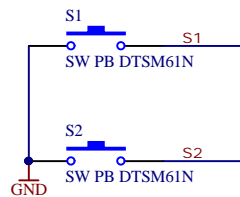
HPD_B

CT_HP

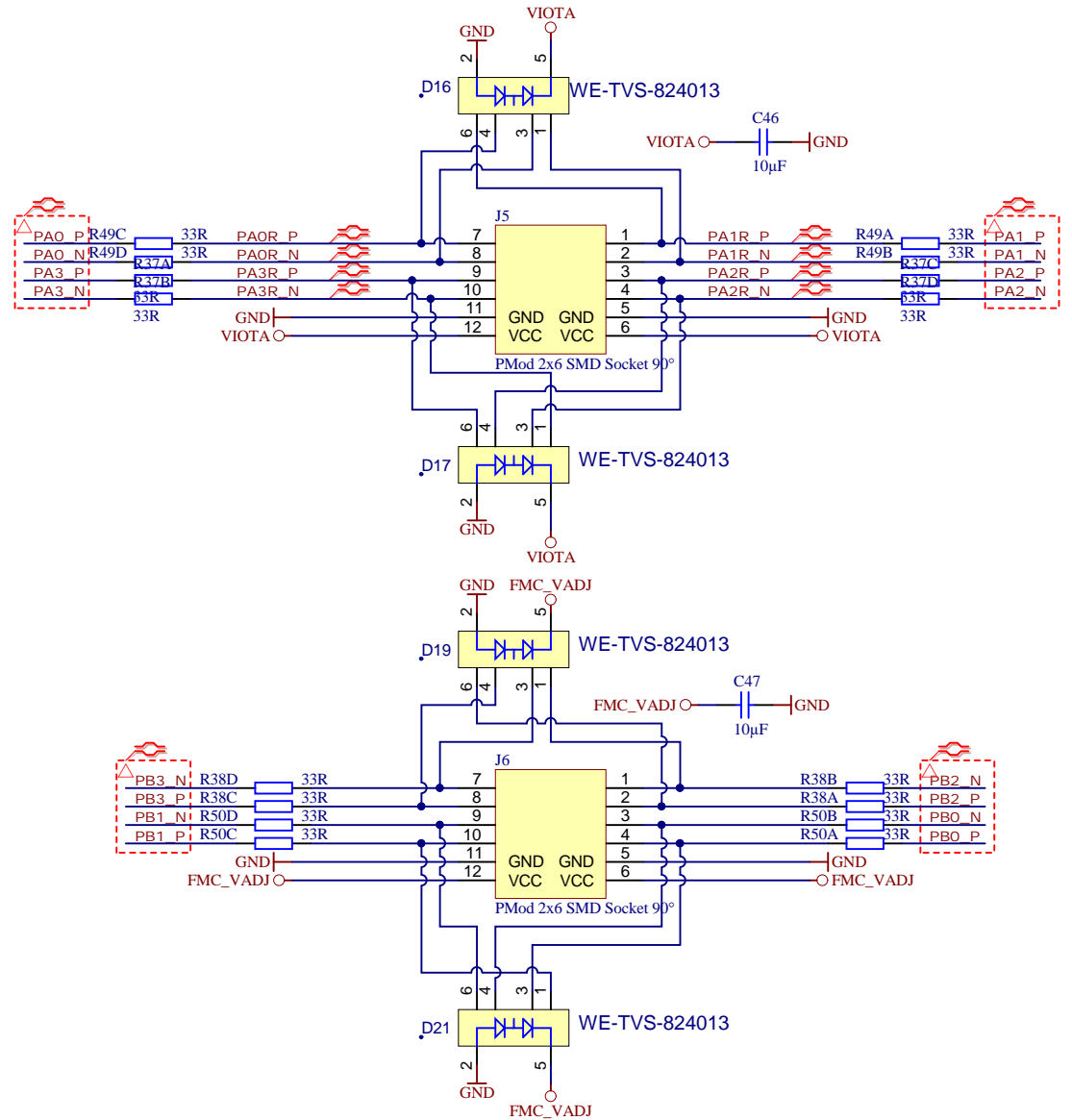
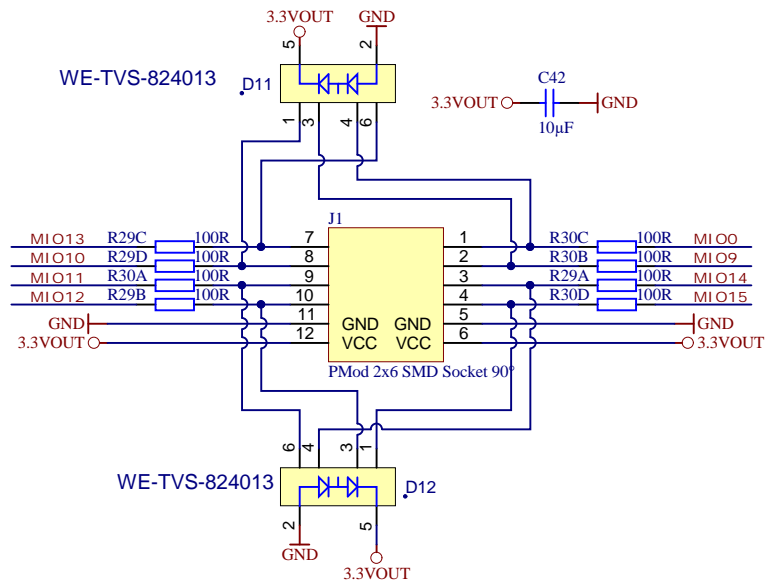
VCC5V


CT_HP

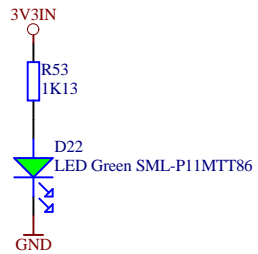
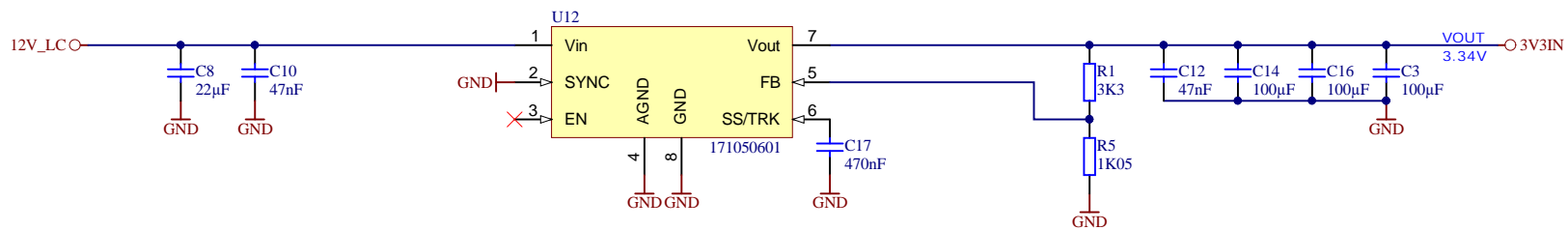
VCC5V



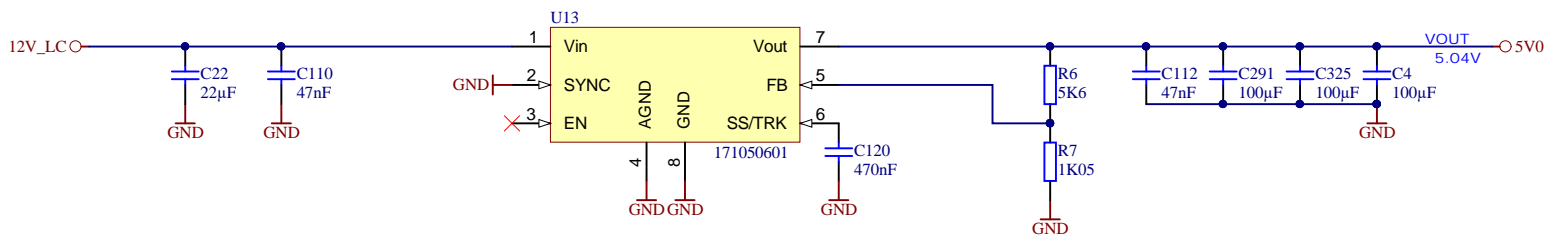
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A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 13 of 19
Filename: LED_SW_BUT.SchDoc		




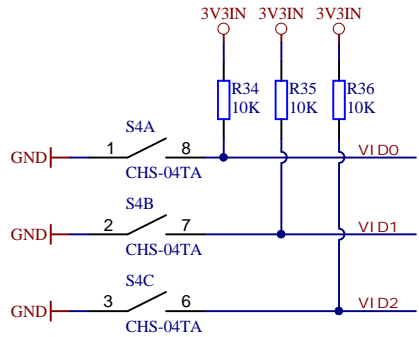
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		A4	Rev. 06
Date: 2016-10-14		Page 14 of 19	
Filename: PMODs.SchDoc			



Title: TE0701 - 3.3V POWER		
A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	
Filename: POWER_3V3.SchDoc		Page 15 of 19

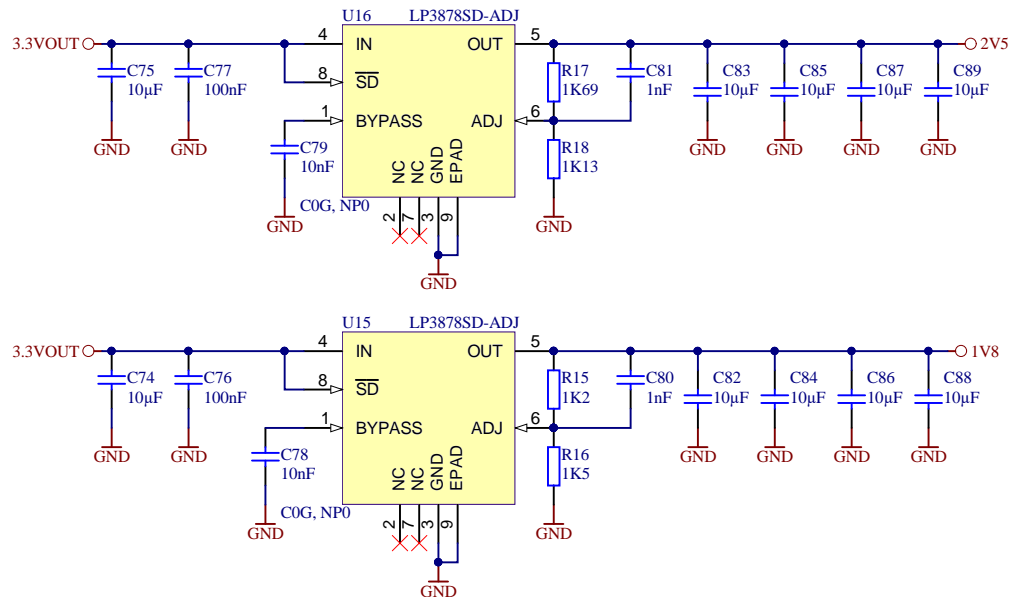
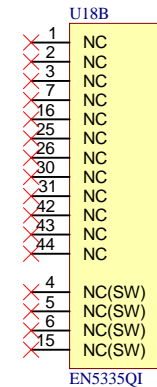
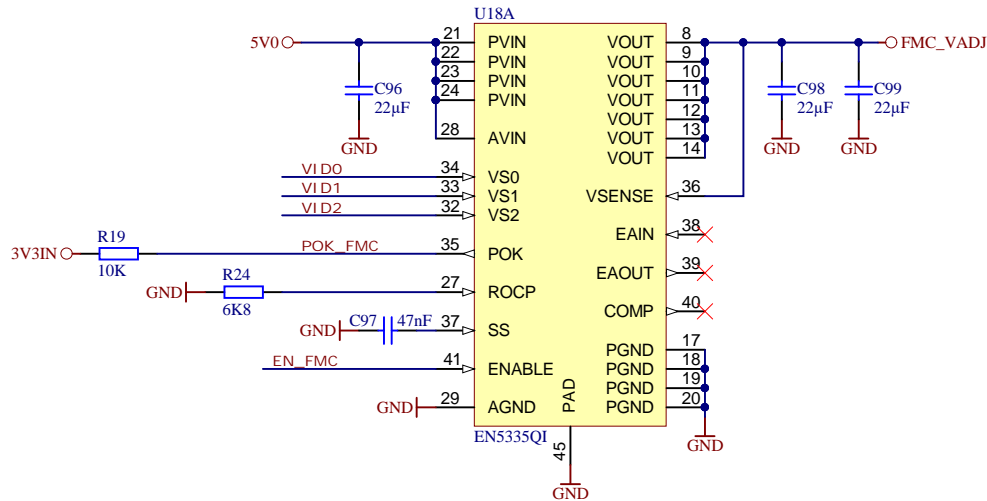



	Title: TE0701 - 5V POWER		
	A4	Number: TE0701 default	Rev. 06
	Date: 2016-10-14	Copyright: Trenz Electronic GmbH	Page 16 of 19
	Filename: POWER_5V0.SchDoc		



VS2 | VS1 | VS0 | Output Voltage

0	0	0	3.3V
0	0	1	2.5V
0	1	0	1.8V
0	1	1	1.5V
1	0	0	1.25V

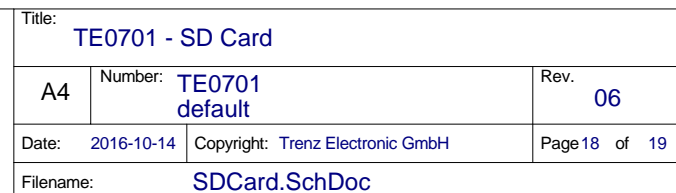
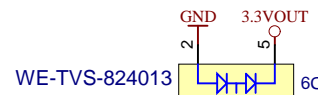
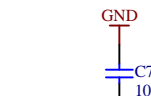
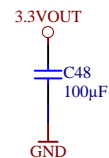




Title: **TE0701 - MISC POWER**

A4	Number: TE0701 default	Rev. 06
Date: 2016-10-14	Copyright: Trenz Electronic GmbH	
Page 17 of 19		

Filename: **POWER_MISC.SchDoc**



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CHANGES REV05 to REV06:

- 1) Removed holes for Xilinx JTAG Flying Wire Adaptor (1 row header)
- 2) Added jumper J16. Set a power supply on VIOTA to FMC_VADJ
- 3) Added feature to select Vadj voltage by dip switch S4
- 4) U3 FT232RL: new package -56Q
- 5) Changed RJ45 Ethernet connector (J14) 7499111121A to TRJG4820G4NL
- 6) Changed micro USB connector (J12)
- 7) Changed power supply on PMOD connector (J5) 3.3VOUT->VIOTA
- 8) Added resistors R9..R11,R33 for FMC_CLK as swap option
- 9) Added thermal vias to mount holes
- 10) Added serial visual serial number (traceability pad)
- 11) Lib components update
- 12) Changed PMOD connectors THT to SMT
- 13) Removed resistor (R43) on CMD (SD card). R43 is not needed and caused problems with some SD Cards

A

E

C

□



Title:

TE0701 - Revision history

A4

Number:	TE0701 default
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Rev.	06
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Date: 2016-10-14

4 Copyright: Trenz Electronic GmbH

Page 19 of 19

Filename:

Revision_Changes.SchDoc

1

2

3

4