# Dan Bostan's Portfolio

#### Dan Bostan

- Electrical Engineer & Patent Agent
- Experienced in developing test, video, data and telecommunications equipment
- Specialized in high speed design and signal integrity for boards and systems

### The Brightlink Tb crossconnect



- Terrabit STS-1 non-blocking cross-connect, based upon a hyper-torus switch design
- The first multi-chassis system using Paroli VCSEL connectors
- 3500W/chassis
- In trials with major carriers



## The Brightlink Tb cross-connect



- Architected the system
- Designed the line card with OC48 interfaces
- Designed the timing sub-system
- Hired the HW engineering team



### The Atoga Systems God box

- The first shipped integrated router & cross-connect with tunable DWDM interfaces
- The optical tunable interfaces were built on board
- 1000W
- Shipped to customers



The 2<sup>nd</sup> generation used an ATCA 14 slots system



## The Atoga Systems God box

- Led and managed the HW system design group
- Architected the system
- Designed the OC48 line card



Led the FCC and NEBS system certification



## The Allegro Networks multi-router

- The first multi-router with multiple physically distinct routers
- Fully redundant switch fabrics for the data plane and for the control plane
- Channelized SONET interfaces
- 4500W





## The Allegro Networks multi-router

- Designed the channelized SONET interfaces
- Designed the maintenance/alarm subsystem
- In carrier trials







# Qlogic Corporation

- iSCSI controler chip validation
- Network Interface Cards Design
- Signal IntegritySimulation
- Customer support in using Qlogic's chips







- Architected & designed validation & evaluation platforms for Framers (Yahara, PQx-12 lanes @ 11.3Gbps, and X24- 24 lanes @ 11.3Gbps + 4 lanes @ 31.79Gbps)
- Architected & designed
  validation/evaluation platforms
  for Phys (Gearbox and QPSK
  MUX-12 lanes @ 11.3Gbps + 4
  lanes @ 31.79Gbps)



AMCC PQx Framer Platform



### **Applied Micro Circuits Corp.**

- System chip architecture
- Led the HW system engineers
- •Optimized the system cost, given the level of performance required

•Full vendor selection and management



AMCC X24 Framer Platform