

FunTAL: Reasonably Mixing a Functional Language with Assembly

Daniel Patterson,* Jamie Perconti,*
Christos Dimoulas,[†] Amal Ahmed*

June 20, 2017

* Northeastern University, [†] Harvard University

Mixed language programs

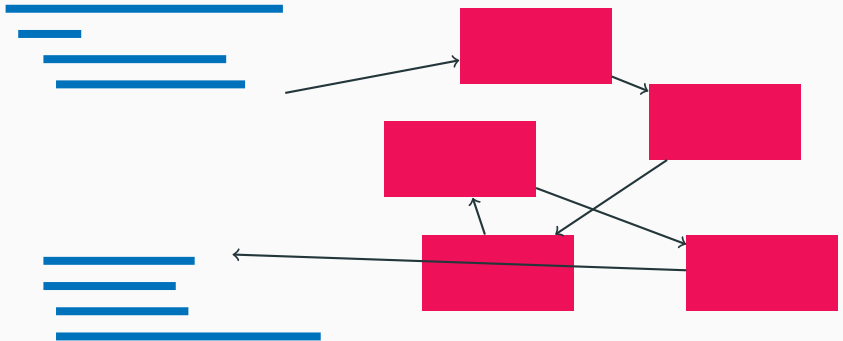
Functional program



Mixed language programs

Functional program

Inline assembly



Questions we want to answer

How to safely mix assembly with high-level code?

How to reason about equivalence of mixed programs?

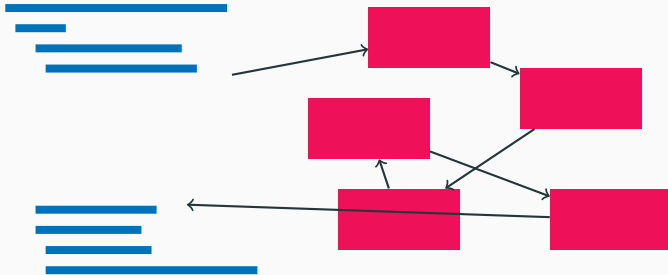
Questions we want to answer

How to safely mix assembly with high-level code?

How to reason about equivalence of mixed programs?

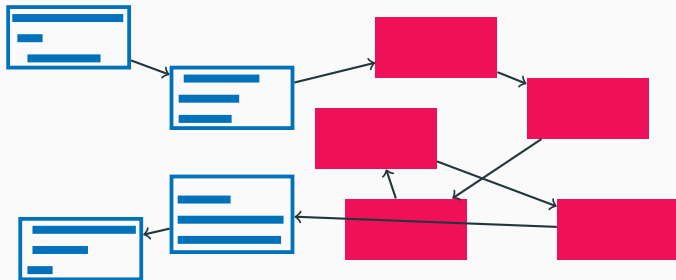
Prior approaches to mixing

Option 1: Translate high-level code into continuation-passing style to match assembly control-flow.



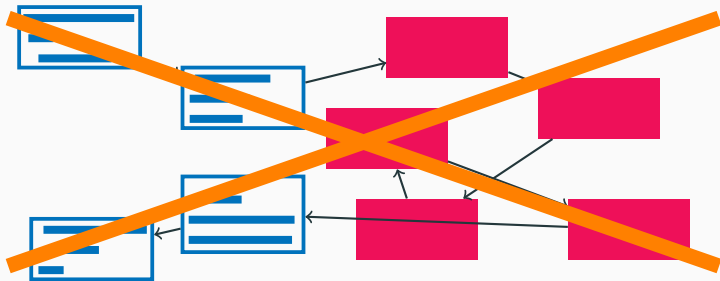
Prior approaches to mixing

Option 1: Translate high-level code into continuation-passing style to match assembly control-flow.



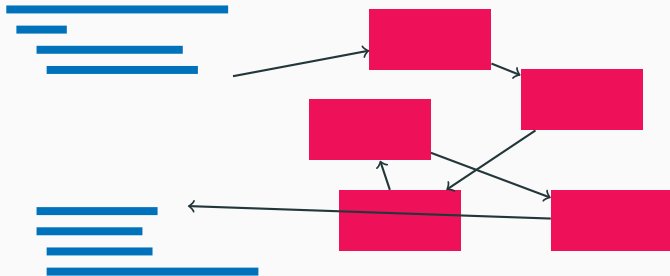
Prior approaches to mixing

Option 1: Translate high-level code into continuation-passing style to match assembly control-flow.



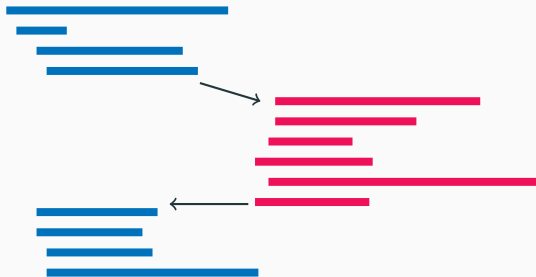
Prior approaches to mixing

Option 2: Impose high-level call-stack control-flow onto assembly.



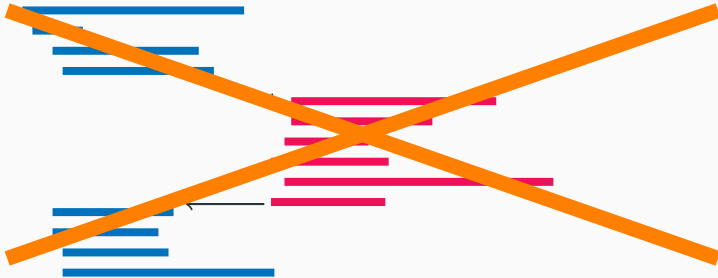
Prior approaches to mixing

Option 2: Impose high-level call-stack control-flow onto assembly.



Prior approaches to mixing

Option 2: Impose high-level call-stack control-flow onto assembly.



Our contributions

Allow safe mixing that allows **high-level code to remain high-level** and **low-level code to remain low-level**.

Our contributions

Allow safe mixing that allows **high-level code to remain high-level** and **low-level code to remain low-level**.

We do this via a novel notion of a **return marker**, which allows us to define the notion of an assembly **component**.

Fun: Functional language

- Simply typed lambda calculus (STLC)
- with (iso-)recursive types

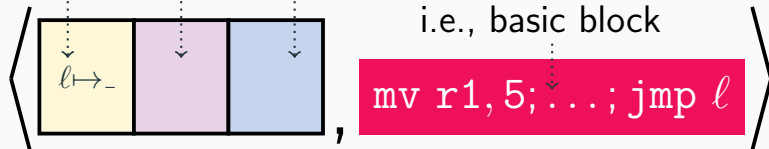
TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]

heap registers stack

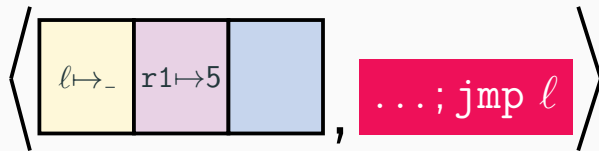
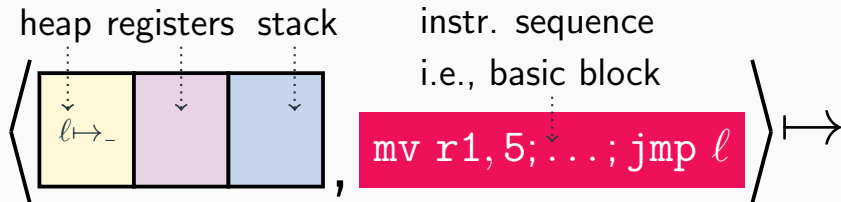
instr. sequence

i.e., basic block



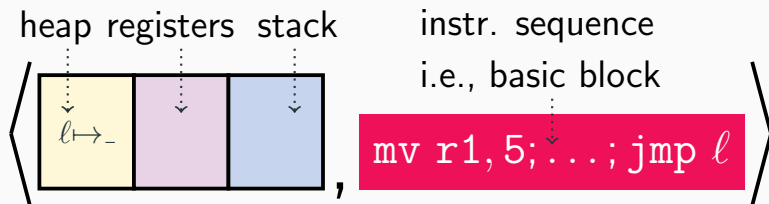
TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]



TAL: Typed Assembly Language

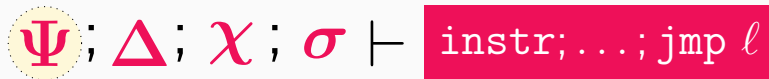
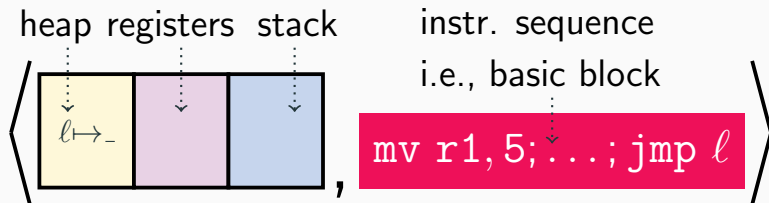
[Morrisett, Crary, Glew, Walker 98]



$\Psi; \Delta; \chi; \sigma \vdash \text{instr}; \dots; \text{jmp } l$

TAL: Typed Assembly Language

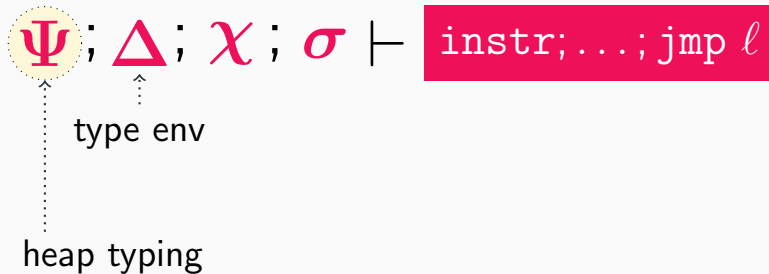
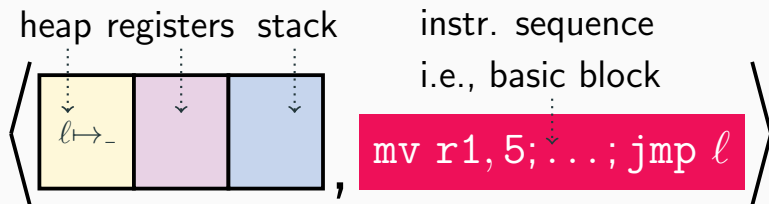
[Morrisett, Crary, Glew, Walker 98]



heap typing

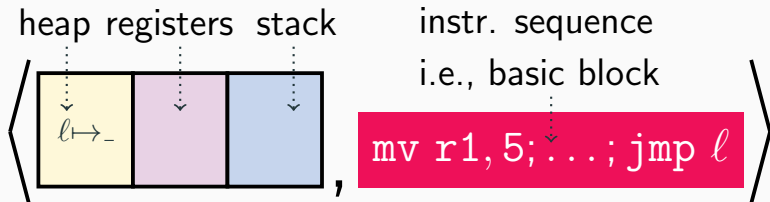
TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]



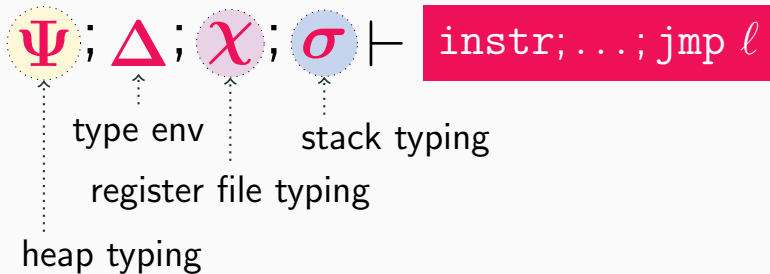
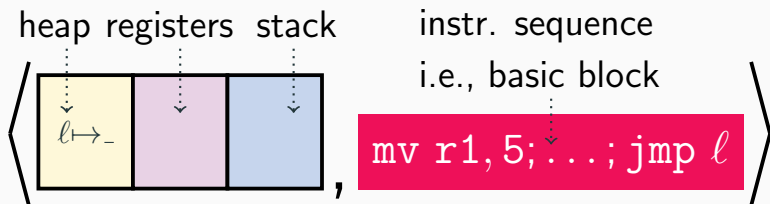
TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]



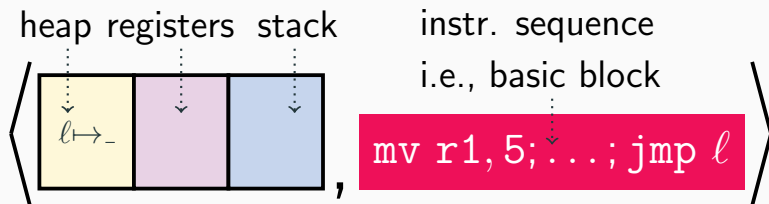
TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]



TAL: Typed Assembly Language

[Morrisett, Crary, Glew, Walker 98]



$\Psi; \Delta; \chi; \sigma \vdash \text{instr}; \dots; \text{jmp } \ell$

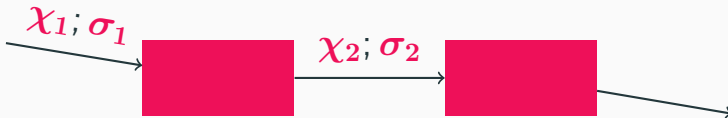
$\text{instr}; \dots; \text{jmp } \ell : \forall [\Delta]. \{ \chi; \sigma \}$

TAL types are preconditions

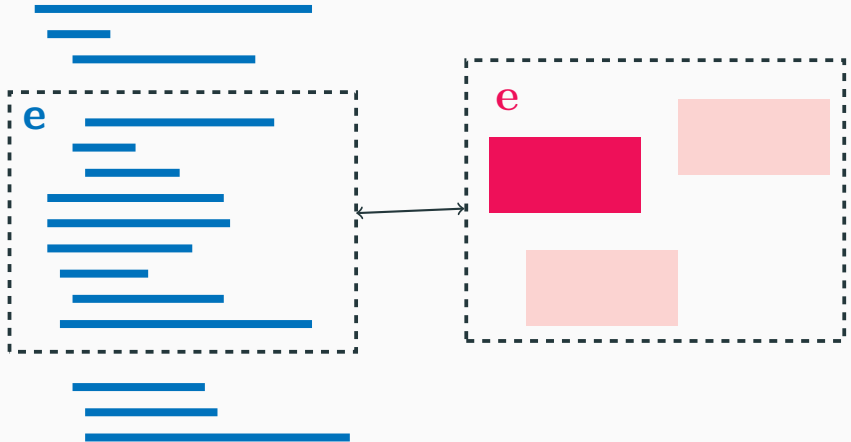
$$\boxed{\phantom{\text{type}}} : \forall[\Delta].\{\chi; \sigma\}$$

TAL types are preconditions

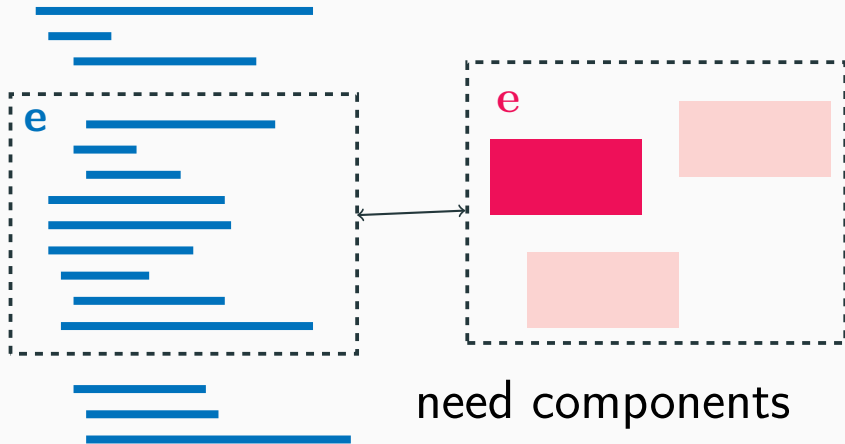
$$\boxed{} : \forall[\Delta].\{\chi; \sigma\}$$



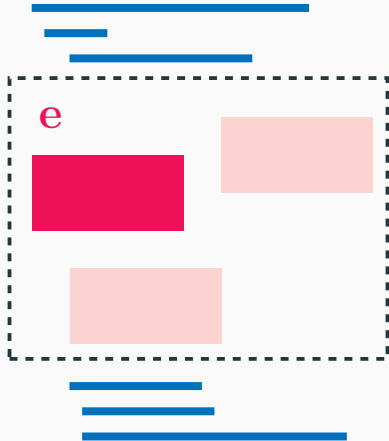
Mixing is different



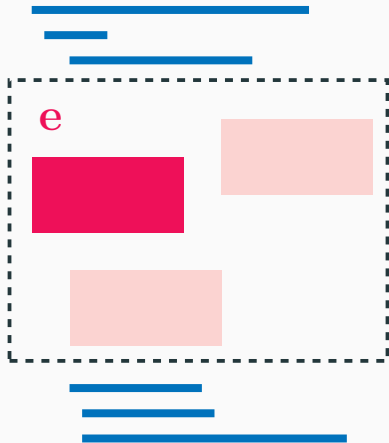
Mixing is different



Mixing is different

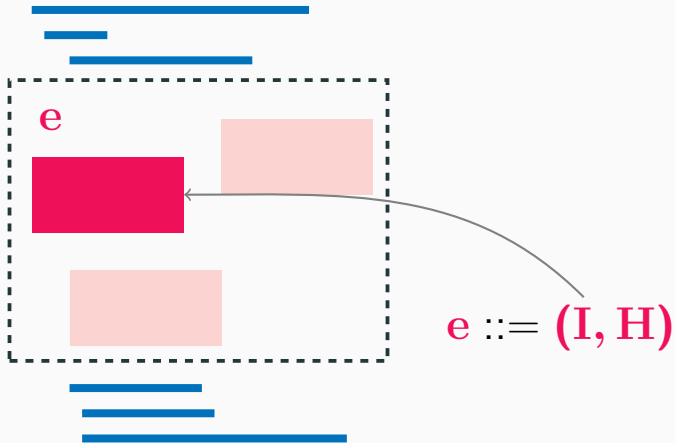


Mixing is different

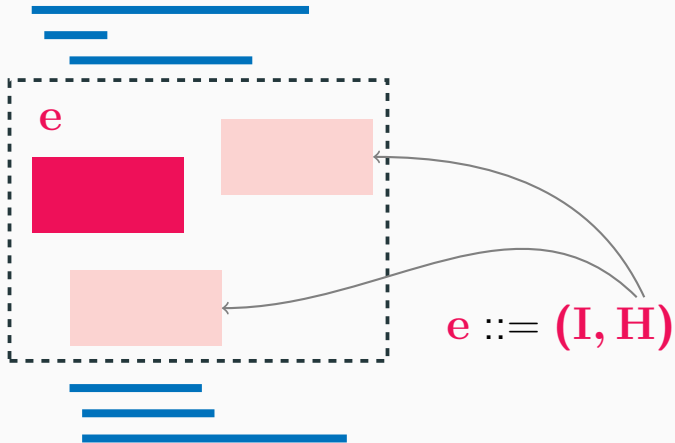


$$e ::= (I, H)$$

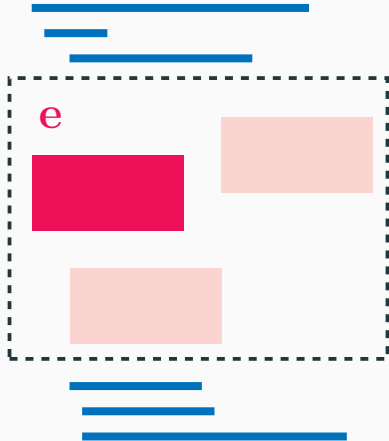
Mixing is different



Mixing is different



Mixing is different



writing this
program requires
multi-language

Multi-languages in general

[Matthews-Findler 07]

Combine syntaxes from languages S and T and introduce boundary terms.

$${}^{\tau}ST(e_T) \mapsto^* {}^{\tau}ST(v_T) \mapsto v_S$$

$$TS^{\tau}(e_S) \mapsto^* TS^{\tau}(v_S) \mapsto v_T$$

“Reduce under boundary and then translate value.”

Multi-languages in general

[Matthews-Findler 07]

Combine syntaxes from languages S and T and introduce boundary terms.

$${}^{\tau}ST(e_T) \mapsto^* {}^{\tau}ST(v_T) \mapsto v_S$$

$$TS^{\tau}(e_S) \mapsto^* TS^{\tau}(v_S) \mapsto v_T$$

“Reduce under boundary and then translate value.”

Multi-languages in general

[Matthews-Findler 07]

Combine syntaxes from languages S and T and introduce boundary terms.

$${}^{\tau}ST(e_T) \mapsto^* {}^{\tau}ST(v_T) \mapsto v_S$$

$$TS^{\tau}(e_S) \mapsto^* TS^{\tau}(v_S) \mapsto v_T$$

“Reduce under boundary and then translate value.”

Multi-languages in general

[Matthews-Findler 07]

Combine syntaxes from languages S and T and introduce boundary terms.

$${}^{\tau}ST(e_T) \mapsto^* {}^{\tau}ST(v_T) \mapsto v_S$$

$$TS^{\tau}(e_S) \mapsto^* TS^{\tau}(v_S) \mapsto v_T$$

“Reduce under boundary and then translate value.”

Multi-languages in general

[Matthews-Findler 07]

Boundary translations rely on a cross-language type translation $(\cdot)^+$.

$$\frac{e_S : \tau}{TS^\tau(e_S) : \tau^+} \qquad \frac{e_T : \tau^+}{{}^\tau ST(e_T) : \tau}$$

Multi-languages in general

[Matthews-Findler 07]

Boundary translations rely on a cross-language type translation $(\cdot)^+$.

$$\frac{e_S : \tau}{TS^\tau(e_S) : \tau^+} \qquad \frac{e_T : \tau^+}{{}^\tau ST(e_T) : \tau}$$

Multi-languages in general

[Matthews-Findler 07]

Boundary translations rely on a
cross-language type translation $(\cdot)^+$.

$$\frac{e_S : \tau}{TS^\tau(e_S) : \tau^+} \qquad \frac{e_T : \tau^+}{{}^\tau ST(e_T) : \tau}$$

Multi-language for FunTAL

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

Multi-language for FunTAL

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$(\mathbf{I}, \mathbf{H}) : ?$$

Multi-language for FunTAL

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$(\mathbf{I}, \mathbf{H}) : ?$$

$$(\mathbf{I}, \mathbf{H}) \mapsto^* ?$$

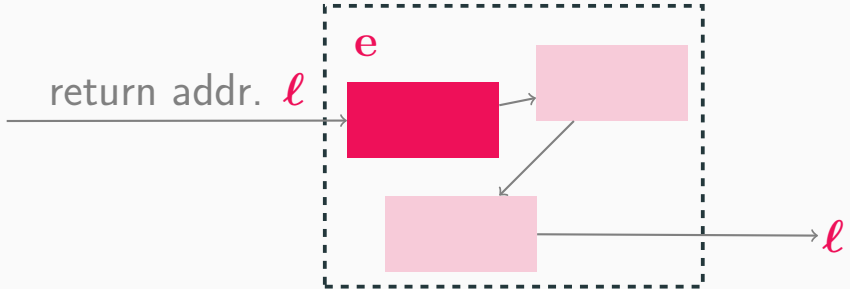
Multi-language for FunTAL

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

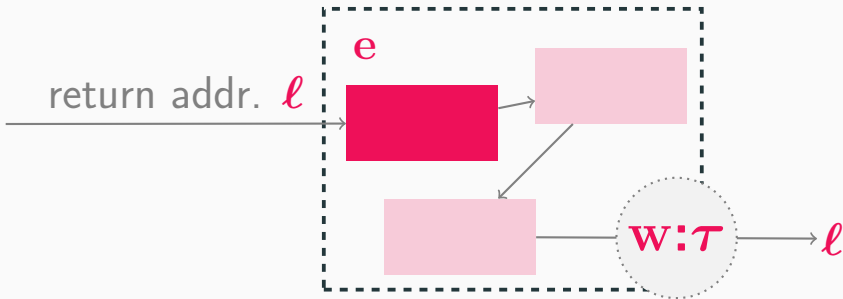
$$(\mathbf{I}, \mathbf{H}) : ?$$

$$(\mathbf{I}, \mathbf{H}) \mapsto^* ?$$

TAL components return to address



Return value passed to address



When will component **e** return **τ** ?

When will component **e** return **τ** ?

$$\Psi; \Delta; \chi; \sigma; \mathbf{q} \vdash (\mathbf{I}, \mathbf{H}) : \tau, \sigma'$$

return marker




When will component **e** return **τ** ?

$$\Psi; \Delta; \chi; \sigma; \text{ra} \vdash (I, H) : \tau, \sigma'$$

 {**ra** : type of codeblock expecting to be passed a **τ** and stack **σ'** }

When will component **e** return **τ** ?

$\Psi; \Delta; \chi; \sigma; \text{ra} \vdash (\text{I}, \text{H}) : \tau, \sigma'$

 $\{\text{ra} : \text{type of codeblock expecting to be passed a } \tau \text{ and stack } \sigma'\}$

When will component **e** return **τ** ?

$$\Psi; \Delta; \chi; \sigma; 2 \vdash (I, H) : \tau, \sigma'$$

at **2** : type of codeblock expecting to be
passed a **τ** and stack **σ'**

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

$$\Psi; \Delta; \chi[r1 : \tau]; \sigma; \text{end}\{\tau; \sigma\} \vdash \\ (\text{halt } r1, \mathbf{H}) : \tau, \sigma$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

$$\Psi; \Delta; \chi[r1 : \tau]; \sigma; \text{end}\{\tau; \sigma\} \vdash \\ (\text{halt } r1, \mathbf{H}) : \tau, \sigma$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

$$\Psi; \Delta; \chi[\mathbf{r1} : \tau]; \sigma; \text{end}\{\tau; \sigma\} \vdash \\ (\text{halt } \mathbf{r1}, \mathbf{H}) : \tau, \sigma$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

$$\Psi; \Delta; \chi[r1 : \tau]; \sigma; \text{end}\{\tau; \sigma\} \vdash \\ (\text{halt } r1, \mathbf{H}) : \tau, \sigma$$

What corresponds to **Fun** values?

$$\tau_{\mathcal{FT}}(\mathbf{I}, \mathbf{H}) \mapsto^* \tau_{\mathcal{FT}}(\mathbf{v}) \mapsto \mathbf{v}$$

$$\mathbf{v} ::= (\text{halt } r_d, \mathbf{H})$$

$$\Psi; \Delta; \chi[r1 : \tau]; \sigma; \text{end}\{\tau; \sigma\} \vdash \\ (\text{halt } r1, \mathbf{H}) : \tau, \sigma$$

Embedding Fun in TAL

$$\tau_{\mathcal{F}\mathcal{T}} \left(\begin{array}{c} \boxed{\tau_{\mathcal{F}\mathcal{T}} \left(\begin{array}{c} \text{---} \\ \text{---} \\ \text{---} \end{array} \right)} \\ \text{---} \\ \boxed{\text{---}} \end{array} \right)$$

How to embed an expression in TAL?

import $r_d, \mathcal{TF}^{\tau}(\mathbf{v}) \mapsto mv\ r_d, w$

where $\mathbf{v}:\tau \rightsquigarrow \mathbf{w}:\tau^+$

How to embed an expression in TAL?

$\text{import } r_d, \mathcal{TF}^{\mathcal{T}}(\mathbf{v}) \mapsto \text{mv } r_d, w$

where $\mathbf{v}:\mathcal{T} \rightsquigarrow \mathbf{w}:\mathcal{T}^+$

How to embed an expression in TAL?

import $r_d, \mathcal{TF}^{\mathcal{T}}(\mathbf{v}) \mapsto \text{mv } r_d, \mathbf{w}$

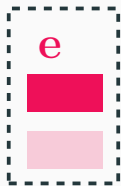
where $\mathbf{v}:\tau \rightsquigarrow \mathbf{w}:\tau^+$

How to embed an expression in TAL?

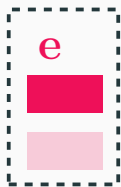
import $r_d, \mathcal{TF}^{\mathcal{T}}(v) \mapsto mv\ r_d, w$

where $v:\tau \rightsquigarrow w:\tau^+$

So far, **q** can be **ra**, **n**, **end** $\{\tau; \sigma\}$



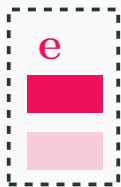
So far, **q** can be **ra**, **n**, **end** $\{\tau; \sigma\}$



$$\Psi; \Delta; \chi; \sigma; \text{ra} \vdash e : \tau, \sigma'$$

With these return markers, preconditions on **e** must specify all subsequent return markers.

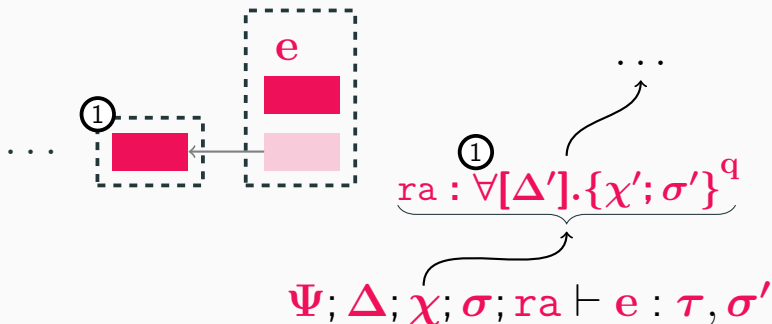
So far, **q** can be **ra**, **n**, **end**{ $\tau; \sigma$ }



$$\Psi; \Delta; \chi; \sigma; \text{ra} \vdash e : \tau, \sigma'$$

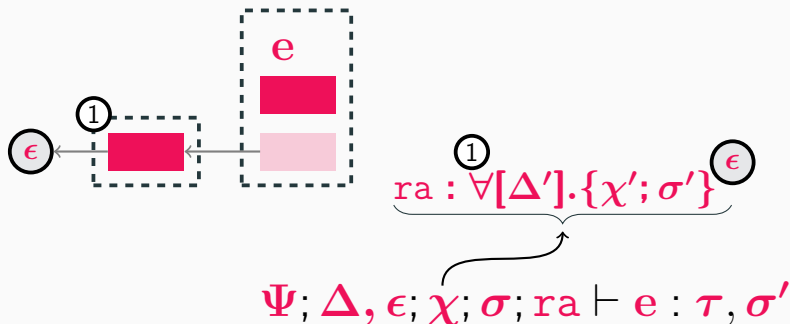
With these return markers, preconditions on **e** must specify all subsequent return markers.

So far, **q** can be **ra**, **n**, **end** $\{\tau; \sigma\}$



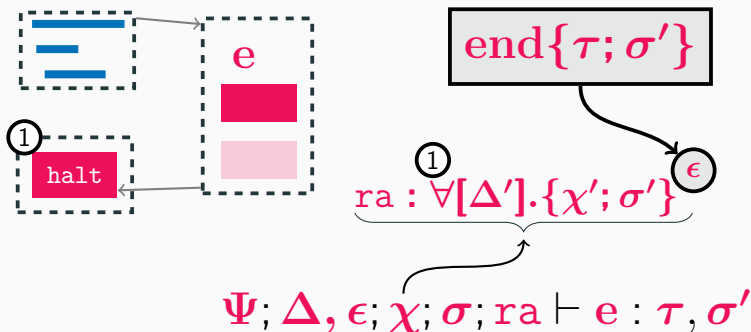
With these return markers, preconditions on **e** must specify all subsequent return markers.

Components need polymorphic q



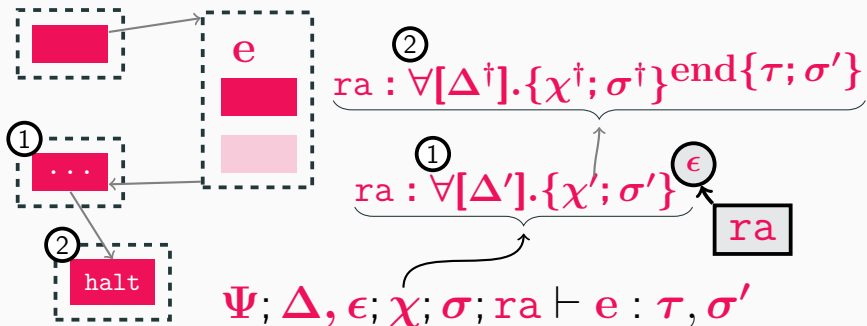
With polymorphic return marker ϵ , caller instantiates with where control flows next.

Components need polymorphic q



With polymorphic return marker ϵ , caller instantiates with where control flows next.

Components need polymorphic **q**



With polymorphic return marker **ε**, caller instantiates with where control flows next.

Questions we want to answer

How to safely mix assembly with high-level code?

How to reason about equivalence of mixed programs?

Questions we want to answer

How to safely mix assembly with high-level code? ✓

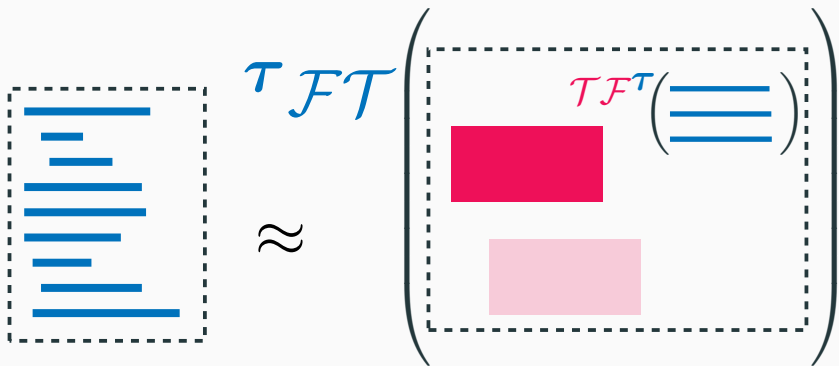
How to reason about equivalence of mixed programs?

Questions we want to answer

How to safely mix assembly with high-level code? ✓

How to reason about equivalence of mixed programs?

Proving program equivalence



Need logical relation for multi-language.

How logical relations work

$\mathbf{e}:\tau \approx {}^\tau \mathcal{FT}(\mathbf{e}:\tau^+)$ means

$$\mathbf{e} \mapsto^* \mathbf{v}_1 \iff \mathcal{FT}(\mathbf{e}) \mapsto^* \mathbf{v}_2$$

and $\mathbf{v}_1 \approx \mathbf{v}_2$

How logical relations work

$\mathbf{e}:\tau \approx {}^\tau \mathcal{FT}(\mathbf{e}:\tau^+)$ means

$$\mathbf{e} \mapsto^* \mathbf{v}_1 \iff \mathcal{FT}(\mathbf{e}) \mapsto^* \mathbf{v}_2$$

and $\mathbf{v}_1 \approx \mathbf{v}_2$

How logical relations work

$e:\tau \approx {}^\tau \mathcal{FT}(e:\tau^+)$ means

$$e \mapsto^* v_1 \iff \mathcal{FT}(e) \mapsto^* v_2$$

and $v_1 \approx v_2$

How logical relations work

$\mathbf{e}:\tau \approx {}^\tau \mathcal{FT}(\mathbf{e}:\tau^+)$ means

$$\mathbf{e} \mapsto^* \mathbf{v}_1 \iff \mathcal{FT}(\mathbf{e}) \mapsto^* \mathbf{v}_2$$

and $\mathbf{v}_1 \approx \mathbf{v}_2$

How logical relations work

$\mathbf{e}:\tau \approx {}^\tau \mathcal{FT}(\mathbf{e}:\tau^+)$ means

$$\mathbf{e} \mapsto^* \mathbf{v}_1 \iff \mathcal{FT}(\mathbf{e}) \mapsto^* \mathbf{v}_2$$

and $\mathbf{v}_1 \approx \mathbf{v}_2$

Write $\mathbf{v}_1 \approx \mathbf{v}_2$ as $(\mathbf{v}_1, \mathbf{v}_2) \in \mathcal{V}(\tau)$.

Equivalence of functions

$$(\lambda x. e_1, \lambda x. e_2) \in \mathcal{V}(\tau_1 \rightarrow \tau_2)$$

Equivalence of functions

$$(\lambda x. e_1, \lambda x. e_2) \in \mathcal{V}(\tau_1 \rightarrow \tau_2)$$

“Related inputs result in related outputs”

Equivalence of functions

$$(\lambda x. e_1, \lambda x. e_2) \in \mathcal{V}(\tau_1 \rightarrow \tau_2)$$

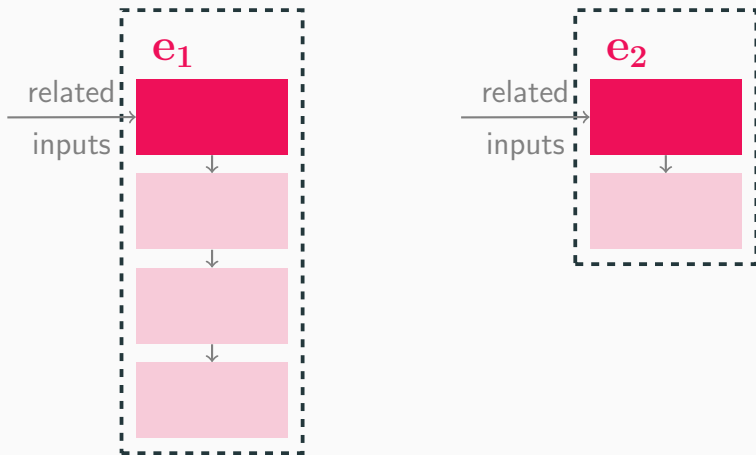
$$\text{if } (v_1, v_2) \in \mathcal{V}(\tau_1) \implies$$

$$e_1[x \mapsto v_1] \approx e_2[x \mapsto v_2]$$

“Related inputs result in related outputs”

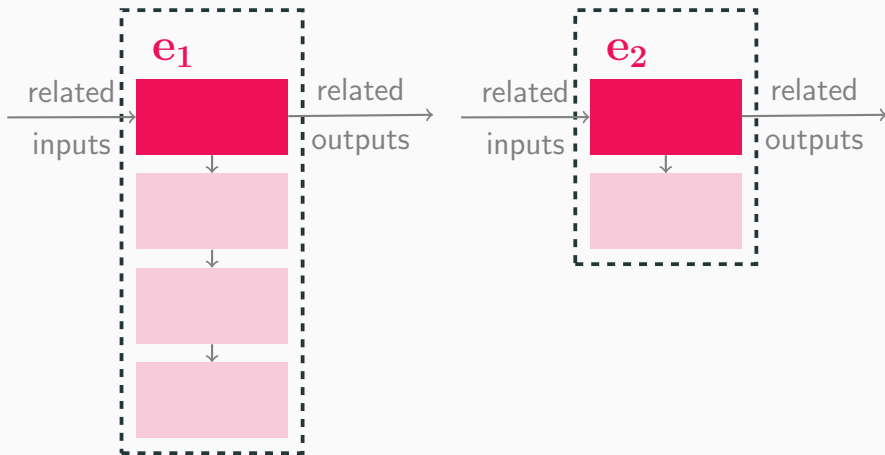
Equivalence of code blocks

$$\mathbf{e}_1 \approx \mathbf{e}_2$$



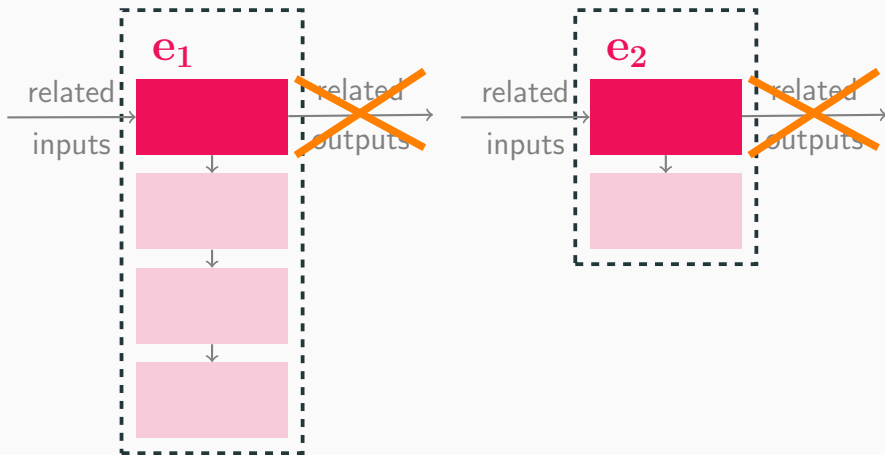
Equivalence of code blocks

$$e_1 \approx e_2$$



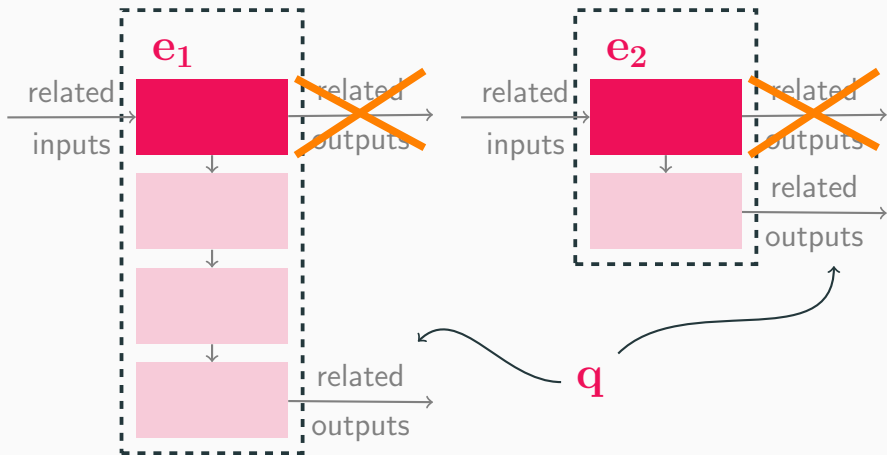
Equivalence of code blocks

$$e_1 \approx e_2$$



Equivalence of code blocks

$$\mathbf{e}_1 \approx \mathbf{e}_2$$



Questions we want to answer

How to safely mix assembly with high-level code? ✓

How to reason about equivalence of mixed programs?

Questions we want to answer

How to safely mix assembly with high-level code? ✓

How to reason about equivalence of mixed programs? ✓

Future work

Future work

- Verification of some types of JIT transformations.

Future work

- Verification of some types of JIT transformations.
- Correctness for compilers targeting TAL (as suggested by *[Perconti-Ahmed 14]*).

Future work

- Verification of some types of JIT transformations.
- Correctness for compilers targeting TAL (as suggested by *[Perconti-Ahmed 14]*).
- Using return markers for slightly higher level (i.e., SSA-like) languages.

Conclusion

Return markers allow **safe mixing of components** where high-level code remains high-level and low-level remains low-level.

See paper for (much) more detail and a web-based interpreter for **FunTAL**.