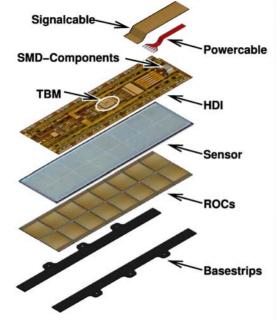
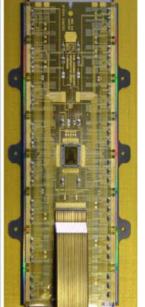


half-module ≜ 8 ROCs





full-module 

16 ROCs