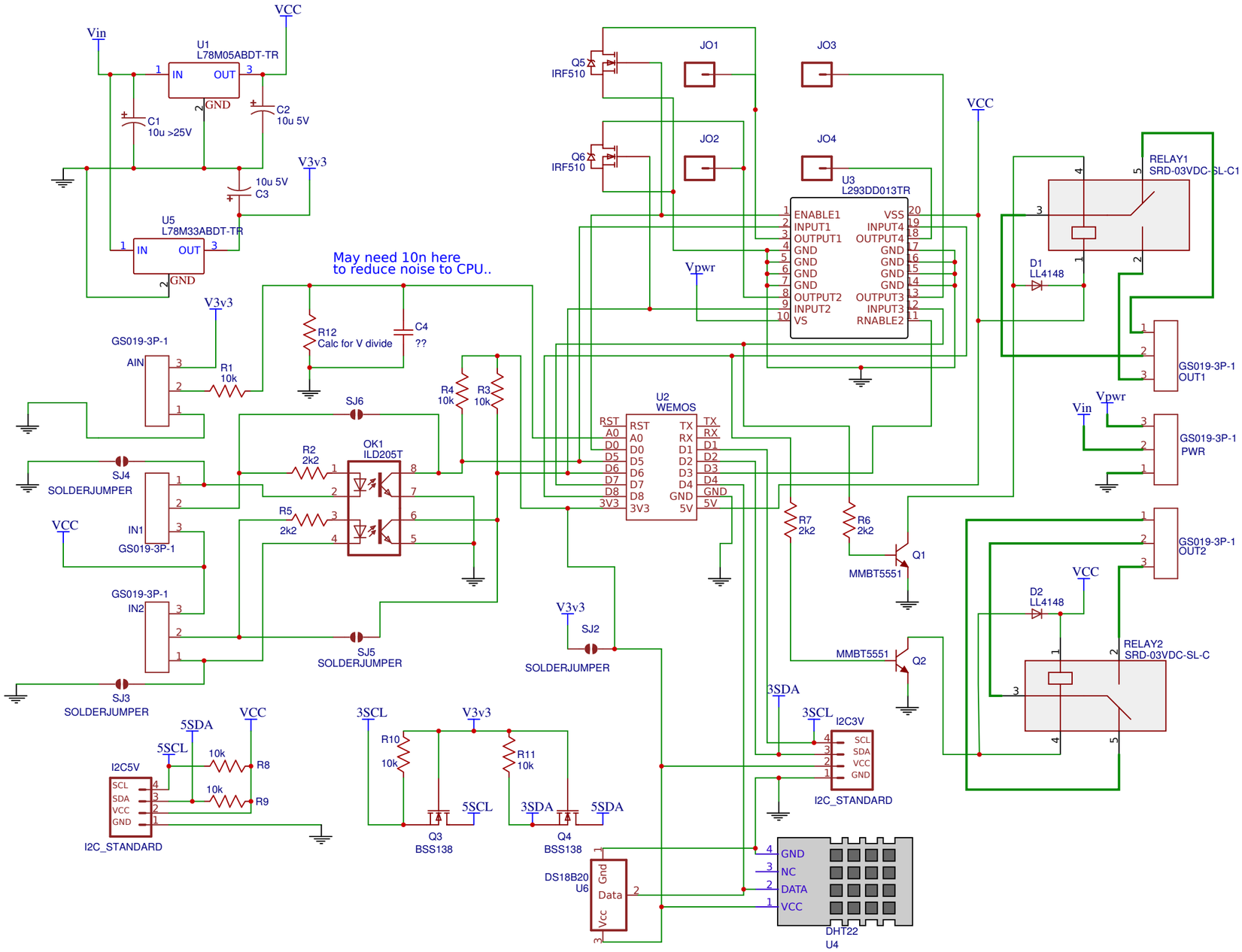

Input and out put pins on U3 can be wire linked
(or Resistors) to allow CPU to be wire to terminals

Q1, Q2, D1 & D2 only required if relays fitted
Relays and U5 not to be fitted at same time

SJ1 and SJ2 select 3v3 source
SJ3 and SJ4 ground input Gnd to system Gnd
SJ5 and SJ5 direct input to CPU

Q3, Q4, R8 & R9 provide optional logic level convertor for 5V I2C

JO1,JO2,JO3,JO4 are simple solder pads, which are placed
near relay contacts on PCB to provide wire link to Terminals

Digital Inputs , cannot be used with 4 output or H Bridge mode

1. 5-24 volt, isolatated input, fit OK1, R2, R3, R4, R5, R2 and R5 may need adjusting above 12V
   apply input to Gnd. and Input pins on terminal
2. contact, (buffered) fit OK1, R2, R3, R4, R5, cannot be used with 4 output of H Bridge mode, link SJ3 and SJ4
   contact close across 5V and input pins on PCB
3. direct CPU input
   do not fit OK1, R2,R3,R4, R5, link SJ3, SJ4, SJ5 and SJ6
   3.3 Volts only between Gnd. and Input on temrinal blocks

Analog input, 3.3V max input to CPU, chose R1 and R12 to divide down higher voltages

Outputs have 5 confguration options

1. 250V 5 AMP rated volt free contacts, fit relays, Q1,Q2,D1,D2, , do not FIT U3
2. 1Amp up to 24V PMW, H Bridge , fit U3, link pad JO1,2,3,4 to near by (on PCB) relay pads, H bride acress OUT1 and OUT2
   Do not fit relay, Q1,Q2,D1,D2,
3. Direct CPU 2 outputs. Do not fit relay, Q1,Q2,D1,D2, U3
   wire link U3 pins 10-11 and 14-15, link pad JO3,4 to near by (on PCB) relay pads
4. Direct CPU 4 outputs. Do not fit relay, Q1,Q2,D1,D2, U3
   wire link U3 pins 2-3, 6-7, 10-11 and 14-15, link pad JO1,2,3,4 to near by (on PCB) relay pads
5. TWO MOSFET open drain. Do not fit relay, Q1,Q2,D1,D2, U3
   FIT Q5 and Q6, open drain available on Output1

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EasyEDA V4.1.3	Drawn By: species5618