

Joo Yong Daniel Chung

jy chung1998@gmail.com • Atlanta, Georgia • 224-392-4454

EDUCATION

Emory University School of Law

May 2028

Candidate for Juris Doctor

GPA: Expected January 2026

- Activities: *IL Representative*, Non-Traditional Law Students Association; *Member*, Asian Pacific American Law Students Association; *Member*, Emory Law Intellectual Property Society

University of Illinois at Urbana-Champaign (UIUC)

May 2021

Bachelor of Science in Computer Engineering - Highest Honors

GPA: 3.82/4.00

Bachelor of Science in Statistics - Highest Distinction

Minor in Computational Science

- Activities: *Resident Advisor*, Upper-Graduate Housing; *President*, Electrical & Computer Engineering Student Government; *President*, Engineering Outreach Society
- Honors and Scholarships: James Scholar Honors, IEEE Eta Kappa Nu (HKN) Honors Society, Jules D. Falzer Memorial Scholarship, Sargent and Lundy Engineering Scholarship, TechnipFMC Scholarship

WORK EXPERIENCE

Marshall Gerstein & Borun

Chicago, IL

Technical Specialist / Patent Agent

February 2024 – Current

- Drafted over 20+ patent applications of different inventions in various domains, including Cryptography, Generative Artificial Intelligence, Virtual Reality, Blockchain, Databases, and Autonomous Vehicles
- Conducted 10+ interviews with patent examiners to discuss rejections raised in Office Actions, assessed the legal and technical arguments, and evaluated different strategies for overcoming those rejections
- Filed responses to 20+ Non-Final and Final Office Actions addressing rejections under 35 U.S.C. 101, 102, 103, and 112 received from the USPTO, resulting in the allowance of 10+ applications
- Performed detailed patentability searches to assess whether inventions were eligible for patent protection by analyzing relevant prior art, including patents and publications disclosing similar technologies

Kilpatrick Townsend & Stockton

San Francisco, CA

Patent Engineer

August 2021 – March 2023

- Participated in over 25+ invention disclosure meetings with clients to identify patentable inventive concepts from their inventions and provide legal advice on drafting and filing patent applications
- Drafted over 25+ patent applications based on the inventive concepts drawn with inventors, covering a range of engineering topics, including claims, background, and detailed explanations of the inventions
- Composed over 20+ instructional letters and recommendations to foreign law associates, providing amendments to claims and arguments for the amendments to overcome foreign office actions

Raytheon Missile System

Tucson, AZ

Software and Electrical Engineering Intern

May 2019 – August 2019

- Registered 10+ new circuit component models in C++ in the circuit software with inputs (e.g., max/min voltage) given in the component datasheets, resulting in a 15% increase in simulation speed
- Developed over 10+ reuse design blocks in C++ that are commonly used in circuit schematic design, leading to a reduction in the cycle time of building new circuits and preventing the duplication of work
- Analyzed the newly redesigned amplifier circuits for Evolved SeaSparrow Missile (ESSM) to identify schematic mistakes in the design and created general block diagrams for technical design reviews

ADDITIONAL INFORMATION

- **Programming Languages:** Python, R, C++, C, SQL, Mongo-DB, JSX, Javascript, HTML, CSS, Java
- Fluent in **Korean**; Volunteer experiences teaching Korean in Korean Learning Schools
- A portfolio of patent applications and software engineering projects I developed is available at <https://dchung1940.github.io>