

Joo Yong Daniel Chung

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EDUCATION

Emory University School of Law <i>Candidate for Juris Doctor</i>	May 2028 GPA: 4.00/4.00
University of Illinois at Urbana-Champaign (UIUC) <i>Bachelor of Science in Computer Engineering - Highest Honors</i> <i>Bachelor of Science in Statistics - Highest Distinction</i> <i>Minor in Computational Science</i>	May 2021 GPA: 3.82/4.00

- 1L Representative of Non-Traditional Law Students Association

- Resident Advisor for Upper-Graduate Housing, President of ECE (Electrical & Computer Engineering) Student Government, President of Engineering Outreach Society
- James Scholar Honors, IEEE Eta Kappa Nu (HKN) Honors Society, Certificate in Data Science, Jules D. Falzer Memorial Scholarship, Sargent and Lundy Engineering Scholarship, TechnipFMC Scholarship

WORK EXPERIENCE

Marshall Gerstein & Borun <i>Technical Specialist / Patent Agent</i>	Chicago, IL February 2024 - Current
<ul style="list-style-type: none">• Drafted over 20+ patent applications of different inventions in various domains including Cryptography, Generative Artificial Intelligence, Virtual Reality, Blockchain, Databases, and Autonomous Vehicles• Conducted 10+ interviews with patent examiners to discuss rejections raised in the Office Actions, assess the legal and technical arguments, and to evaluate different strategies for overcoming those rejections• Filed responses to 20+ Non-Final and Final Office Actions addressing rejections under 35 U.S.C. 101, 102, 103, and 112 received from the USPTO, resulting in the allowance of 10+ applications• Performed detailed patentability searches to assess whether inventions were eligible for patent protection by analyzing relevant prior art, including patents and publications disclosing similar technologies	
Kilpatrick Townsend & Stockton <i>Patent Engineer</i>	San Francisco, CA August 2021 - March 2023

- Participated in over 25+ invention disclosure meetings with clients to identify patentable inventive concepts from their inventions and provide legal advice on drafting and filing patent applications
- Drafted over 25+ patent applications based on the inventive concepts drawn with inventors, covering a range of engineering topics, including claims, background, detailed explanations of the inventions, etc.
- Composed over 20+ instructional letters and recommendations to foreign law associates, providing amendments to claims and arguments for the amendments to overcome foreign office actions

Raytheon Missile System <i>Software and Electrical Engineering Intern</i>	Tucson, AZ May 2019 - August 2019
<ul style="list-style-type: none">• Registered 10+ new circuit component models in C++ in the circuit software with inputs (e.g., max/min voltage) given in the component datasheets, resulting in 15% increase in simulation speed• Developed over 10+ reuse design blocks in C++ that are commonly used in circuit schematic design, leading to reduction in the cycle time of building new circuits and prevent duplication of work• Analyzed the newly redesigned amplifier circuits for Evolved SeaSparrow Missile (ESSM) to identify schematic mistakes in the design and created general block diagrams for technical design reviews	

ADDITIONALS

- **Programming Languages:** Python, R, C++, C, SQL, Mongo-DB, JSX, Javascript, HTML, CSS, Java
- Fluent in **Korean**; Volunteer experiences teaching Korean in Korean Learning Schools
- Born and raised in Chicago; Passionate Chicago Bulls and Bears fan;
- Developed a Variety of Software Engineering Projects, which can be found in my [website](#)