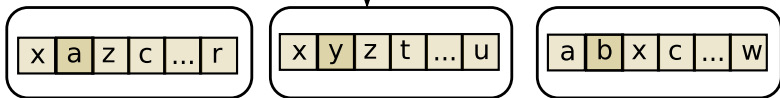
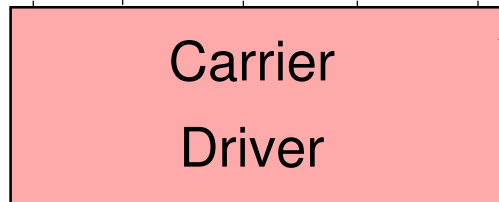
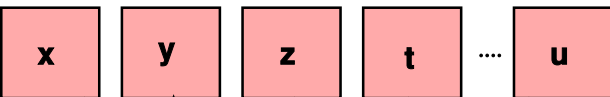


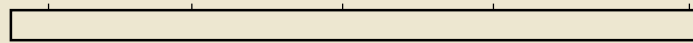
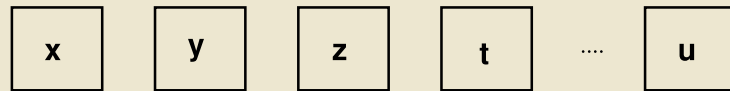
## Core Drivers



Application bitstreams

## Gateway

### IP Cores



internal bus (Wishbone)

## Board

MZ  
ID

FPGA