

REPRESENTING DATA

Machine language

Computers understand only one language: *machine language*.

Machine language consists of sets of instructions made of ones and zeros.

Binary Code

Example of a single instruction:

00000	10011110
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Everything in the computer is stored as a binary number that codifies specific information.

For example, the values of the data processed by programs are stored as binary numbers.

Unsigned Integer Numbers

An **unsigned integer number** is a whole number $\in \mathbb{N}$ (not a fractional number) that does not have a sign (i.e., it can be positive or zero).

Example: representing the integer number 45 in 1 byte (8 bits):

0	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

Unsigned Integer Numbers – MIN and MAX

The minimum and maximum values you can represent depend on the number of bits you have at your disposal.

With 8 bits:

- the **minimum** representable unsigned integer number is **0**

0	0	0	0	0	0	0	0
---	---	---	---	---	---	---	---

- the **maximum** representable unsigned integer number is **$2^8-1 = 255$**

1	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---

Signed Integer Numbers

A **signed integer number** is a whole number $\in \mathbb{N}$ (not a fractional number) that has a sign (i.e., it can be positive, negative, or zero).

Representing **positive** signed integer numbers is the same as representing unsigned integer numbers.

Example: representing the signed integer number +45 in 1 byte (8 bits):

0	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

There are two ways to represent a **negative** signed integer number:

1. Signed magnitude
2. 2's complement

Signed Integer Numbers – Signed Magnitude

Reserve the first bit as sign:

- **0** stands for +
- **1** stands for -

Example: representing the signed integer number +45 in 1 byte (8 bits):

0	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

Example: representing the signed integer number -45 in 1 byte (8 bits):

1	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

- invert all bits (i.e., bitwise not), then
- add a place value of **1** to the entire inverted number.

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Example: representing the signed integer number -45 in 1 byte (8 bits):

INVERT:

0	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

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Example: representing the signed integer number -45 in 1 byte (8 bits):

INVERT:	0	0	1	0	1	1	0	1	
	<hr/>								
ADD 1:	1	1	0	1	0	0	1	0	+
								1	=
	<hr/>								

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								1	=
	<hr/>								
								1	

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<hr/>									
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<hr/>									
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<hr/>									
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								1	=
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<hr/>									
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								1	=
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2's complement:	1	1	0	1	0	0	1	1	

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INVERT:

0	0	1	0	1	1	0	1
---	---	---	---	---	---	---	---

ADD 1:

1	1	0	1	0	0	1	0
---	---	---	---	---	---	---	---

+


1

=

2's complement:

1	1	0	1	0	0	1	1
---	---	---	---	---	---	---	---

The most significant bit must be equal to 1



Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

- invert all bits (i.e., bitwise not), then
- add a place value of **1** to the entire inverted number.

Example: representing the signed integer number -6 in 1 byte (8 bits):

0	0	0	0	0	1	1	0
---	---	---	---	---	---	---	---

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

- invert all bits (i.e., bitwise not), then
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Example: representing the signed integer number -6 in 1 byte (8 bits):

INVERT:	0	0	0	0	0	1	1	0
	1	1	1	1	1	0	0	1

Signed Integer Numbers – 2's complement

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Example: representing the signed integer number -6 in 1 byte (8 bits):

INVERT:	0	0	0	0	0	1	1	0	
<hr/>									
ADD 1:	1	1	1	1	1	0	0	1	+
								1	=
<hr/>									

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

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Example: representing the signed integer number -6 in 1 byte (8 bits):

INVERT:	<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr></table>	0	0	0	0	0	1	1	0	
0	0	0	0	0	1	1	0			
<hr/>										
ADD 1:	<table><tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr></table>	1	1	1	1	1	0	0	1	+
1	1	1	1	1	0	0	1			
					carry →	<table><tr><td>1</td></tr></table>	1	=		
1										
<hr/>										
	<table><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0</td></tr></table>								0	
							0			

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Example: representing the signed integer number -6 in 1 byte (8 bits):

INVERT:	<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr></table>	0	0	0	0	0	1	1	0	
0	0	0	0	0	1	1	0			
<hr/>										
ADD 1:	<table><tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr></table>	1	1	1	1	1	0	0	1	+
1	1	1	1	1	0	0	1			
<hr/>										
	<table><tr><td></td><td></td><td></td><td></td><td></td><td></td><td>1</td><td>0</td></tr></table>							1	0	=
						1	0			

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INVERT:	<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td>1</td><td>0</td></tr></table>	0	0	0	0	0	1	1	0	
0	0	0	0	0	1	1	0			
<hr/>										
ADD 1:	<table><tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr></table>	1	1	1	1	1	0	0	1	+
1	1	1	1	1	0	0	1			
<hr/>										
=										
<hr/>										
2's complement:	<table><tr><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>0</td><td>1</td><td>0</td></tr></table>	1	1	1	1	1	0	1	0	
1	1	1	1	1	0	1	0			

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

- invert all bits (i.e., bitwise not), then
- add a place value of **1** to the entire inverted number.

Example: representing the signed integer number -1 in 1 byte (8 bits):

0	0	0	0	0	0	0	1
---	---	---	---	---	---	---	---

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

- invert all bits (i.e., bitwise not), then
- add a place value of **1** to the entire inverted number.

Example: representing the signed integer number -1 in 1 byte (8 bits):

INVERT:	0	0	0	0	0	0	1
	1	1	1	1	1	1	0

Signed Integer Numbers – 2's complement

The 2's complement of a number is calculated as follows:

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- add a place value of **1** to the entire inverted number.

Example: representing the signed integer number -1 in 1 byte (8 bits):

INVERT:	0	0	0	0	0	0	1	
<hr/>								
ADD 1:	1	1	1	1	1	1	0	+
							1	=
<hr/>								

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The 2's complement of a number is calculated as follows:

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Example: representing the signed integer number -1 in 1 byte (8 bits):

INVERT:	0	0	0	0	0	0	1	
<hr/>								
ADD 1:	1	1	1	1	1	1	0	+
							1	=
<hr/>								
2's complement:	1	1	1	1	1	1	1	

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- invert all bits (i.e., bitwise not), then
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Example: representing the signed integer number -1 in 1 b

INVERT:	0	0	0	0	0	0	0
ADD 1:	1	1	1	1	1	1	0
							1
2's complement:	1	1	1	1	1	1	1

*2'S COMPLEMENT
IS USED IN C AND
C++ TO REPRESENT
SIGNED INTEGER
NUMBERS*

Signed Integer Numbers – MIN and MAX

The minimum and maximum values you can represent depend on the number of bits you have at your disposal.

With 8 bits and 2's complement representation:

- the **minimum** representable unsigned integer number is:

1	0	0	0	0	0	0	1
---	---	---	---	---	---	---	---

- the **maximum** representable unsigned integer number is:

0	1	1	1	1	1	1	1
---	---	---	---	---	---	---	---

Which numbers are these?
Let's make the math

Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ MIN = ???

➤ MAX = ???

Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ MIN = ???

➤ MAX = ???

domain of a signed integer number with 8 bits

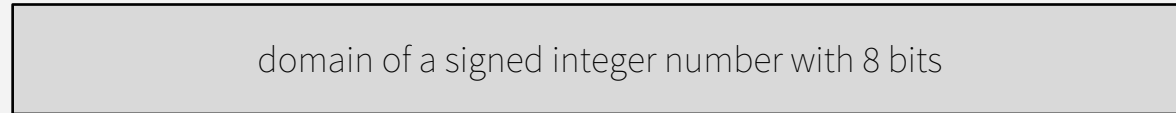
Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ MIN = ???

➤ MAX = ???



2^8 possible values

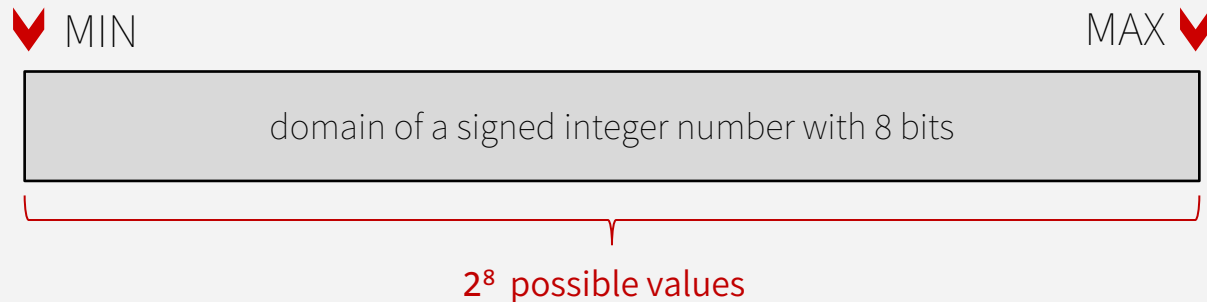
Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ MIN = ???

➤ MAX = ???



Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ $\text{MIN} = -2^8/2$

➤ $\text{MAX} = +2^8/2$



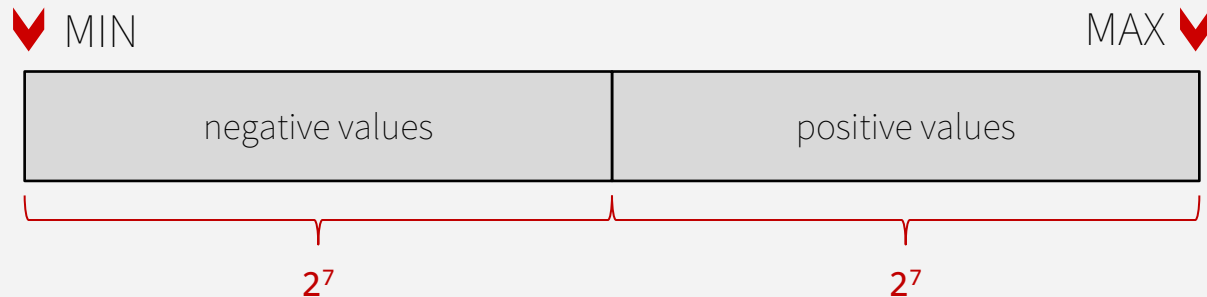
Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ $\text{MIN} = -2^7$

➤ $\text{MAX} = +2^7$



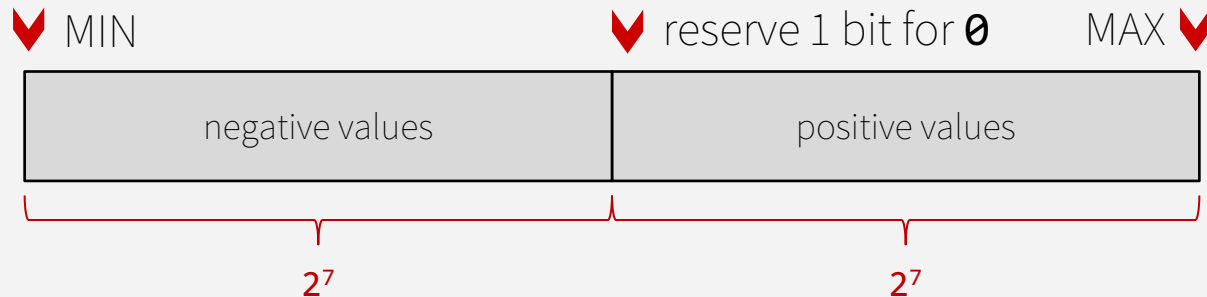
Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ $\text{MIN} = -2^7$

➤ $\text{MAX} = +2^7 - 1$



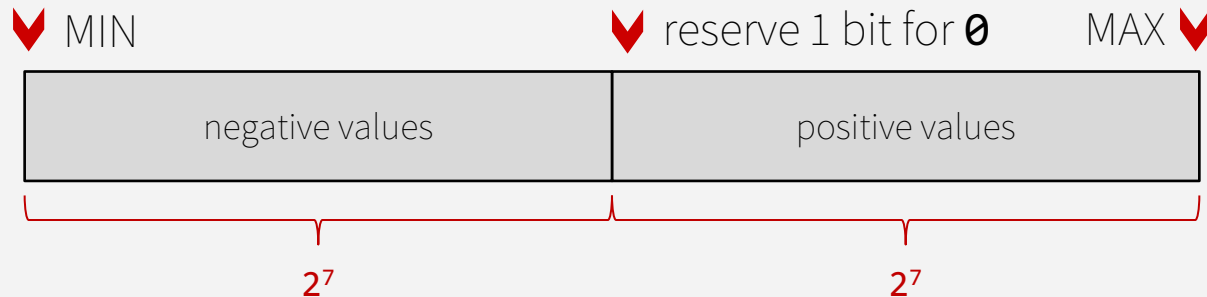
Signed Integer Numbers – MIN and MAX

Calculations:

With 8 bits:

➤ $\text{MIN} = -2^7 = -128$

➤ $\text{MAX} = +2^7 - 1 = 128 - 1 = 127$



Signed Integer Numbers – MIN and MAX

The minimum and maximum values you can represent depend on the number of bits you have at your disposal.

With 8 bits and 2's complement representation:

- the **minimum** representable unsigned integer number is:

$$\begin{array}{|c|c|c|c|c|c|c|c|} \hline 1 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ \hline \end{array} \longrightarrow (-128)_{10}$$

- the **maximum** representable unsigned integer number is:

$$\begin{array}{|c|c|c|c|c|c|c|c|} \hline 0 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\ \hline \end{array} \longrightarrow (+127)_{10}$$

Floating-Point Numbers

A **floating-point number** is a fractional/real number $\in \mathbb{R}$. It has a sign (i.e., it can be positive, negative, or zero).

They have their own protocol for representation (i.e., a set of specific rules that allow for codification and de-codification of the information) .

Floating-Point Numbers

A **floating-point number** is a fractional/real number $\in \mathbb{R}$. It has a sign (i.e., it can be positive, negative, or zero).

For example, the *IEEE 754 double-precision binary floating-point format*, which represents a real number with 64 bits:

-
-
-

Example:



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For example, the *IEEE 754 double-precision binary floating-point format*, which represents a real number with 64 bits:

- **1 bit** for sign $\rightarrow 2^1 = 2$ possibilities for the sign
-
-



Example:

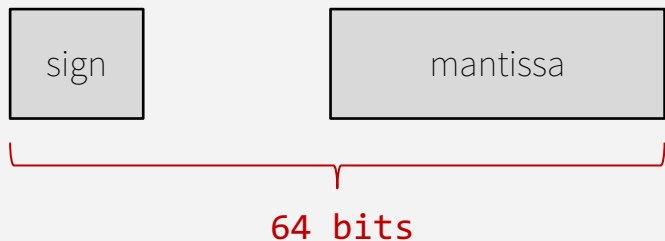


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For example, the *IEEE 754 double-precision binary floating-point format*, which represents a real number with 64 bits:

- **1 bit** for sign $\rightarrow 2^1 = 2$ possibilities for the sign
-
- **52 bits** for mantissa $\rightarrow 2^{52} = 4.5036\text{E}+15$ possibilities for mantissa



Example:

- 0.3213242

↑
Sign

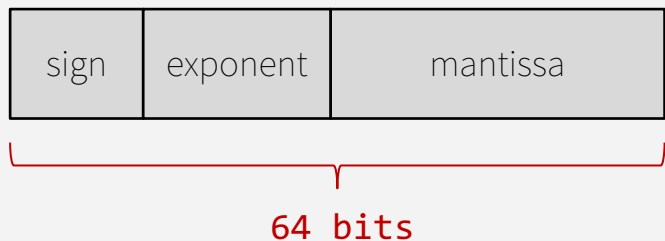
0.3213242
Mantissa

Floating-Point Numbers

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For example, the *IEEE 754 double-precision binary floating-point format*, which represents a real number with 64 bits:

- **1 bit** for sign $\rightarrow 2^1 = 2$ possibilities for the sign
- **11 bits** for exponent $\rightarrow 2^{11} = 2048$ possibilities for exponent
- **52 bits** for mantissa $\rightarrow 2^{52} = 4.5036\text{E}+15$ possibilities for mantissa



Example:

$$\begin{array}{c} \text{Sign} \uparrow - \\ 0.3213242 \cdot 10^{\text{Exponent} \uparrow 345} \end{array}$$

The diagram shows the decimal representation of a floating-point number. A red arrow points from the label 'Sign' to the minus sign. Another red arrow points from the label 'Exponent' to the '345' in the power of 10. A red bracket is placed under the decimal part '0.3213242' with the label 'Mantissa' below it.

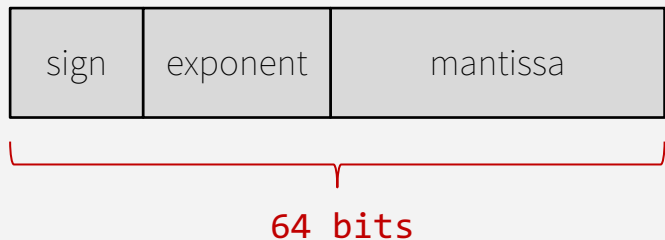
Floating-Point Numbers

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- **1 bit** for sign $\rightarrow 2^1 = 2$ possibilities for the sign
- **11 bits** for exponent $\rightarrow 2^{11} = 2048$ possibilities for exponent
- **52 bits** for mantissa $\rightarrow 2^{52} = 4.5036\text{E}+15$ possibilities for mantissa

If you make the math, this is $2^1 \cdot 2^{52} \cdot (2^{11} - 2) = 1.84287\text{E}+19$ discrete values!



Example:

- 0.3213242 · 10³⁴⁵

Sign Mantissa Exponent

Floating-Point Numbers – MIN and MAX

The minimum and maximum values you can represent depend on the number of bits you have at your disposal.

EXERCISE FOR HOME:

Calculate the minimum and maximum representable numbers with the *IEEE 754 double-precision binary floating-point* format.