# DAVID TOLOSA ALARCÓN

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in david-tolosa-alarcon  ○ dd8bb  🚗 B Driving License

## FORMATION

### Master in Telecommunication Engineering, "MUIT"

**School of Telecommunications Engineering – Universitat Politècnica de València**

📅 2019 – 2021

– Honorable Mention in Thesis: "Music Segmentation Based On Deep Learning"

### Bachelor's Degree in Telecommunication Technologies and Services Engineering

**School of Telecommunications Engineering – Universitat Politècnica de València**

📅 2011 – 2019

## EXPERIENCE & PROJECTS

### Database developer/analyst

**Ford Almussafes**

📅 Nov 2018 – June 2019          📍 Almussafes, Valencia, Spain

– Intership made in Supply Chain Management Department
– Full-Stack Development of an app for managing databases at Ford Almussafes' warehouses
– Storage analysis for budget reduction in supply chain
– Action proporsals and tracking for cost reductions
– Create in-app reports for warehouses supply prediction and logs
• Techs : SQL, C++, Microsoft Office, VBA, MS-DOS
• Competencies : Data analysis, Big Data, Programming, Prediction

### Software Technician

**F1 Connecting**

📅 Nov 2016 – Oct 2017          📍 Valencia, Spain

– Intership made in Hotline Department
– Monitoring and management of IT incidents
– Database management
– Creation of bash scripts to automate/improve tasks on servers
• Techs : Linux, Shell, SQL, C++
• Competencies : IT Services,Database Management,Scripting

### Master's Thesis: Music Segmentation based on Deep Learning

**Universitat Politècnica de Valencia**

📅 Feb 2021 – Sep 2021          📍 Valencia,Spain

– Design and Training of CNNs for detecting music structures on EDM songs, implementation of audio features extraction algorithms and model's evaluation
• Techs : Python, Numpy, Tensorflow, Keras, CUDA.
• Competencies : Machine Learning, Data Science, Music Information Retrieval

### Development of a embedded Chroma Key on a FPGA Altera using NIOS II processor

**Universitat Politècnica de Valencia**

📅 Nov 2019 – Feb 2020          📍 Valencia, Spain

– Architecture design along with the implementation of the funcionallities
• Techs : Verilog,C++,Quartus II
• Competencies : Embedded Systems, Programming

## ABOUT ME

- As part of my career perspective, I am looking for an internship or junior position. I have a great passion for software development and very interested in leading my work-career through that.

## COMPETENCIES

Software Development

Digital Signal Processing

Learning capacity | Data Analysis

Machine Learning

## INFORMATIC SKILLS

| Skill | Level |
|---|---|
| SQL | ●●●●●○ |
| C++ | ●●●●○○ |
| Linux | ●●●●○○ |
| Shell | ●●●●○○ |
| Python | ●●●●●○ |
| Matlab | ●●●●○○ |
| Git | ●●●●○○ |
| HTML/CSS/Js | ●●●○○○ |
| Tensorflow/Keras | ●●●●○○ |
| Numpy | ●●●●○○ |

## LANGUAGES

| Language | Level |
|---|---|
| Spanish | ●●●●● |
| English | ●●●●○ |

## CERTIFICATES

### CAE: First Certificate in English

**University of Cambridge**

📅 June 2011

### Complete Python Course

**Udemy, Dr. Angela Yu**

📅 Oct 2020 - Sep 2021

### Deep Learning Specialization Program

**Coursera, Andrew Ng**

📅 Feb 2021 - Apr 2021

## HOBBIES

Musician

Sciences & Technologies

Nature & Animals | Videogames