Chunghwa Picture Tubes, Ltd.
Technical Specification

Date : 2009/08/17

TFT LCD
CLAA173UA01A

ACCEPTED BY :

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Modification Record List

NO.	Issue Date	Modification Index
1	2009/06/25	Tentative version

Table of Content

1. OVERVIEW

CLAA173UA01A is 17.3" color (16：9) TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, LVDS driver ICs, control circuit and backlight. By applying 6 bit digital data, 1600×RGB (3) ×900, 262K-color images are displayed on the 17.3" diagonal screen. General specifications are summarized in the following table：

ITEM	SPECIFICATION
Display Area (mm)	382.08(H)×214.92(V) (17.3-inch diagonal)
Number of Pixels	1600×3(H)×900(V)
Pixel Pitch (mm)	0.2388(H)×0.2388(V)
Color Pixel Arrangement	RGB vertical stripe
Display Mode	Normally white
Number of Colors	262,144(6bits) (LVDS)
Gamut	56%(min)/60% (Typ)
Optimum Viewing Angle	6 o'clock
Response Time (ms)	6ms (Typ)
Surface Treatment	Glare 3H
Viewing Angle	45°、45° /15°、35° (Typ)
Brightness (cd/m^2)	220 cd/m^2 (5point)/6 mA (Typ.) 200 cd/m^2 (5point)/6 mA (Min.)
Uniformity	5point：80% 13point：65%
Consumption of Power (W)	6.74W (typ)
Module Size (mm)	398.6(W)×233.3(H)×5.8(D) (Max)
Module Weight (g)	550 (Max)

The LCD Products listed on this document are not suitable for use of aerospace equipment, submarine cable, and nuclear reactor control system and life support systems. If customers intend to use these LCD products for applications listed above or those not included in the "Standard" list as follows, please contact our sales in advance.

Standard：Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tool, Industrial robot, Audio and Visual equipment, Other consumer products.

2. ABSOLUTE MAXIMUM RATINGS

The following are maximum value, which if exceeded, may cause faulty operation or damage to the unit.

ITEM	SYMBOL	MIN	MAX	UNIT	NOTE
LCD Power Voltage	VCC	0	4.0	V	
LED Driver Input Voltage	VBL+	0	25	V	
Operation Temperature	Top	0	50	℃	*1).2).3).4)
Storage Temperature	Tstg	-25	65	℃	*1).2).3)

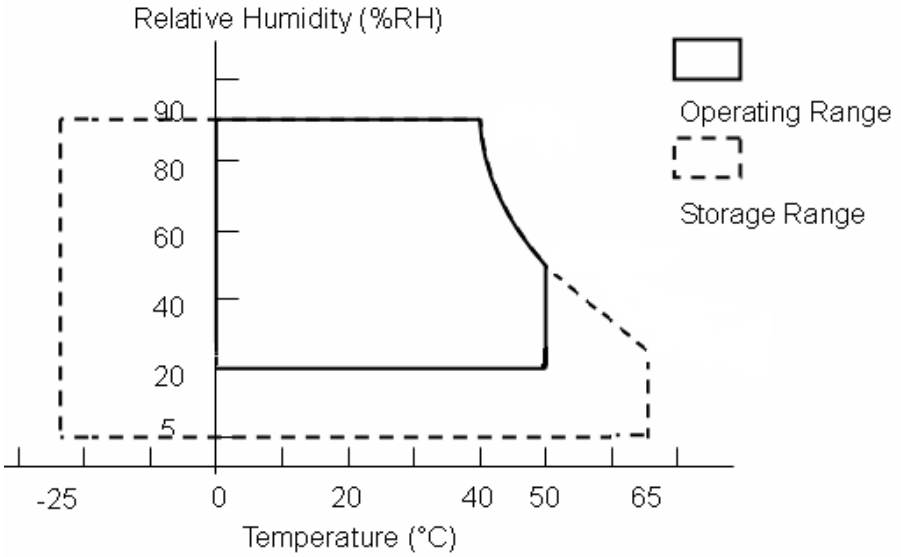
【Note】

*1) The relative temperature and humidity range are as below sketch, 90%RH Max. (Ta≦40℃)

*2) The maximum wet bulb temperature ≦39℃(Ta＞40℃) and without dewing.

*3) If product in environment which over the definition of the relative temperature and humidity out of range too long, it will affect visual of LCD.

*4) If you operate LCD in normal temperature range, the center surface of panel should be under 50℃.



3. ELECTRICAL CHARACTERISTICS

(A) TFT LCD

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LCD Power Voltage		VCC	3.0	3.3	3.6	V	*1)
LCD Power Current		ICC	-	400	455	mA	*2)
Rush Current		Irush	-	-	3	A	*4)
Logic Input Voltage (LVDS: IN+,IN-)	Common Voltage	VCM	1.125	1.25	1.375	V	*3)
	Differential Input Voltage	| VID |	250	350	450	mV	*3)
	Threshold Voltage (HIGH)	VTH	-	-	100	mV	*3) When VCM = +1.2V
	Threshold Voltage (LOW)	VTL	-100	-	-	mV	

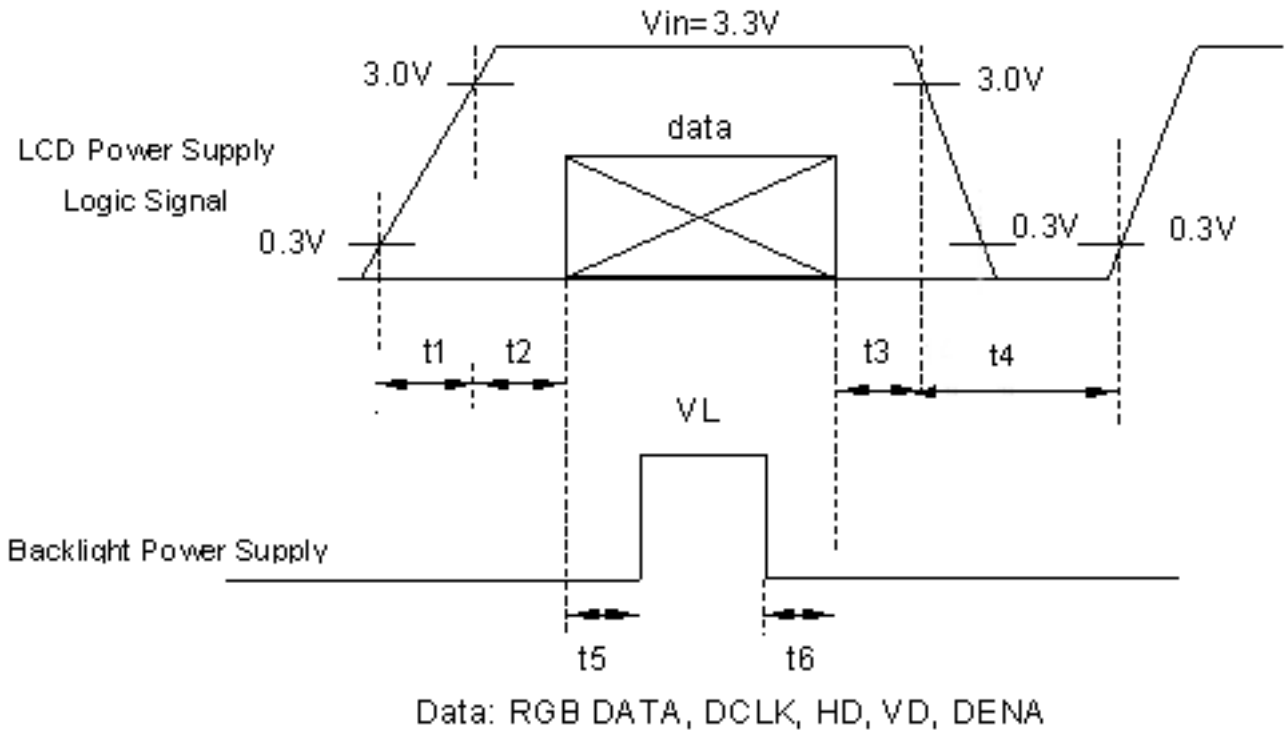
【Note】

*1) Power Sequence：

0.50 ms≦t1 ≦10 ms　　500 ms≦t4

0.01 ms＜t2 ≦50 ms　　200 ms≦t5

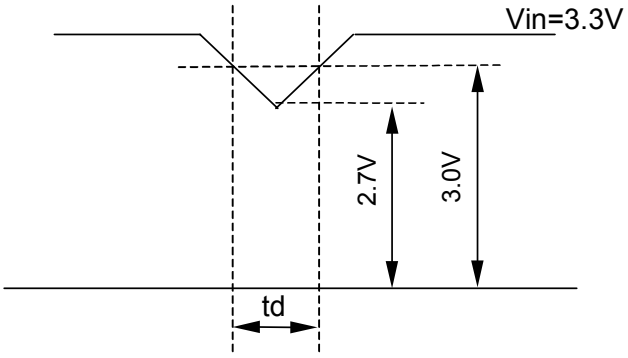
0.01 ms＜t3 ≦50 ms　　200 ms≦t6



VCC-dip state

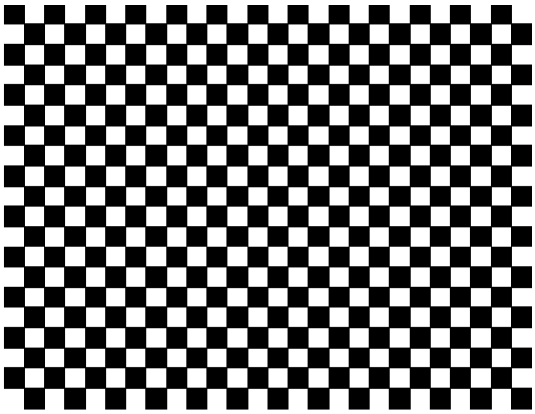
(1)when 3.0V＞VCC≧2.7V，td≦10 ms.

(2)when VCC＜2.7V，VCC-dip condition should as the VCC-turn-off condition.



*2) Typical value is Mosaic (32*36 Checker board) Pattern：900 line mode.

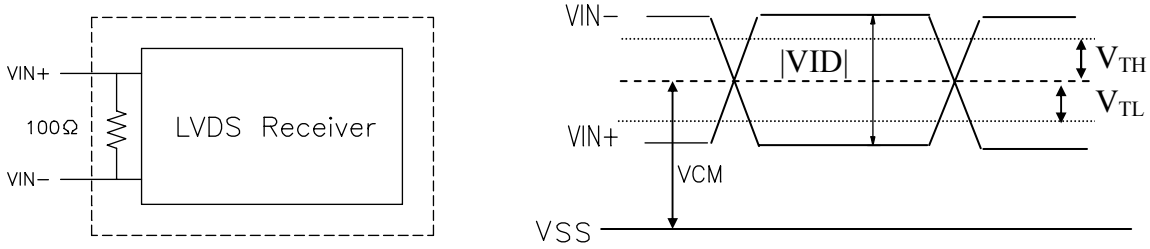
Circuit condition (Typ)：VCC=3.3 V，f_V=60 Hz f_H=55.56 kHz，f_{CLK}=48.89 MHz (one of LVDS dual port).



Max value is Black Pattern：900 line mode.

Circuit condition (Max)：VCC=3.3 V，f_V=60 Hz f_H=55.56 kHz，f_{CLK}=48.89 MHz (one of LVDS dual port).

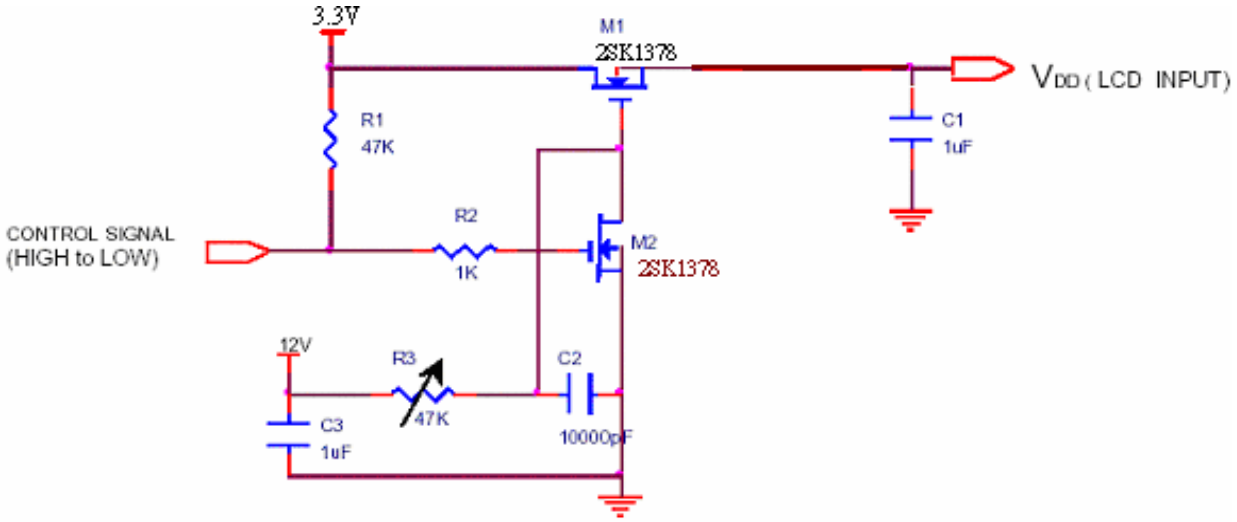
*3) LVDS Signal Definite：

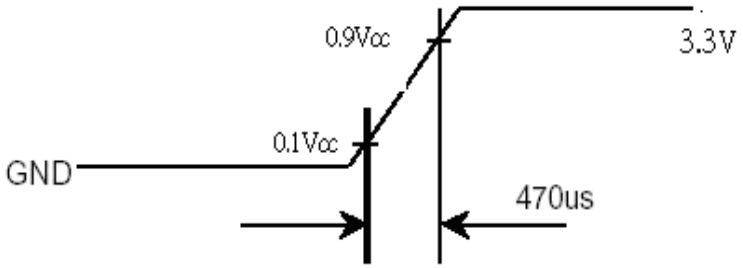


VIN+：Positive differential DATA & CLK Input

VIN-：Negative differential DATA & CLK Input

*4) Irush measure condition





(B) BACK LIGHT

(a.) ELECTRICAL CHARACTERISTICS

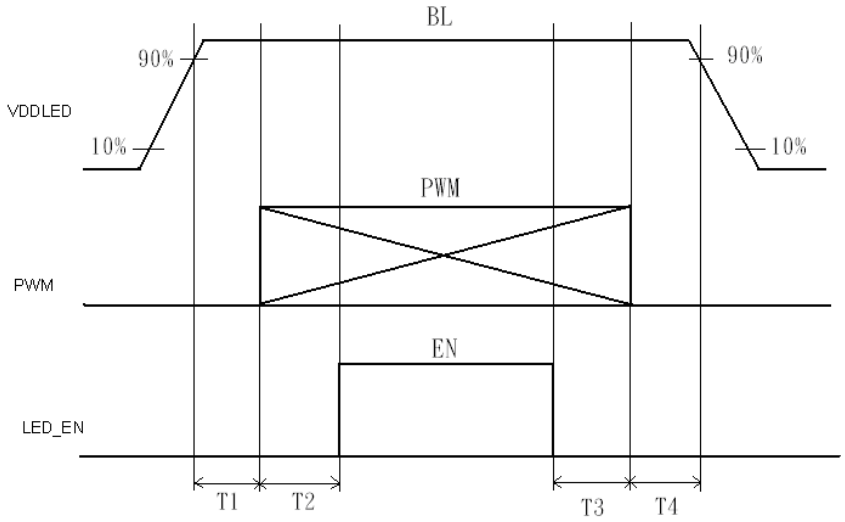
Ta=25℃

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LED Driver Input Voltage	VBL+	7.5	-	21	V	
LED Driver Input Current	IBL+	-	-	950	mA	*1)
Forward Voltage	V_F	2.9	3.2	3.5	V	*2) I_F=20mA
Forward Current	I_F	19.5	20	20.5	mA	*2) I_F=20mA
Power Consumption	PLED	-	5.42	5.93	W	*2)*3) I_F=20mA
PWM Frequency	PWM_BL	180	-	1K	Hz	
Duty ratio	Dim	10	-	100	%	
PWM High Level Voltage	PWM_H	2.5	-	5	V	
PWM Low Level Voltage	PWM_L	0	-	0.5	V	
LED_EN High Level Voltage	LED_EN _H	2.5	-	5	V	
LED_EN Low Level Voltage	LED_EN _L	0	-	0.5	V	

(b.) LED LIFE – TIME

ITEM	CONDITION	MIN	TYP	MAX	UNIT	NOTE
Life Time	I_F=20mA、Ta=25℃	15000	-	-	hrs	*4)

(c.) LED ON/OFF Sequence：



10ms ≦ T1　　0ms ≦ T3

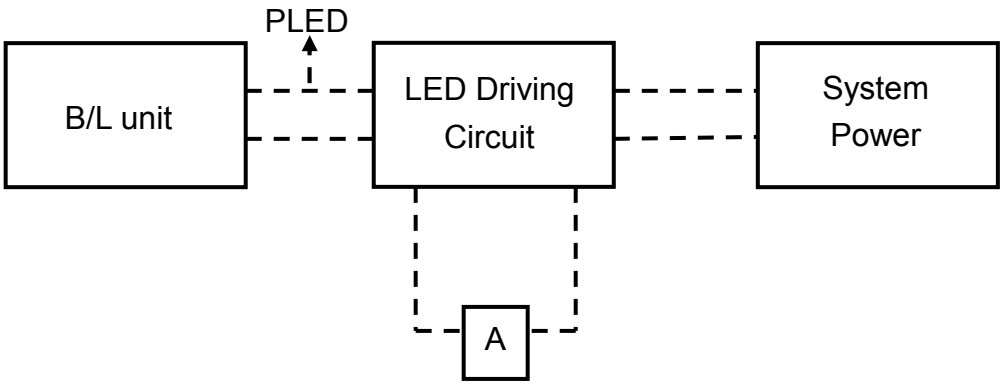
10ms ≦ T2　　10ms ≦ T4

Note：The duty of LED dimming signal should be more than 20% in T2 and T3

*1) Maximum LED Driver Input Current at 7.5V Input Voltage/PWM Duty 100%.

*2) Measure method：a. LED current is measured by utilizing a current meter as show below.

b. System power PLED is measured at input voltage 12V.



*3) Calculator value for reference $I_F \times V_F \times N = PLED$

*4) Life time means that estimated time to 50% degradation of initial luminous intensity.

4. Connector Interface PIN & Function

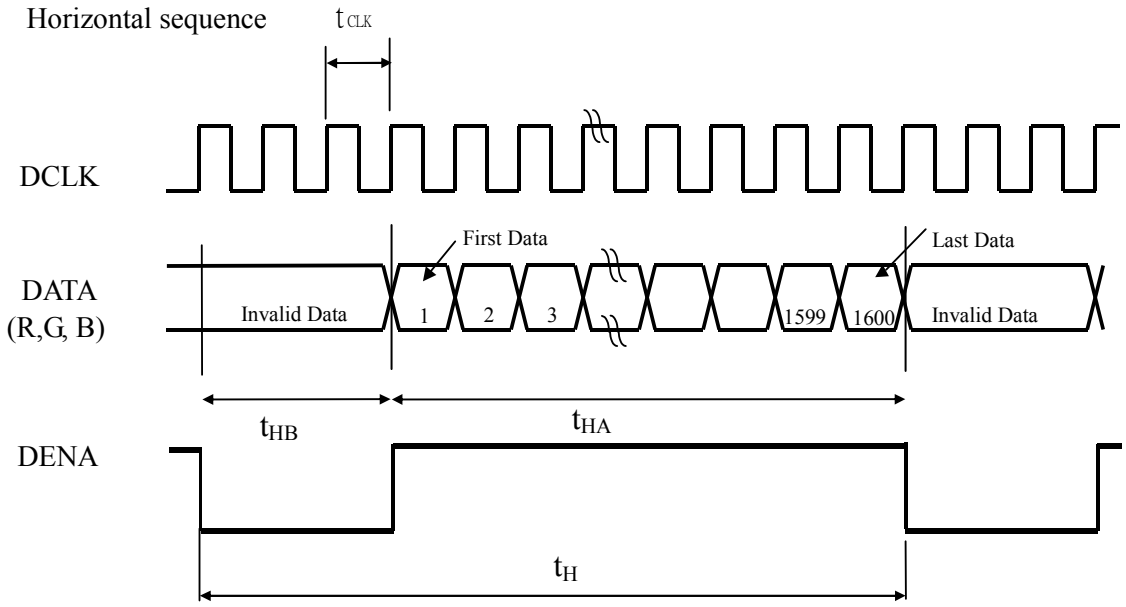
CN (Interface signal)

Outlet connector: 20455-040E-12 (I-PEX)

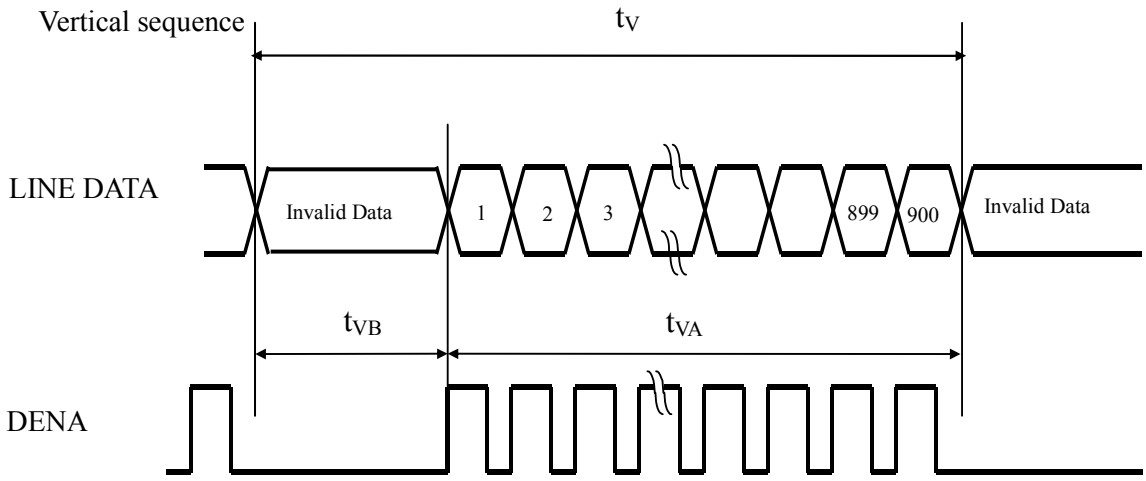
Pin No.	SYMBOL	FUNCTION
1	DIAG_LOOP	Diagnosis pin for testing. Pin 1 & 34 must be connected together on the PCB board
2	VDD	Power Supply +3.3V (typical)
3	VDD	Power Supply, 3.3 V (typical)
4	VEDID	EDID +3.3V Power
5	NC	No connect.(LCD use only)
6	CLKEDID	EDID Clock Input
7	DATAEDID	EDID Data Input
8	RXO0-	minus signal of odd channel 0(LVDS)
9	RXO0+	plus signal of odd channel 0(LVDS)
10	VSS	Ground
11	RXO1-	minus signal of odd channel 1(LVDS)
12	RXO1+	plus signal of odd channel 1(LVDS)
13	VSS	Ground
14	RXO2-	minus signal of odd channel 2(LVDS)
15	RXO2+	plus signal of odd channel 2(LVDS)
16	VSS	Ground
17	RXOC-	minus signal of odd clock channel (LVDS)
18	RXOC+	plus signal of odd clock channel (LVDS)
19	VSS	Ground
20	RXE0-	minus signal of even channel 0(LVDS)
21	RXE0+	plus signal of even channel 0(LVDS)
22	VSS	Ground
23	RXE1-	minus signal of even channel 1(LVDS)
24	RXE1+	plus signal of even channel 1(LVDS)
25	VSS	Ground
26	RXE2-	minus signal of even channel 2(LVDS)
27	RXE2+	plus signal of even channel 2(LVDS)
28	VSS	Ground
29	RXEC-	minus signal of even clock channel (LVDS)
30	RXEC+	plus signal of even clock channel (LVDS)
31	VLED_GND	LED Ground
32	VLED_GND	LED Ground
33	VLED_GND	LED Ground
34	DIAG_LOOP	Diagnosis pin for testing. Pin 1 & 34 must be connected together on the PCB board
35	BLIM	PWM for luminance control
36	BL_on	Backlight On/Off Control
37	NC	Reserved
38	VLED	LED Power Supply 7.5V-21V
39	VLED	LED Power Supply 7.5V-21V
40	VLED	LED Power Supply 7.5V-21V

5. INTERFACE TIMING CHART

(1)(a) LVDS input time sequence



(b) LCD input time sequence



(2) Timing Chart

ITEM				SYNBOL	MIN	TYP	MAX	UNIT
LCD Timing	Frame Rate			-	--	60	--	Hz
	DCLK		Frequency	f_{CLK}	46.67	48.89	50.60	MHz
			Period	t_{CLK}	19.76	20.45	21.43	ns
	DENA	Horizontal	Horizontal Total time	t_H	850	880	900	t_{CLK}
			Horizontal Active time	t_{HA}	800	800	800	t_{CLK}
			Horizontal Blank time	t_{HB}	50	80	100	t_{CLK}
		Vertical	Vertical Total time	t_V	915	926	937	t_H
			Vertical Active time	t_{VA}	900	900	900	t_H
			Vertical Blank time	t_{VB}	15	26	37	t_H
LVDS Spread Spectrum Range *3)					-2		2	%

【Note】

*1) DENA (DATA ENABLE) usually is positive.

*2) During the whole blank period, DCLK should keep input.

*3) At 100kHz modulation rate (LVDS=90MHz at one of LVDS dual port).

(3) DATA mapping

Color	Input Data	R DATA						G DATA						B DATA					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Green(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

【Note】

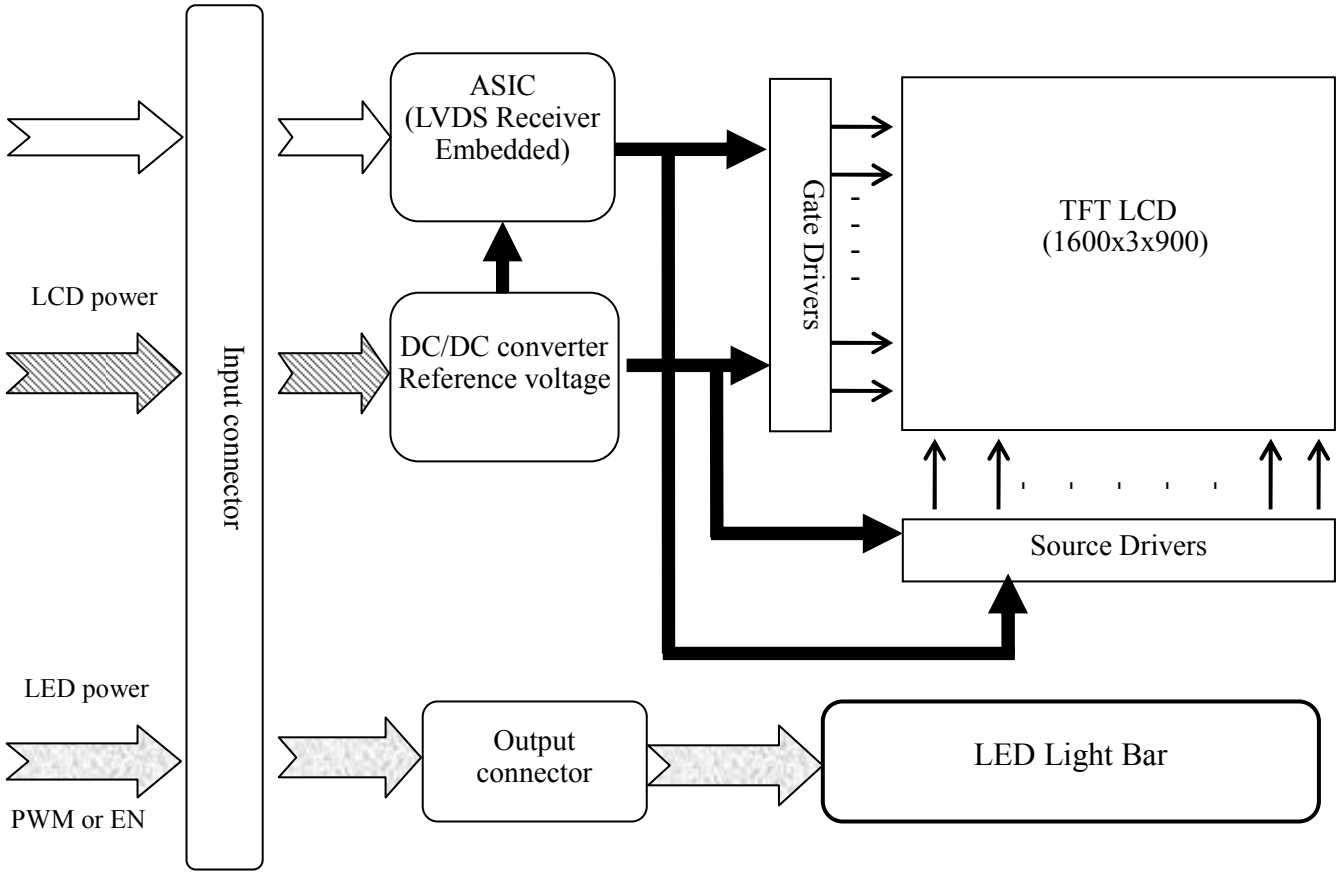
1) Gray level:

Color(n)：n is level order; higher n means brighter level.

2) DATA:

1: high，0: low

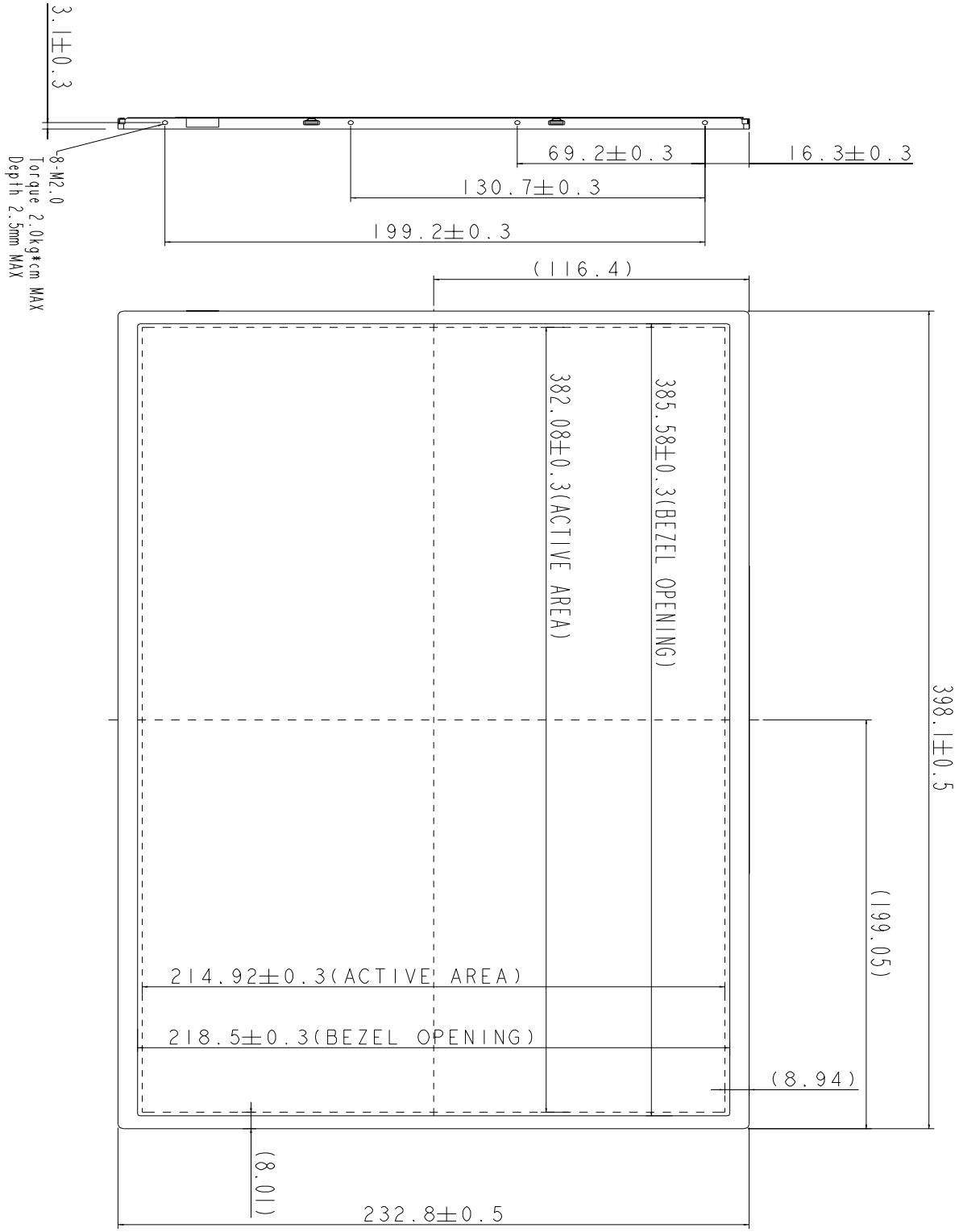
6. BLOCK DIAGRAM



7. MECHANICAL SPECIFICATION

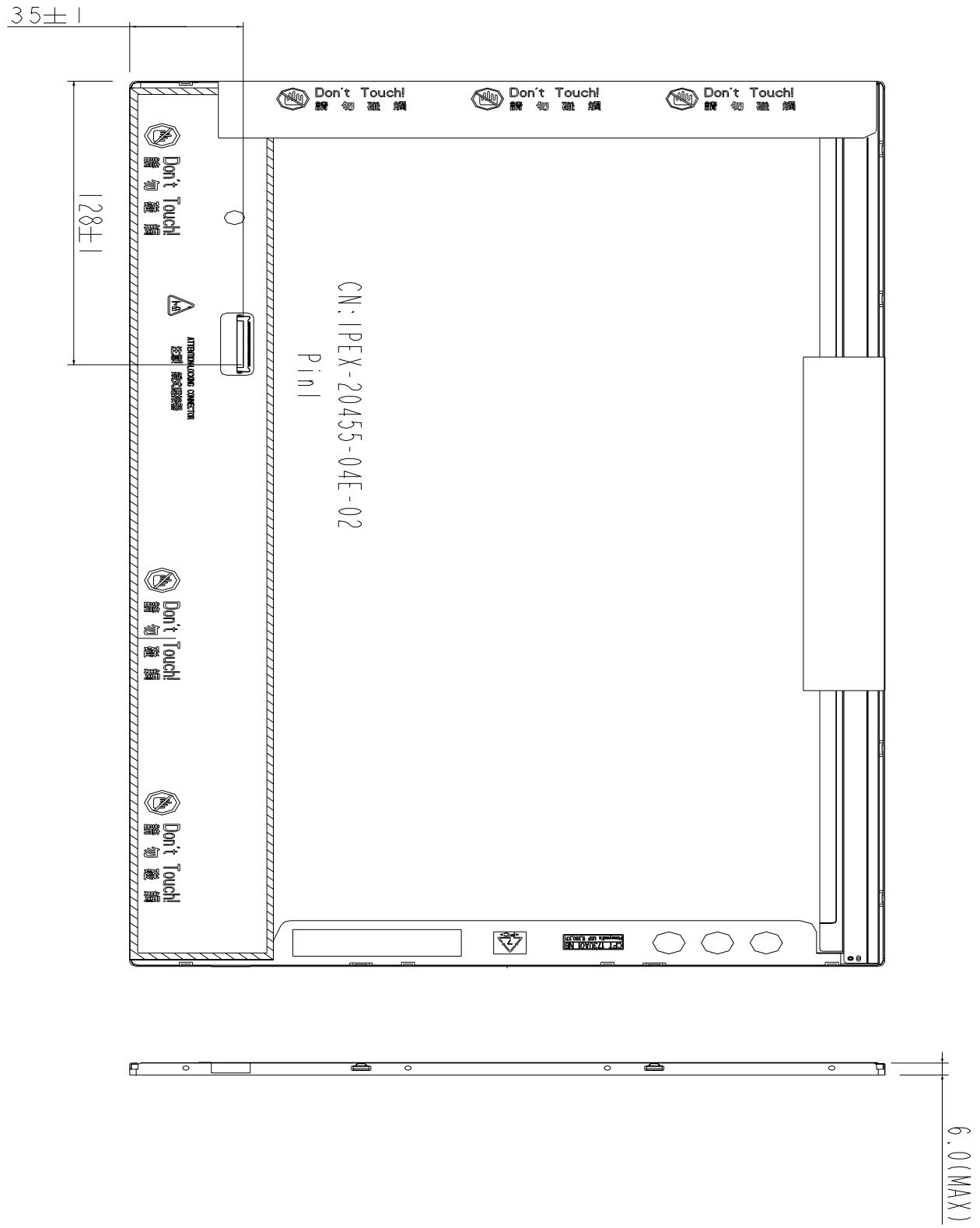
(1) Front side

The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**



(2) Rear side

The tolerance, not show in the figure, is ±0.5 mm. **[Unit：mm]**



8. OPTICAL CHARACTERISTICS

Ta=25℃，VDD=3.3V

ITEM		SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	NOTE
Contrast Ratio		CR	θ=ψ= 0°	400	600		--	*1) 2)
Luminance (5P)		L	θ=ψ= 0°	200	220		cd/m^2	*1) 3)
Uniformity(5P)		ΔL	θ=ψ= 0°	80			%	*1) 3)
Response Time		Tr	θ=ψ= 0°		2	4	ms	*5)
		Tf	θ=ψ= 0°		4	6	ms	*5)
Cross Talk		CT	θ=ψ= 0°			1	%	*6)
View Angle	Horizontal	ψ	CR≧10	40/-40	45/-45		°	*4)
	Vertical	θ		10/-30	15/-35		°	*4)
Color Temperature Coordinate	W	X	θ=ψ= 0°	0.283	0.313	0.343		*3)
		Y		0.299	0.329	0.359		
	R	X		0.590	0.620	0.650		
		Y		0.310	0.340	0.370		
	G	X		0.300	0.330	0.360		
		Y		0.540	0.570	0.600		
	B	X		0.120	0.150	0.180		
		Y		0.030	0.060	0.090		
Gamut			θ=ψ= 0°	56	60		%	
Gamma		γ	GL	2.0	2.2	2.4		*7)

Color coordinate and color gamut are measured by SRUL1R, response time is measured by TRD-100, and all the other items are measured by BM-5A (TOPCON). All these items are measured under the dark room condition (no ambient light).

Measurement Condition: IL= 20mA (each LED)

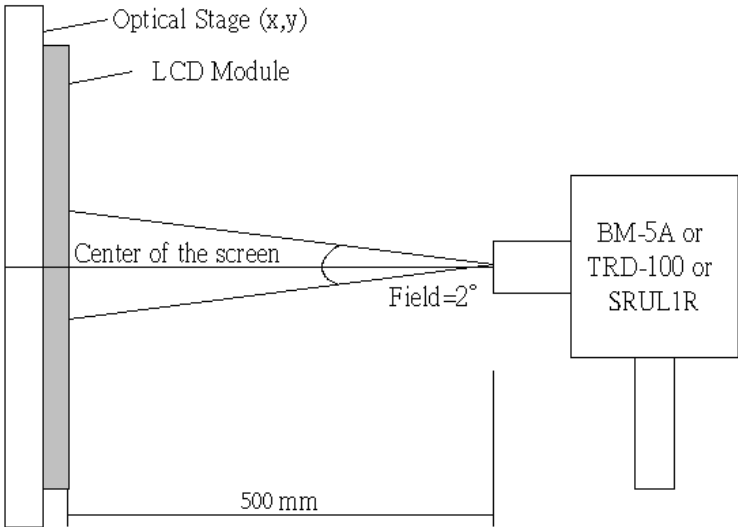
Definition of these measurement items is as follows:

***1) Setup of Measurement Equipment**

The LCD module should be turn-on to a stable luminance level to be reached. The measurement should be executed after lighting Backlight for 20 minutes and in a dark room.

***2) Definition of Contrast Ratio**

CR=ON (White) Luminance/OFF (Black) Luminance



***3) Definition of Luminance and Luminance uniformity**

Central luminance: The white luminance is measured at the center position "5" on the screen, see Fig.1 below.

5P Luminance (AVG): The white luminance is measured at measuring points 5、10、11、12、13, see Fig.1 below.

5P Uniformity: Δ L = (Lmin / Lmax) ×100%

13P Uniformity: Δ L = (Lmin / Lmax) ×100%

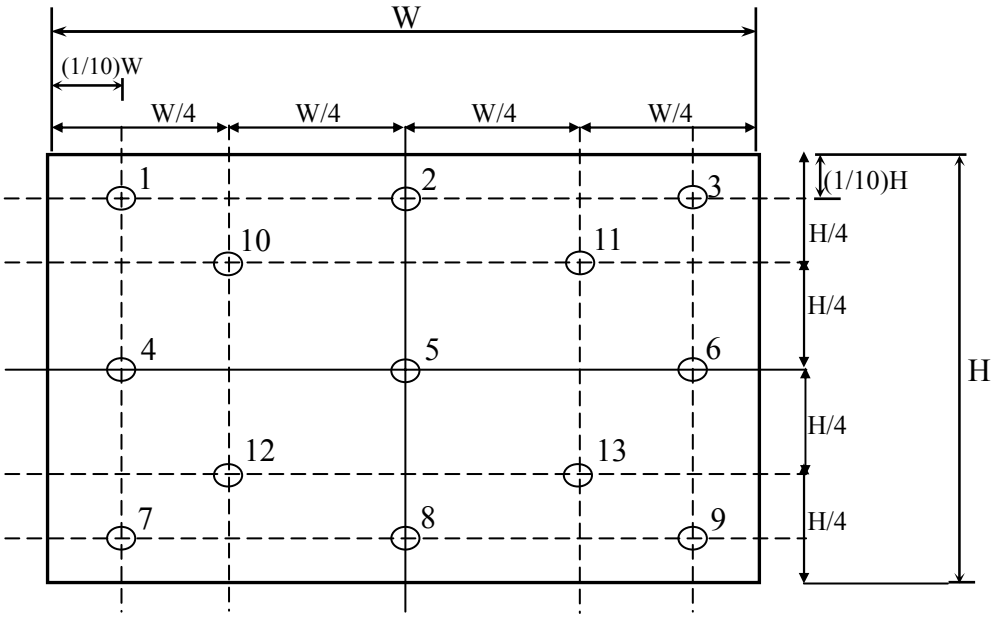
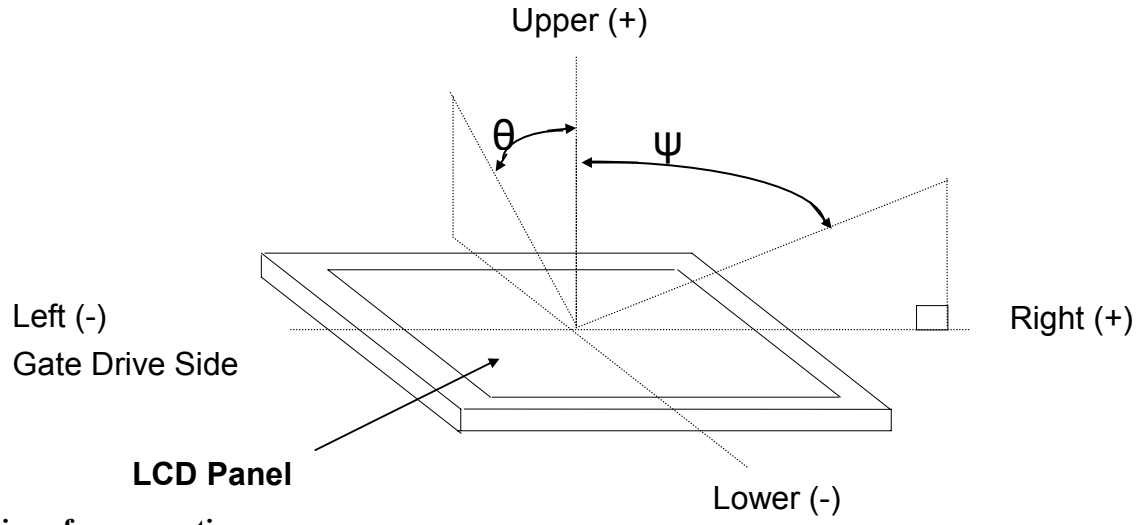
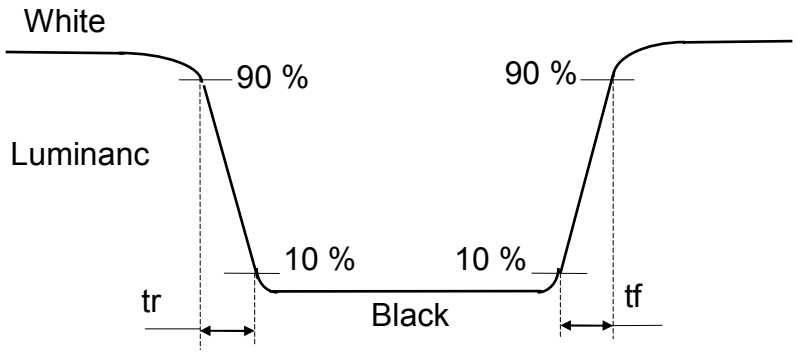


Fig.1 Measure point (Active area)

***4) Definition of view angle(θ，ψ)**



***5) Definition of response time**



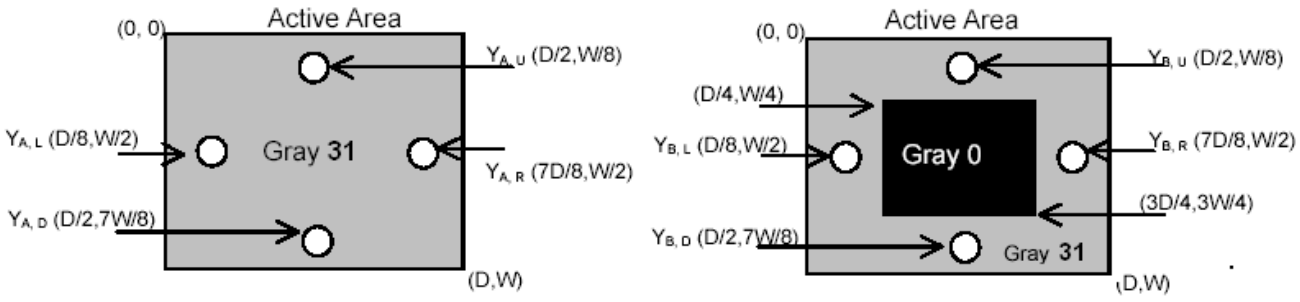
***6) Crosstalk Modulation Ratio:**

$CT= | Y_B - Y_A | / Y_A \times 100\%$

Y_A、Y_B measure position and definition

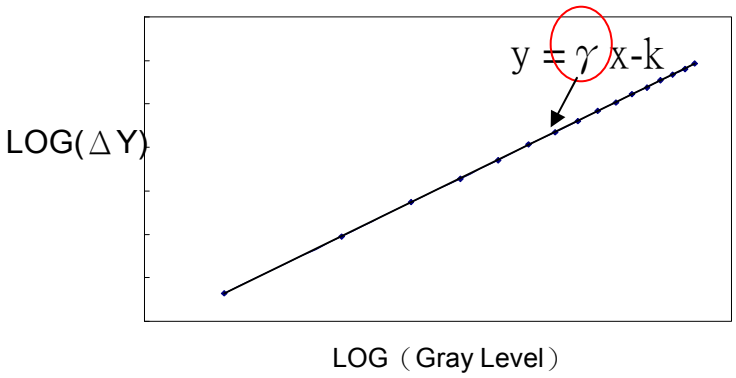
Y_A means luminance at gray level 31(exclude gray level 0 pattern)

Y_B means luminance at gray level 31(include gray level 0 pattern)



***7) Definition of Gamma (VESA)**

Based on Customer Sample, take the average value as a standard center value and the variation range of gamma value caused by loop voltage error should be between +/- 0.2. the bellow figure shows how to obtain the gamma curve and γ (from gray level: 0、4、8-----60、63).



9. RELIABILITY TEST CONDITIONS

(1) Temperature and Humidity

TEST ITEMS	CONDITIONS
High Temperature Operation	50°C ；240Hrs
High Temperature Storage	65°C ；240Hrs
High Temperature High Humidity Operation	40°C ；95% RH；240Hrs
High Temperature High Humidity Storage	60°C ；90% RH；48 Hrs
Low Temperature Operation	0°C ；240 Hrs
Low Temperature Storage	-25°C ；240 Hrs
Thermal Shock	-20°C(0.5 Hr)～65°C(0.5 Hr), Ramp<20°C, 100 CYCLES
Temperature & Pressure Storage	-30°C ；260hPa，24 Hrs

(2) Shock & Vibration

TEST ITEMS	CONDITIONS
Shock (Non-Operation)	210G, 2 ms, half sin ewave, ± X,± Y,± Z 1time each
Vibration (Non-Operation)	Vibration level : 14.7m/s^2 (1.5G), sinusoidal wave (each x, y, z axis : 1hr, total 3hrs) Frequency range : 5～500 Hz Sweep speed : 0.5 Octave/min.

(3) ESD

	Surface discharge(Panel display area、Frame、PWB、Panel back side)		Electrics capacity of Connector
	Contact	Air	Contact
Capacity	150 pF	150 pF	200 pF
Resistance	330 Ω	330 Ω	0 Ω
Voltage	±8kV	±15kV	±250 V
Interval	1 sec	1 sec	1 sec
Times(single point)	25	25	1

(4) MTBF without B/L: 200,000 Hrs (min) lifetimes.

(5) Judgment standard

The judgment of the above test should be made as follow:

Pass：Normal display image with no obvious non-uniformity and no line defect.

Partial transformation of the module parts should be ignored.

Fail：No display image, obvious non-uniformity, or line defects.

10. HANDLING PRECAUTIONS FOR TFT-LCD MODULE

Please pay attention to the followings in handling- TFT-LCD products.

10.1 ASSEMBLY PRECAUTION

(1) Please use the mounting hole on the module side in installing and do not beading or wrenching LCD in assembled. And please do not drop, bend or twist LCD module in handling.

(2) Please design display housing in accordance with the following guidelines.

- Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0 mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
- Design the inverter location and connector position carefully so as not to give stress to lamp cable, or not to interface the LCD module by the lamp cable.
- Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interface the LCD module. Approximately 1.0mm of the clearance in the design is recommended.

(3) Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands. (Polarizer film, surface of LCD panel is easy to be flawed.)

(4) Please do not press any parts on the rear side such as source TCP, gate TCP, control circuit board and FPCs during handling LCD module. If pressing rear part is unavoidable, handle the LCD module with care not to damage them.

(5) Please wipe out LCD panel surface with absorbent cotton or soft clothe in case of it being soiled.

(6) Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.

(7) Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

(8) Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.

(9) Please pay attention to handling lead wire of backlight so that it is not tugged in connecting with inverter.

10.2 OPERATING PRECAUTIONS

(1) Please be sure to turn off the power supply before connecting and disconnecting signal input cable.

(2) Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.

(3) Please consider that LCD backlight takes longer time to become stable of radiation characteristics in low temperature than in room temperature.

(4) A condensation might happen on the surface and inside of LCD module in case of sudden change of

ambient temperature.

(5) Please pay attention to displaying the same pattern for very long time. Image might stick on LCD. If then, time going on can make LCD work well.

(6) Please obey the same caution descriptions as ones that need to pay attention to ordinary electronic parts.

10.3 PRECAUTIONS WITH ELECTROSTATICS

(1) This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.

(2) Please remove protection film very slowly on the surface of LCD module to prevent from electrostatics occurrence.

10.4 STORAGE PRECAUTIONS

(1) When you store LCDs for a long time, it is recommended to keep the temperature between 0℃ ~40℃ without the exposure of sunlight and to keep the humidity less than 90%RH.

(2) Please do not leave the LCDs in the environment of high humidity and high temperature such as 60℃ 90%RH.

(3) Please do not leave the LCDs in the environment of low temperature; below -20℃.

10.5 SAFETY PRECAUTIONS

(1) When you waste LCDs, it is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged-glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

10.6 OTHERS

(1) A strong incident light into LCD panel might cause display characteristics' changing inferior because of polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land strong UV rays.

(2) Please pay attention to a panel side of LCD module not to contact with other materials in preserving it alone.

(3) For the packaging box, please pay attention to the followings:

- Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.
- Please do not pile them up more than 3 boxes. (They are not designed so.) And please do not turn over.
- Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.
- Packing box and inner case for LCDs are made of cardboard. So please pay attention not to get them wet. (Such like keeping them in high humidity or wet place can occur getting them wet.)