

# **CUSTOMER APPROVAL SHEET**

Company Name							
MODEL	A070SN01 V3						
CUSTOMER							
APPROVED							
☐ APPROVAL FOR SPECIFICAT	TIONS ONLY (Spec. Ver. <u> 0.1 )</u>						
☐ APPROVAL FOR SPECIFICAT	APPROVAL FOR SPECIFICATIONS AND ES SAMPLE (Spec. Ver. <u>0.1</u> )						
APPROVAL FOR SPECIFICATIONS AND CS SAMPLE (Spec. Ver. 0.1)							

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**CUSTOMER REMARK:** 

Comment:



Doc. version :	0.0	
Total pages :	28	

Date : 2010/03/11

# **Product Specification**

# 7.0" COLOR TFT-LCD MODULE

Model Name: A070SN01 V3

Planned Lifetime: From 2010/Feb. To 2010/Dec.

Phase-out Control: From 2009/Jul. To 2010/Dec.

EOL Schedule: 2010/Dec.

< >Preliminary Specification

<->Final Specification

Note: The content of this specification is subject to change.

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#### Record of Revision

Version	Revise Date	Page	Content
0.0	2010/3/11	All	First Draft.
0.1	2010/3/17	0	P/N correction.



0.0





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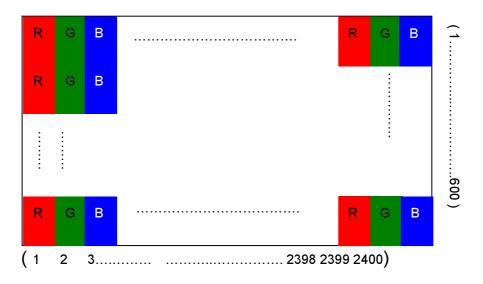
# A. General Information

This product is for portable DVD and digital photo frame application.

NO.	Item	Unit	Specification	Remark
1	Screen Size	inch	7.0 (Diagonal)	
2	Display Resolution	dot	800(H)×600 RGB(V)	
3	Overall Dimension	mm	154(H) × 119.2(V) × 6.44(T)	Note 1
4	Active Area	mm	141.6(H)×106.2(V)	
5	Pixel Pitch	mm	0.177(H)×0.177(V)	
6	Color Configuration	R. G. B. Stripe		Note 2
7	Color Depth		16M Colors	Note 3
8	NTSC Ratio	%	50	
9	Display Mode		Normally White	
10	Touch Panel surface Treatment		AG 8%, Hard coating 3H	
11	Weight	g	123	
12	Backlight Power Consumption	W	1.2	

Note 1: Not include blacklight cable and FPC. Refer next page to get further information.

Note 2: Below figure shows dot stripe arrangement.



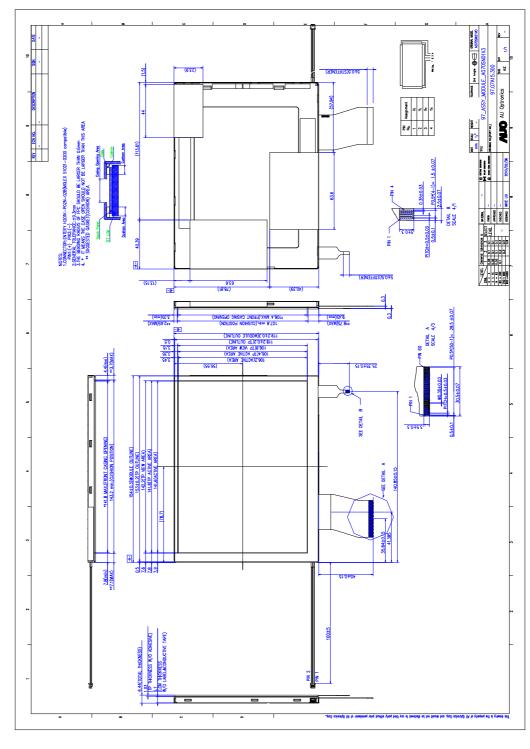
Note 3: The full color display depends on 24-bit data signal (pin 4~27).



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# B. Outline Dimension

# 1. TFT-LCD Module - Front View& Rear View



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# C. Electrical Specifications

# 1. TFT LCD Panel Pin Assignment

Recommended connector:

Pin No.	Symbol	I/O	Description	Remark
1	AGND	Р	Analog Ground	
2	AVDD	Р	Analog Power	
3	VDD	Р	Digital Power	
4	R0	I	Data input (LSB)	
5	R1	I	Data input	
6	R2	I	Data input	
7	R3	I	Data input	
8	R4	I	Data input	
9	R5	I	Data input	
10	R6	I	Data input	
11	R7	I	Data input (MSB)	
12	G0	I	Data input (LSB)	
13	G1	I	Data input	
14	G2	I	Data input	
15	G3	I	Data input	
16	G4	I	Data input	
17	G5	I	Data input	
18	G6	I	Data input	
19	G7	I	Data input (MSB)	
20	В0	I	Data input (LSB)	
21	B1	I	Data input	
22	B2	I	Data input	
23	В3	I	Data input	
24	B4	I	Data input	
25	B5	I	Data input	
26	В6	I	Data input	
27	В7	I	Data input (MSB)	
28	DCLK	I	Clock input	
29	DE	I	Data enable signal	
30	NC	-	No connect	
31	NC	-	No connect	



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32	Left./Right	I	Left./Right	Note 1						
	(optional)	_	zere, rugite	TVOIC I						
33	Up/Down (optional)	I	Up/Down	Note 1						
34	Display	H/L	H = Display on, L = Diplay off							
35	NC	-	No connect							
36	VDD	P	Digital Power							
37	NC	-	No connect							
38	GND	P	Digital ground							
39	AGND	P	Analog ground							
40	AVDD	P	Analog Power							
41	VCOM	I	For external VCOM DC input							
			Dithering setting							
42	DITU	DITU	DITU	DITH	DITU	DITU	DITH	I/O	DITH = "H" 6bit resolution(LSB last 2 bits of input data turncated)	
42	DIIII	1/0	(Default setting)							
			DITH = "L" 8bit resolution							
43	NC	I/O	No connect							
44	VCOM	I	For external VCOM DC input							
45	V10	P	Gamma correction voltage reference							
46	V9	P	Gamma correction voltage reference							
47	V8	P	Gamma correction voltage reference							
48	V7	P	Gamma correction voltage reference							
49	V6	P	Gamma correction voltage reference							
50	V5	P	Gamma correction voltage reference							
51	V4	P	Gamma correction voltage reference							
52	V3	P	Gamma correction voltage reference							
53	V2	P	Gamma correction voltage reference							
54	V1	P	Gamma correction voltage reference							
55	NC	-	No connect							
56	VGH	P	Positive power for TFT							
57	VDD	P	Digital Power							
58	VGL	P	Negative power for TFT							
59	GND	P	Digital Ground							
60	NC	-	No connect							

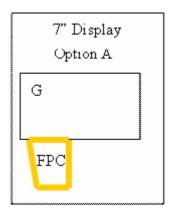
I: Input pin; P: Power pin; G: Ground pin; C: capacitor pin

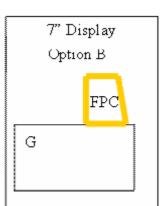


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Note 1: Option A: Up to down (UPDN = H), left to right (SHLR = H).

Option B: down to up (UPDN = L), right to left (SHLR = L).







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# 2. Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
	VDD	GND=0	-0.3	+5.0	V	
Power voltage	AVDD	GND=0	-0.3	+14.0	V	
	VGH		-0.3	42	V	
	VGL	GND=0	-20	0.3	V	
Storage temperature	Tstg	-	-20	70	0	Ambient temperature
Operating	Тора	-	-10	60	0	Ambient temperature

Note 1: Functional operation should be restricted under ambient temperature (25□).

Note2: Maximum ratings are those values beyond which damages to the device may occur. Functional operation should be restricted to the limits in the Electrical Characteristics chapter.



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# D. Electrical DC Characteristics

Ite	em	Symbol	Min.	Тур.	Max.	Unit	Remark
		$V_{DD}$	3.1	3.3	3.5	V	
		AVdd	11.5	11.6	11.8	V	
Power	supply	$V_{GH}$	14	15	16	V	
Tower	Suppry	$V_{GL}$	<i>-</i> 7.5	-7	-6.5	V	
	wer mption	P	-	1.2	-	mW	Black Pattern
VC	ОМ	Vcdc	-	4.9	-	V	DC component
Input	H Level	Vih	0.7*VDD	-	VDD	V	
signal	L Level	Vil	0	-	0.3*VDD	V	
_	level of ~V5	Vx	0.4*AVDD	-	AVDD-0.1		Positive gamma correction voltage Note 1
Input level of V6~V10		Vx	0.1	-	0.6*AVDD		Negative gamma correction voltage Note 1

Note 1 : AGND <V10<V9V<8V7<V6<V5<V4<V3<V2<V1<AVDD



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#### 1. Current Consumption (AGND=GND=0V)

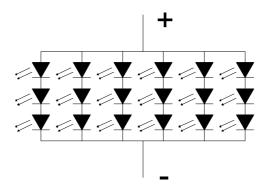
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Input current for VDD	IVDD	VDD=3.3V	-	17	25	mA	
Input current for AVDD	IAVDD	AVDD = 11.6V	-	25	30	mA	
Inpur current for VGH	Ivgh	VGH=15V	1	0.2	0.4	mA	Under black pattern
Inpur current for VGL	Ivgl	VGL= -7V		-0.2	-0.4	mA	P mooral
Inpur current for VCOM	Ivcom	VCOM= xV		0.004	0.010	mA	

#### 2. Backlight Driving Conditions

The backlight (LED module, Note 1) is suggested to drive by constant current with typical value.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
LED light bar Voltage	${ m I}_{ m L}$	-	120	-	mA	Note 1
Power Consumption	$P_{BL}$	-	1.2	-	W	Note 1
LED Life Time	$\mathbf{L}_{\mathrm{L}}$	10,000			Hr	Note 2, 3

Note 1: The LED driving condition is defined for LED module (18 LED).



Note 2: Define "LED Lifetime": brightness is decreased to 50% of the initial value. LED Lifetime is restricted under normal condition, ambient temperature = 25® and LED lightbar current = 120mA.

Note 3: If it uses larger LED lightbar voltage more than 120mA, it maybe decreases the LED lifetime.

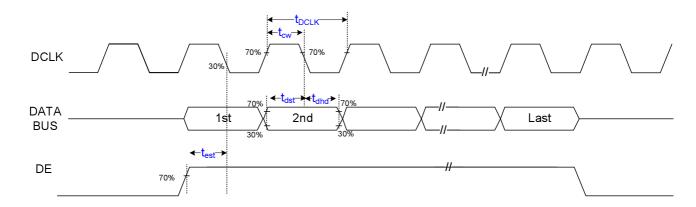


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# E. Electrical AC Characteristics

# 1. Signal AC Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK duty cycle		40	50	60	%	tcw/tdclk x100%
DE setup time	Test	8			ns	
Data setup time	tdst	8			ns	
Data hold time	<b>t</b> dhd	8			ns	



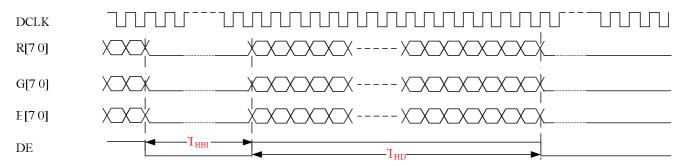


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# 2. Input Timing Setting

# **Horizontal Timing:**

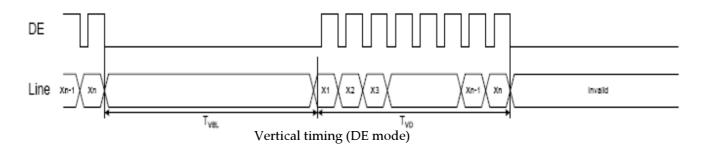
Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
DCLK frequency	Fdclk	35	40	45	MHz	
DCLK period	Tdclk	22	25	28	ns	
Hsync period (= Thd + Thbl)	Тн	-	1000	-	DCLK	
Active Area	Тнр	-	800	-	DCLK	
Horizontal blanking (= The + The)	THBL		200		DCLK	



Horizontal input timing (DE mode)

# 3. Vertical timing:

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
Vsync period (= Tvd + Tvbl)	Tv	-	660	-	Th	
Active lines	Tvd	ı	600	-		
Vertical blanking (= Tvf + Tve)	Tvbl	-	60	-	Th	



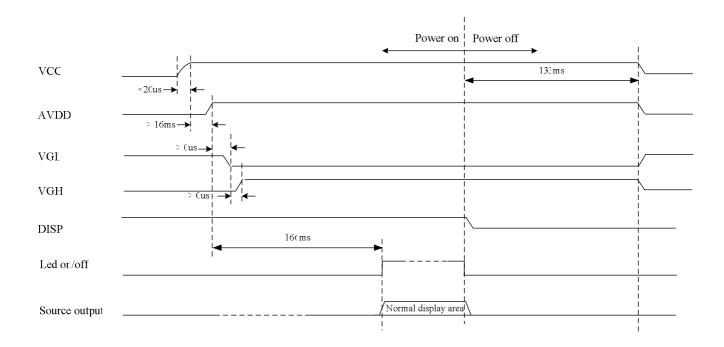


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# F. Power On/Off Characteristics

# Recommended Power On/off Sequence

The suggested LCD power on/off sequence is below:





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# G. Optical Specification

All optical specification is measured under typical condition (Note 1, 2)

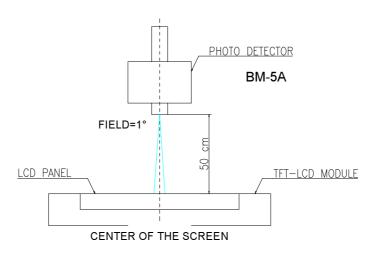
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response T	ime							
Rise		Tr	θ=0°		3	6	ms	Note 3
Fall		Tf	0-0		13	26	ms	
Contrast ra	atio	CR	At optimized viewing angle	400	500			Note 4
	Top				60			
Mississa Amala	Bottom		CR≥10		60		deg.	Note 5
Viewing Angle	Left		CK≦10		70			
	Right				70			
Brightne	SS	YL	θ=0°	160	200		cd/m <sup>2</sup>	Note 6
	T471 ·	X	θ=0°	0.25	0.30	0.35		
	White	Y	θ=0°	0.27	0.32	0.37		
	D.J	Х	θ=0°	0.53	0.58	0.63		
Clause atiaita	Red	Y	θ=0°	0.28	0.33	0.38		
Chromaticity	C	Х	θ=0°	0.28	0.33	0.38		
	Green	Y	θ=0°	0.51	0.56	0.61		
	Dlas	Х	θ=0°	0.10	0.15	0.20		
	Blue	Y	θ=0°	0.06	0.11	0.16		
Uniformi	ty	$\Delta Y_L$	%	70	75		%	Note 7

Note 1. Ambient temperature =25 $^{\circ}$ C, and LED lightbar current I<sub>L</sub> = 120 mA. To be measured in the dark room.



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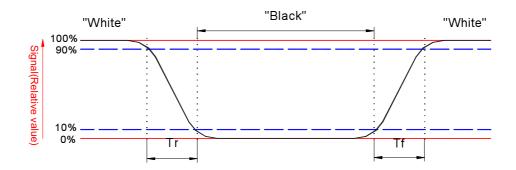
Note 2. To be measured on the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-5A, after 15 minutes operation.



Note 3. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively.

The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4. Definition of contrast ratio:

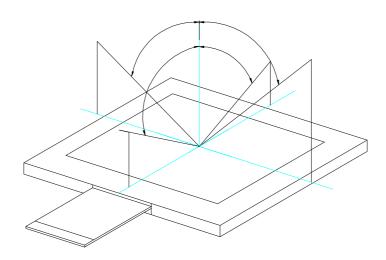
Contrast ratio is calculated with the following formula.

Contrast ratio (CR) =  $\frac{\text{Photo detector output when LCD is at "White" status}}{\text{Photo detector output when LCD is at "Black" status}}$ 



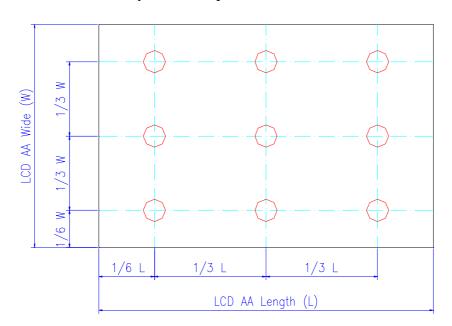
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Note 5. Definition of viewing angle,  $\theta$ , Refer to figure as below.



Note 6. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 7: Luminance Uniformity of these 9 points is defined as below:



Uniformity =  $\frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$ 



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# H. Touch Screen Panel Specifications

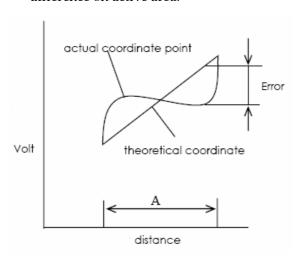
#### 1. FPC Pin Assignment

Pin No.	Symbol	I/O	Description	
1	$\chi_{\scriptscriptstyle  m L}$	I/O	Touch panel left electrode (L)	
2	$Y_L$	I/O	Touch panel bottom electrode (B)	
3	Хн	I/O	Touch panel right electrode (R)	
4	Yн	I/O	Touch panel top electrode (U)	

#### 2. Electrical Characteristics

Item		Min.	Тур	Max.	Unit	Remark	
Rate DC Voltage				7	V		
Resistance	X (Film)	100		950	Ω	At connector	
Resistance	Y (Glass)	150		550	1.2	At connector	
Linearity		-1.5%		1.5%		Note 1	
Response Time				20	ms		
Insulation Resist	ance	20			ΜΩ	DC 25V	

Note 1: Measurement condition of Linearity: difference between actual voltage & theoretical voltage is an error at any points. Linearity is the value max. error voltage divided by voltage difference on active area.





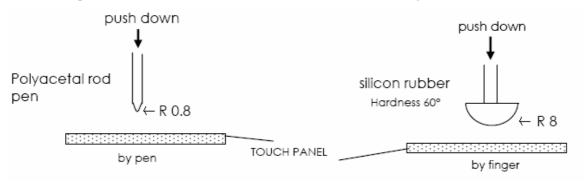
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#### 3. Mechanical Characteristics

Item	Min.	Max.	Unit	Remark
Hardness of Surface	3		Н	JIS K-5600
Operation Force (Pen or Finger)	80	xxx	gf	Note 1, 2

Note 1: Within "active area", but not near the active area boundary and on the dot-spacer.

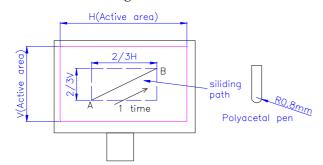
Note 2: Operation force measurement is under test condition as figure below.



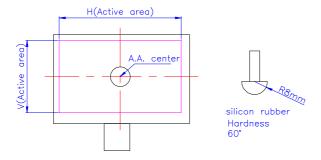
#### 4. Life Test Condition

Item	Min.	Max.	Unit	Remark
Notes Life	$10^{5}$		lines	Note 1, 2
Input Life	106		times	Note 1, 3

Note 1: Notes Life test condition (by pen): slide on central 2/3 of active area and use R 0.8mm polyacecal pen, input force: 250gf, frequency: 60mm/sec. Sliding from A to B complete 1 time. shown as figure 2.



Note 2: Input Life test condition (by finger): test position on active area center and use R8.0mm silicon rubber (hardness 60°), test force: 250gf, frequency: 2times/sec. shown as figure.



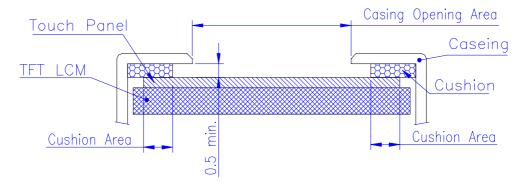


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#### 5. Attention

Please pay attention for below matters at mounting design of touch panel of LCD module.

- 1) Do not design casing opening area pressing the active area to prevent from miss input. Suggest casing opening area shown as mechanical drawing. Suggest the gap between caseing and touch panel surface at least 0.5mm to avoid miss input.
- 2) Cushion area must not contact with active area. Suggest cushion area shown as mechanical drawing.
- 3) Use elastic or non-conductive material to enclosure touch panel.
- 4) Do not bond film of touch panel with casing.
- 5) The touch panel edge is conductive. Do not touch it with any conductive part after mounting.



- 6) If user wants to cleaning touch panel by air gun, pressure 2kg/cm<sup>2</sup> below is suggested. Not to blow glass from FPC site to prevent FPC peeled off.
- 7) Do not put a heavy shock or stress on touch panel and film surface. Ex. Don't lift the panel by film face with vacuum.
- 8) Do not lift LCD module by FPC.
- 9) Please use dry cloth or soft cloth with neutral detergent (after wring dry) or one with ethanol at cleaning. Do not use any organic solvent, acid or alkali liquor.
- 10) Do not pile touch panel. Do not put heavy goods on touch panel.



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# I. Reliability Test Items

No.	Test items	Cond	itions	Remark
1	High Temperature Storage	Ta= 70 □	240Hrs	
2	Low Temperature Storage	Ta= -20□	240Hrs	
3	High Ttemperature Operation	Tp= 60 □	240Hrs	
4	Low Temperature Operation	Ta= -10□	240Hrs	
5	High Temperature & High Humidity	Tp= 50 □. 80% RH	240Hrs	Operation
6	Heat Shock	-30□~70□, 50 cy	cle, 1Hrs/cycle	Non-operation
7	Electrostatic Discharge	Contact = $\pm 4 \text{ kV}$ , class B Air = $\pm 8 \text{ kV}$ , class B		Note 4
8	Image Sticking	25□, 4hrs		Note 5
		Frequency range	: 10~55Hz	TTG C=004
		Stoke	: 1.5mm	JIS C7021, A-10
9	Vibration	Sweep	: 10 ~ 55 ~ 10Hz	Condition A
		2 hours for each direc	Condition 71	
		(6 hours for total)		
10	Mechanical Shock	100G . 6ms, ±X,±Y,±Z 3 times for each direction		Non-operation JIS C7021, A-7 condition C
11	Vibration (With Carton)	Random vibration: 0.015G²/Hz from 5~200Hz -6dB/Octave from 200~500Hz		IEC 68-34
12	Drop (With Carton)	Height 1 corner, 3 ed		
13	Pressure	5kg,	5sec	Note 6

Note 1. Ta: Ambient Temperature. Tp: Panel Surface Temperature

Note 2. In the standard conditions, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.

Note 3. All the cosmetic specification is judged before the reliability stress.



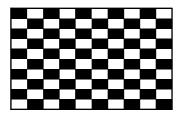
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Note4. All test techniques follow IEC6100-4-2 standard.

Pls be noticed that product needs grounding design on customer system side.

Test Condition		Note
Pattern		
Procedure And Set-up	Contact Discharge: 330Ω, 150pF, 1sec, 8 point, 25times/point  Air Discharge: 330Ω, 150pF, 1sec, 8 point, 25times/point	
Criteria	B – Some performance degradation allowed. No data lost. Self-recoverable hardware failure.	
Others	Gun to Panel Distance     No SPI command, keep default register settings.	

Note 5. Operate with chess board pattern as figure and lasting time and temperature as the conditions. Then judge with 50% gray level, the mura is less than JND 2.5

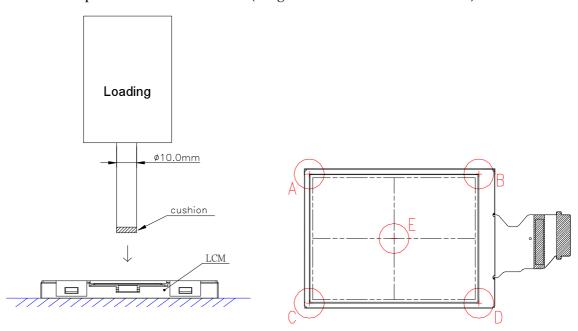


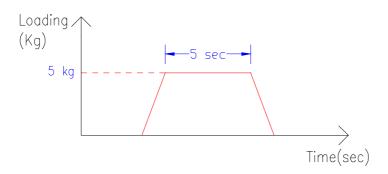




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Note 6. The panel is tested as figure. The jig is  $\psi10$  mm made by Cu with rubber(KE951U,1.0t) and the loading speed is 3mm/min on position A~E. After the condition, no glass crack will be found and panel function check is OK.( no guarantee LC mura  $\cdot$  LC bubble)



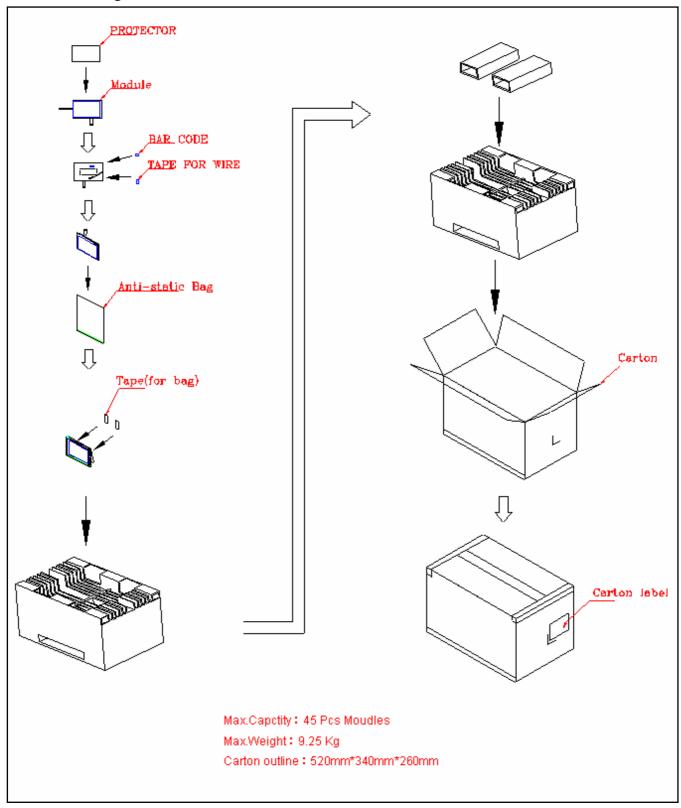




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# J. Packing and Marking

# 1. Packing Form





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#### 2. Module/Panel Label Information

The module/panel (collectively called as the "Product") will be attached with a label of Shipping Number which represents the identification of the Product at a specific location. Refer to the Product outline drawing for detailed location and size of the label. The label is composed of a 22-digit serial number and printed with code 39/128 with the following definition:

#### ABCDEFGHIJKLMNOPQRSTUV

For internal system usage and production serial numbers.

LAUO Module or Panel factory code, represents the final production factory to complete the Product Product version code, ranging from 0~9 or A~Z (for Version after 9)

-Week Code, the production week when the product is finished at its production process

#### 3. Carton Label Information

The packing carton will be attached with a carton label where packing Q'ty, AUO Model Name, AUO Part Number, Customer Part Number (Optional) and a series of Carton Number in 13 or 14 digits are printed. The Carton Number is apparing in the following format:

#### ABC-DEFG-HIJK-LMN

DEFG appear after first "-" represents the packing date of the carton Date from 01 to 31

- Month, ranging from 1~9, A~C. A for Oct, B for Nov and C for Dec.

-A.D. year, ranging from 1~9 and 0. The single digit code reprents the last number of the year

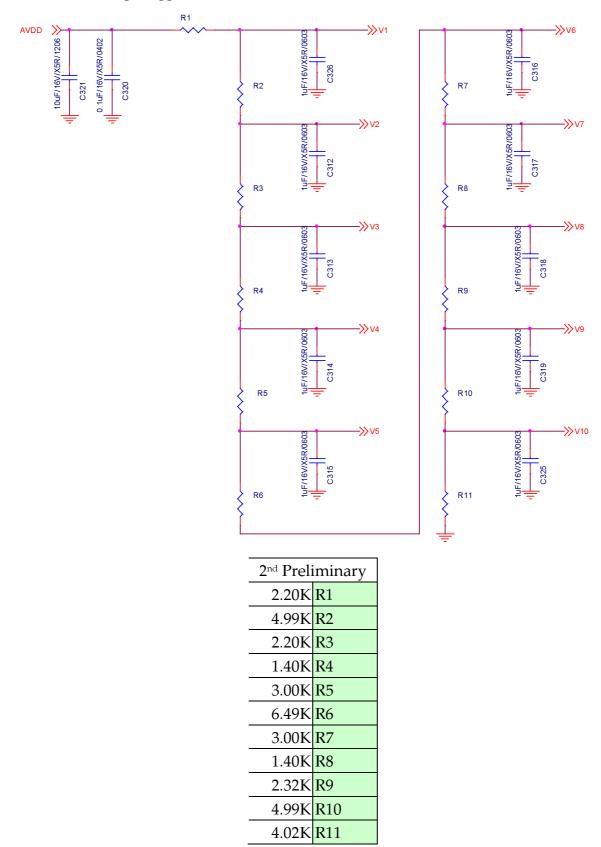
Refer to the drawing of packing format for the location and size of the carton label.



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# J. Application Note

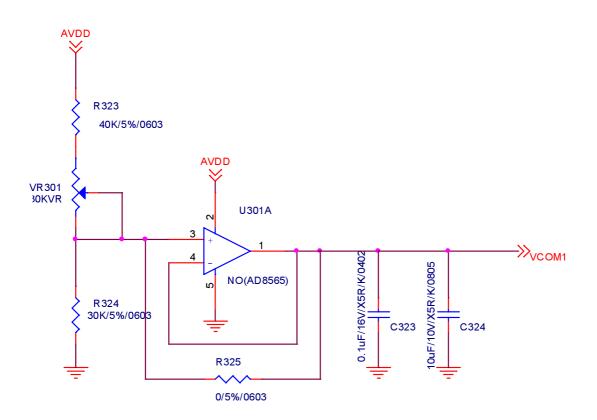
1. Gamma voltage suggested circuit is as follows





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# 2. Vcom buffer suggested circuit is as follows





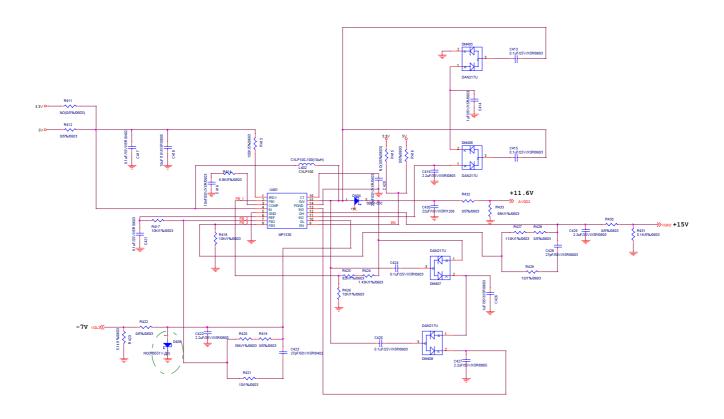
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# 3. Application Circuit

J1 AGND AVDD VDD R0 R1 R2 R3 R4 R5 R6 R7 R6 G1 B1 B2 B3 B4 B6 B7 DCLK DE NC SHLR NC SHLR UDDSP NC
VDD
NC
GND
AGND
AGND
VCOM
DITH
NC
VCOM
V10
V8
V7
V6
V4
V3
V2
V1
NC
VGH
VDD
VGL 36 37 38 VDD 39 40 41 42 43 44 45 DITH 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 CON60



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#### K. Precautions

- 1. Do not twist or bend the module and prevent the unsuitable external force for display module during assembly.
- 2. Adopt measures for good heat radiation. Be sure to use the module with in the specified temperature.
- 3. Avoid dust or oil mist during assembly.
- 4. Follow the correct power sequence while operating. Do not apply the invalid signal, otherwise, it will cause improper shut down and damage the module.
- 5. Less EMI: it will be more safety and less noise.
- 6. Please operate module in suitable temperature. The response time & brightness will drift by different temperature.
- 7. Avoid to display the fixed pattern (exclude the white pattern) in a long period, otherwise, it will cause image sticking.
- 8. Be sure to turn off the power when connecting or disconnecting the circuit.
- 9. Polarizer scratches easily, please handle it carefully.
- 10. Display surface never likes dirt or stains.
- 11. A dewdrop may lead to destruction. Please wipe off any moisture before using module.
- 12. Sudden temperature changes cause condensation, and it will cause polarizer damaged.
- 13. High temperature and humidity may degrade performance. Please do not expose the module to the direct sunlight and so on.
- 14. Acetic acid or chlorine compounds are not friends with TFT display module.
- 15. Static electricity will damage the module, please do not touch the module without any grounded device.
- 16. Do not disassemble and reassemble the module by self.
- 17. Be careful do not touch the rear side directly.
- 18. No strong vibration or shock. It will cause module broken.
- 19. Storage the modules in suitable environment with regular packing.
- 20. Be careful of injury from a broken display module.
- 21. Please avoid the pressure adding to the surface (front or rear side) of modules, because it will cause the display non-uniformity or other function issue.