TOSHIBA

LIQUID CRYSTAL DISPLAY DIVISION PRODUCT INFORMATION

26cm COLOUR TFT-LCD MODULE (10.4 TYPE)

LTM10C348S (p-Si TFT)

TENTATIVE

FEATURES

- (1) 10.4"SVGA display size for notebook PC
- (2) LVDS interface system
- (3) Slim(5.2mmMAX)

MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (Typ.)	242.4(W) x 173.2(H) x 5.2max(D) mm
Number of Pixels	800(W) x 600(H) pixels
Active Area	211.2(W) x 158.4(H) mm
Pixel Pitch	0.264(W) x 0.264(H)
Weight (approximately)	$(265) \pm 20 \text{ g}$
Backlight	Single CCFL, Sidelight type

ABSOLUTE MAXIMUM RATINGS

Item		Min.	Max.	Unit
Supply Voltage	(V_{DD})	-0.3	4.0	V
	(V_{FL})	-	2.0	kV(rms)
FL Driving Frequ	FL Driving Frequency (f _{FL})		100	kHz
Input Signal Volta	Input Signal Voltage (V _{IN})		V _{DD} +0.3	V
Operating Temperature		0	50	°C
Storage Tempera	ature	-20	60	°C
Storage Humidity	/	10	90	%(RH)

ELECTRICAL SPECIFICATION

Item	Min.	Тур.	Max.	Unit	Remarks	
Supply Voltage	$(V_{ m DD})$	3.0	3.3	3.6	V	
	(V_{FL})	560	610	660	V(rms)	I_{FL} =4.2 mA(rms)
FL Start Voltage (Ta=0°C)		1200	•	1600	V(rms)	
Receiver Input Voltage		0		2.4	V	
Differential Input High Thresho	ld(V _{TH})*1		-	V _{os} +0.1	V	V _{os} :Offset Mode Voltage
Differential Input Low Thresho	$d(V_{TL})*1$	V _{os} -0.1	-		V	V _{OS} =+1.2V
Current Consumption	*2 (I _{DD})		250	390	mA	
	*3 (I _{FL})	2.5	4.2	6.0	mA(rms)	
*2 *3 Power Consumption		3.4		W	@130cd/m ²	

^{*1 :} Refer to DF90CF364 Specification by National Semiconductor Corporation. This LCD module conforms to LVDS standard (TIA/EIA-644)

OPTICAL SPECIFICATION (*T*a=25°C)

Item	Min.	Тур.	Max.	Unit	Remarks	
Contrast Ratio (CR)	100	250				
Response Time	(t_{ON})			50	ms	
	(t_{OFF})			50	ms	
Luminance (L)		90	130		cd/m ²	I_{FL} =4.2mA(rms)
		(150)	(190)		cd/m ²	I_{FL} =6.0mA(rms)

^{*}The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba or others.

^{*2 : 8} color bars pattern

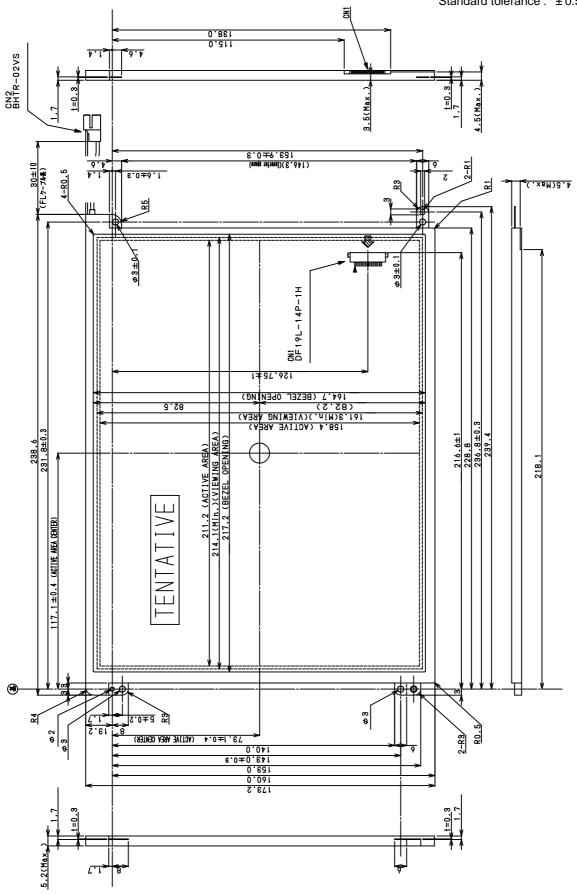
^{*3 :} Excepting the efficiency FL inverter

^{*4:} Not use Hsync nor Vsync. Only ENAB control.

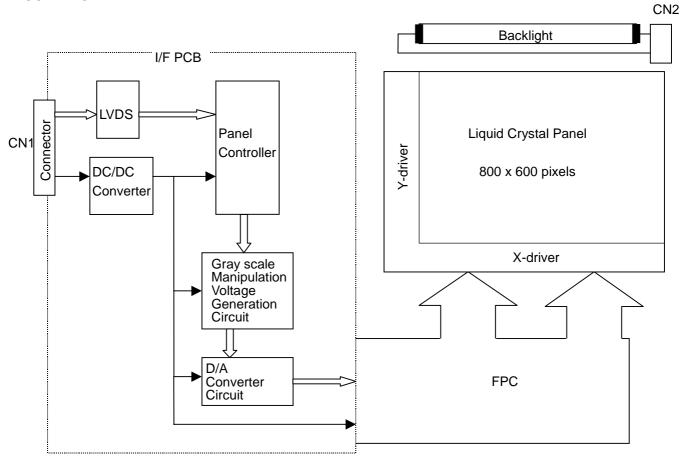
^{*}The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba before proceeding with the design of equipment incorporating this product.

Unit: mm

Standard tolerance : ± 0.5

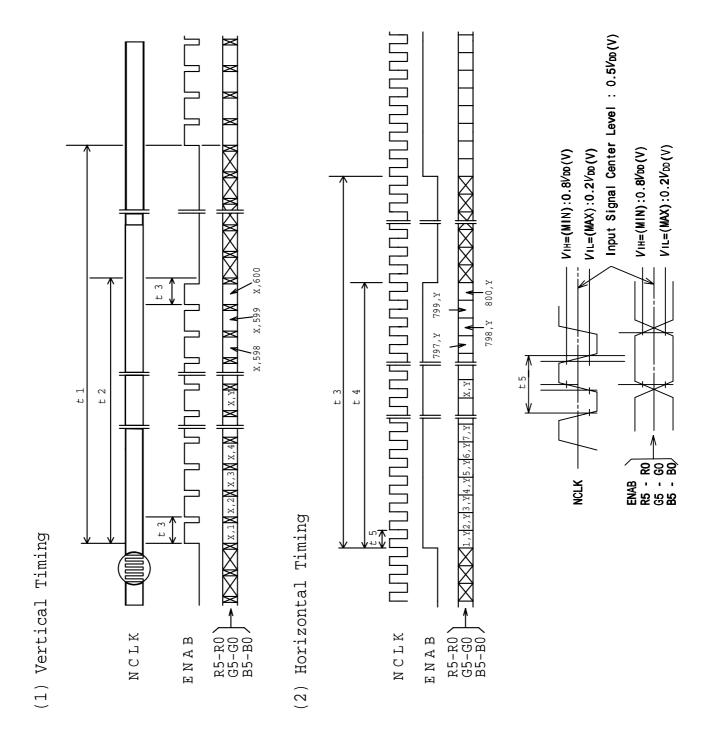


BLOCK DIAGRAM



•		800 pixels		-	
1, 1	2, 1	X2 _{n-1} , 1	X2 _n , 1	800, 1	A
1, 2					
1, Y		X _{2n-1} , Y	X _{2n} , Y		600 pixels
					loon bixer
1, 600				800,600	\downarrow

TIMING CHART



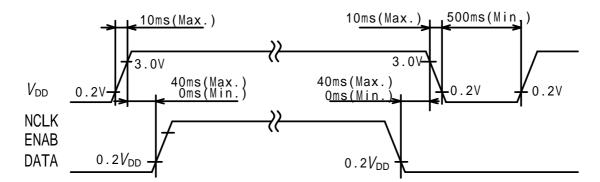
TIMING SPECIFICATION 1)2) 3) 4)5)

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Frame Period	<i>t</i> 1	604 × <i>t</i> 3	628 × <i>t</i> 3	677 × <i>t</i> 3	-	
		-	16.58	17.86	ms	
Vertical	<i>t</i> 2	600 × <i>t</i> 3	600 × <i>t</i> 3	600 × <i>t</i> 3		
Display Term					-	
One Line Scanning	ť3	958 × <i>t</i> 5	1056 × <i>t</i> 5	1064 × <i>t</i> 5	-	
Time		26.3	26.4		μs	
Horizontal	<i>t</i> 4	800 × <i>t</i> 5	800 × <i>t</i> 5	800 × <i>t</i> 5		
Display Term					-	
Clock Period	<i>t</i> 5	24.7	25.0	27.8	ns	

- Note 1) Refer to TIMING CHART and LVDS (DF90CF364MTD) specifications by National Semiconductor.
- Note 2) If ENAB is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.
- Note 3) If NCLK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.
- Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

 There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving Condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.
- Note 5) Do not make t1,t2 and t3 fluctuate. If t1,t2 and t3 are fluctuate, the panel displays black.
- Notes 6) Do not hold NCLK on "H" level nor "L" level during VDD(+3.3V) Is supplied. When it holds on, DC voltage supplies to liquid crystal materials and it may cause damage to liquid crystal materials.

SEQUENCE OF POWER SUPPLIES AND SIGNALS



CONNECTOR PIN ASSIGNMENT FOR INTERFACE

CN1 INPUT SIGNAL

Connector: DF19L-14P-1H / HIROSE ELECTRIC CO.,LTD.

Mating Connector : DF19G-14S-1C / HIROSE ELECTRIC CO.,LTD.

Terminal No.	Symbol	Function
1	$V_{\scriptscriptstyle m DD}$	Power Supply: +3.3V
2	$V_{\scriptscriptstyle m DD}$	Power Supply: +3.3V
3	GND	
4	GND	
5	INO-	Transmission Data of Pixels 0 (Negative : -)
6	IN0+	Transmission Data of Pixels 0 (Positive : +)
7	IN1-	Transmission Data of Pixels 1 (Negative : -)
8	IN1+	Transmission Data of Pixels 1 (Positive : +)
9	IN2-	Transmission Data of Pixels 2 (Negative : -)
10	IN2+	Transmission Data of Pixels 2 (Positive : +)
11	CLK-	Sampling Clock (Negative : -)
12	CLK+	Sampling Clock (Positive : +)
13	GND	
14	GND	

CN2 CCFL POWER SOURCE

Connector: BHTR-02VS / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

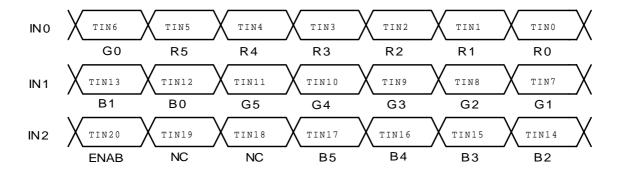
Mating Connector: SM02B-BHTS-B-TB / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

Terminal No.	Symbol	Function
1	<i>V</i> FLH	CCFL Power Supply (high voltage)
2	<i>V</i> FLL	CCFL Power Supply (low voltage)

RECOMMENDED TRANSMITTER (DS90CF363) TO LTM10C348S INTERFACE ASSIGNMENT

Case1: 6bit Transmitter

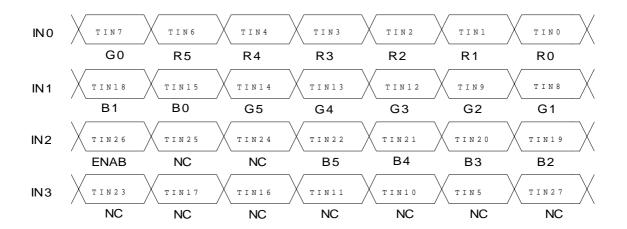
	DS90CF363					C348S
Input T	Input Terminal No.		Input Signal		Inte	rface
·			(Graphics controller output signal)	raphics controller output signal) Signal (CN1)		N1)
Symbol	DS90CF363	Symbol	Function Symbol		Terminal	Symbol
TIN0	44	R0	Red Pixels Display Data (LSB)			
TIN1	45	R1	Red Pixels Display Data			
TIN2	47	R2	Red Pixels Display Data	TOUT0-	No.12	INO-
TIN3	48	R3	Red Pixels Display Data	TOUT0+	No.11	IN0+
TIN4	1	R4	Red Pixels Display Data			
TIN5	3	R5	Red Pixels Display Data (MSB)			
TIN6	4	G0	Green Pixels Display Data (LSB)			
TIN7	6	G1	Green Pixels Display Data			
TIN8	7	G2	Green Pixels Display Data			
TIN9	9	G3	Green Pixels Display Data	TOUT1-	No.10	IN1-
TIN10	10	G4	Green Pixels Display Data	TOUT1+	No.9	IN1+
TIN11	12	G5	Green Pixels Display Data (MSB)			
TIN12	13	В0	Blue Pixels Display Data (LSB)			
TIN13	15	B1	Blue Pixels Display Data			
TIN14	16	B2	Blue Pixels Display Data			
TIN15	18	В3	Blue Pixels Display Data			
TIN16	19	B4	Blue Pixels Display Data	TOUT2-	No.8	IN2-
TIN17	20	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.7	IN2+
TIN18	22	NC	Non Connection (open)			
TIN19	23	NC	Non Connection (open)			
TIN20	25	ENAB	Compound Synchronization Signal			
CLK IN	26	NCLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.6 No.5	CLK IN- CLK IN+



RECOMMENDED TRANSMITTER (DS90CF383) TO LTM10C348S INTERFACE ASSIGNMENT

Case2: 8bit Transmitter

			DS90CF383		LTM10	C348S
Input T	erminal No.		Input Signal	Output		rface
			(Graphics controller output signal)	Signal	(CN1)	
Symbol	DS90CF383	Symbol	Function	Symbol	Terminal	Symbol
TIN0	51	R0	Red Pixels Display Data (LSB)			
TIN1	52	R1	Red Pixels Display Data			
TIN2	54	R2	Red Pixels Display Data	TOUT0-	No.12	INO-
TIN3	55	R3	Red Pixels Display Data	TOUT0+	No.11	IN0+
TIN4	56	R4	Red Pixels Display Data			
TIN6	3	R5	Red Pixels Display Data (MSB)			
TIN7	4	G0	Green Pixels Display Data(LSB)			
TIN8	6	G1	Green Pixels Display Data			
TIN9	7	G2	Green Pixels Display Data			
TIN12	11	G3	Green Pixels Display Data	TOUT1-	No.10	IN1-
TIN13	12	G4	Green Pixels Display Data	TOUT1+	No.9	IN1+
TIN14	14	G5	Green Pixels Display Data(MSB)			
TIN15	15	B0	Blue Pixels Display Data (LSB)			
TIN18	19	B1	Blue Pixels Display Data			
TIN19	20	B2	Blue Pixels Display Data			
TIN20	22	В3	Blue Pixels Display Data			
TIN21	23	B4	Blue Pixels Display Data	TOUT2-	No.8	IN2-
TIN22	24	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.7	IN2+
TIN24	27	NC	Non Connection (open)			
TIN25	28	NC	Non Connection (open)			
TIN26	30	ENAB	Compound Synchronization Signal			
TIN27	50	NC	Non Connection (open)			
TIN5	2	NC	Non Connection (open)			
TIN10	8	NC	Non Connection (open)	TOUT3-		
TIN11	10	NC	Non Connection (open)	TOUT3+		
TIN16	16	NC	Non Connection (open)			
TIN17	18	NC	Non Connection (open)			
TIN23	25	NC	Non Connection (open)			
CLK IN	31	NCLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.6 No.5	CLK IN- CLK IN+



256k (k=1024) COLORS COMBINATION TABLE

				Gray Scale
	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0	Level
	Black	LLLLL		-
	Blue			-
	Green	LLLLL	H H H H H L L L L L L	-
Basic	Light Blue	LLLLL	H H H H H H H H H H H	-
Color	Red	H H H H H		-
	Purple	H H H H H	LLLLLHHHHHH	-
	Yellow	ннннн		-
	White	H H H H H		-
	Black			L 0
		LLLLH		L 1
Gray	Dark	LLLLHL		L 2
Scale of	↑	:	: : :	L3
Red	\downarrow	:	: :	L60
	Light	H H H L H		L61
		HHHHHL		L62
	Red	H H H H H H		Red L63
	Black	LLLLL		L 0
				L 1
Gray	Dark	LLLLL		L 2
Scale of	1	:	: :	L3
Green	\downarrow	:	: : :	L60
	Light		H H H H L H L L L L L L	L61
			H H H H H L L L L L L L	L62
	Green		H	Green L63
	Black			L 0
	Bidok			L 1
Gray	Dark			L 2
Scale of	1	:	: : :	L3
Blue	\downarrow	:		L60
	Light	1 1 1 1 1		
				L61 L62
	Blue			
	Black			Blue L63 L 0
Gray	DIACK			L 1
Scale of	Dark			L 1
White &	†			L3
Black	<u> </u>	:		L3 L60
	Light	•	·	
	Ligit	H H H H L H	H H H H L H H H H H L H	L61
		HHHHL	H H H H H L H H H H L	L62
	White	H H H H H H		White L63



FOR SAFETY

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.

In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA LCD MODULES".

Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

1) SPECIAL PURPOSES

- A) Toshiba's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
- B) Since Toshiba's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba's published specification limits.
- C) In addition, since Toshiba Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba doses not warrant the module, if customer disassembled or modified it.

3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.