Toshiba Matsushita Display Technology Co., Ltd

LIQUID CRYSTAL DISPLAY DIVISION PRODUCT INFORMATION

31cm COLOUR TFT-LCD MODULE (12.1 TYPE)

LTM12C505D (p-Si TFT)

FEATURES

- (1) 12.1"XGA(1024x768 pixels) display size for notebook PC
- (2) Light weight design
- (3) LVDS interface system (H-Sync, V-Sync)



MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (typ.)	261.5Max(W) x 199.5Max (H) x 5.0Max(D) mm
Number of Pixels	1024(W) x 768(H) pixels
Active Area	245.76(W) x 184.32(H) mm
Pixel Pitch	0.24(W) x 0.24(H)
Weight (approximately)	275 g Max
Backlight	Single CCFL, Sidelight type

ABSOLUTE MAXIMUM RATINGS

Item		Min.	Max.	Unit
Supply Voltage	LCD	-0.3	4.0	٧
	Inverter	8.5	21.5	V
Input Voltage	LCD	-0.3	V _{DD} +0.3	٧
	Inverter	2.0/-1.0	5VSUS+1/1.0	V
Operating Ambie	ent Temperature	0	50	°C
Operating Ambie	ent Humidity	10	90	%(RH)
Storage Tempera	ature	-20	+60	°C
Storage Humidit	у	10	90	%(RH)
Operating Temp	erature for Panel	0	+60	°C

ELECTRICAL SPECIFICATION

Item		Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply Voltage 1)	LCD	V_{DD}	3.0	3.3	3.6	V	
	Inverter	V _{INV}	9.0	14.8	21.0	٧	
Differential Input \	/oltage ²⁾	V _{ID}	0.1	-	0.6	V	
Common Mode In	put Voltage 2)	V _{CM}	1.0	-	2.4 -1/2xV _{ID}	٧	
Backlight ON/OFF	Backlight ON/OFF voltage		2.0	-	5VSUS+1.0	٧	
		V _{OFF}	-1.0	-	0.8	٧	
5VSUS, 5VALW			4.85	5.0	5.25	٧	
Dimming Duty	Minimum	-	7	10	13	%	1 V _{INV} =9.0~21.0V
Cycle	Maximum	-	-	-	100		
Power LCD		P _{LCD}	-	-	(0.9)	W	Color bar
	Inverter	P _{INV}	-	-	(8.5)	W	
EEDID	EEDID			VESA S	рес		SPWG version2.0

OPTICAL SPECIFICATION (Ta=25°C)

Item		Min.	Тур.	Max.	Unit	Remarks
Contrast Ratio (CR)		(200)	350			
Response Time	(ton)			50	ms	
	(t _{OFF})			50	ms	
Luminance (L)		(140)	(165)		cd/m ²	SMBus (00H)

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^{*}The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display technology before proceeding with the design of equipment incorporating this product.

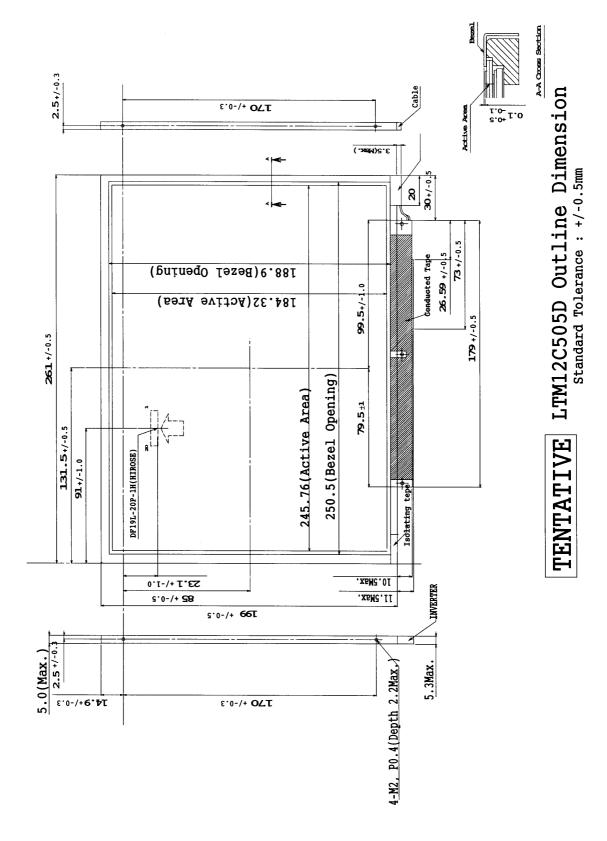
DIMENSIONAL OUTLINE

TENTATIVE

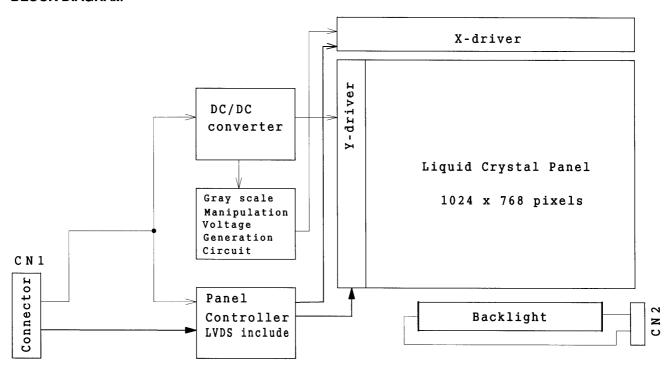
Unit: mm

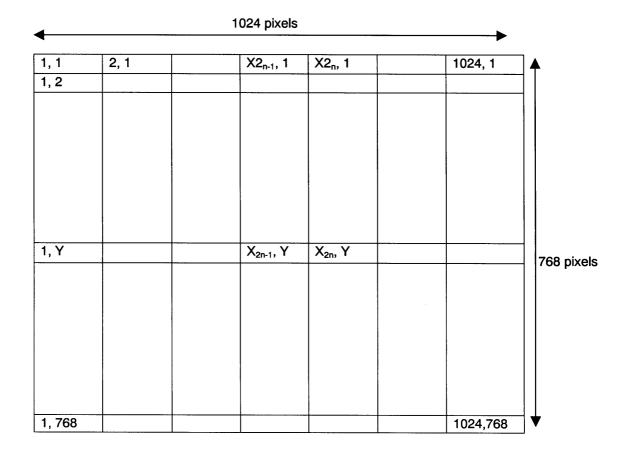
Standard tolerance: ±0.5

We accept Dell's required dimension of Pebble program.

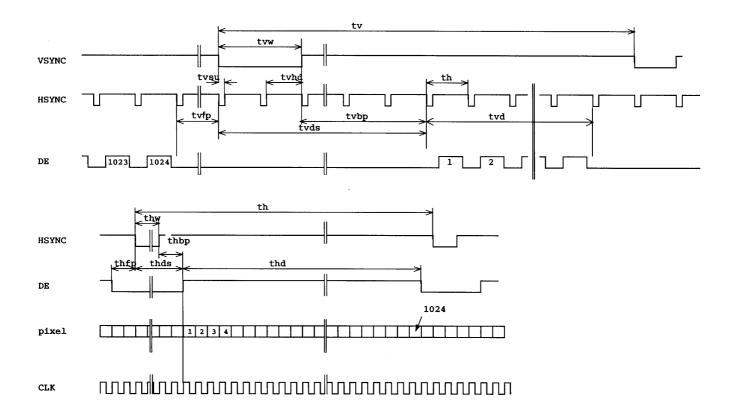


BLOCK DIAGRAM





TIMING CHART



TIMING SPECIFICATION 1) 2) 3) 4) 5) 6) 7)

Item	Symbol	min.	typ.	max.	unit
Horizontal Scanning Term	<i>t</i> h	1334 x tc	1344 x tc	-	clock
H-sync Pulse Width	<i>t</i> hw	4 x tc	136 x tc	•	clock
Horizontal Front Porch	<i>t</i> hfp	4 x tc	24 x tc	-	clock
Horizontal Back Porch	<i>t</i> hbp	24 x tc	160 x tc	-	clock
Horizontal Data Sync Period	<i>t</i> hds	32 x tc	296 x tc	-	clock
Horizontal Display Term	<i>t</i> hd	1024 x tc	1024 x tc	1024 x tc	clock
Frame Period	tv .	778 x th	806 x th	860 x th	line
V-sync Pulse Width	t/w	2 x th	6 x <i>t</i> h	-	line
V-sync Set Up Time (to H-sync)	<i>t</i> vsu	8 x tc	-	-	clock
V-sync Hold Time	<i>t</i> ∨hd	(thbp+16) x tc	•	-	clock
Vertical Front Porch	<i>t</i> √fp	1 x <i>t</i> h	3 x th	-	line
Vertical Back Porch	<i>t</i> √bp	2 x <i>t</i> h	29 x th	-	line
Vertical Data Sync Period	<i>t</i> vds	8 x th	35 x th	-	line
Vertical Display Term	<i>t</i> ∨d	768 x th	768 x th	768 x th	line
Clock Period	tc	15.0	15.38	-	ns

Note 1) Refer to "Timing Chart" and LVDS (THC63LVDF84A-85) specifications by THine Electronics, Inc. corporation.

Note 2) If DE is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.

Note 3) If NCLK is fixed to "H" or "L" level for certain period while DE is supplied, the panel may be damaged.

Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.

Note5) Do not make tv, tvhd and tvds fluctuate.

If tv, tvhd, and tvds are fluctuate, the panel displays black.

Note6) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note7) NCLK count of each Horizontal Scanning Time should be always the same.

V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer)

Frame period should be always the same.

CONNECTOR PIN ASSIGNMENT FOR INTERFACE

CN1 INPUT SIGNAL

. CN1 INPUT SIGNAL (DF19K-20P-1H / HIROSE)

[Mating Connector :Wire Type FI-X30H(Housing), FI-XC3-1-15000(Contact) FPC Type FI-X30M or FI-X30MR, Coax Type FI-X30C or FI-X30C2(Housing), FI-X30CH-7000(Shell)]

Terminal No.	Symbol	Function				
1	V _{SS}	GND				
2	$V_{ extsf{DD}}$	POWER SUPPLY : +3.3V				
3	$V_{ extsf{DD}}$	POWER SUPPLY : +3.3V				
4	$V_{ ext{EDID}}$	DDC 3.3V POWER SUPPLY: +3.3V				
5	NC	Non-Connection				
6	CLK _{EDID}	DDC Clock				
7	DATA _{EDID}	DDC Data				
8	RxIN0-	Negative LVDS differential data input, [R0-R5, G0]				
9	RxIN0+	Positive LVDS differential data input, [R0-R5, G0]				
10	V _{SS}	GND				
11	RxIN1-	Negative LVDS differential data input, [G1-G5, B0-B1]				
12	RxIN1+	Positive LVDS differential data input, [G1-G5, B0-B1]				
13	V _{SS}	GND				
14	RxIN2-	Negative LVDS differential data input, [B2-B5, HS, VS, DE]				
15	RxIN2+	Positive LVDS differential data input, [B2-B5, HS, VS, DE]				
16	V _{ss}	GND				
17	RxCLKIN-	Negative LVDS differential clock input				
18	RxCLKIN+	Positive LVDS differential clock input				
19	V _{ss}	GND				
20	V _{ss}	GND				

CN2 CCFL POWER SOURCE

CN2 INVERTER INPUT SIGNAL (LVC-D20SFYG / HONDA)

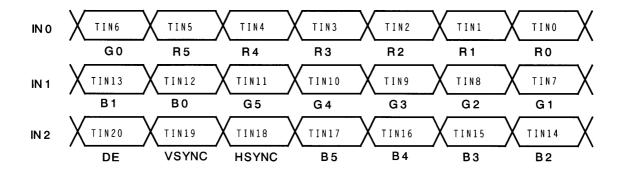
Terminal No.	Symbol	Function
1	V _{IN}	Supply Voltage: 9V~21V
2	V _{IN}	Supply Voltage: 9V~21V
3	V _{IN}	Supply Voltage: 9V~21V
4	NC	Non connection
5	GND	GND
6	5VSUS	Power Source for regulator : 4.85V~5.2V
7	5VALW	Power Source for SMBus : 4.85V~5.2V
8	GND	GND
9	SMB_DATA	SMBus interface for sending brightness information (Data pin)
10	SMB_CLK	SMBus interface for sending brightness information (Clock pin)
11	GND	GND
12	V _{ON/OFF}	Backlight ON/OFF control signal (1-ON, 0-OFF)
13	GND	GND
14	LAMP_STAT	Lamp Status, ON/OFF from control chip
15	NC	Non connection
16	NC	Non connection
17	NC	Non connection
18	NC	Non connection
19	NC	Non connection
20	NC	Non connection

RECOMMENDED TRANSMITTER (THC63LVDF63A,THC63LVDM63A,THC63LVDM63A-85) TO LTM12C505S INTERFACE ASSIGNMENT

Case1: 6bit Transmitter

	LTM12C505S						
Input Te	rminal No.		Input Signal	Output Signal	Interface		
			(Graphics controller output signal)	(CN1)			
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol	
TIN0	44	R0	Red Pixels Display Data (LSB)				
TIN1	45	R1	Red Pixels Display Data				
TIN2	47	R2	Red Pixels Display Data	TOUT0-	No.5	INO-	
TIN3	48	R3	Red Pixels Display Data	TOUT0+	No.6	IN0+	
TIN4	1	R4	Red Pixels Display Data				
TIN5	3	R5	Red Pixels Display Data (MSB)				
TIN6	4	G0	Green Pixels Display Data (LSB)				
TIN7	6	G1	Green Pixels Display Data				
TIN8	7	G2	Green Pixels Display Data				
TIN9	9	G3	Green Pixels Display Data	TOUT1-	No.8	IN1-	
TIN10	10	G4	Green Pixels Display Data	TOUT1+	No.9	IN1+	
TIN11	12	G5	Green Pixels Display Data (MSB)				
TIN12	13	B0	Blue Pixels Display Data (LSB)		L		
TIN13	15	B1	Blue Pixels Display Data				
TIN14	16	B2	Blue Pixels Display Data				
TIN15	18	B3	Blue Pixels Display Data		Į.		
TIN16	19	B4	Blue Pixels Display Data	TOUT2-	No.11	IN2-	
TIN17	20	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.12	IN2+	
TIN18	22	HSYNC	H-Sync				
TIN19	23	VSYNC	V-Sync				
TIN20	25	DE	Compound Synchronization Signal				
CLK IN	26	CLK	Data Sampling Clock	TCLK OUT-	No.14	CLK-	
				TCLK OUT+	No.15	CLK+	

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.

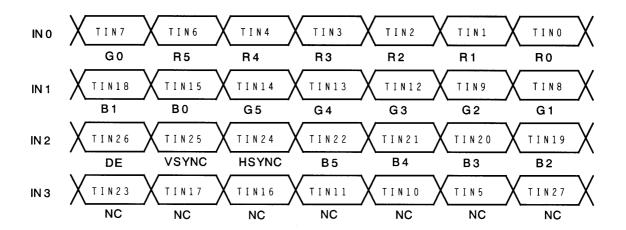


RECOMMENDED TRANSMITTER (THC63LVDF63A,THC63LVDM63A,THC63LVDM63A-85) TO LTM12C505S INTERFACE ASSIGNMENT

Case2: 8bit Transmitter

	LTM12C505S					
Input Te	erminal No.	Output		rface		
			(Graphics controller output signal)	Signal	(C	N1)
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol
TIN0	51	R0	Red Pixels Display Data (LSB)			
TIN1	52	R1	Red Pixels Display Data	1		
TIN2	54	R2	Red Pixels Display Data	TOUT0-	No.5	INO-
TIN3	55	R3	Red Pixels Display Data	TOUT0+	No.6	INO+
TIN4	56	R4	Red Pixels Display Data	1		
TIN6	3	R5	Red Pixels Display Data (MSB)			
TIN7	4	G0	Green Pixels Display Data(LSB)			
TIN8	6	G1	Green Pixels Display Data			
TIN9	7	G2	Green Pixels Display Data			
TIN12	11	G3	Green Pixels Display Data	TOUT1-	No.8	IN1-
TIN13	12	G4	Green Pixels Display Data	TOUT1+	No.9	IN1+
TIN14	14	G5	Green Pixels Display Data(MSB)			
TIN15	15	B0	Blue Pixels Display Data (LSB)			
TIN18	19	B1	Blue Pixels Display Data			
TIN19	20	B2	Blue Pixels Display Data			
TIN20	22	B3	Blue Pixels Display Data			
TIN21	23	B4	Blue Pixels Display Data	TOUT2-	No.11	IN2-
TIN22	24	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.12	IN2+
TIN24	27	HSYNC	H-Sync			
TIN25	28	VSYNC	V-Sync			
TIN26	30	DE	Compound Synchronization Signal			ł
TIN27	50	NC	Non Connection (open)			
TIN5	2	NC	Non Connection (open)	1		
TIN10	8	NC	Non Connection (open)	TOUT3-		
TIN11	10	NC	Non Connection (open)	TOUT3+		
TIN16	16	NC	Non Connection (open)	1		
TIN17	18	NC	Non Connection (open)	1		
TIN23	25	NC	Non Connection (open)	1		
CLK IN	31	CLK	Data Sampling Clock	TCLK OUT-	No.14	CLK-
				TCLK OUT+	No.15	CLK+

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.



256k (k=1024) COLORS COMBINATION TABLE

																					Gray Scale
	Display	R!	5 F	₹4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	В5	B4	В3	B2	В1	В0	Level
	Black		_	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue		L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	-
	Green	П	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	-
Basic	Light Blue	T	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
Color	Red	1	Ŧ	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple		1	Н	Н	Н	Н	Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	-
	Yellow	H	1	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	-
	White	1	1	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
	Black		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L 0
_			L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L 1
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	Black	+	L	<u> </u>	<u> </u>	<u> </u>	<u>L</u>	<u>L</u>	L	<u> </u>	L	<u> </u>	<u>L</u>	L	H	<u>H</u>	<u>H</u>	<u>H</u>	<u>H</u>	Н	Blue L63
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		_	H	Н	Н	Н	Н	L	Н	Н	H	H	Н	L	Н	Н	Н	Н	Н	L	L62
	White		<u>H</u>	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	White L63



FOR SAFETY

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.

In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY LCD MODULES".

Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

1) SPECIAL PURPOSES

- A) Toshiba Matsushita Display technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
- B) Since Toshiba Matsushita Display technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display technology's published specification limits.
- C) In addition, since Toshiba Matsushita Display technology Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba Matsushita Display technology doses not warrant the module, if customer disassembled or modified it.

3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.