# Toshiba Matsushita Display Technology Co., Ltd

21cm COLOR TFT-LCD MODULE (8.4 TYPE)

LT084AC37000 (p-Si TFT)

## PRODUCT INFORMATION

# All information is subject to change without notice. Please read bottom notes.

**FEATURES:** (1)8.4" XGA color display with High Brightness (400cd/m<sup>2</sup>).

(2) Wide viewing angle and wide operating temperature.

(3)Built in Long Life LEDs (MTBF:70,000 h).

(Conditions / Ta:25°C, PWM=100%(continuing lighting))

(4)Replaceable structure of LED units.

(5) Mounting compatible with LTM08C351 series and LTA084C270F.



RoHS compatible

## **MECHANICAL SPECIFICATIONS**

Item	Specifications
Dimensional Outline (Typ.)	199.5 (W) x 149.5 (H) x 12.0max (D) mm
Number of Pixels	1024 (W) x 768 (H) pixels
Active Area	170.496 (W) x 127.872 (H) mm
Pixel Pitch	0.1665 (W) x 0.1665 (H)
Weight (approximately)	(TBD)
Backlight	Sidelight ( LEDs )

### **ABSOLUTE MAXIMUM RATINGS**

Item		Min.	Max.	Unit
Supply Voltage	Supply Voltage $(V_{DD})$		4.0	V
	(V <sub>LED</sub> )	0	20	V
Input Signal Voltage	(V <sub>IN</sub> )	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature		-20	70	°C
Storage Temperature		-30	80	°C
Storage Humidity (Max. wet bulb temp. = 39°C)		10	90	%(RH)

# ELECTRICAL SPECIFICATION (Ta=25°C) (RECOMMENDED OPERATION CONDITION)

Item	Min.	Тур.	Max.	Unit	Remarks	
Supply Voltage	$(V_{DD})$	3.0	3.3	3.6	V	
	(V <sub>LED</sub> )	10.8	12.0	13.2	V	
Differential Input Voltage	$(V_{ID})$	0.25		0.45	V	
Common Mode Input Voltage	$(V_{CM})$	1.0	1.25	2.0	V	
High Level Input Voltage	(V <sub>IH</sub> )	2.2		$V_{DD}$	V	U/D, L/R
Low Level Input Voltage	$(V_{IL})$	0		0.7	V	U/D, L/R
Backlight ON/OFF signal	(V <sub>ONOFF</sub> )	2.0		$V_{DD}$	V	ON
		0		0.8	V	OFF
Luminance control signal	$(V_{PWM})$	0		3.3	V	
Current Consumption	$(I_{\rm DD})^{*1}$		TBD		mA	
	(I <sub>LED</sub> )		(335)		mA	PWM=100%
Power Consumption*1			TBD		W	PWM=100%

<sup>\*1:8</sup> color bars pattern

### **OPTICAL SPECIFICATION** (*T*a=25°C)

Iter	Min.	Тур.	Max.	Unit	Remarks	
Contrast Ratio	(CR)	(250)	(400)			
Viewing Angle	(Upper+Lower)		100		0	
( <i>CR</i> ≥ 10)	(Left+Right)		120		0	
Response Time	$(t_{ON})$		8		ms	
	(t <sub>OFF</sub> )		17		ms	
Luminance	(L)	(280)	(400)		cd/m <sup>2</sup>	PWM=100%
Lamp Life Time (MTBI		70,000		h	PWM=100%	

<sup>\*2 :</sup> Conditions ; Ta=25°C, continuous lighting

<sup>\*3 :</sup> Definitions of failure ; 1) Lcd luminance becomes half of the minimum value. 2) LED doesn't light normally.

<sup>\*</sup>The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba Matsushita Display Technology or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba Matsushita Display Technology or others.

<sup>\*</sup>The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display Technology before proceeding with the design of equipment incorporating this product.

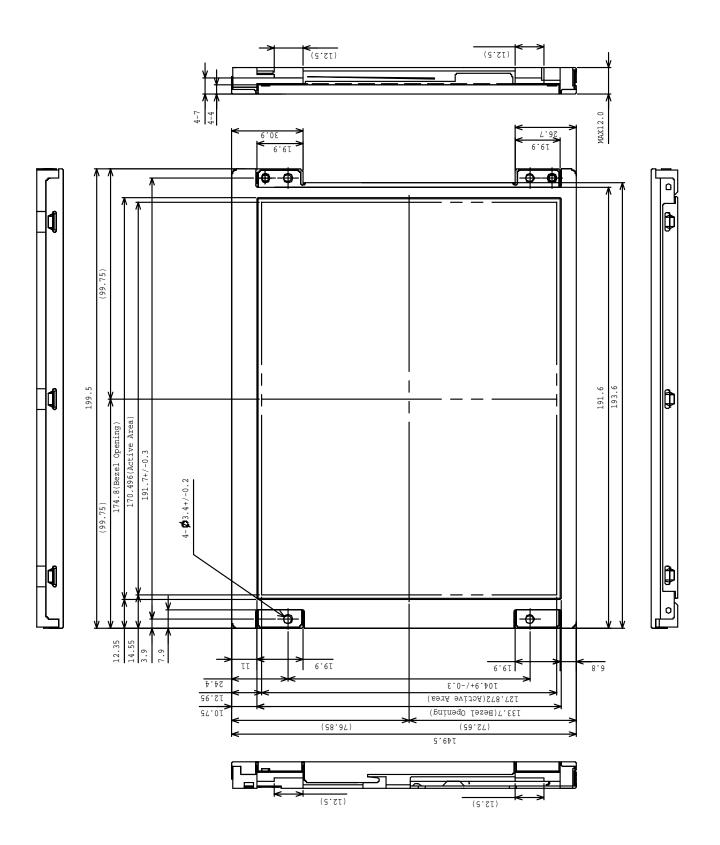
# **DIMENSIONAL OUTLINE**

**TENTATIVE** 

Unit: mm

Standard tolerance: +/-0.5

<Front side>

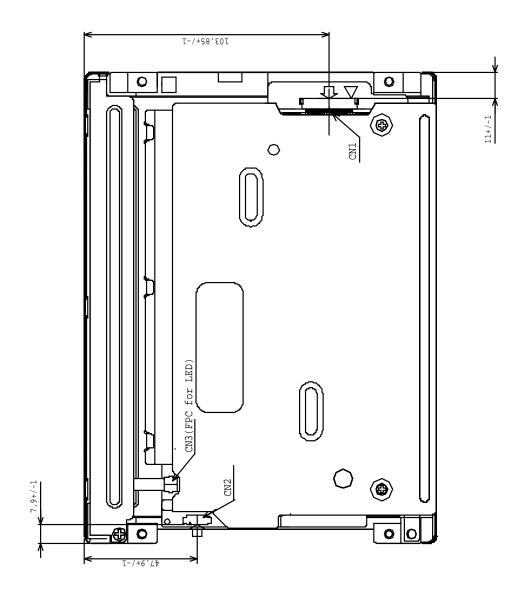


<Rear side>

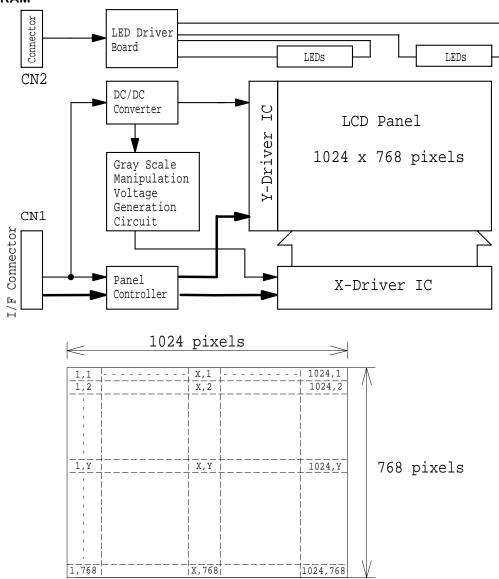
# **TENTATIVE**

Unit: mm

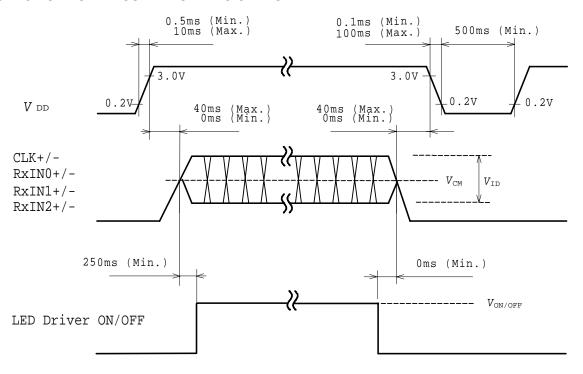
Standard tolerance: +/-0.5



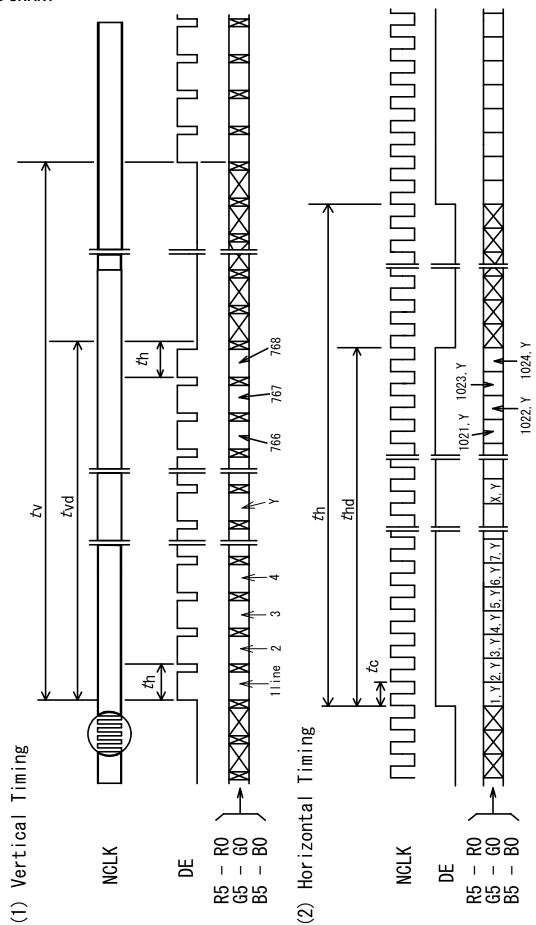
### **BLOCK DIAGRAM**



# **SEQUENCE OF POWER SUPPLIES AND SIGNALS**



# **TIMING CHART**



# TIMING SPECIFICATION \*1 \*2 \*3 \*4 \*5 \*6

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Frame Period	tv	778 x th	806 x th	860 x th		
			16.66	17.86	ms	
Vertical	<i>t</i> vd	768 x th	768 x th	768 x th		
Display Term						
One Line	<i>t</i> h	1334 x tc	1344 x tc	1462 x tc		
Scanning Time		20.52	20.68		μS	
Horizontal	<i>t</i> hd	1024 x tc	1024 x tc	1025 x tc		
Display Term						
Clock Period	tc	15.0	15.384		ns	

- Note 1) Refer to "TIMING CHART" and LVDS (THC63LVDF84A-85) specifications by THine Electronics,Inc.
- Note 2) If ENAB is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.
- Note 3) Don't fix NCLK to "H" or "L" level while the VDD is supplied.

  If NCLK is fixed to "H" or "L" level for certain period while ENAB is spplied, the panel may be damaged.

  When It holds on, DC voltage supplies to liquid crystal materials and It may cause damage to liquid crystal
- Note 4) Please adjust LCD operating signal timing and LED PWM frequency, to optimize the display quality.

  There is a possibility that flicker is observed by the interference of LCD operating signal timing and PWM condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown on page 1.
- Note 5) Do not make t1, t2 and t3 fluctuate.

  If t1, t2 and t3 are fluctuate, the panel displays black.
- Note 6) Keep constant the number of clock within one line scanning time and the number of scanning line within one flame period.

### **CONNECTOR PIN ASSIGNMENT FOR INTERFACE**

## CN1 INPUT SIGNAL

Connector: 20268-020E-03F / I-PEX CO., LTD.

Mating Connector: 20230-020B-F or 20230-T20-F or 20230-W20B-F / I-PEX CO.,LTD.

DF19G-20S-1C(Plug), DF19A-2830SCFA(Crimp contact) / HIROSE ELECTRIC CO., LTD.

Terminal No.	Symbol	Function
1	$V_{DD}$	+3.3V POWER SUPPLY
2	$V_{DD}$	+3.3V POWER SUPPLY
3	GND*1	
4	GND*1	
5	RxIN0-	Negative LVDS differential data input (R0-R5, G0)*3
6	RxIN0+	Positive LVDS differential data input (R0-R5, G0) *3
7	GND*1	
8	RxIN1-	Negative LVDS differential data input (G1-G5, B0-B1) *3
9	RxIN1+	Positive LVDS differential data input (G1-G5, B0-B1) *3
10	GND*1	
11	RxIN2-	Negative LVDS differential data input (B2-B5, NC, NC, DE) *3
12	RxIN2+	Positive LVDS differential data input (B2-B5, NC, NC, DE) *3
13	GND*1	
14	CLK-	Clock Signal (-)
15	CLK+	Clock Signal (+)
16		DDC clock
17	U/D	Vertical Reverse("L" level or Open : Normal, "H" level : Reverse)
18	L/R	Horizontal Reverse("L" level or Open : Normal, "H" level : Reverse)
19	NC* <sup>2</sup>	Non Connection (open)
20	DATA <sub>EDID</sub>	DDC data

Note 1) Please connect GND pin to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.

Note 3) Refer to next page.

## CN2 LED INPUT SIGNAL

Connector: 53261-0671 / MOLEX Inc.

Mating Connector: 51021-0600 / MOLEX Inc.

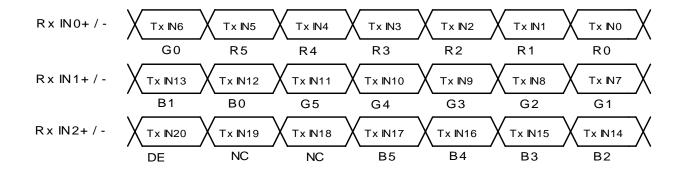
Terminal No.	Symbol	Function
1	VLED	Power Supply: +12V
2	VLED	Power Supply: +12V
3	VSS	GND
4	VSS	GND
5	ON/OFF	"H(+3.3V)":ON, "L(GND or Open)":OFF
6	PWM	1-100%(200Hz)

# RECOMMENDED TRANSMITTER (THC63LVDF63A, THC63LVDM63A by Thine Electronics, Inc.) TO LT084AC37000 INTERFACE ASSIGNMENT

# Case1: 6bit Transmitter

THC63LVDF63A, THC63LVDM63A					LT084A	AC37000
Input Terminal No.			Input Signal	Output Signal	Interface	
input ten	minai No.		(Graphics controller output signal)	Symbol	(CN1)	
Symbol	Terminal	Symbol	Function	Gymbol	Terminal	Symbol
TA0	44	R0	Red Pixels Display Data (LSB)			
TA1	45	R1	Red Pixels Display Data		No.5 No.6	
TA2	47	R2	Red Pixels Display Data	TA-		RxIN0-
TA3	48	R3	Red Pixels Display Data	TA+		RxIN0+
TA4	1	R4	Red Pixels Display Data	IAT		IXXIINUT
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data (LSB)			
TB0	6	G1	Green Pixels Display Data		No.8 No.9	RxIN1- RxIN1+
TB1	7	G2	Green Pixels Display Data			
TB2	9	G3	Green Pixels Display Data	ТВ-		
TB3	10	G4	Green Pixels Display Data	TB+		
TB4	12	G5	Green Pixels Display Data (MSB)	TDT	140.9	IXIINIT
TB5	13	B0	Blue Pixels Display Data (LSB)			
TB6	15	B1	Blue Pixels Display Data			
TC0	16	B2	Blue Pixels Display Data			
TC1	18	В3	Blue Pixels Display Data			
TC2	19	B4	Blue Pixels Display Data	ТС-	No.11	RxIN2-
TC3	20	B5	Blue Pixels Display Data (MSB)	TC+	No.11	RXIN2+
TC4	22	NC*1	Non Connection (open)	10+	110.12	IXIINZT
TC5	23	NC*1	Non Connection (open)			
TC6	25	DE	Compound Synchronization Signal			
CLK IN	26	NCLK	Data Sampling Clock	TCLK- TCLK+	No.14 No.15	CLK- CLK+

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.

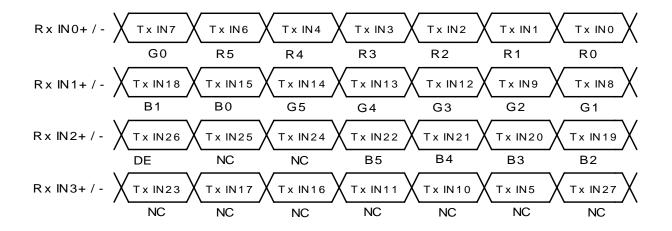


# RECOMMENDED TRANSMITTER (THC63LVDF83A, THC63LVDM83A, THC63LVDM83A-855 by Thine Electronics, Inc.) TO LT084AC37000 INTERFACE ASSIGNMENT

# Case2: 8bit Transmitter

	THC63LVDF83A, THC63LVDM83A, THC63LVDM83A-855					
Input Tor	minal Na		Input Signal	Output Signal	Inte	rface
Input Terminal No.			(Graphics controller output signal)	Symbol	(CN1)	
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol
TA0	51	R0	Red Pixels Display Data (LSB)			
TA1	52	R1	Red Pixels Display Data			RxIN0-
TA2	54	R2	Red Pixels Display Data	TA-	No.5	
TA3	55	R3	Red Pixels Display Data	TA+	No.6	RxIN0+
TA4	56	R4	Red Pixels Display Data	1/17	140.0	IXAIINOT
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data(LSB)	1		
TB0	6	G1	Green Pixels Display Data			
TB1	7	G2	Green Pixels Display Data	1		
TB2	11	G3	Green Pixels Display Data	TB- TB+	No.8 No.9	RxIN1- RxIN1+
TB3	12	G4	Green Pixels Display Data			
TB4	14	G5	Green Pixels Display Data(MSB)	10+		KXIIN I +
TB5	15	B0	Blue Pixels Display Data (LSB)			
TB6	19	B1	Blue Pixels Display Data			
TC0	20	B2	Blue Pixels Display Data			
TC1	22	В3	Blue Pixels Display Data			
TC2	23	B4	Blue Pixels Display Data	TC-	No.11	RxIN2-
TC3	24	B5	Blue Pixels Display Data (MSB)	TC+	No.11 No.12	RxIN2+
TC4	27	NC*1	Non Connection (open)	10+	110.12	IXIINZT
TC5	28	NC*1	Non Connection (open)			
TC6	30	DE	Compound Synchronization Signal			
TD0	50	NC*1	Non Connection (open)			
TD1	2	NC*1	Non Connection (open)			
TD2	8	NC*1	Non Connection (open)	TD-		
TD3	10	NC*1	Non Connection (open)	TD+		
TD4	16	NC*1	Non Connection (open)			
TD5	18	NC*1	Non Connection (open)			
TD6	25	NC*1	Non Connection (open)			
CLK IN	31	NCLK	Data Sampling Clock	TCLK- TCLK+	No.14 No.15	CLK- CLK+
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Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.



# 256k (k=1024) COLORS COMBINATION TABLE

	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0	B5 B4 B3 B2 B1 B0	Gray Scale Level
	Black	LLLLL	LLLLL	LLLLL	
	Blue	LLLLL	LLLLL	H $H$ $H$ $H$ $H$	
	Green	LLLLL	нннннн	LLLLL	
Basic	Light Blue	LLLLL	нннннн	H $H$ $H$ $H$ $H$	
Color	Red	нннннн	LLLLL	LLLLL	
	Purple	нннннн	LLLLL	H $H$ $H$ $H$ $H$	
	Yellow	H $H$ $H$ $H$ $H$	H $H$ $H$ $H$ $H$		
	White	H $H$ $H$ $H$ $H$	H $H$ $H$ $H$ $H$	H $H$ $H$ $H$ $H$	
	Black	LLLLL	LLLLL	LLLLL	L 0
		LLLLLH	LLLLL	LLLLL	L1
0	Dark	LLLLHL	LLLLL	LLLLL	L2
Gray	<b>↑</b>	:	:	:	L3
Scale of Red	$\downarrow$	:	:	:	L60
Reu	Light	H $H$ $H$ $L$ $H$	LLLLL	LLLLL	L61
	-	HHHHL			L62
	Red	ннннн			Red L63
	Black	LLLLL	LLLLL	LLLLL	LO
		LLLLL	LLLLH	LLLLL	L1
	Dark	LLLLL	LLLLHL	LLLLL	L 2
Gray	↑	:	:	:	L3
Scale of	$\downarrow$	:	:	:	L60
Green	Light		H $H$ $H$ $L$ $H$		L61
			H H H H H L		L62
	Green		H H H H H H		Green L63
	Black				L 0
-	Didok				L1
	Dark				L2
Gray	Daik ↑				L3
Scale of	<u> </u>	:	:	:	L60
Blue	↓ Light				
	Ligiti			H H H H L H	L61
	Divis			H H H H H L	L62
	Blue			H H H H H H	Blue L63
	Black				L 0
					L1
Gray	Dark		L L L L H L	L L L L H L	L2
Scale of	<u> </u>				L3
White &	<b>↓</b>	•	•	•	L60
Black	Light	H $H$ $H$ $H$ $L$ $H$	H $H$ $H$ $L$ $H$	H $H$ $H$ $L$ $H$	L61
		H $H$ $H$ $H$ $L$	H $H$ $H$ $H$ $L$	H $H$ $H$ $H$ $L$	L62
	White	нннннн	H H H H H H	H H H H H H	White L63



## **FOR SAFETY**

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.

In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA LCD MODULES".

Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

### 1) SPECIAL PURPOSES

- A) Toshiba Matsushita Display Technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
- B) Since Toshiba Matsushita Display Technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display Technology's published specification limits.
- C) In addition, since Toshiba Matsushita Display Technology Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

#### 2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba Matsushita Display Technology doses not warrant the module, if customer disassembled or modified it.

#### 3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

### 4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

### 5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

### 6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

### 7) RECOMMENDED OPERATION CONDITIONS

The performance and quality of the LCD panel are warranted only when the LCD panel is used within "the recommended operation conditions". Toshiba Matsushita Display Technology Co., Ltd. never warrants the performance and quality of the LCD panel when you use the LCD panel over "the recommended operation conditions", although within "the absolute maximum rating".

To use the LCD panel over "the recommended operation conditions" may have bad influence on the characteristics and reliability of the LCD panel and may shorten the life of the LCD panel.

Therefore, when designing the whole set, not to be over "the recommended operation conditions", you should fully take care of supply voltage change, characteristic of connection parts, serge of input-and-output line, and surrounding temperature.

### 8) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.