Specification of FUJITSU TFT-LCD module

FLC51UXC8V-11L

| | Approval | |
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| Date : | | |
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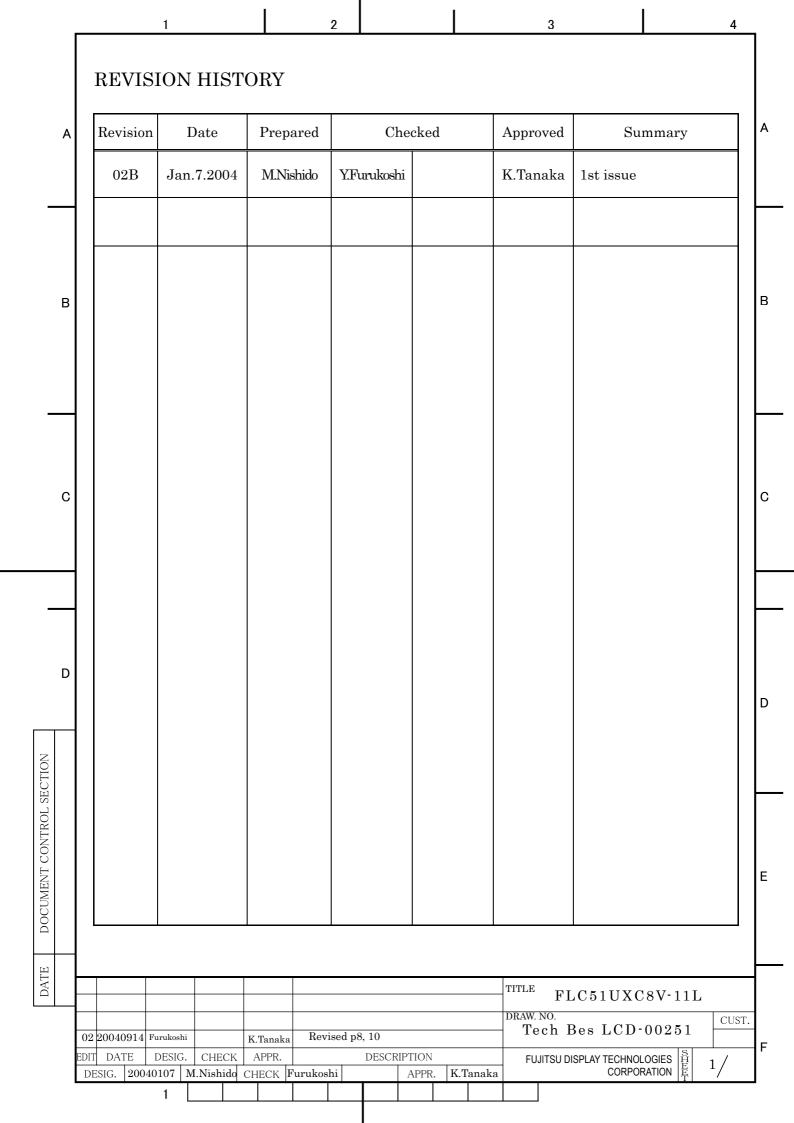
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Specification No.: Tech Bes LCD-00251

Issue Date : September 14, 2004

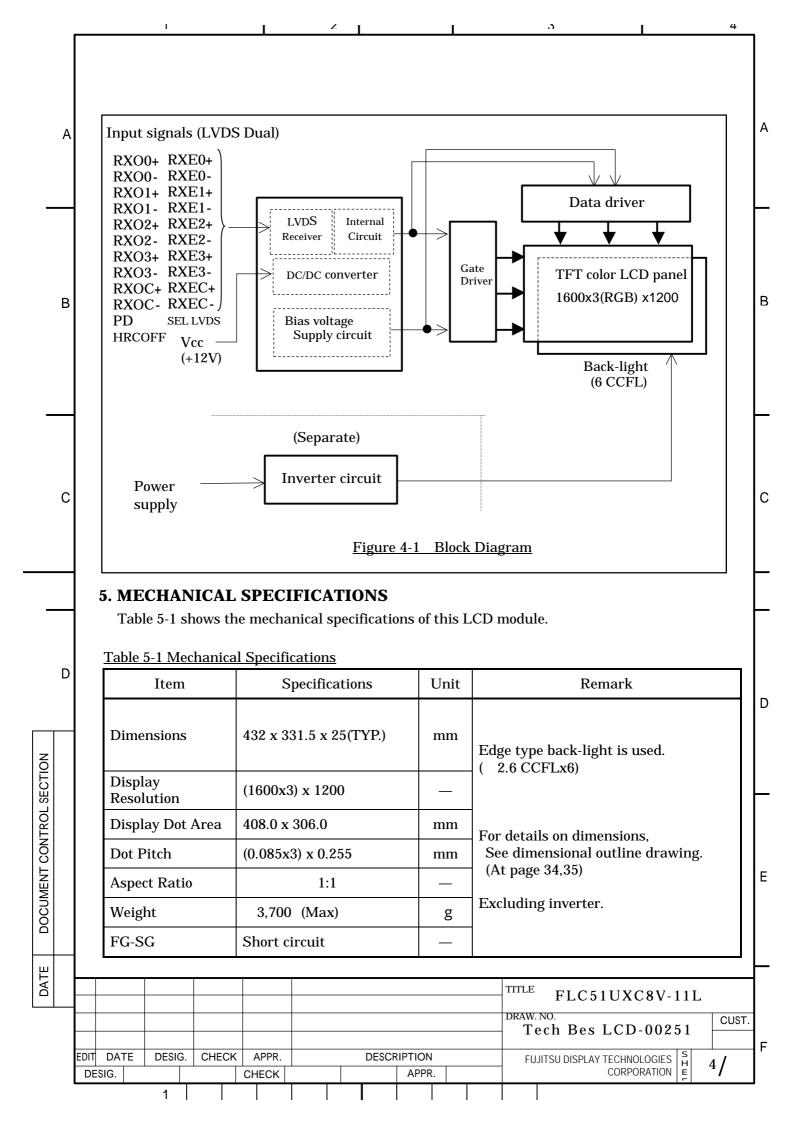
Issued by: Makener Maraka

Katsunori. Tanaka Project Director LCD Products Div.



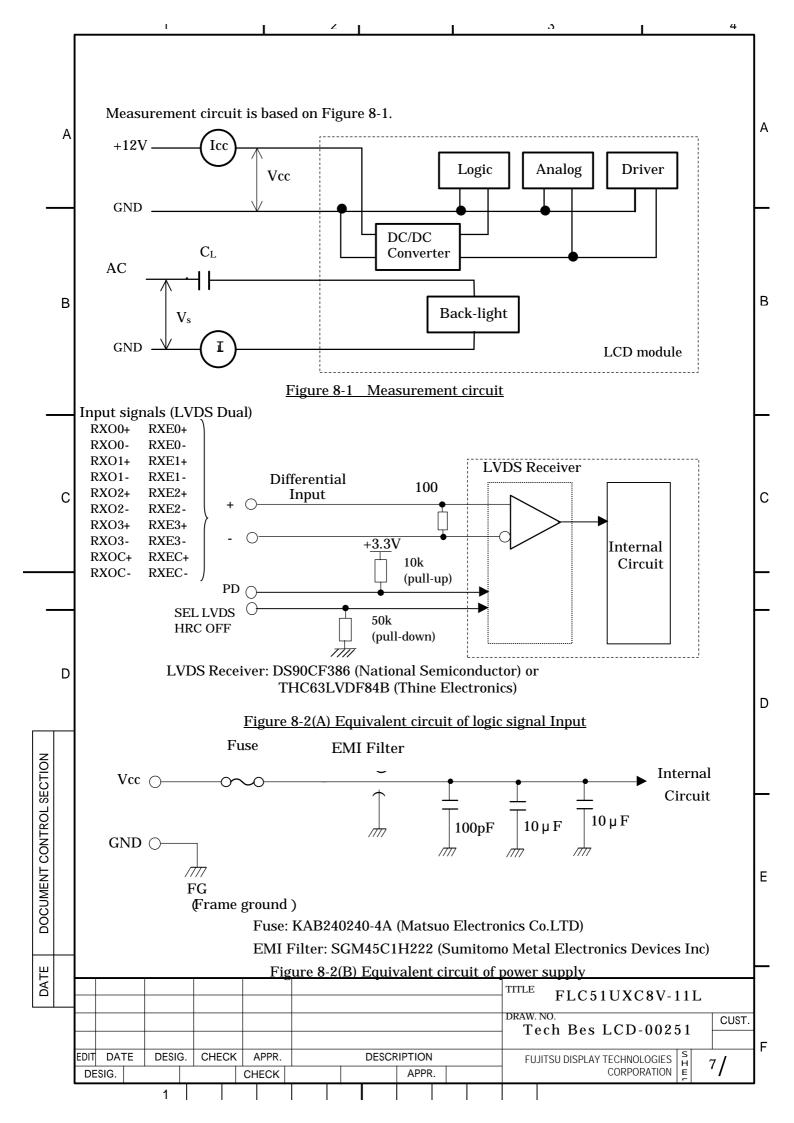
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1. APPLICATION This specification is applied to the 20.1-inch UXGA supported TFT-LCD module. 2. PRODUCT NAME AND MODEL NUMBER <u>2-1 Product Name</u>: **LCD Module** 2-2 Model Name: FLC51UXC8V-11L В В 3. OVERVIEW This LCD module has a TFT active matrix type liquid crystal panel 1600x1200 pixels, and diagonal size of 51cm(20.1-inch). This LCD has a LVDS dual interface and can display 16,777,216 colors. The power supply of this LCD module is +12V DC single. This module has the characteristics for applying TCO'99. This module has equivalent to 100% EBU color filter. C This module has high response time circuit. 4. CONFIGURATION This LCD module consists of a color TFT-LCD panel that is mounted with TFT driver ICs, a cold-cathode fluorescent tube back-light. D The inverter for the backlight is not included. Figure 4-1 shows a block diagram of this LCD module. DOCUMENT CONTROL SECTION Ε DATE TITLE FLC51UXC8V-11L CUST. Tech Bes LCD-00251 CHECK DESCRIPTION EDIT DATE DESIG. APPR. FUJITSU DISPLAY TECHNOLOGIES 3/ CORPORATION DESIG. **CHECK**

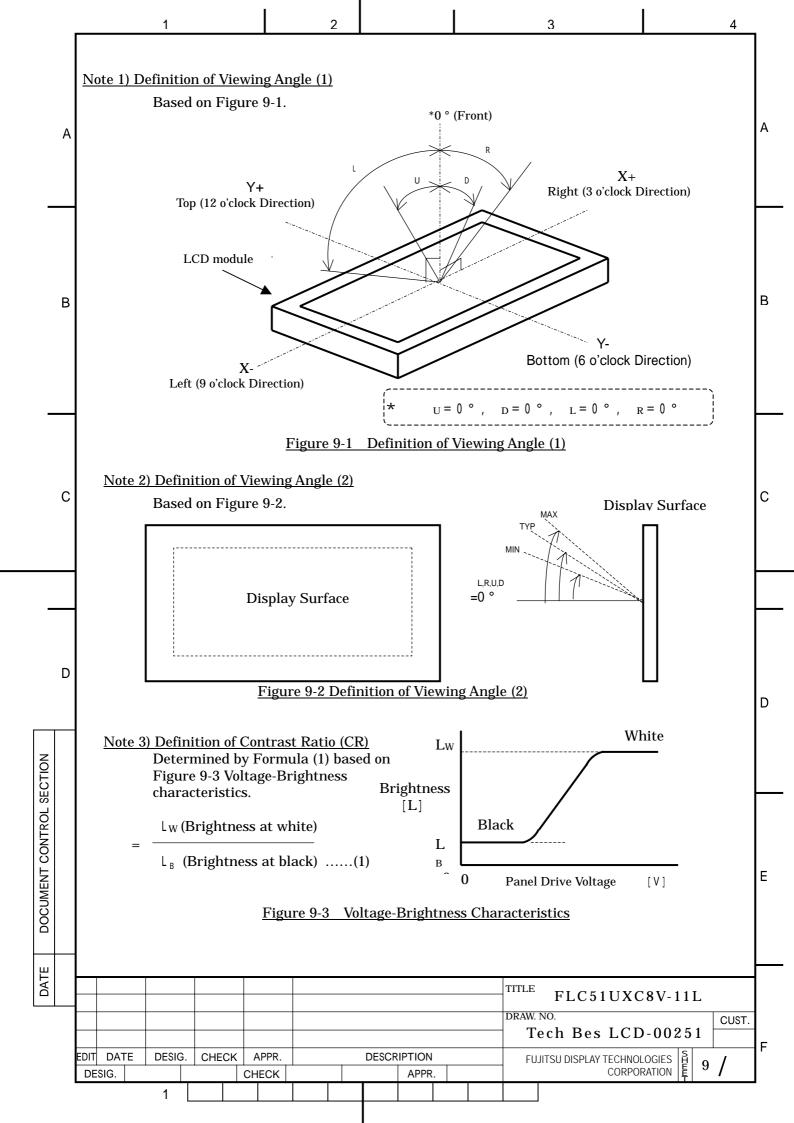


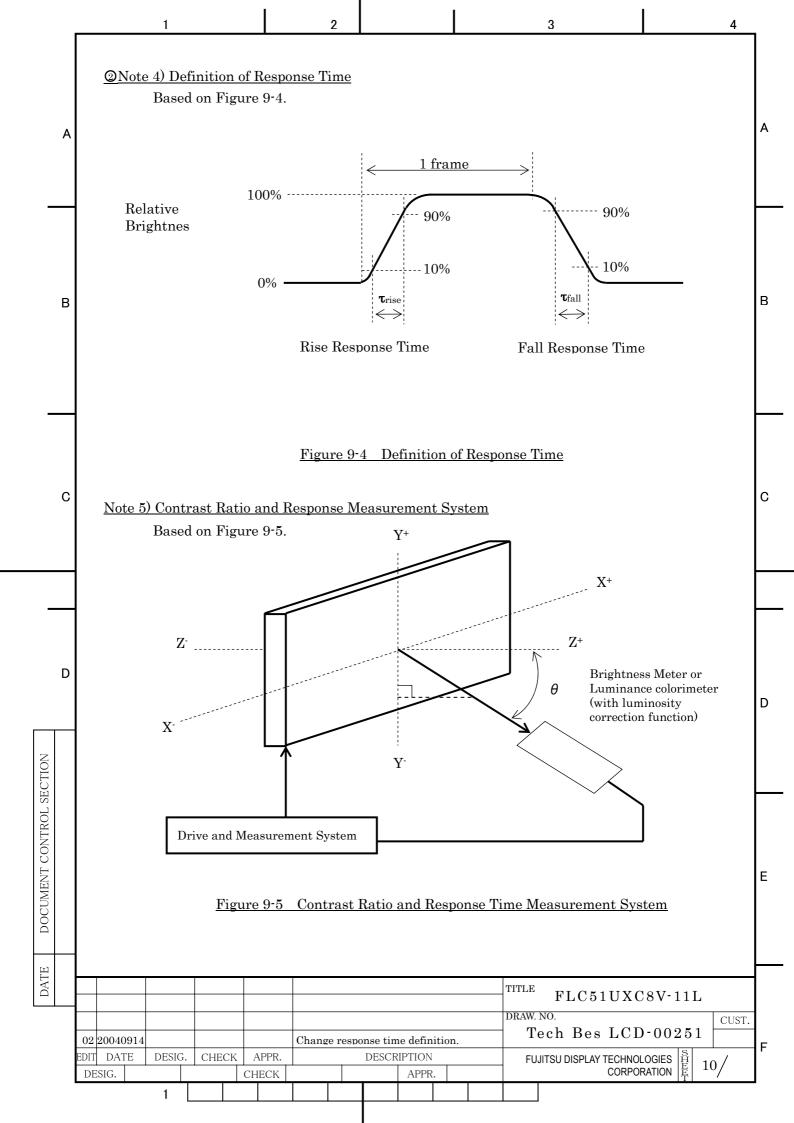
6. ABSOLUTE MAXIMUM RATING Table 6-1 shows the absolute maximum rating of this LCD module. Table 6-1 Absolute Maximum Rating Symbol Condition MIN. TYP. MAX. Unit Item Supply Voltage V_{CC} Ta=25°C -0.314.0 V В В Input Signal Voltage (LVDS signal, PD, V_{IN} Ta=25°C V -0.33.6 SEL LVDS, HRCOFF) 7. RECOMMENDED OPERATING CONDITIONS Table 7-1 shows the recommended operating conditions of this LCD module. С Table 7-1 Recommended Operating Conditions Item Symbol MIN. TYP. MAX. Unit Supply Voltage (Logic) 12.0 12.5 V V_{CC} 11.5 Ripple Voltage 0.1 V V_{CC} V_{RP} D D DOCUMENT CONTROL SECTION Ε DATE TITLE FLC51UXC8V-11L CUST. Tech Bes LCD-00251 F EDIT DATE DESIG. CHECK APPR. DESCRIPTION 5/ DESIG. CHECK

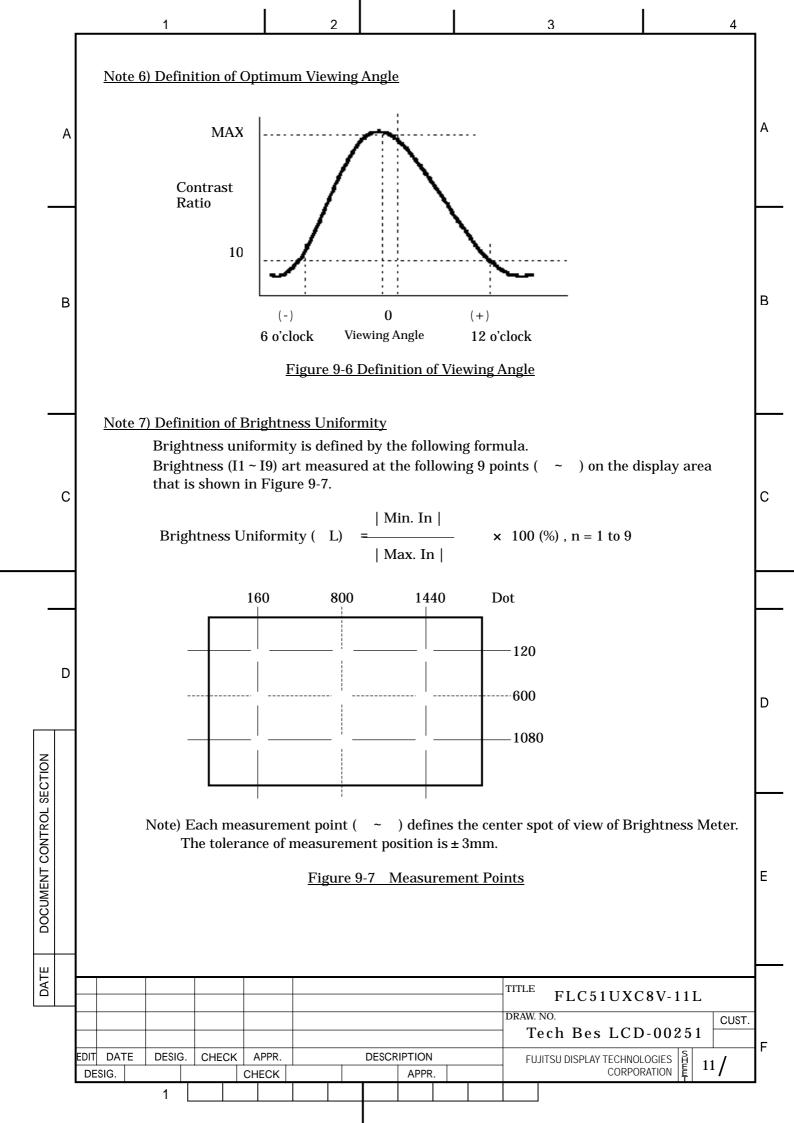
8. ELECTRICAL SPECIFICATIONS Α Table 8-1 shows the electrical specifications of this LCD module. Figure 8-1 shows the measurement circuit. Figure 8-2(A) shows the equivalent circuit of the logic signal input area. Figure 8-2(B) shows the equivalent circuit of the supply voltage Input area. <u>Table 8-1 Electrical Specifications</u> Item Symbol MIN. TYP. MAX. Condition Unit Remark Differential-input V_{IH} 100 mV Voltage (High) $V_{CM}=+1.2V$ Differential-input В В V_{IL} -100mV Voltage (Low) *1 **Supply Current** 600 1200 mA Icc $V_{CC} = +12.0 \pm 0.5 V$ $V_{SS}=0V$ Supply Rush Current I_{SCC} 5.8 Α DCLK=81MHz *2 60Hz **Supply Rush Current** T_{SCC} 0.2. ms **Duration (1A excess)** $f_L=50kHz$, 1600 1800 Vrms *4 Ta=25°C В CCFL Turn on V_{S} Α С Voltage $f_L=50kHz$, *4 1850 Vrms C Ta=0°C K $f_1=50kHz$ Lighting Voltage 800 $V_{\rm L}$ Vrms Ι $I_L=6mA$ G Н **Lighting Frequency** $V_L=800Vrms$ 30 50 60 **KHz** f_L $f_L=50kHz$ **Tube Current** 7 *3 I_L 3 6 mArms $V_1 = 800 V rms$ D (*1) Typical current situation : Color bar pattern. Vcc=12.0VMaximum current situation: 2-pixel checker pattern. Vcc=11.5VWithout rush current. (*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of Vcc is not prescribed. DOCUMENT CONTROL SECTION (*3) Tube current (I_I) shows the value of the current that is consumed at one lamp. This LCD module has 6 lamps. Each 3 lamps are placed at upper side and lower side of the display. 3 lamps are connected in parallel. Each low voltage terminals are connected with separate Cable to Back-light connecter. (*4) The voltage above this value should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on. DATE TITLE FLC51UXC8V-11L CUST. Tech Bes LCD-00251 EDIT DATE DESIG. CHECK APPR. DESCRIPTION FUJITSU DISPLAY TECHNOLOGIES 6/ CORPORATION DESIG. CHECK



9. OPTICAL SPECIFICATIONS Table 9-1 shows the optical specifications of this LCD module. Table 9-1 Optical Specifications Ta=25°C,Signal timing=Typ. **Specifications** Remark Item Symbol Condition Unit MIN. TYP. MAX. Note Horizontal $\theta_{\rm U, D} = 0^{\circ}$ 85 deg $\theta_{L,R}$ (1)(2)Visual $CR \ge 10$ (3)(5)Vertical $\theta_{L,R}=0^{\circ}$ 85 deg $\theta_{
m U,\,D}$ Angle (6) All Direction 80 deg (1)(2)White/ Contrast Ratio CR $\theta_{\rm L,\,R,\,U,\,D}$ =0° 600 400 Black (3)(5)В В $Ta=25^{\circ}C$ 16 $\theta_{L,R,}$ ②Response ms(1) $\tau_{\rm rise}$ Time (Rise+Fall) (4)U, D $(B \rightarrow W \rightarrow B)$ $=0^{\circ}$ $\tau_{\rm fall}$ (5)Ta=0°C 34 ms Response $\theta_{L,R,}$ Average of Ta=25°C Time (Rise or Fall) 210 ms Response τ_{avg} U, D 50-60Hz Time (All gray scale) $=0^{\circ}$ $\theta_{\rm L, R, U, D} = 0^{\circ}$ Brightness Ι cd/m^2 (1)(5)200 250 $V_{CC}=12.0V$ Brightness (1)(5)I_L=6mA % $\triangle I$ 70 White Uniformity (7) $f_1=50kHz$ С *1 С R*,G*,B*Signal 0.283 0.313 0.343 Х W =All"H" 0.299 0.329 0.359 У (1)Red (0.641, 0.350) Typ. Chromaticity (5) \mathbf{R} G (x, y)Green (0.287, 0.595) Typ. В Blue (0.142,0.070) Typ. LCD Panel Type TFT Color Display Mode Normally Black D Wide Viewing Angle Technology **MVA** Optimum Viewing Angle (Symmetry) (6)Display Color 16,777,216 (8-bit color) DOCUMENT CONTROL SECTION Color of non-display area Black Anti-glare Surface Treatment (Haze value: (25%), 2H) (*1) Value at $15\sim20$ minutes after lighting on. (Note) •CS-1000 (MINOLTA Co. Ltd.) Field=1°, L=500mm Ε Back-light current = 6mA, Dark room condition (1 lux or less) •Be careful that the luminance meter, which you use, may not be able to get correct brightness If it's no set correctly. DATE TITLE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 02 20040914 Change Response time. FUJITSU DISPLAY TECHNOLOGIES HE EDIT DATE DESIG. CHECK APPR. DESCRIPTION **CORPORATION** DESIG. CHECK APPR.







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10. INTERFACE SPECIFICATIONS

10-1 Signal descriptions

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DOCUMENT CONTROL SECTION

Table 10-1 shows the description and configuration of interface signals (CN1).

Table 10-1 Interface signals (CN1)

| Pin No. | Symbol | I/O | Function |
|---------|-----------|-----|----------------------------------|
| 1 | RxO0- | I | Negative differential input |
| 2 | RxO0+ | I | Positive differential input |
| 3 | RxO1- | I | Negative differential input |
| 4 | RxO1+ | I | Positive differential input |
| 5 | RxO2- | | Negative differential input |
| 6 | RxO2+ | I | Positive differential input |
| 7 | GND | | Ground |
| 8 | RxOC- | I | Negative differential input |
| 9 | RxOC+ | I | Positive differential input |
| 10 | RxO3- | I | Negative differential input |
| 11 | RxO3+ | I | Positive differential input |
| 12 | RxE0- | I | Negative differential input |
| 13 | RxE0+ | I | Positive differential input |
| 14 | GND | | Ground |
| 15 | RxE1- | I | Negative differential input |
| 16 | RxE1+ | I | Positive differential input |
| 17 | GND | | Ground |
| 18 | RxE2- | I | Negative differential input |
| 19 | RxE2+ | I | Positive differential input |
| 20 | RxEC- | I | Negative differential input |
| 21 | RxEC+ | I | Positive differential input |
| 22 | RxE3- | Ι | Negative differential input |
| 23 | RxE3+ | I | Positive differential input |
| 24 | GND | | Ground |
| 25 | SELL LVDS | I | Select LVDS data order *1 |
| 26 | PD | I | LVDS Core Power Down |
| 27 | HRCOFF | I | Fast response function control*2 |
| 28 | Vcc | | +12V power supply |
| 29 | Vcc | | +12V power supply |
| 30 | Vcc | | +12V power supply |

Connector : FI-X30S-HF (Japan Aviation Electronics) User's connector : FI-X30M (Japan Aviation Electronics)

FI-X30H, FI-X30C

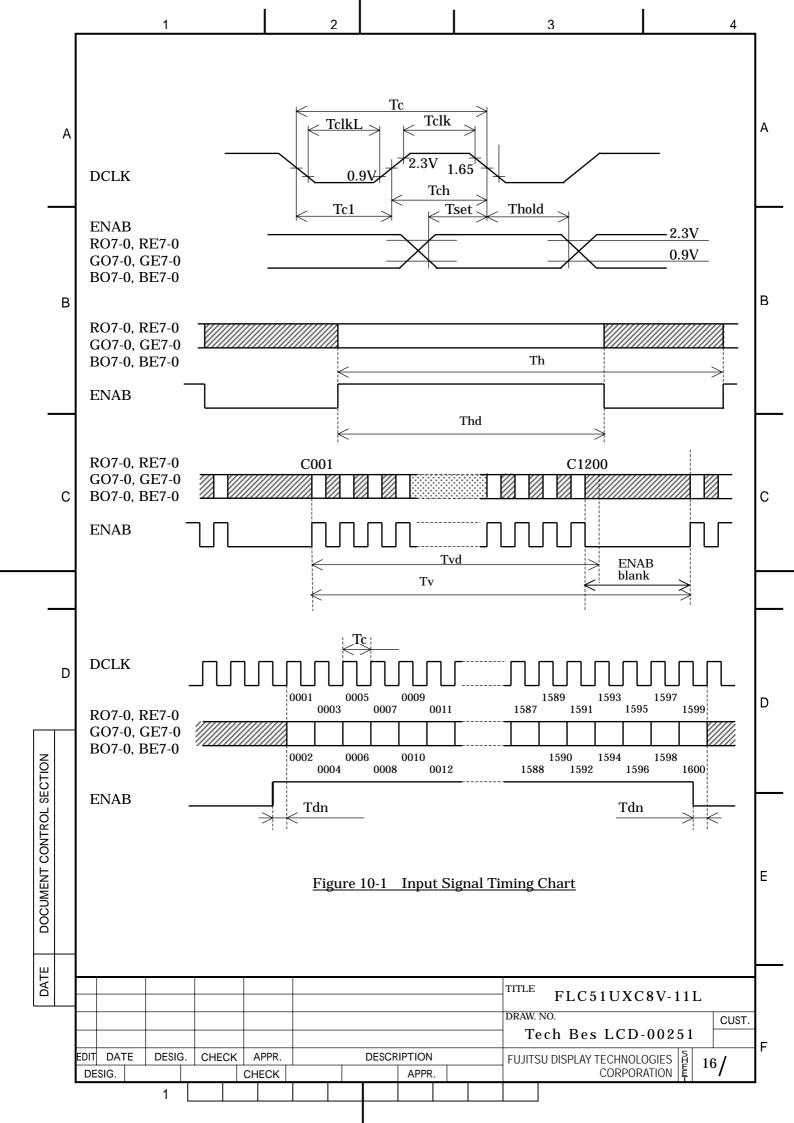
- *1: 3.3V CMOS Signal input. (High or Low)
- $^{*}2$: The fast response function is OFF at "H". Refer to item 10.4 for change over of timing.
- *3: When using a connector other than the recommended one, a defect in the initial stage or a problem concerning long term reliability may occur.

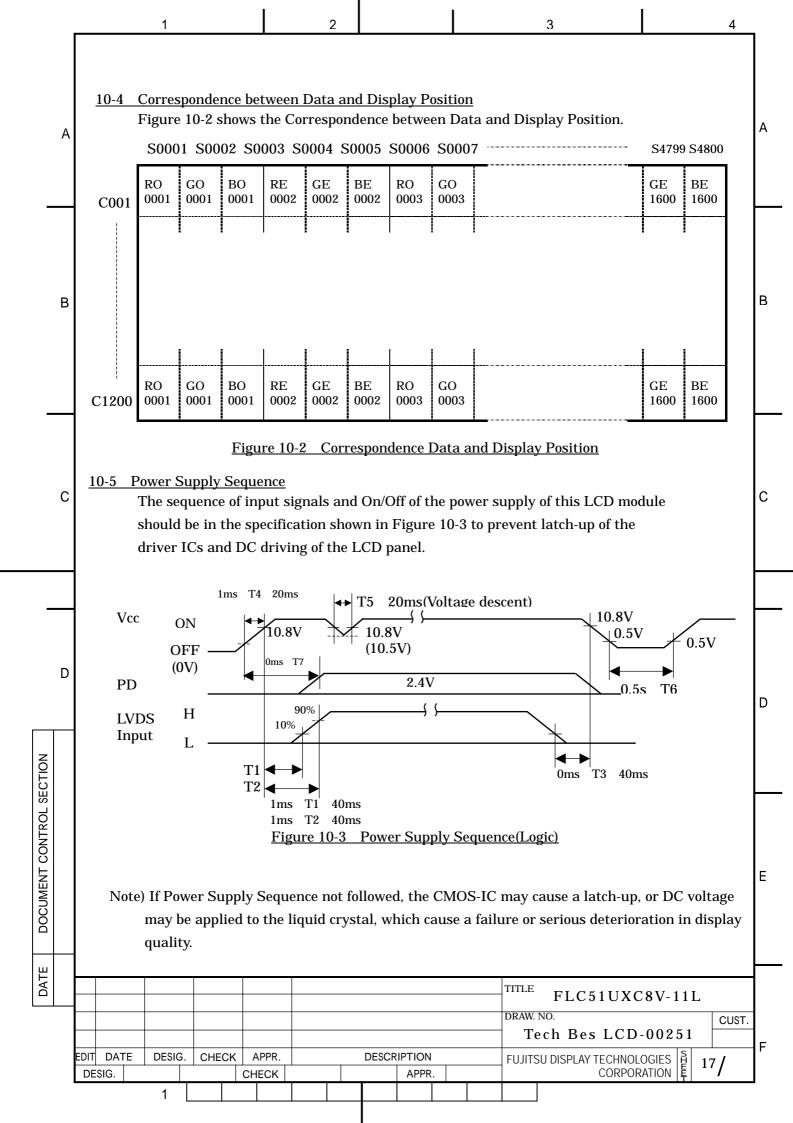
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2 3 10-2 LVDS Data Assignment Table 10-2 shows the LVDS Data Assignment. Table 10-2 LVDS Data Assignment Α LCD input Transmitter Receiver Input signal *1 Interface connector DS90CF383,C385 DS90CF386 (Sel LVDS) SEL LCD module Low High **INPUT OUTPUT** System side pin pin LVDS Low High pin RO2 RO0 51 TxIN0 27 RxOUT0 RO2 RO0 TxIN1 RxOUT1 RO3 RO1 52 29 RO3 RO₁ Tx OUT0+ 2 RxO0+RO4 RO2 54 TxIN2 30 RxOUT2 RO4 RO₂ RO₅ RO3 55 TxIN3 32 RxOUT3 RO5 RO3 RO6 RO4 56 TxIN4 33 RxOUT4 RO6 RO4 Tx OUT0-RxO0-1 RO7 RO5 3 TxIN6 RxOUT6 RO7 RO₅ 35 GO₂ GO₀ 4 TxIN7 37 RxOUT7 GO2GO₀ GO3 GO1 6 TxIN8 38 RxOUT8 GO3 GO1 TxIN9 RxOUT9 GO4 GO2 7 39 GO4 GO₂ Tx OUT1+ RxO1+ В 4 В GO₅ GO3 11 TxIN12 43 RxOUT12 GO₅ GO3 GO6GO4 TxIN13 RxOUT13 GO612 GO₄ 45 GO7 GO₅ 14 TxIN14 46 RxOUT14 GO7 GO₅ Tx OUT1-3 RxO1-BO2 BO0 15 TxIN15 47 RxOUT15 BO₂ BO0 BO3 BO₁ 19 TxIN18 51 RxOUT18 BO₃ BO₁ **LVDS** BO4 BO₂ 20 TxIN19 53 RxOUT19 BO4 BO₂ BO₅ BO3 BO3 22 TxIN20 54 RxOUT20 BO₅ Odd Tx OUT2+ 6 RxO2+BO6 BO₄ 23 TxIN21 55 RxOUT21 BO6 BO₄ BO7 BO₅ 24 TxIN22 1 RxOUT22 BO7 BO₅ RSVD **RSVD** 27 TxIN24 3 RxOUT24 Not use Not use Tx OUT2-RxO2-**RSVD RSVD** 28 TxIN25 5 RxOUT25 Not use Not use **ENAB ENAB ENAB** 30 TxIN26 6 RxOUT26 ENAB RO0 RO6 50 TxIN27 RxOUT27 RO0RO6 RO1 RO7 2 TxIN5 34 RxOUT5 RO1 RO7 Tx OUT3+ RxO3+ 11 GO0 GO6 8 TxIN10 41 RxOUT1 GO0 GO6 GO7 10 RxOUT11 GO₁ TxIN11 42 GO₁ GO7 C BO0 BO6 16 TxIN16 49 RxOUT16 BO0 BO6 Tx OUT3-10 RxO3-BO₁ BO7 18 TxIN17 50 RxOUT17 BO₁ BO7 RSVD **RSVD** TxIN23 RxOUT23 Not use Not use TxCLK OUT+ RxCLK IN+ 9 DCLK 31 TxCLK IN 26 RxCLK OUT **DCLK** TxCLK OUT-RxCLK IN-8 RE2 RE0 51 TxIN0 27 RxOUT0 RE2 RE0 RE1 TxIN1 RxOUT1 RF3 52 29 RF3 RE1 Tx OUT0+ RxE0+13 RF4 RF2 54 TxIN2 30 RxOUT2 RF4 RF2 RE5 RE3 55 TxIN3 32 RxOUT3 RE5 RE3 RE6 RE4 TxIN4 33 RxOUT4 RE6 RE4 Tx OUT0-12 RxE0-RE7 RE5 TxIN6 35 RxOUT6 RE7 RE5 3 TxIN7 GE2 GE₀ 4 37 RxOUT7 GE2 GE₀ GE3 GE1 6 TxIN8 38 RxOUT8 GE3 GE₁ GE4 GE2 7 TxIN9 39 RxOUT9 GE4 GE2 Tx OUT1+ D 16 RxE1+ GE5 GE3 11 TxIN12 43 RxOUT12 GE5 GE3 GE6 GE4 12 TxIN13 45 RxOUT13 GE6 GE4 D GE7 GE5 14 TxIN14 46 RxOUT14 GE7 GE5 Tx OUT1-15 RxE1-BE2 BE0 15 TxIN15 47 RxOUT15 BE2 BE0 BE1 BE3 19 TxIN18 51 RxOUT18 BE3 BE1 **LVDS** BE4 BE2 20 TxIN19 53 RxOUT19 BE4 BE2 22 BE5 BE3 TxIN20 RxOUT20 BE5 BE3 Even 54 Tx OUT2+ RxE2+ 19 SECTION BE6 BE4 23 TxIN21 55 RxOUT21 BE6 BE4 BE7 BE5 24 TxIN22 RxOUT22 BE7 BE5 **RSVD** RSVD 27 3 RxOUT24 TxIN24 Not use Not use Tx OUT2-18 RxE2-**RSVD RSVD** TxIN25 RxOUT25 28 5 Not use Not use RSVD **RSVD** 30 TxIN26 6 RxOUT26 DOCUMENT CONTROL Not use Not use RE0 RE6 50 TxIN27 7 RxOUT27 RE0 RE6 RE1 RE7 2 TxIN5 34 RxOUT5 RE1 RE7 Tx OUT3+ 23 RxE3+ 8 GE0 GE6 TxIN10 41 RxOUT10 GE₀ GE6 10 TxIN11 RxOUT11 GE1 GE7 42 GE1 GE7 16 TxIN16 RxOUT16 BE0 BE6 49 BE₀ BE₆ Ε Tx OUT3-22 RxE3-BE1 BE7 18 TxIN17 50 RxOUT17 BE1 BE7 **RSVD** 25 TxIN23 RxOUT23 Not use Not use TxCLK OUT+ RxCLK IN+ 21 **DCLK** 31 TxCLK IN 26 RxCLK OUT Not use TxCLK OUT-RxCLK IN-20 *1 ·RSVD (reserved) pin on a transmitter should be connected with Ground. Input odd or even data depending on the display position of the LCD module. DATE TITLE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 DATE DESIG. CHECK APPR. **DESCRIPTION** EDIT FUJITSU DISPLAY TECHNOLOGIES 13/ CORPORATION DESIG. CHECK **APPR**

10-3 Color Data Assignment Table 10-3 shows the Color Data Assignment. Α Table 10-3 Color Data Assignment Color R Input data G Input data B Input data Odd R7 R6 R5 R4 R3 R2 R1 R0 G7 G6 G5 G4 G3 G2 G1 G0 B7 B6 B5 B4 B3 B2 B1 B0 B7 B6 B5 B4 B3 B2 B1 B0 Even R7 R6 R5 R4 R3 R2 R1 R0 G7 G6 G5 G4 G3 G2 G1 G0 **Black** Blue Green Cyan В В Red 0 Magenta Yellow White **Black** 0 仓 1 仓 Û 253 **Brighter** 0 254 0 C Red 255 0 0 0 0 **Black** 0 0 0 0 0 仓 1 仓 Green **Brighter** 253 254 Green 255 **Black** 0 D Û 1 0 0 0 0 0 0 1 Û Û : **Brighter** 253 0 DOCUMENT CONTROL SECTION 254 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 1 Blue 255 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 Note.1) Definition of gray scale:Color (n)..."n" indicates gray scale level. Larger number means brighter level. Note.2) Data; 1:High, 0:Low Ε Note 3) Color data consist of 8 bit red, green and blue data of odd and even number pixel data. Total data number is 48 signals. This module is able to display 16,777,216 colors because each red, green and blue data is controlled independently. DATE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 14/ CORPORATION DESIG. CHECK **APPR**

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| | A | Table 1 | Signal Timing 0-4 and Figure | | | the I | nput Sigr | nal Timir | ng at I | | | | A |
| | | Table 10-4 T | <u>Fiming Charac</u> Item | teristics | <u>s</u> Syml | hol | Min. | Тур. | | (Ta=0 Max. | 0~45°C, \ Unit | /cc=12±0.5V) Remark | 1 |
| | | DCLK | Period Frequency | | Tc 1/T | : | 11.765 50.000 | 12.345 81.000 | 5 20 | 0.000 5.000 | ns MHz | Remark | |
| | В | signal (Clock) DCLK-Data Timing | Duty High time Low time Setup time Hold time Horizontal Pe | eriod | Tch/ Tclk Tclk Tse Tho | Tc H L t | 45 3.5 3.5 3 2 865*1 | 50 — — — — — — 1080 | 1 | 55 — — — — — — — — — — — | % ns ns ns ns DCLK | | В |
| _ | | ENAB signal | Hor. Period Hor. Display Vertical Period Ver. Frequence | od cy | Th The Tv | v V | 13.0 800 1207*1 50 1200 | 13.3 800 1250 60 1200 | 1 | 4.65 800 280*1 62 1200 | μs DCLK Th Hz Hz Hz | *2 | - |
| | | | Ver. Display p Data-ENAB t Vertical ENA | timing | Tdı | AΒ | 5500 | 0 | | _ | Th DCLK DCLK | *3 | |
| | С | The data | blank. display position latched at falling isplay position is | g edge of | DCLK | the ris | rise of EN | AB is disp | | | | |] C |
| | | The data | latched at the ris | se of ENA | AB is di | isplay | ed at the t | op line of | the dis _l | play are | ea. | | |
| | D | *3)•If ENAB d display at *4)•If Vertical | loes not synchror | nize with ne shorte | the eff | ective 5500I | display da | nta, the dis | splay p | osition | | t of the display are | D D |
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11. BACK-LIGHT SPECIFICATION

11-1 Pin configuration for Back-light

Table 11-1 shows the description and Pin assignment of the connectors (CN-A to D) for the Back-light of this LCD module.

Table 11-1 Pin Assignment of CN-A to CN-D

| | Pin No. | Symbol | Function | Cable Color |
|-------|------------|------------------|-----------------------------|-------------|
| | 1 | V_{H1} | Power supply (High voltage) | Pink |
| | 2 | V _{H2} | Power supply (High voltage) | Orange |
| CN-A | 3 | - | NC | |
| | 4 | V _L 1 | Power supply (Low voltage) | Blue |
| | 5 | V _{L 2} | Power supply (Low voltage) | Gray |
| CN-B | 1 | V _{H3} | Power supply (High voltage) | Red |
| CIV-D | 2 | V _L 3 | Power supply (Low voltage) | White |
| | 1 | V_{H4} | Power supply (High voltage) | Pink |
| | 2 | V _{H5} | Power supply (High voltage) | Orange |
| CN-C | 3 | - | NC | |
| | 4 | V _L 4 | Power supply (Low voltage) | Blue |
| | 5 | V _L 5 | Power supply (Low voltage) | Gray |
| CND | 1 | V _{H6} | Power supply (High voltage) | Red |
| CN-D | 2 | V _{L 6} | Power supply (Low voltage) | White |

Housing(CN-A, CN-C):BHR-05VS-1 Connector

(CN-B, CN-D):BHSR-02VS-1

Contact SBH-001T-P0.5

SBHS-002T-P0.5

User's Connector: Post with base: SM04(9-E2)B-BHS-1

SM02B-BHSS-1-TB

Supplier Japan Solder less Terminal manufacturing Company LTD. (J.S.T.)

11-2 CCFL

Supplier: SANKEN ELECTRONICS Co.LTD. Part No.: SS26E4175E8550C2882710S

<u>11-3 Life</u>

The life of the back-light is a minimum of 50,000 hours at the following conditions.

(1) Working conditions

Ambient temperature: 25 ± 5

Tube current (I_L) (6mA or less)

(2) Definition of life

Brightness becomes 50% or less than the minimum brightness value shown in Table 9-1. Flashing.

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11-4 <u>Lamp assembly set (for replacement)</u>

Lamp assembly set (with charge) is prepared for replacing old lamp to new one.

This set consists of an upper lamp assembly and a lower lamp assembly.

Type number: FLCL-26

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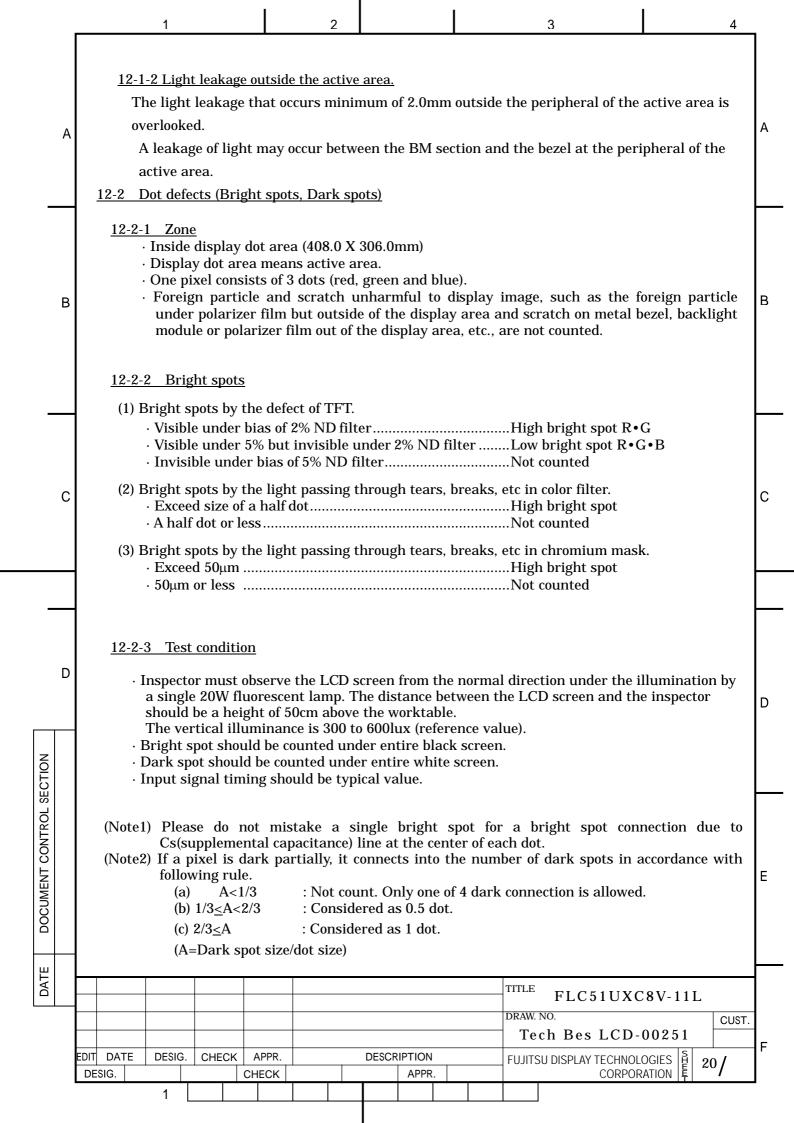
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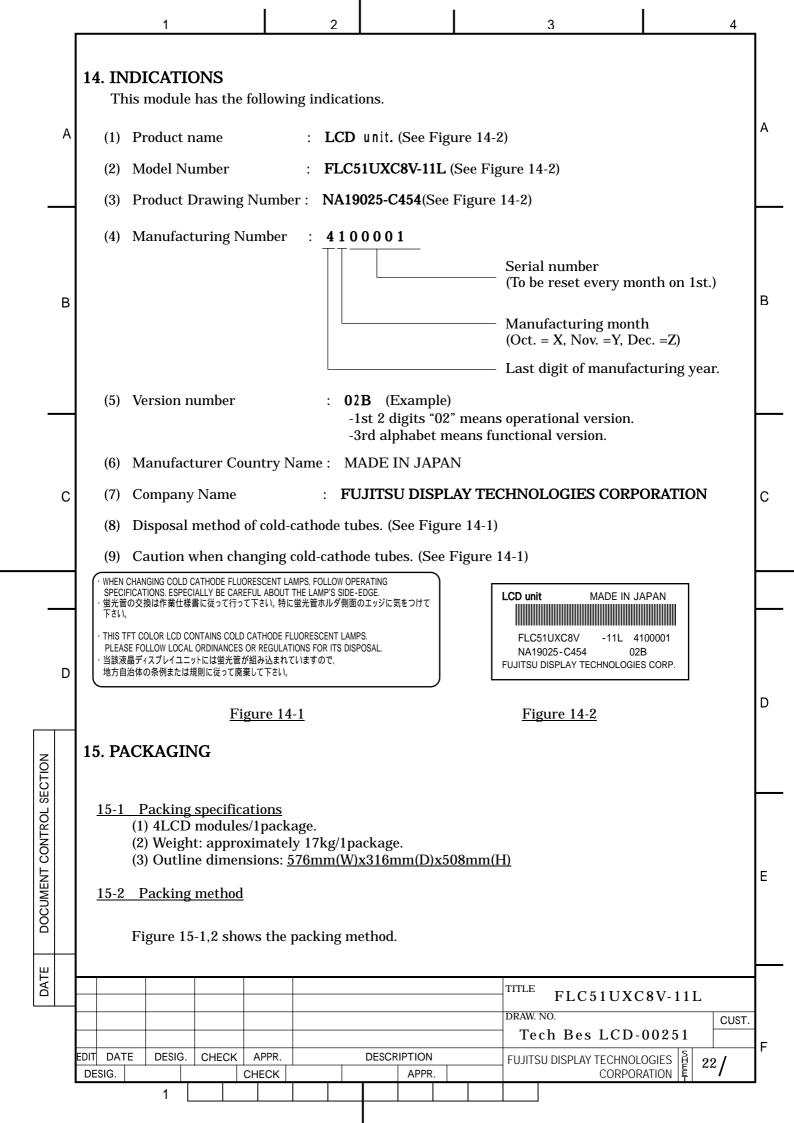
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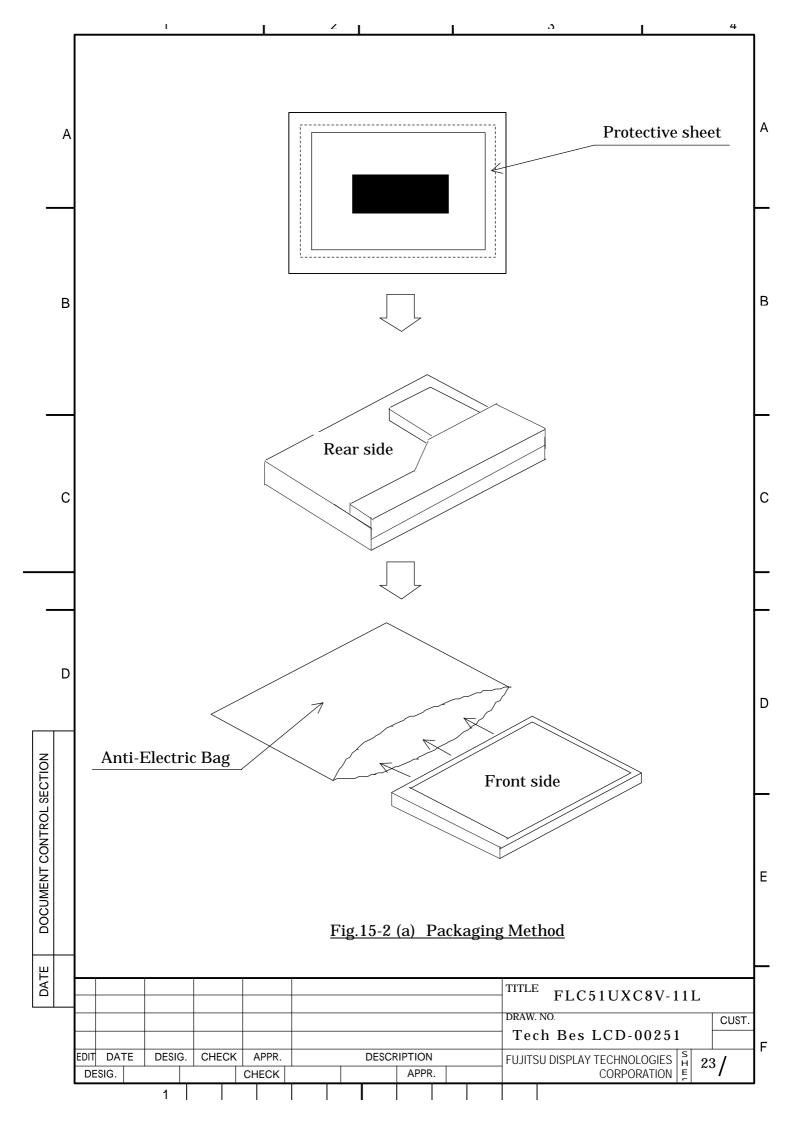
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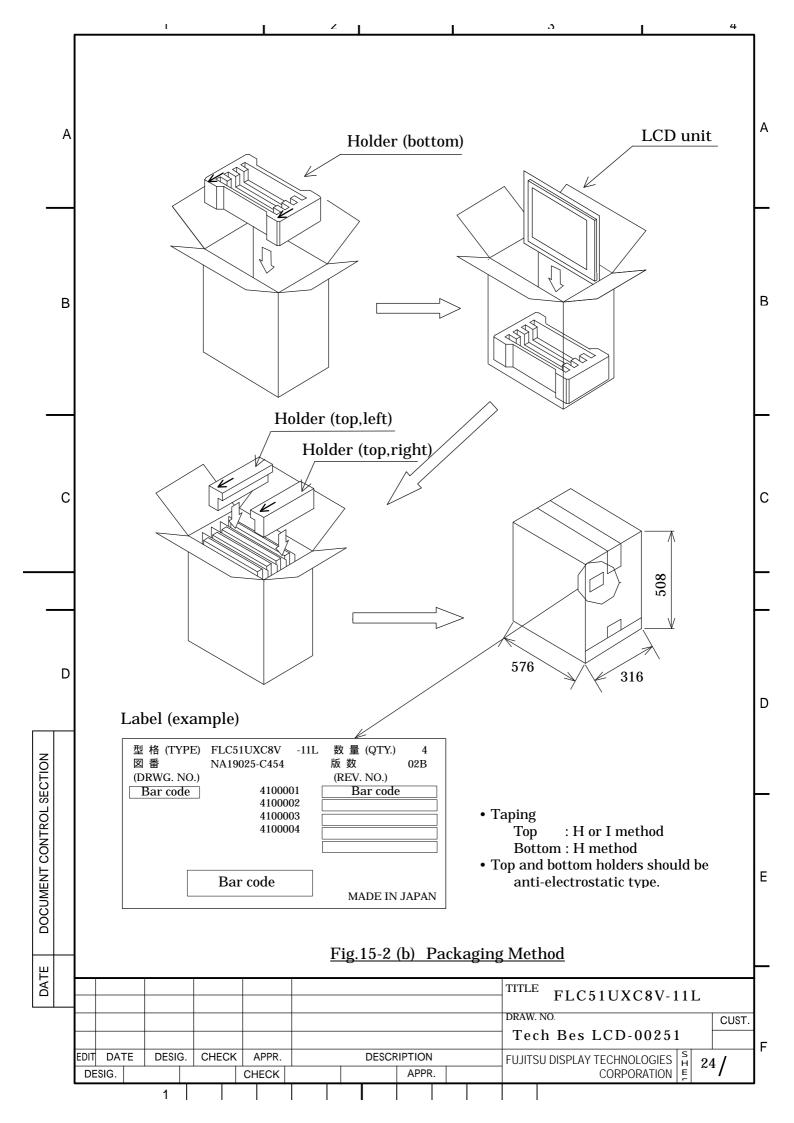
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| | 2 | Bright spot (high Bright spot conne (high and low) | | ≤0 dots ≤2 pair | | (Note 1) (Note 1) | |
| - | 3 | Total of bright spo | ot | <u><6</u> dots | | | |
| | 4 | Dark spot | | <u>≤</u> 10 dots | | (Note 2) |) |
| | 5 | Dark spot connect | tion | <u>≤</u> 4pairs | | (Note 2) | |
| | 6 | Total of dark spot | | ≤10 dots | | (Note 2) |) |
| В | 7 | Total of dot defect (bright and dark) | | <u>≤</u> 10 dots | | | |
| | 8 | Distance of | high-hgh | <u>≥</u> 15mm | | | |
| | | bright spot | others | <u>≥</u> 5mm | | | |
| | 9 | Distance of dark s | | <u>≥</u> 5mm | î | T- | |
| | 10 | Scratch on polariz | zer, | W <u>≤</u> 0.03 | 1.0 | Ignore | |
| _ | | line shape | | 0.02 44/ 40.05 | L≤6 6 <l≤12< td=""><td>Ignore</td><td></td></l≤12<> | Ignore | |
| | | | | 0.03 <w<u>≤0.05</w<u> | 12 <l< td=""><td><u>≤</u>7 0</td><td></td></l<> | <u>≤</u> 7 0 | |
| | | | | | L≤0.6 | Ignore | \dashv |
| | | | | 0.05 <w<0.10< td=""><td>0.6<l≤5< td=""><td><u>≤</u>5</td><td></td></l≤5<></td></w<0.10<> | 0.6 <l≤5< td=""><td><u>≤</u>5</td><td></td></l≤5<> | <u>≤</u> 5 | |
| С | | | | = | 5 <l< td=""><td>0</td><td></td></l<> | 0 | |
| Ĭ | | | | 0.10 <w< td=""><td></td><td>0</td><td></td></w<> | | 0 | |
| | 11 | Dent on polarizer, | , | | D≤0.3 | Ignore | |
| | | dot shape | | | D <u>≤</u> 0.4 | <u>≤</u> 9 | |
| - | | | | 0.4< | | 0 | |
| | 12 | Bubble in polarize | er | | D <u>≤</u> 0.3 | Ignore | |
| \dashv | | • | | 0.3< | D <u>≤</u> 0.5 | <u>≤</u> 4 0 | |
| | <u> </u> | | | | 0.15 | Ignore | |
| | 13 | Black white spot | | | D≤0.5 | <u>≤</u> 5 | |
| D | | (Foreign circular | matter) | 0.5< | | 0 | |
| | 14 | Light leakage by | foreign | | D <u><</u> 0.3 | Ignore | |
| | | articles | O | 0.3< | D <u>≤</u> 0.6 | <u>≤</u> 5 | |
| | | | | 0.6< | D | 0 | |
| | 15 | Lints, | | W <u>≤</u> 0.03 | _ | Ignore | |
| | | black/white line | | 0.09 347 20.07 | L≤6 | Ignore | \blacksquare |
| | | | | 0.03 <w<u>≤0.05</w<u> | 6 <l<u>≤12 12<l< td=""><td><u>≤</u>6 0</td><td></td></l<></l<u> | <u>≤</u> 6 0 | |
| | | | | | L≤0.6 | Ignore | |
| | | | | 0.05 <w≤0.10< td=""><td>0.6<l≤5< td=""><td><u>≤</u>3</td><td>\dashv</td></l≤5<></td></w≤0.10<> | 0.6 <l≤5< td=""><td><u>≤</u>3</td><td>\dashv</td></l≤5<> | <u>≤</u> 3 | \dashv |
| | | | | | 5 <l< td=""><td>0</td><td></td></l<> | 0 | |
| | | | | 0.10 <w< td=""><td>(W+L)/2=D</td><td>Conform to No.13</td><td></td></w<> | (W+L)/2=D | Conform to No.13 | |
| | 16 | Mura | | Invisible under | | m center of display. | |
| | <u>L</u> , | D. Avonogo diamente | n [mm] 117. 11 | | | oright spot size)/(dot s | ize) |
| |] | D: Average diamete | er (mm), vv: v | viatn (mm), L: Le | engtn [mm], S=(t | oright spot size)/(dot s | ize) |
| \vdash | | | | | | | |
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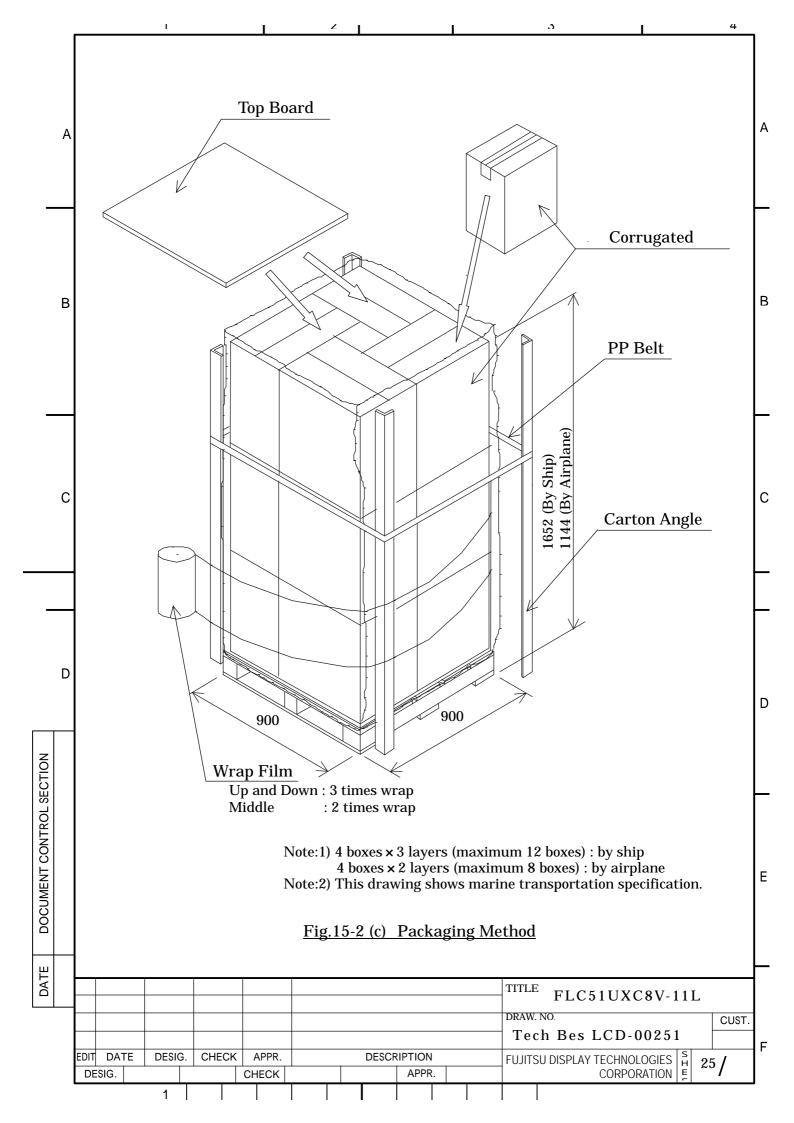


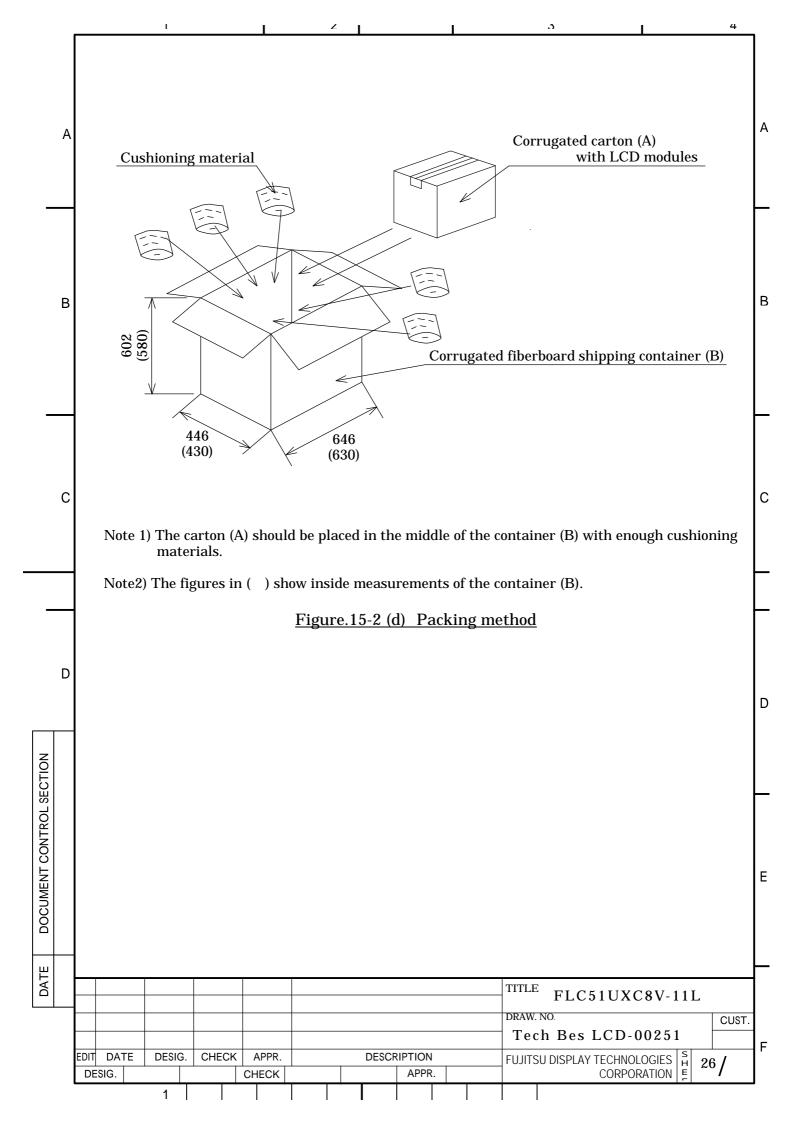
13. ENVIRONMENTAL SPECIFICATIONS Table 13-1 show the environmental specifications. Α Table 13-1 Environmental specifications Item Condition Remark Operation 0~45°C Temperature on surface of LCD **Temperature** panel should be under54 -20~60°C Storage Maximum wet-bulb temperature 20~85%RH Operation should not exceed 29°C. Humidity 5~85%RH Storage No condensation. В В 10~500Hz, 1octave/20minute, Vibration Non-operation 2G, 1.5mm max, 1hour each X, For single module Y and Z directions without package. 30G, 6ms, 1time each Shock Non-operation $\pm X$, $\pm Y$ and $\pm Z$ directions. NOTE: Table 13-2 and Figure 13-1 show the shock resistance standard when module is packaged. Table 13-2 Shock resistance standard when module is packaged С C **Dropping location** Count Dropping height A~J 60cm 1 time G (Top face) J (Rear face) C (Edge) D F (Side face) E (Side face) DOCUMENT CONTROL SECTION B (Edge) A (Corner) I (Front face) D (Edge) H (Bottom face) Figure 13-1 Direction to apply shock to package Ε DATE TITLE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 DESCRIPTION FUJITSU DISPLAY TECHNOLOGIES EDIT DATE DESIG. CHECK APPR. 21/ DESIG. CHECK APPR. **CORPORATION**





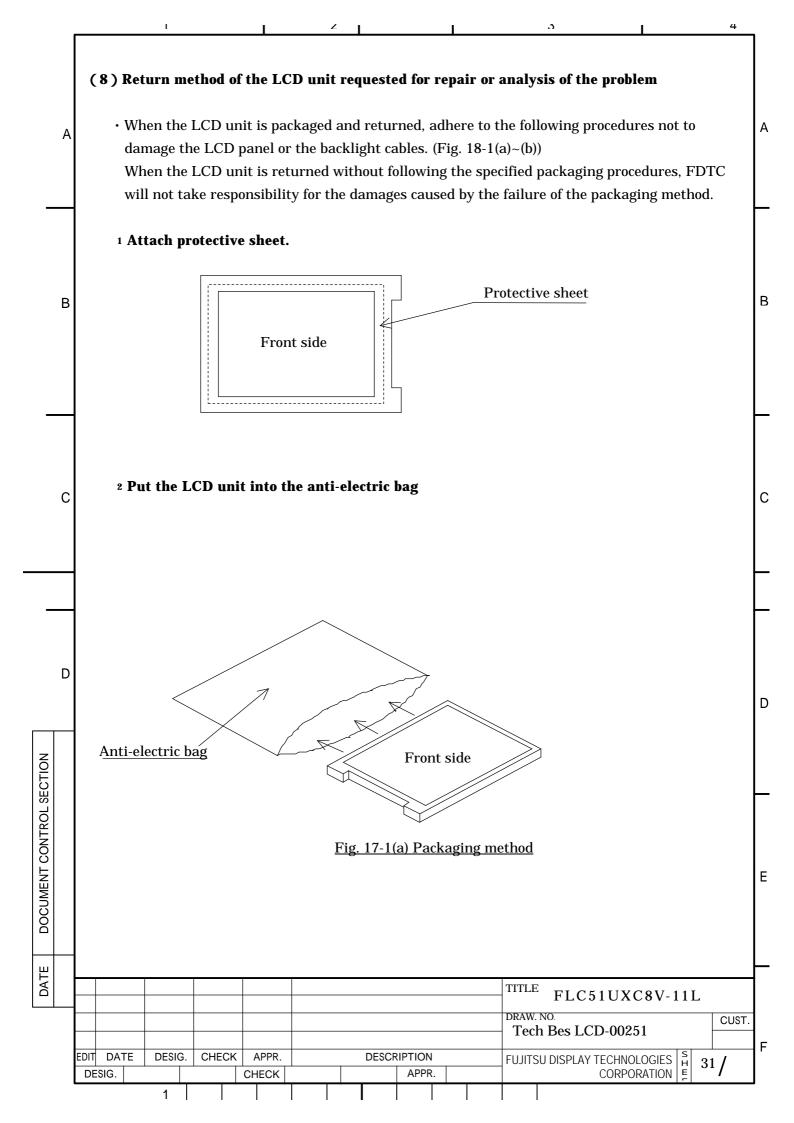


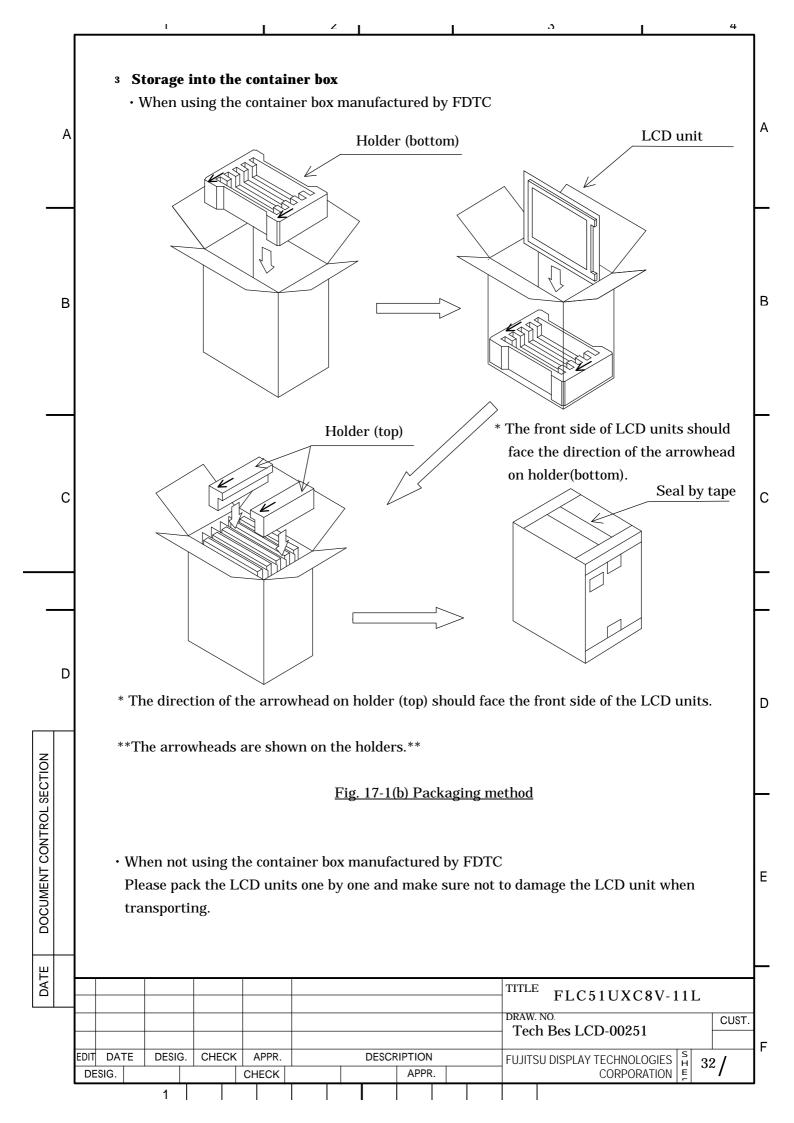




| | 1 2 3 4 |
|----------|---|
| | Do not place or contact objects on the display surface for a long period of time. |
| | This may make some parts of the LCD module distorted and the quality of display may deteriorate. |
| (2) | Handling of LCD module |
| | Do not pull the cold-cathode tube cable strongly. If the cable is pulled with the strength of 2kg or more, the cable may be damaged or may lose reliability. |
| | Assemble the module into user's system in a dust free environment. Conductive foreign matter adheres to the module may cause failures. |
| | Take anti-electrostatic measures for assembling the module. |
| | Since the LCD module contains CMOS-ICs, the following points should be observed. |
| | • For assembling the module, operator should be grounded and wear cotton or conductive gloves. |
| | • Floor of work area and work table to assemble the LCD module should be covered with electrostatic shielding in order to discharge static electricity via an earth wire. |
| | \cdot If necessary, ground operation tools (soldering iron, radio pliers, tweezers, etc.). |
| | \cdot Do not take the module out of the conductive bag until the module is assembled. |
| | \cdot Do not assemble the module under low humidity (50%RH or less). |
| | Do not pull the connecting cable on the rear face of the LCD module strongly. |
| (3 | Disassembly or remodeling of the LCD module may result in malfunctions or deterioration of the display quality and reliability. Precautions in regards of operating the LCD module |
| (0) | Adhere to the specified power supply sequence. |
| | If not followed, the CMOS-IC may cause a latch-up, or DC voltage may be applied to the liquid crystal, which cause a failure or serious deterioration in display quality. |
| | Do not operate the LCD module when condensation occurs. If the LCD module is operated when condensation is on the terminals of the LCD panel, the terminals cause electrochemical reaction, and may reach disconnection. Condensation easily occurs especially when the module is moved from cold environment to warm environment. |
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| <u>.</u> | Trum to |
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| | TITLE FLC51UXC8V-11L DRAW. NO. Tech Bes LCD-00251 |
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The LCD module is recommended to be stored in humidity controlled, cool and dark locations. Recommended storage environment **Place** : Dark (avoid direct sunlight) •Temperature : 10 ~ 35°C Humidity : 50 ~ 60%RH Note) If the module is left in an environment of 60°C and above for a long period of time, optical characteristics may deteriorate. (6) Disposal Method LCD module The components of this LCD module can be grouped into metal, resin, glass and so on. As В В the backlight contains CCFL which includes mercury, it must be disposed according to the local ordinance or regulations. **Package** All the packages are made of recyclable papers except the anti-ESD bag. (7) Others If the LCD panel is damaged, do not inhale and do not swallow the liquid crystal. If the liquid crystal adhere to the body or cloths, wash it off with soap immediately. Follow regular precautions for electronic components. C Flux residue on the printed circuit board is harmless to the quality and reliability of LCD module. Fujitsu has adopted non-wash technology on module assembly process. D DOCUMENT CONTROL SECTION DATE TITLE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 EDIT DATE DESIG. CHECK APPR. DESCRIPTION FUJITSU DISPLAY TECHNOLOGIES 30 / DESIG. **CHECK** CORPORATION





18.OTHERS Specifications of the TFT-LCD panel and other components used in this LCD module are subject to change. Both parties shall discuss together and make the best effort to reach agreement in case of the rising of any doubt to the contents of the specifications. This LCD module is not designed for the purpose where high reliability is required, such as for aero-space equipment, control system of nuclear power and medical life-support equipment. В В C D DOCUMENT CONTROL SECTION Ε DATE TITLE FLC51UXC8V-11L DRAW. NO. CUST. Tech Bes LCD-00251 F EDIT DATE FUJITSU DISPLAY TECHNOLOGIES H CORPORATION E DESIG. CHECK APPR. DESCRIPTION 33 / DESIG. CORPORATION CHECK

