No.	LD -12902
DATE	Sep. 21.2000

TECHNICAL

LITERATURE

FOR

TFT - LCD module

MODEL No. LQ10D368

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DEVELOPMENT ENGINEERING DEPT. II
TFT DIVISION II
TFT LIQUID CRYSTAL DISPLAY GROUP
SHARP CORPORATION

RECORDS OF REVISION

LQ10D368

SPEC No.		DATE	E	REVISED		SUMMARY	NOTE
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1. Application

This technical literature applies to color TFT-LCD module, LQ10D368.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a $640 \times 3 \times 480$ dots panel with 262,144 colors by supplying 18 bit data signal (6bit/color), four timing signals, +5V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use.

Optimum viewing direction is 6 o'clock.

Backlight-driving DC/AC inverter is not built in this module.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H)×158.4(V)	mm
Pixel format	640(H)×480(V)	pixel
	(1 pixel = R + G + B dots)	
Pixel pitch	0.330(H)×0.330(V)	mm
Pixel configuration	R,G,B vertical stripe	Vil
Display mode	Normally white	
Unit outline dimensions *1	246.5(W)×179.4(H)×10.5(D)	mm
Mass	550±20	g
Surface treatment	Anti-glare and hard-coating 3H	
	Haze value = 28%	

^{*1.}Note: excluding backlight cables.

Outline dimensions is shown in Fig.1

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 Used connector:DF9BA-31P-1V (Hirose Electric Co.	, Ltd.)
Corresponding connector: DF9-31S-1V (11)
2 DF9A-31S-1V(IJ)
CN1 pin arrangement from module surface DF9B-31S-1V(")
DF9M-31S-1V (11)

Pm No. Symbol Function Remark 1 GND CK Clock signal for sampling each data signal [Note1] 3 Hsync Horizontal synchronous signal [Note1] 4 Vsync Vertical synchronous signal [Note1] 5 GND [Note1] 6 R0 R E D data signal(LSB) 7 R1 R E D data signal 8 R2 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal 12 GND [SND 13 G0 G R E E N data signal(LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 20 B0 B L U E data signal 21 B1 B L U E data signal 22 B2 B L U E data signal<	r		101/	DF9M-31S-1V(
2 CK Clock signal for sampling each data signal 3 Hsync Horizontal synchronous signal [Note1] 4 Vsync Vertical synchronous signal [Note1] 5 GND [Note1] 6 R0 R E D data signal [Note1] 7 R1 R E D data signal [Note2] 8 R2 R E D data signal [Note3] 9 R3 R E D data signal [Note3] 10 R4 R E D data signal [Note3] 11 R5 R E D data signal [Note3] 12 GND [Note3] [Note3] 13 G0 G R E E N data signal(LSB) [Note3] 14 G1 G R E E N data signal [Note3] 15 G2 G R E E N data signal [Note3] 16 G3 G R E E N data signal [Note3] 17 G4 G R E E N data signal [Note3] 20 B0 B L U E data signal [Note3]	Pin No.	Symbol	Function	Remark
3				
Vsync Vertical synchronous signal [Note1]				
Solution Solution	L			[Note1]
5 GND 6 R0 R E D data signal (LSB) 7 R1 R E D data signal 8 R2 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal (MSB) 12 GND 13 G0 G R E E N data signal (LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal 19 GND 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal 26 GND 27 ENAB	<u> </u>		Vertical synchronous signal	[Note1]
RED data signal		 		
8 R2 R E D data signal 9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal 12 GND GND 13 G0 G R E E N data signal(LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal 19 GND 9 20 B0 B L U E data signal(MSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal(MSB) 26 GND <		R0	RED data signal(LSB)	
9 R3 R E D data signal 10 R4 R E D data signal 11 R5 R E D data signal (MSB) 12 GND 13 G0 G R E E N data signal (LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal 19 GND 9 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal 26 GND 27 ENAB Signal to settle the horizontal display position <t< td=""><td>7</td><td>R1</td><td>RED data signal</td><td></td></t<>	7	R1	RED data signal	
10		R2	RED data signal	
11 R5 R E D data signal (MSB) 12 GND 13 G0 G R E E N data signal (LSB) 14 G1 G R E E N data signal 15 G2 G R E E N data signal 16 G3 G R E E N data signal 17 G4 G R E E N data signal 18 G5 G R E E N data signal 19 GND 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal (MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	9	R3	RED data signal	
12 GND 13 G0 GREEN data signal(LSB) 14 G1 GREEN data signal 15 G2 GREEN data signal	10	R4	RED data signal	
13 G0 GREEN data signal(LSB) 14 G1 GREEN data signal 15 G2 GREEN data signal 16 G3 GREEN data signal 17 G4 GREEN data signal 18 G5 GREEN data signal(MSB) 19 GND 20 B0 BLUE data signal(LSB) 21 B1 BLUE data signal 22 B2 BLUE data signal 23 B3 BLUE data signal 24 B4 BLUE data signal 25 B5 BLUE data signal 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	11	R5	RED data signal(MSB)	
14 G1 GREEN data signal 15 G2 GREEN data signal 16 G3 GREEN data signal 17 G4 GREEN data signal 18 G5 GREEN data signal 19 GND 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	12	GND		
15 G2 GREEN data signal	13	G0	GREEN data signal(LSB)	
15 G2 GREEN data signal 16 G3 GREEN data signal 17 G4 GREEN data signal 18 G5 GREEN data signal(MSB) 19 GND 20 B0 BLUE data signal(LSB) 21 B1 BLUE data signal 22 B2 BLUE data signal 23 B3 BLUE data signal 24 B4 BLUE data signal 25 B5 BLUE data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	14	G1	GREEN data signal	
17 G4 GREEN data signal 18 G5 GREEN data signal(MSB) 19 GND 20 B0 BLUE data signal(LSB) 21 B1 BLUE data signal 22 B2 BLUE data signal 23 B3 BLUE data signal 24 B4 BLUE data signal 25 B5 BLUE data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	15	G2	GREEN data signal	
18 G5 GREEN data signal(MSB) 19 GND 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	16	G3	GREEN data signal	
18 G5 GREEN data signal(MSB) 19 GND 20 B0 BLUE data signal(LSB) 21 B1 BLUE data signal 22 B2 BLUE data signal 23 B3 BLUE data signal 24 B4 BLUE data signal 25 B5 BLUE data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	17	G4	GREEN data signal	
19 GND 20 B0 B L U E data signal (LSB) 21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal (MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	18	G5		
21 B1 B L U E data signal 22 B2 B L U E data signal 23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal (MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	19	GND		
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23 B3 B L U E data signal 24 B4 B L U E data signal 25 B5 B L U E data signal [MSB] 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	21	BI		
24 B4 B L U E data signal 25 B5 B L U E data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	22	B2	BLUE data signal	
25 B5 B L U E data signal (MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	23	В3	BLUE data signal	
25 B5 B L U E data signal(MSB) 26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	24	B4	BLUE data signal	
26 GND 27 ENAB Signal to settle the horizontal display position [Note2] 28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	25	B5		
28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	26	GND		
28 Vcc +5.0V power supply 29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	27	ENAB	Signal to settle the horizontal display position	[Note2]
29 Vcc +5.0V power supply 30 R/L Horizontal display mode select signal [Note3]	28	Vcc		11.01021
30 R/L Horizontal display mode select signal [Note3]	29	Vcc		
21 77/72	30	R/L		[Note3]
	31	U/D		

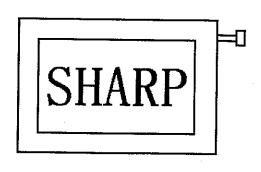
*The shielding case is not connected with GND.

[Note1] 480 line, 400 line or 350 line mode is selected by the polarity combination of the both synchronous signals.

Mode	480 lines	400 lines	350 lines
Hsync	negative	negative	Positive
Vsync	negative	positive	Negative

[Note2] The horizontal display start timing is settled in

accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Don't keep ENAB "High" during operation.

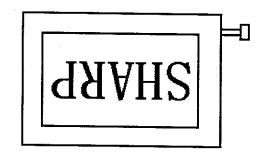




[Note4] R/L = H i g h, U/D = H i g h

 $R/L = L \circ w$, U/D = H i g h





4-2. Backlight driving

Used connector: BHR-03VS-1(JST)

Corresponding connector: SM02(8.0)B-BHS(JST)

Pin no.	Symbol	Function
1	V_{HIGH}	Power supply for lamp
		(High voltage side)
2	NC	This is electrically opened.
3	V_{LOW}	Power supply for lamp
		(Low voltage side)

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	$V_{\rm I}$	Ta=25℃	$-0.3 \sim \text{Vcc} + 0.3$	V	[Note1]
+5V supply voltage	Vcc	Ta=25℃	$0 \sim + 6$	V	
Storage temperature	Tstg	<u></u>	$-25 \sim +70$	°C	[Note2]
Operating temperature (Ambient)	Topa	_	$0 \sim +50$	$^{\circ}$	

[Note1] CK,R0 \sim R5,G0 \sim G5,B0 \sim B5,Hsync,Vsync,ENAB, R/L, U/L

[Note2] Humidity: 95%RH Max. at Ta≤40°C.

Maximum wet-bulb temperature at 39°C or less at Ta>40°C.

No condensation.

6. Electrical Characteristics

6-1.TFT-LCDpaneldriving

 $Ta=25^{\circ}C$

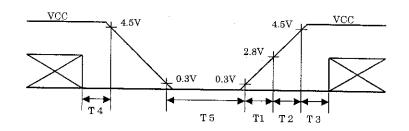
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
+5V Supply voltage	Vcc	+4.5	+5.0	+5.5	V	[Note1]
Current dissipation	Icc		280	450	m A	[Note2]
Permissive input ripple voltage	V_{RF}		_	100	mVp-p	Vcc=+5V
Input voltage (Low)	$V_{ m IL}$	_	_	0.3Vcc	V	
Input voltage (High)	V_{IH}	0.7Vcc		_	V	[Note3]
	I _{OL1}	_	_	1.0	μΑ	V _I =0V
Input current (low)						[Note4]
	I _{OL2}	-	_	60.0	μΑ	V _I =0V
						[Note5]
·	I _{OH1}		_	1.0	μΑ	V _I =Vcc
Input current (High)						[Note6]
	I _{OH2}	_	_	60.0	μΑ	V _I =Vcc
·						[Note7]

[NOTE 1]

Vcc-turn-on conditions

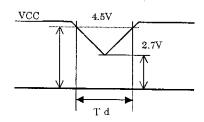
$$T 1 \le 1 5 m s$$

 $0 < T 2 \le 1 0 m s$
 $0 < T 3 \le 2 0 m s$
 $0 < T 4 \le 1 s$
 $1 s < T 5$



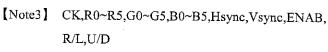
Vcc-dip conditions

- 1) 2. $7 V \le V c c < 4$. $5 V t d \le 10 m s$
- 2) Vcc<2.7V Vcc-dip condition should also follow The Vcc-turn-on conditions



[Note2] Typical current situation : 16-gray-bar pattern.
480 line mode

 $Vcc=\pm 5.0V$

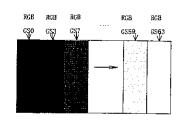


[Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB

[Note5] R/L

[Note6] CK,R0~R5,G0~G5,B0~B5,Hsnc,Vsync

[Note7] ENAB,U/D



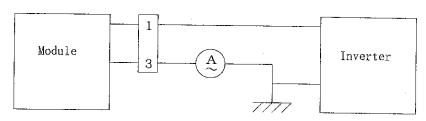
6-2. Backlight driving

The backlight system is an edge-lighting type with single CCFT (Cold Cathode Fluorescent Tube). The characteristics of single lamp are shown in the following table.

Ta=25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current	IL	2.0	6.0	6.5	mArms	
Lamp power consumption	PL		3.0		W	[Note2]
Lamp frequency	FL	20	35	60	KHz	[Note3]
Kick-off voltage	Vs		-	950	Vrms	Ta=25°C
		_		1400	Vrms	Ta =0°C [Note4]
Lamp life time	LL		50000	_	hour	[Note5]

[Notel] Lamp current is measured with current meter for high frequency as shown below.



* 3pin is V LOW

- [Note2] At the condition of $Y_L = 200 \text{cd/m}^2$
- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note4] The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on.
- [Note5] Since lamp is consumables, the life time written above is referencial value and it is not guaranteed in this specification sheet by SHARP.

Lamp life time is defined that it applied either ① or ② under this condition (Continuous turning on at Ta=25°C, IL=6mArms)

- ① Brightness becomes 50% of the original value under standard condition.
- ② Kick-off voltage at Ta=0°C exceeds maximum value,1400 Vrms.

In case of operating under lower temp environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating under for around 1 month under lower temp condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp environment, periodical lamp exchange is recommended.

[Note6] The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur, when you confirm it, the module should be operated in the same condition as it is installed in your instrument.

7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2 - \bigcirc

7-1. Timing characteristics

Para	meter	Symbol	Mode	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Te	all	_	25.18	28.33	MHz	
	High time	Tch	11	5			ns	
	Low time	Tel	"	10	_		ns	
Data	Setup time	Tds	JJ.	5	_	·	ns	
	Hold time	Tdh	IJ	10		_	ns	
Horizontal	Cycle	TH	n	30.00	31.78	_	μs	-
sync. signal			"	7 50	800	900	clock	
	Pulse width	ТНр	· <i>11</i>	2	96	200	clock	
Vertical	Cycle	TV	480	515	525	560	line	
sync. signal		<u> </u>	400	445	449	480	line	
			350	447	449	510	line	
	Pulsewidth	TVp	all	1		34	line	
Horizontal di	splay period	THd	"	640	640	640	clock	
Hsync-Clock		ТНс	11	10	_	Tc-10	ns	
phase difference								
Hsync-Vsync		TVh	n	0	_	тн-тнр	clock	
phase differen	nce							

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding

to the rising edge of ENAB signal is displayed at the left end of the active area.

Parameter		symbol	Min.	Тур.	Max.	Unit	Remark
Enable signal	Setup time	Tes	5		Tc-10	ns	
	Pulse width	Тер	2	640	640	clock	
Hsync-Enable	signal	THe	44		TH-664	clock	
phase differen	ce						

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2-① ~③. Be careful that the module does <u>not</u> work when ENAB is fixed "High". When the phase difference is below 104 clock, keep the "High level of ENAB is signal longer Than 104-The. If it will not be keeped, the display starts from the data of C104(clock).

7-3. Vertical display position

The vertical display position is automatically centered in the active area at each mode

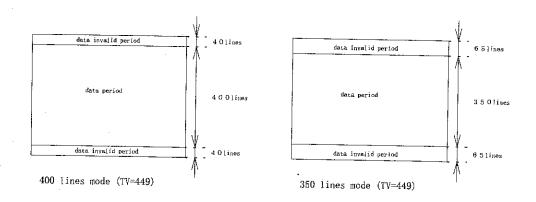
of VGA ,480-,400-,and 350-line mode . Each mode is selected depending on the polarity of the synchronous signals described in 4-1(Note1).

In each mode, the data of TVn is displayed at the top line of the active area. And the display position will be centered on the screen like the following figure when the period of vertical synchronous signal, TV, is typical value.

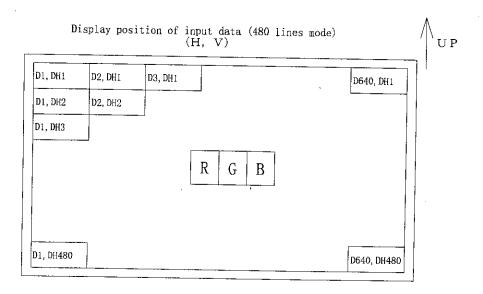
In 400-,and 350-line mode,the data in the vertical data invalid period is also displayed, So ,inputting all data "0" is recommended during vertical data invalid period.

ENAB signal has no relation to the vertical display position.

Mode	V-data start(TVs)		V-display start(TVn)	V-display period	Unit	Remark
480	34	480	34	480	line	
400	34	400	443-TV	480	line	
350	61	350	445-TV	480	line	



7-4. Input Data Signals and Display Position on the screen



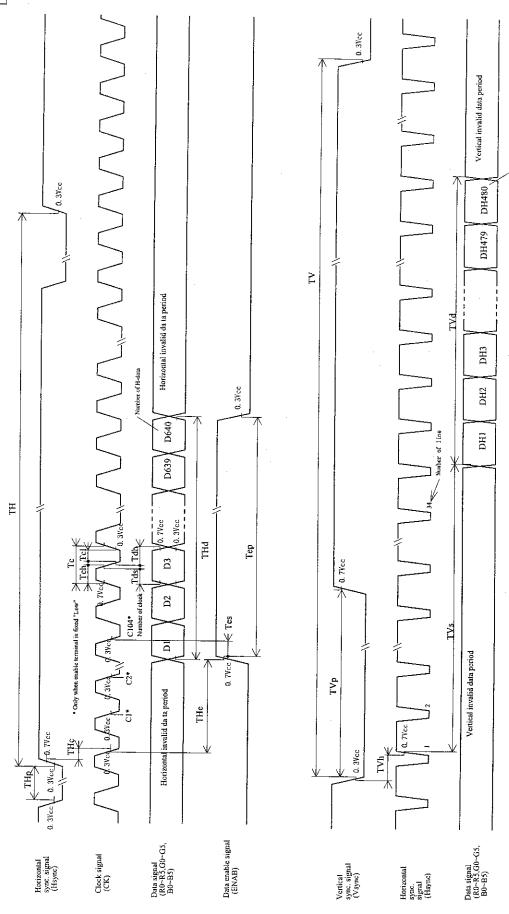


Fig 2-1 Input signal waveforms (480 line mode)

Number of V-data line

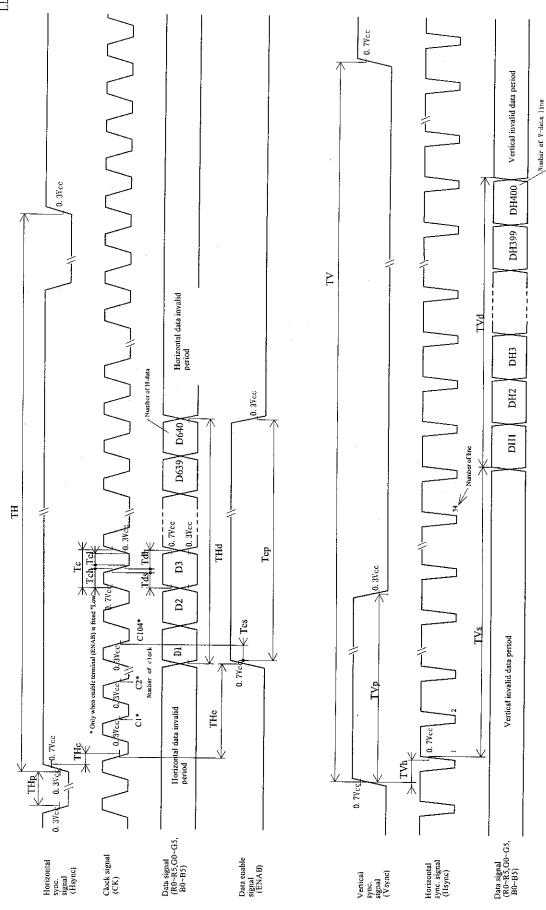


Fig.2-2 Input signal waveforms (400 line mod

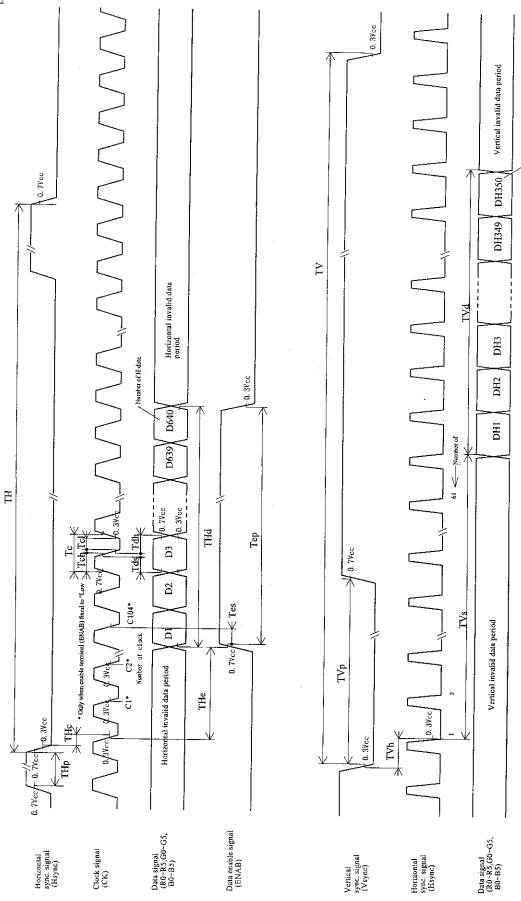


Fig.2-3 Input signal waveforms (350 line mod

8. Input Signals, Basic Display Colors and Gray Scale of Each Col

8. In	put Signals,	Basic D	isplay	Colo	rs and	d Gra	y Sca	le of	Each	Color										
	Colors &	Colors & Data signal																		
	Gray scale	Gray	RA0	RA1	RA2	RA3	RA4	RA5	GA0	GA1	GA2	GA3	GA4	GA5	BA0	BA1	BA2	BA3	BA4	BAS
<u> </u>		Scale	RB0	RB1	RB2	RB3	RB4	RB5	GB0	GB1	GB2	GB3	GB4	GB5	BB0	BBI	BB2	BB3	BB4	BB5
	Black	<u> </u>	0	0	0	_ 0	0_	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Basic	Green	<u> </u>	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
ic (Cyan	<u> </u>	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red		1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
"	Magenta		1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow		1	1	1_	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White		1	1	1	I	1	1	1	1	1	1	1	-1	1	1	I	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	បិ	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
y So	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Û	- ↓	V					4						y						
of	Û	V	V					4						V						
Red	Brighter	GS61	1	0	1	_1	1	I	0	0	0	0	0	0	0	0	0	0	0	0
	û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	î î	GS1	0	0	0	0	0	0	1	0	0	0	0 .	0	0	0	0	0	0	0
Scale	Darker	GS2	0	0	0	0	0 .	0	0	1	0	0	0	0	0	0	0	0	0	0
	Û	↓	4						Ψ					V						
of (û	Ψ	Ψ.					V					V							
Green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
ñ	Û	GS62	0	0	0	0	0	0	0	1	1	1	ı	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Gray Scale	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	Û	_ ↓	Ψ						V					\neg	¥					
of	û	↓	V				V						V							
Blue	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1		1	1
		GS63		0	0	0	0	0	0	0	0	0	0	0	1	1	1			1
$w_0.\Gamma_0$	level voltag	e 1 · Hi	oh lex	zel vo	ltage															

0 :Low level voltage, 1 : High level voltage
Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

ptical Charac	teristics					Т	`a=25°C, V	cc=+5V
Parameter		Symbol	Condition	Min	Тур	Max	Unit	Remark
Viewing	ewing Horizontal θ		C R ≥ 5	60	70	_	Deg.	[Note1, 4]
Angle	Angle Vertical			35	40		Deg.	
Range		θ 12		55	70		Deg.	
Contrast	ratio	CR	θ = 0°	150		_	_	[Note2, 4]
	·		Best Viewing Angle		300			
Response	Rise	τr	$\theta = 0^{\circ}$	_	20		ms	[Note3, 4]
Time	Decay	τd			40		m s	1,000,1
Chromat	Chromaticity of			-	0.313			[Note4]
Wh	White			_	0. 329	_		_ <u>-</u>
Luminance	Luminance of white			160	200	_	cd/m²	·
White Uni	White Unifomity			_	_	1.45		[Note5]
/iewing	Horizontal	θ 21, θ 22	50% of		45	_	Deg.	[Notel]
∖ngle			the					
range as a	Vertical	θ 11	maximum		- 35		Deg.	
Brightness Definition		θ 12	brightness	_	30		Deg.	

*The measurement shall be executed 30 minutes after lighting at rating (typical condition:IL=6.0mArms) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.

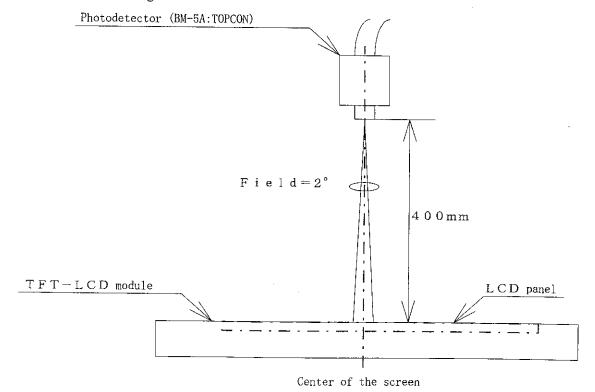
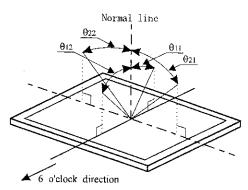


Fig. 3 Optical characteristics measurement method

[Note1] Definitions of viewing angle range:

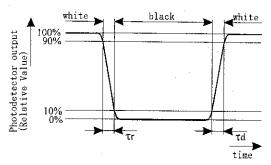


[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

[Note3] Definition of response time:

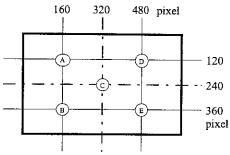
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements $(A \sim E)$.



 $\delta w = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$

10. Display Quantity

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBs on the rear side and should be carefully handled in order not to be stressed.
- j) Laminated film is attached to the module surface to prevent it from being scratched. Peel the film off slowly, just before the use, with strict attention to electrostatic charges. Ionized air shall be blown over during the action. Blow off 'dust' on the polarizer by using an ionized nitrogen gun, etc.
- k) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD. Be careful about the optical interface fringe etc. Which degrades display quality.
- l) Connect GND to 4 place of mounting holes to stabilize against EMI and external noise.
- m) There are high voltage portions on the backlight and very dangerous. Careless touch may lead to electrical shock. When exchange lamps or service. Turn off the power without tail.
- n) Be sure not to apply tensile stress to the lamp lead cable.

12.Packing form

Product country	JAPAN	TAIWAN				
Piling number of cartons	7 cartons	7 cartons				
Packing quantity in one carton	10 modules	10 modules				
Carton size [mm]	298 (W)×362 (D)×295 (H)	$303 \text{ (W)} \times 298 \text{ (D)} \times 374 \text{ (H)}$				
Total mass of one carton filled with full modules	8.0 kg	7.5 kg				
Packing form is shown	Fig .7	Fig .7				

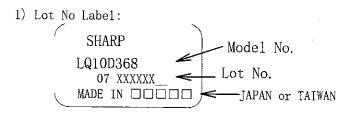
13. Reliability test items

No.	Test item	Conditions
1	High temperature storage test	Ta=70°C 240h
2	Low temperature storage test	Ta=-25°C 240h
3	High temperature	Ta=40°C;95%RH 240h
	& high humidity operation test	(No condensation)
4	High temperature operation test	Ta=50°C 240h
5	Low temperature operation test	Ta=0°C 240h
6	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.075mm
	(non-operating)	: 58~500Hz/Gravity:9.8m/s ²
		Sweep time: 11 minutes
		Test period: 3 hours
		(1 hour for each direction of X,Y,Z)
7	Shock test	Max. gravity : 490m/s ²
	(non-operating)	Pulse width: 11ms, half sine wave
		Direction: $\pm X, \pm Y, \pm Z$
		once for each direction.

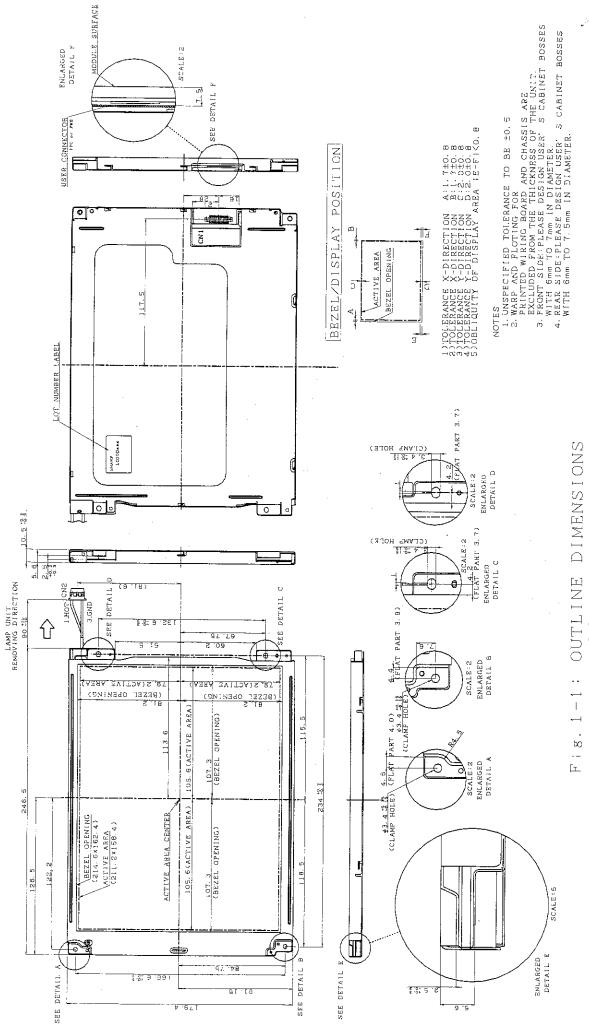
[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

14. Others



- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time
- 5) Do not use LCD module in the atmosphere of corrosive gases, such as sulfide gas or chlorine gases. Polarizer may deteriorated or cause chemical reaction that can lead to short circuits at the terminal Points. Do not use the material, which compounds contain sulfide or chlorine articles in the vicinity of LCD module. At high temperature, these compounds produce corrosive gases.
- 6) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.



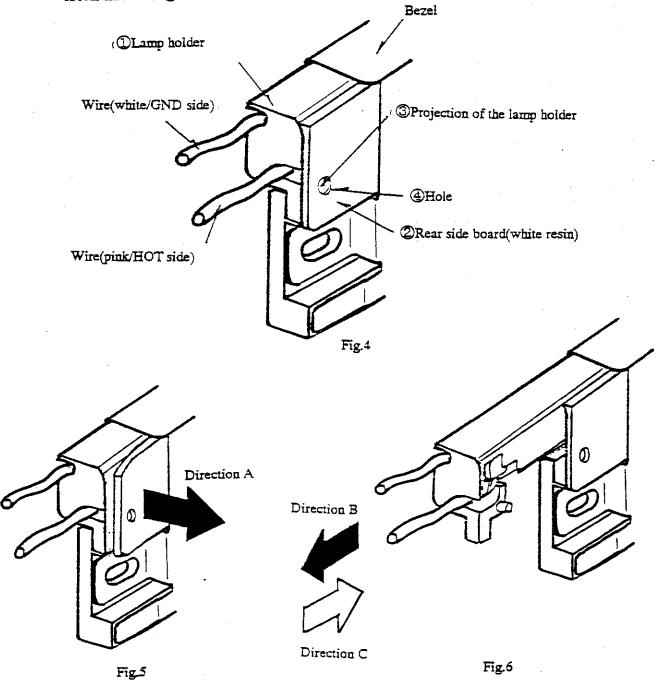
LQ10D368

[EXCHANGING THE LAMP UNIT]

The lamp in the LCD module is consumable and when needed, please replace them including lamp holder in accordance with following 1) - 5) procedure.

XBefore exchanging the lamp unit, turn off the power of the inverter without fail.

- 1) The lamp holder D is fixed with the rear side board D by fitting the projection O of the lamp holder to the hole O of the rear side board. (refer to fig 4)
- 2) The projection can be relieved by bending the rear side board to the direction A with hand or minus driver. (refer to fig 5)
- 3) Draw out the lamp holder (I to the direction B, bending the board (2). (refer to fig 6)
- 4) Insert the spare lamp holder to the direction C (refer to the fig 6)
- 5) Push the lamp holder to the position which the projection 3 is fit in the hole 4 of rear side board 2.



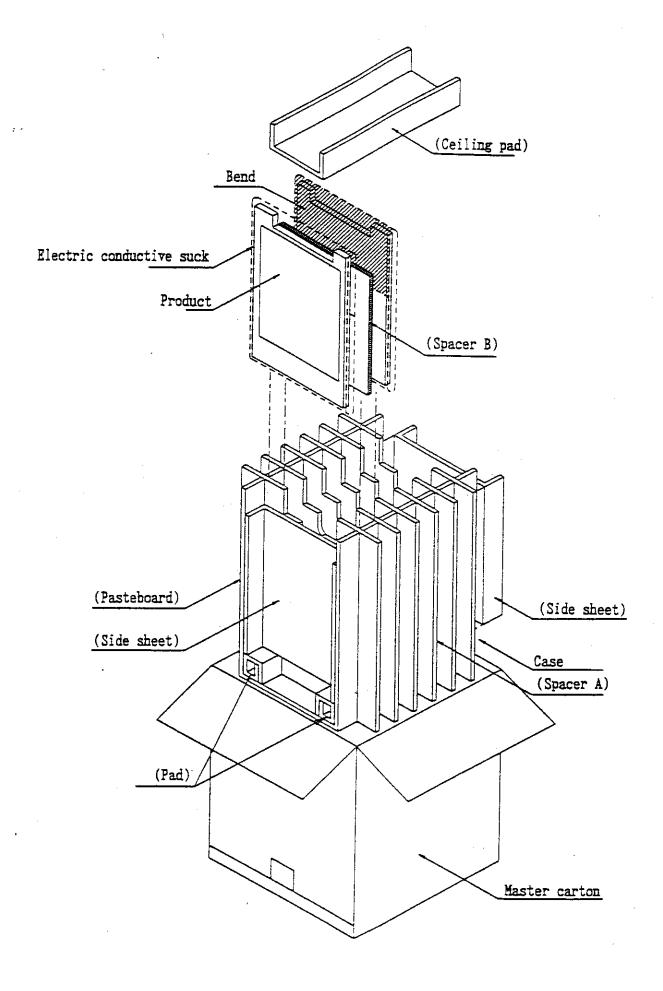


Fig. 7 Packing Form

