

To: Compal Electronics, Inc.

APPROVAL SIGNATURE

## SPECIFICATION

FOR

TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY

TFT-LCD MODULE

**LTM12C324C**

SPECIFICATION No. : \_\_\_\_\_  
NL-LTM12C324C-21

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**Revision History**

Date	Sheet (New)	Item	Old	New	Reason

## Caution and Handling Precaution

For your end user's safety, it is strongly advised that the items with "" should be included in the instruction manual of the system which may be issued by your organization.

### For Safety



#### Warning

- (1) Toshiba Matsushita Display Technology's Standard LCD modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.  
Since they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision System and Air traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.
- (2) DISCONNECT POWER SUPPLY before handling LCD module.  
DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's (hereinafter called "FL") connector or cable in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.
- (3) Make sure to insert the module FL connector to the inverter connector in correct position.  
Do not insert in irregular position.  
If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.  
If there is a possibility that the connector has been inserted incorrectly, please re-insert the connector only after you confirm the module and FL power is completely off.  
DO NOT USE the mating FL connector which Toshiba does not specify.  
Otherwise, Toshiba shall not be liable for any damages caused by the connector.



#### Caution

- (1) DO NOT DISASSEMBLE OR MODIFY the module.  
Sensitive parts inside LCD module may be damaged, and dusts or scratches may mar the displays.  
Toshiba does not warrant the modules, if customer disassembled or modified them.
- \*(2) DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT PERMIT this material to contact the skin, if LCD panel is broken and liquid crystal material spills out.  
In the event of inadvertent contact, immediately rinse the mouth or eyes with adequate water. If this material should inadvertently contact the skin or clothing, wash immediately with alcohol and then rinse thoroughly with water.
- \*(3) BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.
- (4) DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, ambient temperature, etc., otherwise LCD module may be damaged.
- (5) Suitable protection circuit should be applied for each system design.  
DO NOT MODIFY the fuse used in the module. It may cause overheat and/or burning if dusts or metal particles are on the PCBs in the LCD module.

- (6) Be sure that power supply output from the system should be limited to smaller values than listed shown below. (For example Quick Arcing Fuse with listed ratings can be used.)

It is because this LCD module explained in this specification has a current limiter, with such function at power input line(s). But it may be some possibility of overheat and/or burning of LCD module and its peripheral devices before current limiter of the module when open-short test of the module is performed by using power supply higher than following recommended value.

Power supply	Recommended maximum Output current of Power supply	Recommended Fuse Rating (In case of using fuse for current limiter)	Built-in Fuse Rating (for reference)
$V_{DD}$	4.0 A	2.0 A	2.0 A

- (7) Always comply with all applicable environmental regulations, when disposing of LCD.

## For Designing the System

- (1) LCD module should be assembled to the system by using all mounting holes specified in this specification and with the specified screws.

- (2) Make sure to design the enclosure that bending/twisting forces are not applied to LCD module during and after the installation into the system.

Make certain clearance between LCD module and system for not press I/F cable, FPC and pipe light guide etc..

- (3) Power supply lines should be designed as follows.

Power supplies should always be turned on before the input signals are supplied to LCD module, and the input signals should be disconnected before power supplies are turned off.

If the sequence does not satisfy specified conditions, it may cause miss-operation of the panel.

Refer to "2.4.2 Sequence of Power Supplies and Signals" for the detailed specification.

- (4) DO NOT GIVE high voltage to "Low Voltage" side of the FL.

For example, DO NOT USE a floating inverter which gives high voltage to "Low Voltage" side. That's because it has a possibility to burn or smoke around the FL.

- (5) Make sure to connect correctly high-voltage wire and low-voltage wire between FL tube and inverter unit.

- (6) Input FL starting voltage( $V_{SFL}$ ) should not be less than one second.

If it were less than one second, it may cause unstable operation of FL.

Please adjust inverter circuit parameters, such as capacitor, resistor, to assure the display quality is maintained.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency).

- (7) In case of severe environmental condition like outdoor usage, a proper transparent protective cover(lens) over LCD module is recommended to apply in order to prevent scratches, and invasion of dust, water, etc., from the system's window onto LCD module.

Ultra-violet ray cut filter is recommended to apply onto LCD module for outdoor operation. Strong ultra-violet ray may cause damage the panel.

- (8) Design the system not to display same pattern for a long time in order to prevent image sticking on the panel. Note that incorrect sequence of power supplies and input signals may cause the sticking on the panel, too.

## For Installation in Assembly

- (1) The C-MOS LSIs used in LCD module are very sensitive to ESD (Electro-static Discharge).  
Ambient humidity of working area is recommended to be higher than 50%(RH).  
Person handling LCD modules should be grounded with wrist band. Tools like soldering iron and screwdriver, and working benches should be grounded.  
The grounding should be done through a resistor of 0.5-1Mohms in order to prevent spark of ESD.
- (2) When remove protection film from LCD panel, peer off the film slowly (more than three seconds) from the edge of the panel, using a soft-pointed tweezers covered by Teflon or adherent tape.
- (3) Reduce dust level in working area. Especially the level of metal particle should be decreased.  
Use fingerstalls or soft and dust-free gloves in order to keep clean appearance of LCD module when handled for incoming inspection and assembly.
- \* (4) When LCD panel becomes dirty, wipe off the panel surface softly with absorbent cotton or another soft cloth.  
If necessary, breathe upon the panel surface and then wipe off immediately and softly again.  
If the dirt can not be wiped off, absorbent cotton wetted a little with normal-hexane or petroleum benzine can be used for wiping the panel.  
Be careful not to spill this solvent into the inside of LCD module. Driver ICs and PCB area used inside LCD module may be damaged by the solvent.
- \* (5) AVOID THE CONDENSATION OF WATER  
Wipe off a spot or spots of water of mist and chemicals of mist on LCD panel softly with absorbent cotton or another cloth as soon as possible if happened, otherwise discoloration or stain may be caused. If water invade into LCD module, it may cause LCD module damages.
- \* (6) Do not expose LCD module to the gas (which is not normally contained in the atmosphere), it may cause mis-operation or defects.
- \* (7) DO NOT APPLY MECHANICAL FORCES.  
Do not bend or twist LCD module even momentary when LCD module is installed an enclosure of the system.  
Bending or twisting LCD module may cause its damages.  
Make sure to design the enclosure that bending/twisting forces are not applied to LCD module when it is installed in the system.  
Refrain from strong mechanical shock like dropping from the working bench or knocking against hard object.  
These may cause glass of the panel crack, damage of FL or other mis-operation.
- \* (8) Refrain from excessive force like pushing the surface of LCD panel. This may cause damage of the panel or electrical parts on PCB.
- \* (9) Do not put heavy object such as tools, books, etc., and do not pile up LCD modules.  
Be careful not to touch surface of the polarizer laminated to the panel with any hard and sharp object. The polarizer is so soft that it can easily scratched, even the protect film covers it.
- (10) When inserting or disconnecting the connectors to LCD module, be sure not to apply force against PCB, nor connecting cables, otherwise internal connection of PCB and TAB drivers may be damaged.  
Do not fasten screws while putting cables like those for interface or FL between LCD module and the enclosure.  
Make sure to insert the module FL connector to the inverter connector in correct position.  
If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.

- (11) Be careful not to pull the FL cables of the backlight in order to avoid mechanical damage in FL lamp and soldering area. Be careful not to pull or not to hurt the FPC (Flexible Printed Circuit) cables.
- (12) Power supplies should always be turned off in assembling process.  
Do not connect or disconnect the power cables and connectors with power applied to LCD module. This may cause damage of module circuit.  
The signal should be applied after powers are turned on. And the signal should be removed before power supplies are turned off. (Refer to "For Designing The System"(2).)

## For Transportation and Storage

- (1) Do not store LCD module in high temperature, especially in high humidity for a long time (approximately more than one month).  
It is recommended to store LCD module where the temperature is in the range of 0 to 35 degrees Celsius and the relative humidity is lower than 70%.
- (2) Store LCD module without exposure to direct sunlight or fluorescent lamps in order to prevent the module from strong ultra violet ray.
- \*(3) Avoid condensation of water on LCD module, otherwise it may cause mis-operation or defects. Keep away LCD module from such ambient.
- (4) In case of transportation of storage after opening the original packing. LCD module is recommended to be repacked into the original packaging with the same method, especially with same kind of desiccant.

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## 1. Scope

This specification is applicable to Toshiba's 26cm diagonal size TFT-LCD module "LTM12C324C" designed for Notebook Personal Computer.

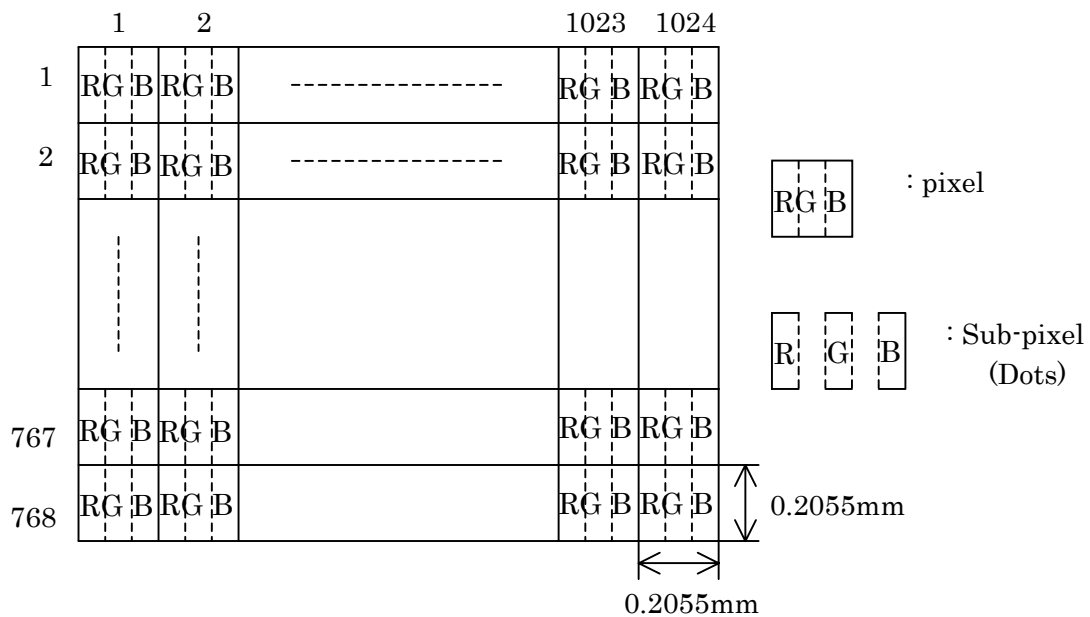
## 2. Product Specifications

### 2.1 General Specifications

Item	Specifications
Display Mode	TN color(64 gray scales, 256k colors) Transmissive type, Normally white (∴ k=1024)
Optimum Viewing Direction	6 o'clock (in direction of maximum contrast)
Driving Method	TFT active matrix
Input Signals	LVDS interface CLK+,CLK- IN0+,IN0- IN1+,IN1- IN2+,IN2- *Use Hsync , Vsync and ENAB control.
Dimensional Outline <sup>1)</sup>	261.0 (W) × 197.5 (H) × 4.9 max. (D) (mm)
Active Area	245.76 (W) × 184.32 (H) (mm)
Viewing Area	250.8 (W) × 189.4 (H) (mm)
Number of Pixels <sup>2)</sup>	1024 (W) × 768 (H) (mm)
Pixel Pitch <sup>2)</sup>	0.240 (W) × 0.240 (H) (mm)
Pixel Arrangement <sup>2)</sup>	RGB vertical stripes
Surface Treatment	Anti-glare and hard coat 3H on LCD surface
Backlight	Single cold-cathode fluorescent lamp for sidelighting

Note 1) Excluding backlight cables.

Note 2)





## 2.2 Absolute Maximum Ratings <sup>1)</sup>

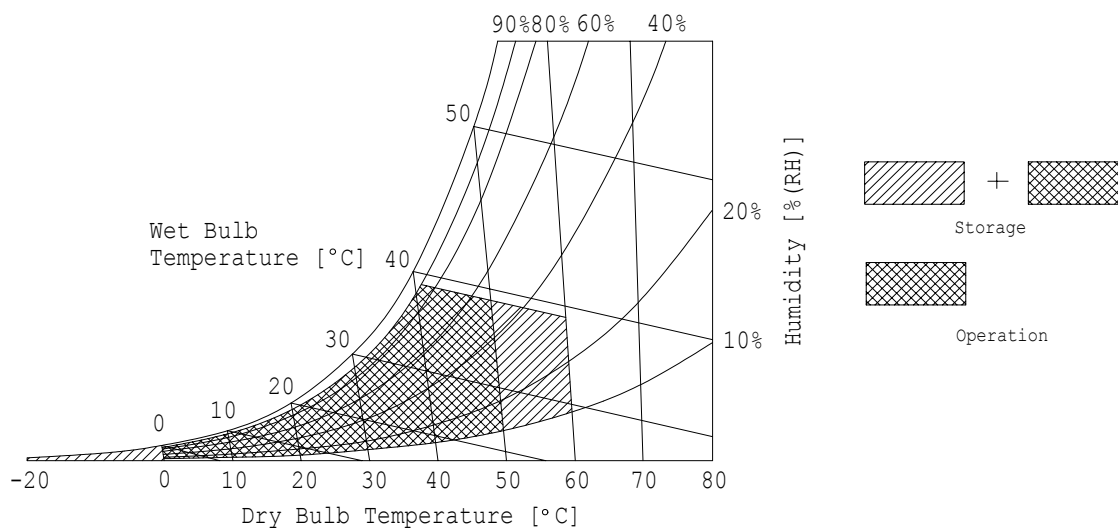
Item	Symbol	Min.	Max.	Unit	Checked Terminal <sup>4)</sup>
Supply Voltage	$V_{DD}$	-0.3	+4.0	V	$V_{DD}$ - GND
Input Voltage of Signals	$V_{IN}$	-0.3	$V_{DD}+0.3$	V	R5 - R0, G5 - G0, B5 - B0 NCLK, ENAB
FL Driving Voltage	$V_{FL}$	-	2.0	kV(rms)	
FL Driving Frequency	$f_{FL}$	-	100	kHz	
Operating Ambient Temperature <sup>2)</sup>	$T_{OP}$	0	+50	°C	
Operating Ambient Humidity <sup>2)</sup>	$H_{OP}$	10	90	%(RH)	
Storage Temperature <sup>2)</sup>	$T_{STG}$	-20	+60	°C	
Storage Humidity <sup>2)</sup>	$H_{STG}$	10	90	%(RH)	
Operating Temperature for Panel <sup>3)</sup>	-	0	+60	°C	

Note 1) Do not exceed the maximum rating values under the worst probable conditions taking into account the supply voltage variation, input voltage variation, variation in part constants, and ambient temperature and so on. Otherwise the module may be damaged.

Note 2) Wet bulb temperature should be 39°C Max, and no condensation of water. See figure below.

Note 3) The surface temperature caused by self heat radiation of cell itself is specified on this item.

Note 4) Refer to 2.4.5



## 2.3 Mechanical Specifications

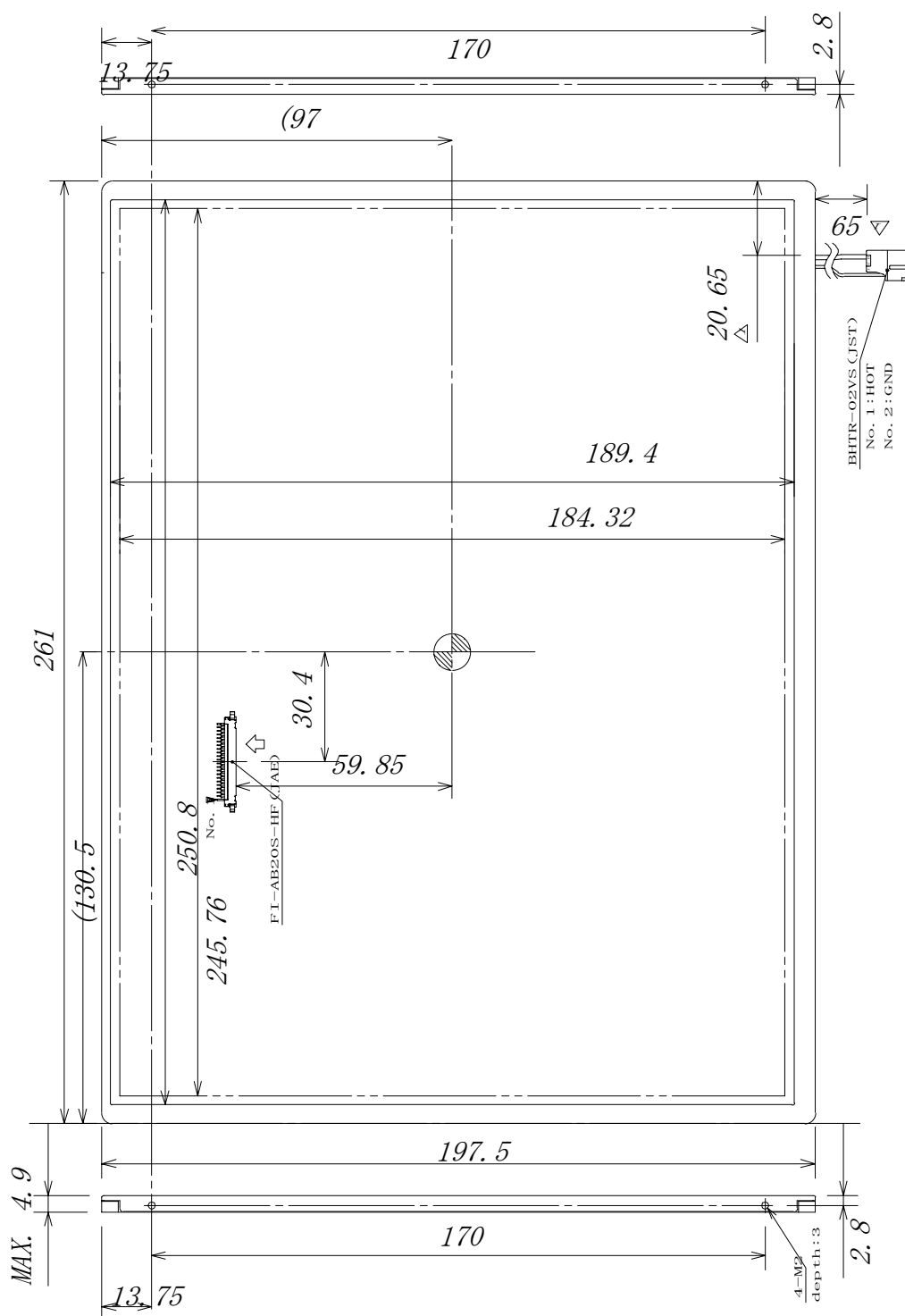
### 2.3.1 Weight

275±20(g)

### 2.3.2 Dimensional Outline (front figure)

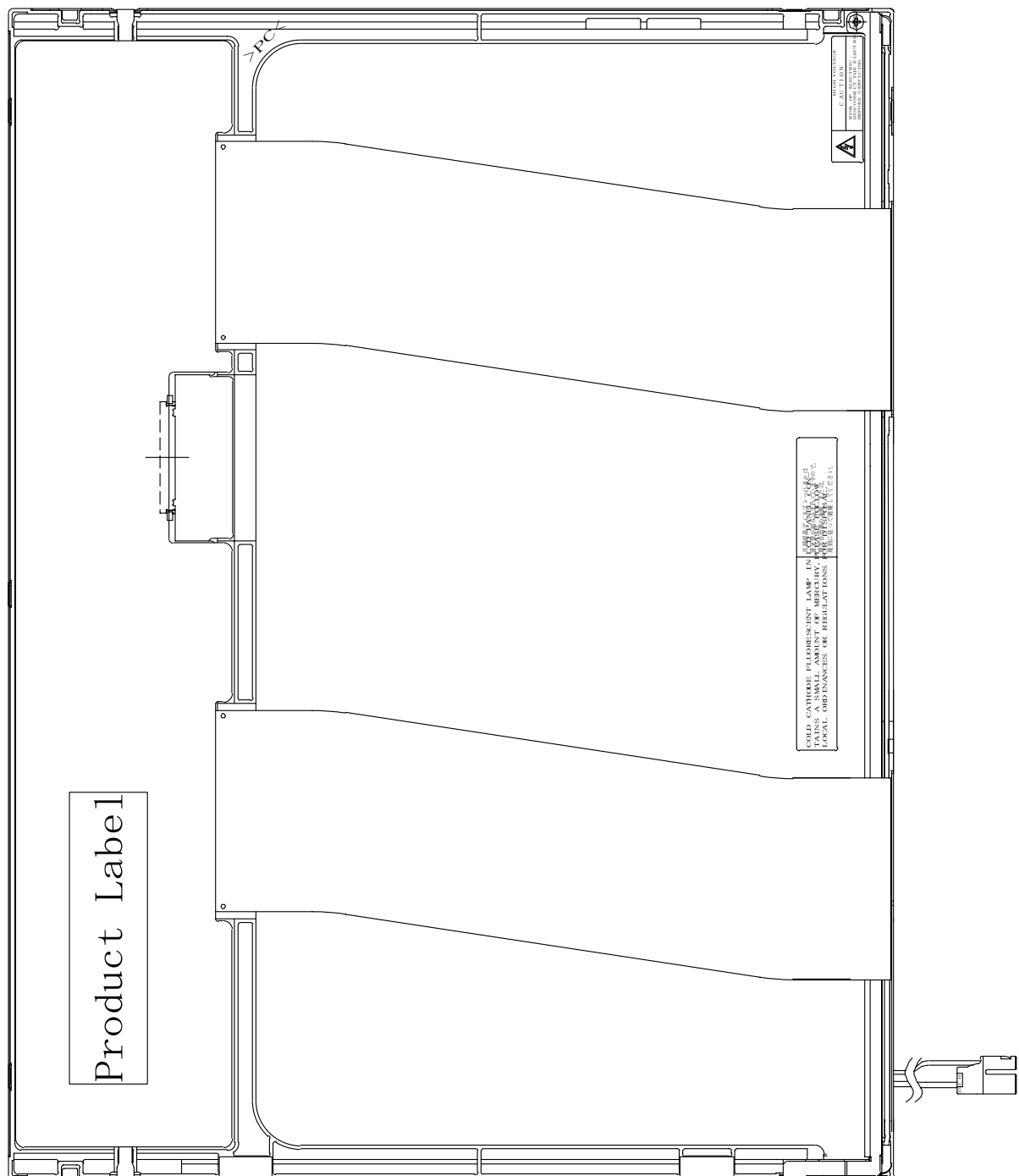
Unit : mm

Standard Tolerance :  $\pm 0.5$



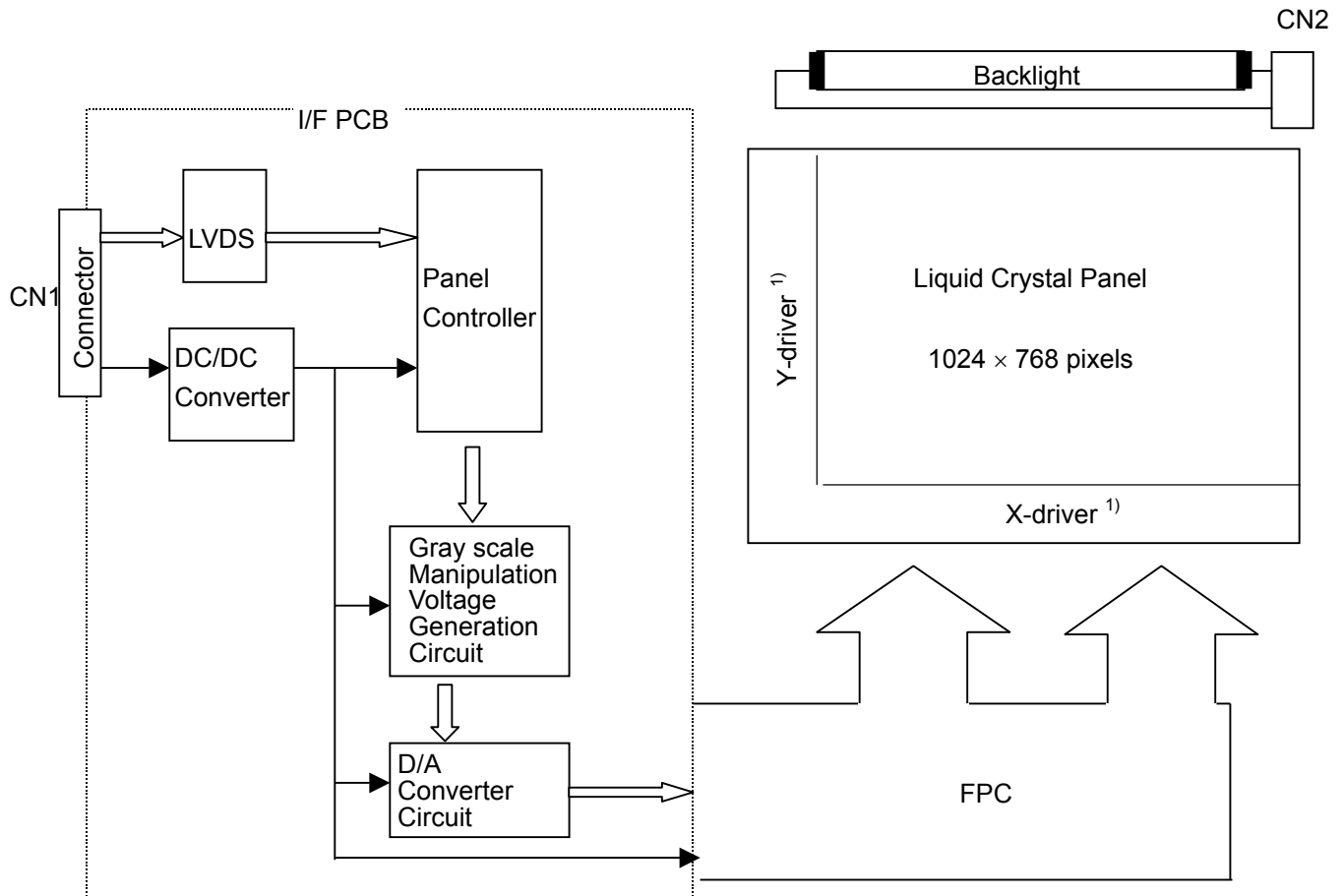
Note 1) When LCD module is assembled to a set, it must be used all attachment holes (four places).

(Rear figure)



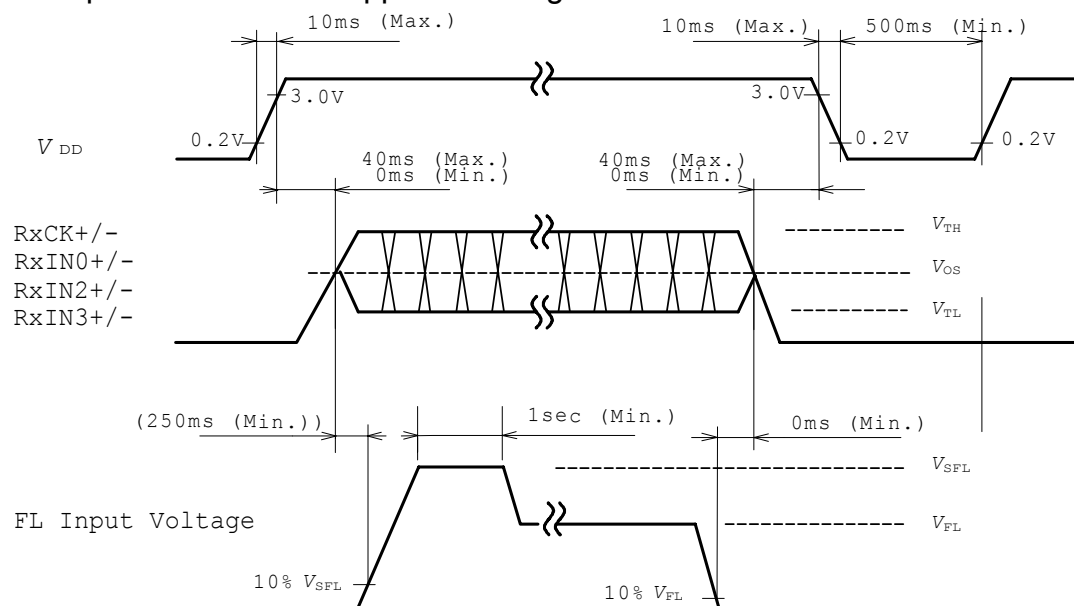
## 2.4 Electrical Specifications

### 2.4.1 Circuit Diagram



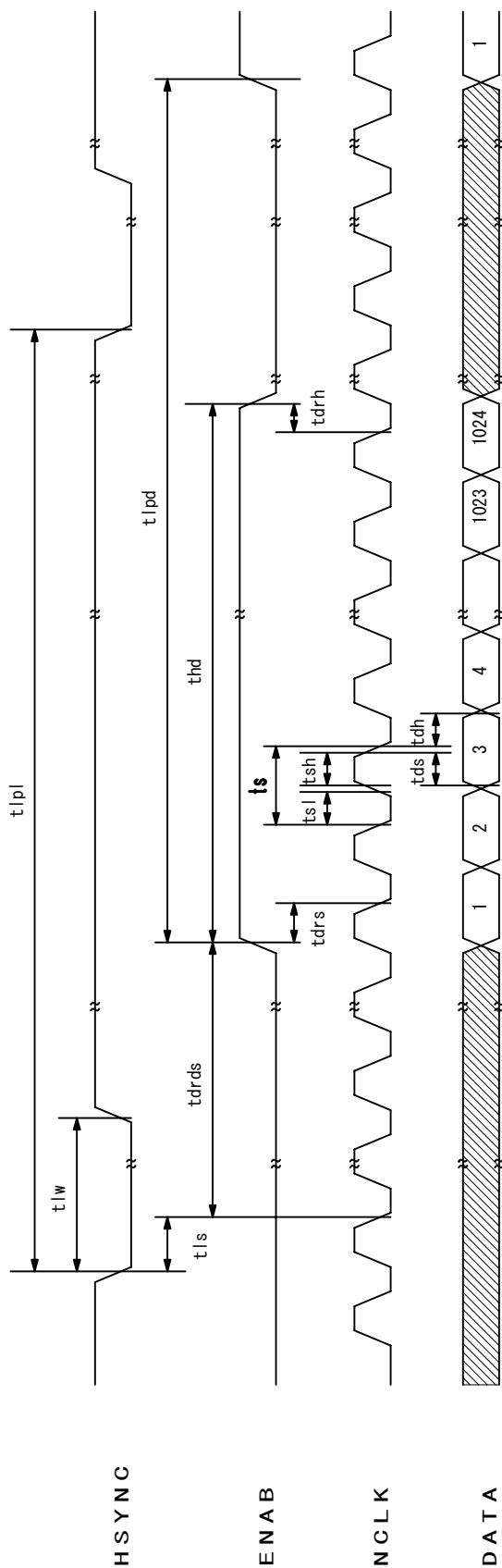
Note 1) Build up LCD drivers on the glass substrate

### 2.4.2 Sequence of Power Supplies and Signals

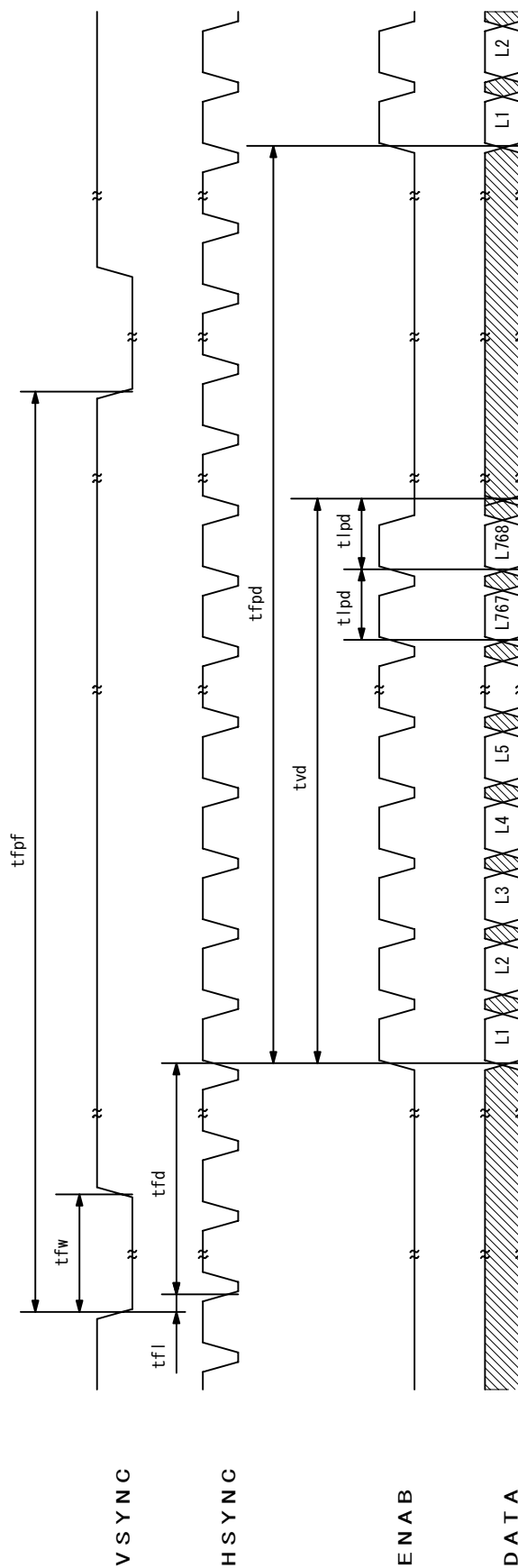


### 2.4.3 Timing Chart

## Horizontal Data Timing



## Vertical Data Timing



2.4.4 Timing Specifications <sup>1) 2) 3) 4) 5) 6)</sup>

Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
NCLK	Clock Period	$t_s$	15	15.38	-	ns	
	Frequency	$1/t_s$	-	65	66.6	MHz	
	High Time	$t_{sh}$	6	-	-	ns	5)
	Low Time	$t_{sl}$	7	-	-	ns	5)
HSYNC	Setup to NCLK	$t_{ls}$	7	-	-	ns	5)
	Pulse Width	$t_{lw}$	$8 \times t_s$	-	-	-	
VSYNC	Pulse Width	$t_{fw}$	$3 \times t_{lpd}$	-	$7 \times t_{lpd}$	-	
	VSYNC to DATA	$t_{fd}$	$7 \times t_{lpd}$	-	-	-	
	Setup to HSYNC	$t_{fl}$	16	-	-	ns	5)
	Line Period	$t_{lpd}=t_{lpl}$	$1319 \times t_s$ 20.04	$1344 \times t_s$ 20.68	$1462 \times t_s$	- $\mu s$	
	Horizontal Display Time	$t_{hd}$	$1024 \times t_s$	$1024 \times t_s$	$1024 \times t_s$	-	
	Frame Frequency	$1/t_{fpd}$	58	60	-	Hz	
	Frame Period	$t_{fpd}=t_{fpf}$	$778 \times t_{lpd}$	$806 \times t_{lpd}$	$860 \times t_{lpd}$	-	
	Vertical Display Time	$t_{vd}$	$768 \times t_{lpd}$	$768 \times t_{lpd}$	$768 \times t_{lpd}$	-	
DATA	Setup	$t_{ds}$	5	-	-	ns	5)
	Hold	$t_{dh}$	7	-	-	ns	5)
ENAB	Setup	$t_{drs}$	10	-	-	ns	5)
	Hold	$t_{drh}$	10	-	-	ns	5)
	Display Start	$t_{drds}$	-	-	$400 \times t_s$	-	

Note 1) Refer to TIMING CHART and LVDS (DF90CF364) specifications by National Semiconductor.

Note 2) If NCLK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.

Note 3) If Hsync or Vsync or ENAB is Fixed to "H" or "L" level for certain period while NCLK is supplied, the panel became display shifted.

Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving Condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.

Note 5) Do not hold NCLK on "H" level nor "L" level during VDD(+3.3V) is supplied. When it holds on, DC voltage supplies to liquid crystal materials and it may cause damage to liquid crystal materials.

Note 6) In case of using the long frame period, the deterioration of

Note 7) These values are determined by the LCD module.

## 2.4.5 Interface Connector

### CN1 INPUT SIGNAL

Using Connector : FI-AB20SR-HF06 / Japan Aviation Electronics Industry, Limited

Mating Connector(Corresponding Connector)

: FI-A20 / Japan Aviation Electronics Industry, Limited

Terminal No.	Symbol	Function
1	V <sub>DD</sub>	Power Supply : +3.3V
2	V <sub>DD</sub>	Power Supply : +3.3V
3	GND <sup>1)</sup>	
4	GND <sup>1)</sup>	
5	IN0-	Transmission Data of Pixels(Differential data) 0 (Negative : - )
6	IN0+	Transmission Data of Pixels(Differential data) 0 (Positive : + )
7	GND <sup>1)</sup>	
8	IN1-	Transmission Data of Pixels(Differential data) 1 (Negative : - )
9	IN1+	Transmission Data of Pixels(Differential data) 1 (Positive : + )
10	GND <sup>1)</sup>	
11	IN2-	Transmission Data of Pixels(Differential data) 2 (Negative : - )
12	IN2+	Transmission Data of Pixels(Differential data) 2 (Positive : + )
13	GND <sup>1)</sup>	
14	CLK-	Sampling Clock (Negative : - )
15	CLK+	Sampling Clock (Positive : + )
16	GND <sup>1)</sup>	
17	NC <sup>2)</sup>	
18	NC <sup>2)</sup>	
19	GND <sup>1)</sup>	
20	GND <sup>1)</sup>	

### CN2 CCFL POWER SOURCE

Using Connector : BHTR-02VS-1 / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

Mating Connector(Corresponding Connector)

: SM02B-BHTS-B-TB / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

Terminal No.	Symbol	Function	Color of Cable
1	VFLH	CCFL Power Supply ( high voltage)	Pink
2	VFLL	CCFL Power Supply (low voltage)	Black

Note 1) Please connect GND pin to ground.

Don't use it as no-connect nor connection with high impedance.

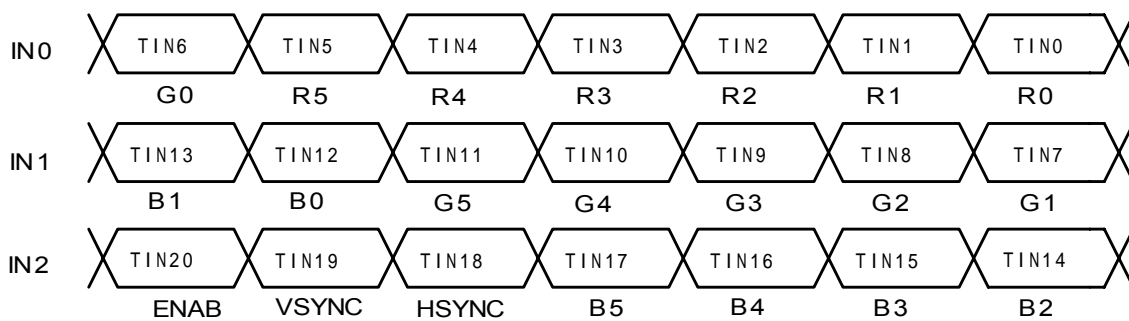
Note 2) NC must keep connection open.

## RECOMMENDED TRANSMITTER (DS90CF363) TO LTM12C324C INTERFACE ASSIGNMENT

## 6bit Transmitter

DS90CF363				LTM12C324C Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	
Symbol	Terminal	Symbol	Function		Terminal Symbol
TIN0	44	R0	Red Pixels Display Data (LSB)	TOUT0-TOUT0+	No.5 No.6 IN0-IN0+
TIN1	45	R1	Red Pixels Display Data		
TIN2	47	R2	Red Pixels Display Data		
TIN3	48	R3	Red Pixels Display Data		
TIN4	1	R4	Red Pixels Display Data		
TIN5	3	R5	Red Pixels Display Data (MSB)	TOUT1-TOUT1+	No.8 No.9 IN1-IN1+
TIN6	4	G0	Green Pixels Display Data (LSB)		
TIN7	6	G1	Green Pixels Display Data		
TIN8	7	G2	Green Pixels Display Data		
TIN9	9	G3	Green Pixels Display Data		
TIN10	10	G4	Green Pixels Display Data	TOUT2-TOUT2+	No.11 No.12 IN2-IN2+
TIN11	12	G5	Green Pixels Display Data (MSB)		
TIN12	13	B0	Blue Pixels Display Data (LSB)		
TIN13	15	B1	Blue Pixels Display Data		
TIN14	16	B2	Blue Pixels Display Data		
TIN15	18	B3	Blue Pixels Display Data	TCLK OUT-TCLK OUT+	No.14 No.15 CLK-CLK+
TIN16	19	B4	Blue Pixels Display Data		
TIN17	20	B5	Blue Pixels Display Data (MSB)		
TIN18	22	HSYNC	H-Sync		
TIN19	23	VSYNC	V-Sync		
TIN20	25	ENAB	Compound Synchronization Signal		
CLK IN	26	CLK	Data Sampling Clock		

Note 1) Please connect NC pin to nothing. Don't connect it to ground nor to other signal input.

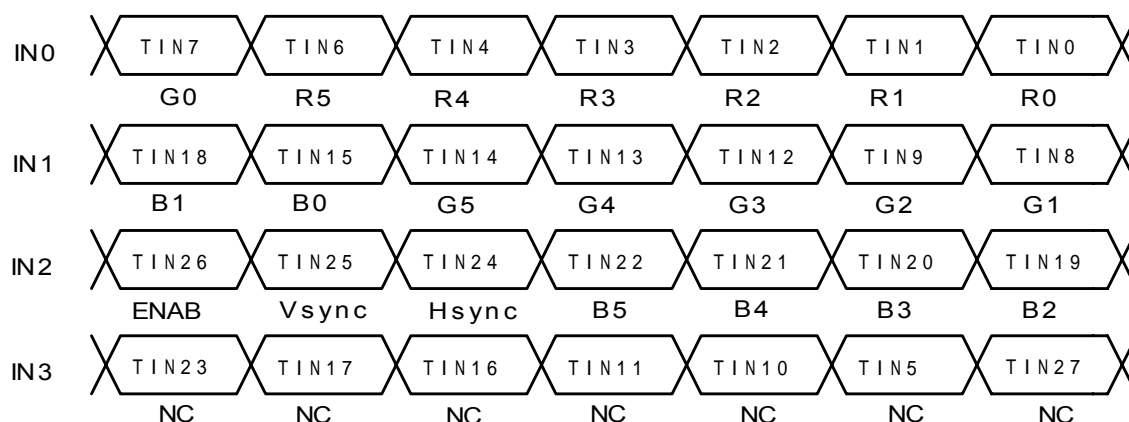




## RECOMMENDED TRANSMITTER (DS90CF383) TO LTM12C324C INTERFACE ASSIGNMENT

## Case2: 8bit Transmitter

DS90CF383				LTM12C324C Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	
Symbol	DS90CF383	Symbol	Function		Terminal Symbol
TIN0	51	R0	Red Pixels Display Data (LSB)	TOUT0- TOUT0+	No.5 No.6 IN0- IN0+
TIN1	52	R1	Red Pixels Display Data		
TIN2	54	R2	Red Pixels Display Data		
TIN3	55	R3	Red Pixels Display Data		
TIN4	56	R4	Red Pixels Display Data		
TIN6	3	R5	Red Pixels Display Data (MSB)	TOUT1- TOUT1+	No.8 No.9 IN1- IN1+
TIN7	4	G0	Green Pixels Display Data(LSB)		
TIN8	6	G1	Green Pixels Display Data		
TIN9	7	G2	Green Pixels Display Data		
TIN12	11	G3	Green Pixels Display Data		
TIN13	12	G4	Green Pixels Display Data	TOUT2- TOUT2+	No.11 No.12 IN2- IN2+
TIN14	14	G5	Green Pixels Display Data(MSB)		
TIN15	15	B0	Blue Pixels Display Data (LSB)		
TIN18	19	B1	Blue Pixels Display Data		
TIN19	20	B2	Blue Pixels Display Data		
TIN20	22	B3	Blue Pixels Display Data	TOUT3- TOUT3+	— — — — —
TIN21	23	B4	Blue Pixels Display Data		
TIN22	24	B5	Blue Pixels Display Data (MSB)		
TIN24	27	Hsync	Horizontal Synchronization Signal		
TIN25	28	Vsync	Vertical Synchronization Signal		
TIN26	30	ENAB	Compound Synchronization Signal	TCLK OUT- TCLK OUT+	No.14 No.15 CLK IN- CLK IN+
TIN27	50	NC	Non Connection (open)		
TIN5	2	NC	Non Connection (open)		
TIN10	8	NC	Non Connection (open)		
TIN11	10	NC	Non Connection (open)		
TIN16	16	NC	Non Connection (open)		
TIN17	18	NC	Non Connection (open)		
TIN23	25	NC	Non Connection (open)		
CLK IN	31	NCLK	Data Sampling Clock		



## 2.4.6 Colors Combination Table

	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0	B5 B4 B3 B2 B1 B0	Gray Scale Level
Basic Color	Black	L L L L L L L	L L L L L L L	L L L L L L L	-
	Blue	L L L L L L L	L L L L L L L	H H H H H H H	-
	Green	L L L L L L L	H H H H H H H	L L L L L L L	-
	Light Blue	L L L L L L L	H H H H H H H	H H H H H H H	-
	Red	H H H H H H H	L L L L L L L	L L L L L L L	-
	Purple	H H H H H H H	L L L L L L L	H H H H H H H	-
	Yellow	H H H H H H H	H H H H H H H	L L L L L L L	-
	White	H H H H H H H	H H H H H H H	H H H H H H H	-
Gray Scale of Red	Black	L L L L L L L	L L L L L L L	L L L L L L L	L 0
	Dark ↑ ↓ Light	L L L L L H L	L L L L L L L	L L L L L L L	L 1
		L L L L L L L	L L L L L L L	L L L L L L L	L 2
		: :	: :	: :	L3... L60
		H H H H L H L	L L L L L L L	L L L L L L L	L61
		H H H H H L L	L L L L L L L	L L L L L L L	L62
	Red	H H H H H H H	L L L L L L L	L L L L L L L	Red L63
Gray Scale of Green	Black	L L L L L L L	L L L L L L L	L L L L L L L	L 0
	Dark ↑ ↓ Light	L L L L L L L	L L L L L L H	L L L L L L L	L 1
		L L L L L L L	L L L L L H L	L L L L L L L	L 2
		: :	: :	: :	L3... L60
		L L L L L L L	H H H H L H L	L L L L L L L	L61
		L L L L L L L	H H H H H L L	L L L L L L L	L62
	Green	L L L L L L L	H H H H H H H	L L L L L L L	Green L63
Gray Scale of Blue	Black	L L L L L L L	L L L L L L L	L L L L L L L	L 0
	Dark ↑ ↓ Light	L L L L L L L	L L L L L L L	L L L L L H L	L 1
		L L L L L L L	L L L L L L L	L L L L H L L	L 2
		: :	: :	: :	L3... L60
		L L L L L L L	L L L L L L L	H H H H L H L	L61
		L L L L L L L	L L L L L L L	H H H H H L L	L62
	Blue	L L L L L L L	L L L L L L L	H H H H H H H	Blue L63
Gray Scale of White & Black	Black	L L L L L L L	L L L L L L L	L L L L L L L	L 0
	Dark ↑ ↓ Light	L L L L L H L	L L L L L H L	L L L L L H L	L 1
		L L L L L L L	L L L L L H L	L L L L H L L	L 2
		: :	: :	: :	L3... L60
		H H H H L H L	H H H H L H L	H H H H L H L	L61
		H H H H H L L	H H H H H L L	H H H H H L L	L62
	White	H H H H H H H	H H H H H H H	H H H H H H H	White L63

Note1 L: Low level voltage, H: High level voltage

3. Recommended Operating Conditions <sup>1) 2) 3)</sup>

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Supply Voltage <sup>4)</sup>	$V_{DD}$	3.0	3.3	3.6	V	
Receiver Input Range		0	-	2.4	V	
Differential Input High Threshold <sup>5)</sup>	$V_{TH}$	-	-	$V_{OS}+0.1$	V	$V_{OS}$ :Offset Mode Voltage $V_{OS}=1.2V$
Differential Input Low Threshold <sup>5)</sup>	$V_{TL}$	$V_{OS}-0.1$	-	-	V	
FL Input Current <sup>6) 7) 8)</sup>	$I_{FL}$	2.0	4.2	6.0	mA(rms)	
FL Driving Voltage <sup>6)</sup>	$V_{FL}$	600	650	700	V(rms)	$I_{FL}=5.0mA(rms)$ (Reference)
		530	580	630	V(rms)	$I_{FL}=6.0mA(rms)$ (Reference)
FL Driving Frequency <sup>6) 9)</sup>	$f_{FL}$	30	50	80	kHz	
FL Starting Voltage <sup>6) 10)</sup>	$V_{SFL}$	1220	-	-	V(rms)	at 0 °C

Note 1) The module should be always operated within these ranges. The "Typ." shows the recommendable value.

Note 2) Recommended LVDS transmitter : DF90CF363MTD,DF90CF383MTD(made by National Semiconductor)

LVDS receiver included in this module is DF90CF364MTD.

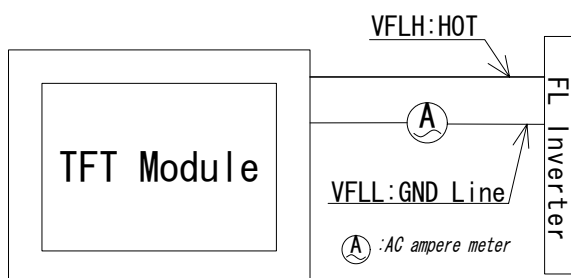
Refer to LVDS specifications.

Note 3) This TFT-LCD module conforms to LVDS standard TIA/EIA-644

Note 4) Checked Pin Terminal:  $V_{DD}$ , GND(0V)

Note 5) Checked Pin Terminal: IN0-~CLK+, GND(0V)

Note 6) Checked Pin Terminal: VFLH-VFLL



Note 7) If FL input current is higher than 6.0mA(rms), then FL lifetime becomes shorter.

Note 8) Measuring Method of  $I_{FL}$

Note 9) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above recommended operating conditions and timing specifications shown in 2.4.4.

Note10) Input FL starting voltage( $V_{SFL}$ ) should not be less than one second.

If it were less than one second, it may cause unstable operation of FL.



## 5. Optical Characteristics

### 5.1 Test Conditions

It is same as 4.1

The measuring method is shown in 11.

### 5.2 Optical Specifications <sup>1)2)</sup>

Item	Symbol	Conditions	Specifications			Unit	Remark
			Min.	Typ.	Max.		
Viewing Angle	$\theta$	$CR \geq 10$	$\phi = 180^\circ$	10	-	-	$^\circ$
			$\phi = 0^\circ$	30	-	-	$^\circ$
			$\phi = 90^\circ$	35	-	-	$^\circ$
			$\phi = -90^\circ$	35	-	-	$^\circ$
Contrast Ratio	CR	$\theta=0^\circ, \phi=0^\circ$	140	-	-	-	
Response Time	$t_{ON}$	$\theta=0^\circ, \phi=0^\circ$	-	-	50	ms	
	$t_{OFF}$		-	-	50	ms	
Luminance	L	$\theta=0^\circ, \phi=0^\circ$ Gray Scale Level=L63 (White)	105	130	-	$cd/m^2$	$I_{FL}=4.2mA$ (rms)
			130	150	-		$I_{FL}=6.0mA$ (rms) (Reference)
Luminance Uniformity	LUNF	$\theta=0^\circ, \phi=0^\circ$ Gray Scale Level=L63 (White)	55	-	-	%	
Chromaticity	Red	$x_R$	0.57	0.61	0.65	-	
		$y_R$	0.31	0.34	0.37	-	
	Green	$x_G$	0.28	0.31	0.34	-	
		$y_G$	0.52	0.55	0.58	-	
	Blue	$x_B$	0.12	0.15	0.18	-	
		$y_B$	0.10	0.14	0.18	-	
	White	$x_W$	0.29	0.34	0.39	-	
		$y_W$	0.29	0.34	0.39	-	

Note 1) Refer to "11. Measuring Method".

Note 2) It is the value of Toshiba standard inverter use.

## 6. Quality

### 6.1 Inspection AQL

Total of Major Defects : AQL 0.65 %

Total of Minor Defects : AQL 1.5 %

Sampling Method: ANSI/ASQC Z1.4(level II)

### 6.2 Test Conditions

- 1) Ambient Temperature :  $25 \pm 5^{\circ}\text{C}$
- 2) Ambient Humidity :  $65 \pm 20\%(\text{RH})$
- 3) Illumination : Approximately 500 lx under the fluorescent lamp
- 4) Viewing Distance : Approximately 30cm by the eyes of the inspector from the module
- 5) Inspection Angle :  $\theta = 0^{\circ}$ ,  $\phi = 0^{\circ}$

### 6.3 Dimensional Outline

The products shall conform to the dimensions specified in 2.3.2.

Definition of Major and Minor defects are as follows.

Item	Description	Class
Important Dimensions	Dimensional outline, Dimensional between the mounting holes.	Major
Others	Dimensions specified in this specifications	Minor

## 6.4 Appearance Test

### 6.4.1 Test Conditions

- 1) Condition : Non-operating, operating  
Same as 6.2

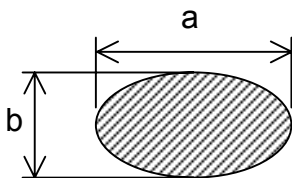
### 6.4.2 Specifications

Item	Description	Class												
PCB Appearance	Pattern peeling snapping, electrically short	Major												
	Repair portion on PCB is not covered by epoxy resin													
Soldering	Cold solder joint, lead move when pulled	Minor												
Bezel, Frame, Connectors	Distinct stain, rust or scratch	Major												
Black and White Spots/Lines <sup>1)2)</sup>	<table><tr><th>Line width(mm)</th><th>Length(mm)</th><th>Acceptable count</th></tr><tr><td><math>W &lt; 0.01</math></td><td><i>neglect</i></td><td>neglect</td></tr><tr><td><math>0.01 \leq W &lt; 0.2</math></td><td><math>L \leq 3</math></td><td><math>n \leq 2</math></td></tr><tr><td><math>0.2 \leq W</math></td><td>-</td><td><math>n = 0</math></td></tr></table>		Line width(mm)	Length(mm)	Acceptable count	$W < 0.01$	<i>neglect</i>	neglect	$0.01 \leq W < 0.2$	$L \leq 3$	$n \leq 2$	$0.2 \leq W$	-	$n = 0$
			Line width(mm)	Length(mm)	Acceptable count									
			$W < 0.01$	<i>neglect</i>	neglect									
			$0.01 \leq W < 0.2$	$L \leq 3$	$n \leq 2$									
	$0.2 \leq W$	-	$n = 0$											
	<table><tr><th>Average diameter(mm)</th><th>Acceptable count/side</th></tr><tr><td><math>0.2 \leq D &lt; 1.0</math></td><td><math>n \leq 2</math></td></tr></table>		Average diameter(mm)	Acceptable count/side	$0.2 \leq D < 1.0$	$n \leq 2$								
			Average diameter(mm)	Acceptable count/side										
$0.2 \leq D < 1.0$			$n \leq 2$											

Note 1) Inspection area should be within viewing area.

Note 2) Dusts which are bigger not less than 0.15mm ( $0.1 \leq W$ ) shall be judged by "Average Diameter".

Average Diameter  $D = (a+b)/2$



## 6.5 Display Quality

### 6.5.1 Test Conditions

- 1) Inspection Area : Within viewing area
- 2) Condition : Same as test conditions shown in 4.1 and 6.2
- 3) Test Pattern : White display pattern (gray scale level L63) and black display pattern (gray scale level L0)

### 6.5.2 Specifications

Item	Description / Specifications	Class
Function	No display, Malfunction	Major
Display Quality <sup>1)2)3)</sup>	Missing line	Major
	Missing Sub-Pixels Apply from '02 Dec. to '03 Feb. 1) Bright defects : 6pcs. maximum 2) Dark defects : 6pcs. maximum 3) Total sub-pixel defects : 8pcs. maximum 4) Minimum distance between sub-pixels : 15 mm minimum 5) 2 Consecutive defect : 1 sets maximum  Apply from '03 Mar.(Manufacturing Base) 6) Bright defects : 4 pcs. maximum 7) Dark defects : 5 pcs. maximum 8) Total sub-pixel defects : 5pcs. maximum 9) Minimum distance between sub-pixels : 15 mm minimum 10) 2 Consecutive defect : 1 sets maximum	Minor
	Inconspicuous flicker, crosstalk, Newton's ring and other defects : neglect	-
	Black and White Spots/line	-
Backlight	Missing (Non-operating)	Major

Note 1) Defects of both color filter and black matrix are counted as bright or dark defects.

Inspection area should be within the active area.

Note 2) Bright defect means a bright spot(sub-pixel) on the display pattern of gray scale L0.

Dark defect means a dark spot(sub-pixel) on the display pattern of gray scale L63.

Note 3) Bright spot which can not be found by using 5%ND-Filter shall not be counted as a defect.



## 6.6 Reliability Test

### 6.6.1 Test Conditions

- 1) The module should be driven and inspected under normal test conditions(refer to 4.1).
- 2) The module should not have condensation of water (moisture) on the module.
- 3) The module should be inspected after two or more hours storage in normal conditions (15 - 35°C,45-5%(RH)).
- 4) A module shall be used only for one test.

### 6.6.2 Specifications

Test Item	Test Conditions	Result
High Temperature Operation <sup>1)</sup>	50°C 192 h	OK 3p/3p
High Temperature Storage <sup>2)</sup>	60°C 192 h	OK 3p/3p
High Temperature High Humidity operation <sup>1)</sup>	50°C 80% 192 h	OK 3p/3p
Low Temperature Operation <sup>1)</sup>	0°C 192 h	OK 3p/3p
Low Temperature Storage <sup>2)</sup>	-20°C 192 h	OK 3p/3p
Temperature Shock <sup>2)</sup>	-20°C ⇔ 60°C 0.5h 0.5h 50 cycles	OK 3p/3p

Note 1) Operating

Note 2) Non-Operating

Definitions of failure for judgment shall be as follows:

- 1) Function of the module should be maintained.
- 2) Current consumption should be smaller than the specified value.
- 3) Appearance and display quality should not have distinguished degradation.
- 4) Luminance should be larger than 50% of the minimum value specified in 5.2.

## 6.7 Labels

## (1) Product Label

Serial number :  $\triangle\triangle$   $\blacktriangle$   $1A$   $000001$   
                             ①   ②   ③   ④

① : Module type code

② : Manufacturing code

③ : Lot code   1   A

(1) (2)

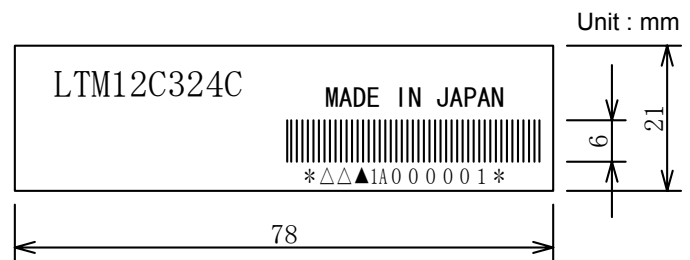
(1):Year code-end of the A.D.

(2):Month code-alphabet

→Jan. : A - Dec. : L

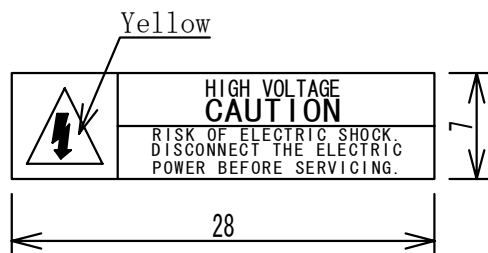
④ : Serial code

decimal, 6 figures

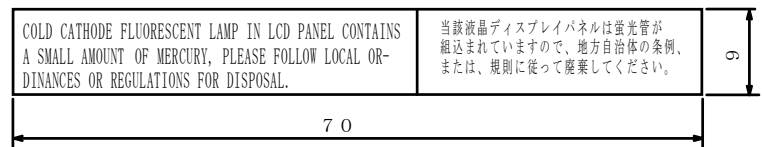


## (2) Caution Labels

## • High Voltage

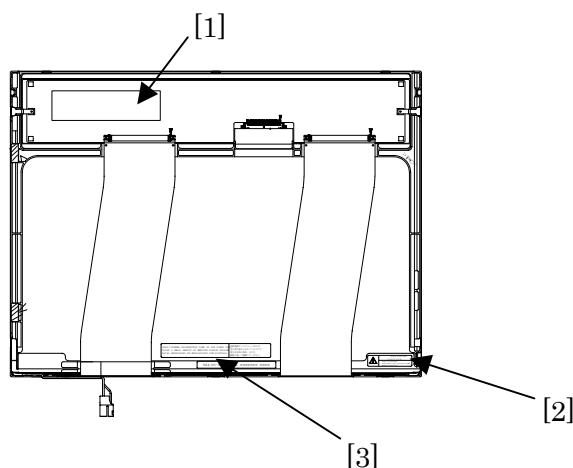


## • Disposal of CCFL



Unit : mm

## (3) Label Locations



[1] Product Label

[2] Caution Label

[3] Disposal Label

## 7. Lifetime

### 7.1 Module (except lamp)

MTTF (Mean Time To Failure) : 50,000 h

(This value is not assurance time but inference value by following conditions.)

Conditions : Ambient temperature :  $25\pm 5^{\circ}\text{C}$  (No wind)

Ambient humidity : 65%(RH)

## 7.2 Lamp

### 7.2.1 Test Conditions

Ambient temperature :  $25\pm 5^{\circ}\text{C}$  (No wind)

Lamp current : 5.0mA(rms)

Lighting condition : continuous lighting

Driving frequency : 50kHz

### 7.2.2 Specifications

MTBF : 10,000 h

Definitions of failure for judgment shall be as follows.

- 1) LCD luminance becomes half of the minimum value.
- 2) Lamp doesn't light normally.

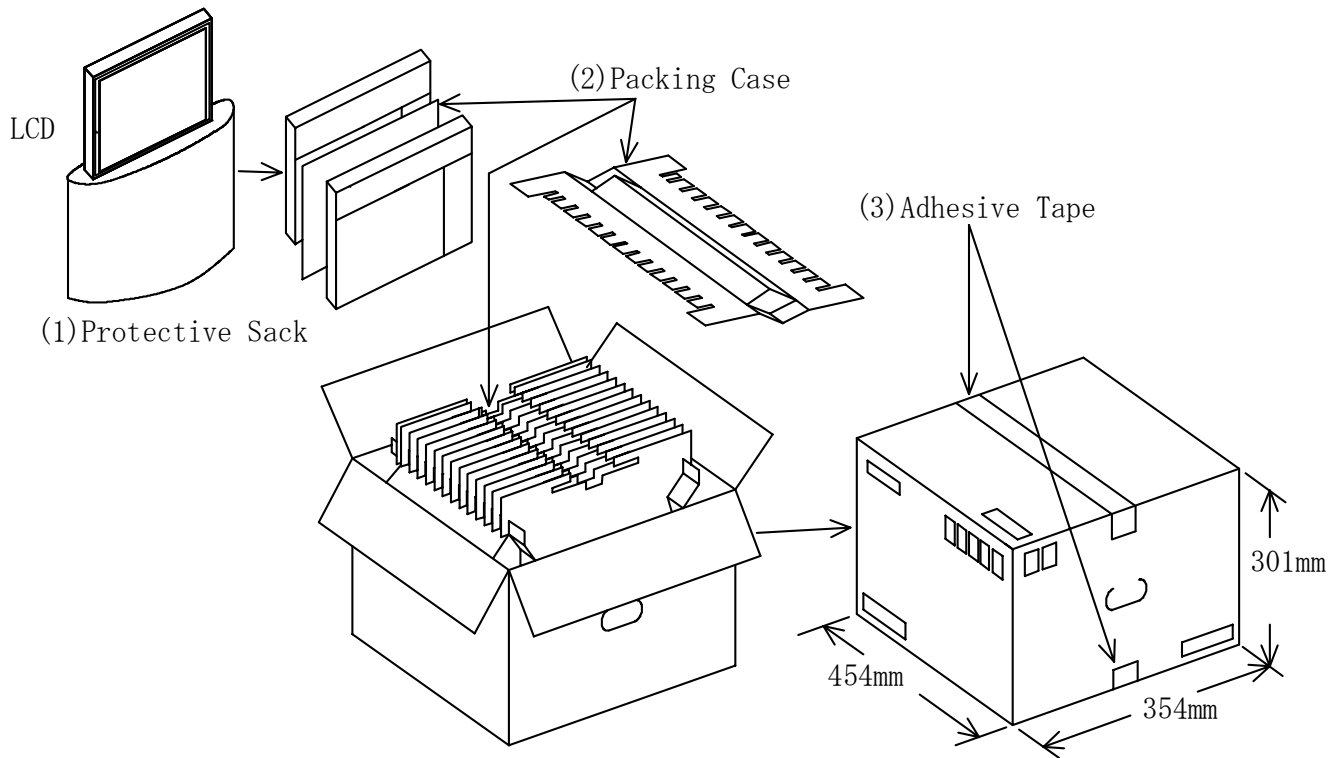
## 8. Packaging

### 8.1 Carton (internal package)

#### (1) Packaging Form

Corrugated cardboard box and polyethylene foam as shock absorber

#### (2) Packaging Method <sup>1)2)</sup>



Note 1) : Acceptable number of piling : 25 sets

#### (3) Packaging Material

Number	Quantity	Description
(1)	25	Static electricity Protective sack
(2)	1 set	Packing case
(3)	1 set	Plastics adhesive tape

## 9. Warranty

Warranty clause will be decided separately.

## 10. Regulation

The set (which our LCD module is assembled into) to conform the regulations below, take measures in set side.

Toshiba is not liable for the regulations to the complete set, nor can guarantee our LCD module conform the regulation by itself.

### a) Examples of EMI Regulations

FCC : PART15 CLASS B

VCCI : CLASS B

CISPR : CLASS B

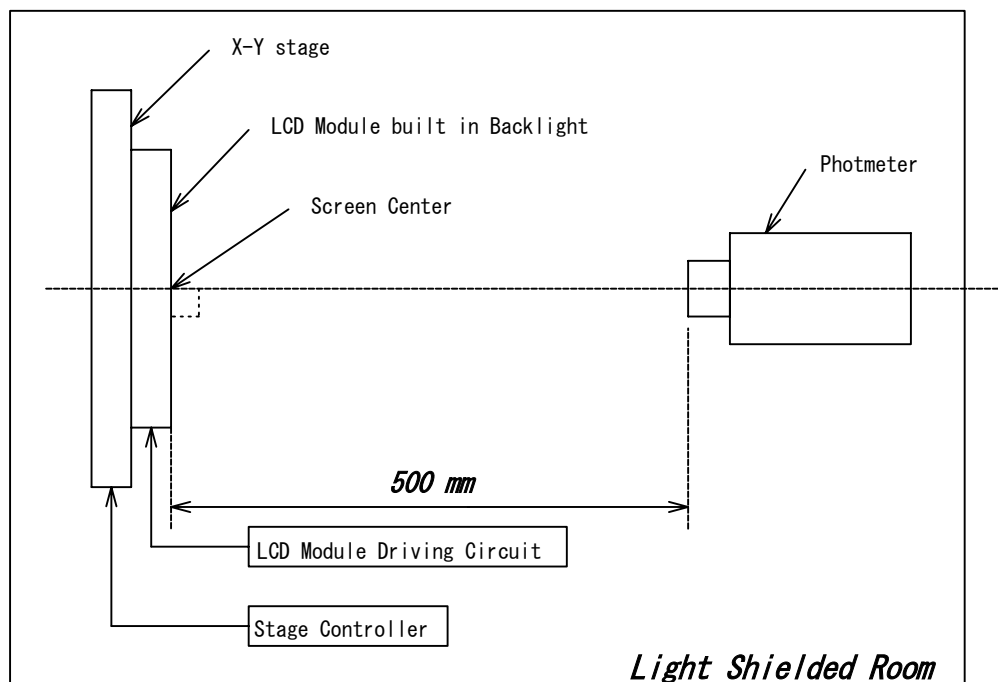
### b) Examples of Safety Regulations

IEC 950

UL 1950

## 11. Measuring Method

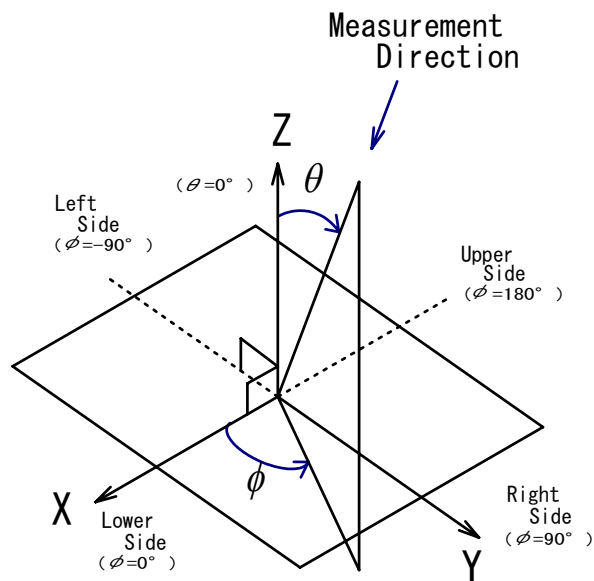
### 11.1 Measuring System



(1) The measurement point is the center of the active area except the measurement of Luminance Uniformity.

(2) Photometer : BM-7/BM-5A TOPCON (Aperture 2° )

(3) Definition of  $\phi$  and  $\theta$ :



## 11.2 Measuring Methods

(1) Luminance:

The luminance of the center on a white raster (gray scale level L63) shall be measured.  
Measurement shall be executed 30 minutes after the lamp is lit up.

(2) Contrast Ratio:

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L63 / L0$$

L63 : Luminance on the white raster (gray scale level L63)

L 0 : Luminance on the black raster (gray scale level L0)

(3) Viewing Angle

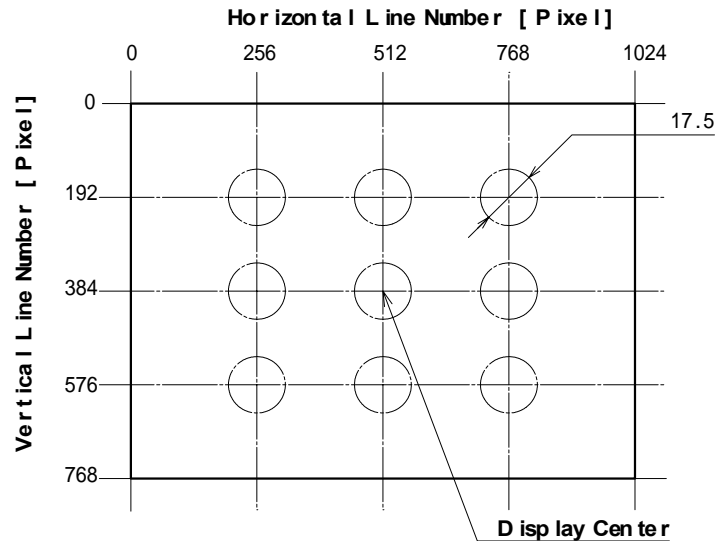
Viewing angle is defined as the angles( $\theta, \phi$ ), in which specified contrast ratio can be obtained.  
(Refer to 11.1(3) for the axes.)

## (4) Luminance Uniformity:

The Luminance should be measured at 9 positions on white raster(gray scale level L63).

Uniformity can be calculated by the following expression.

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance}}{\text{Maximum Luminance}} \times 100\%$$

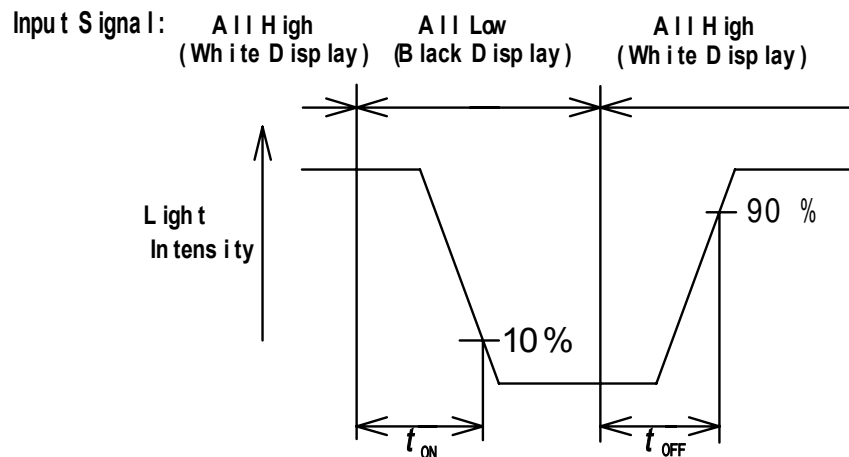


## (5) Chromaticity :

The values(x,y) of chromaticity coordinates should be measured for the White, Red, Green and Blue Raster(gray scale level L63) each with a photometer.

## (6) Response Time :

The response time ( $t_{ON}$ ,  $t_{OFF}$ ) is measured with a photo detector (photodiode) which measures the light intensity of the pixels.



$t_{ON}$  : Turn on time is the time for a photo detector output waveform to go from its maximum value to 10% of its maximum.

$t_{OFF}$  : Turn off time is the time for a photo detector output waveform to go from zero to 90% of its maximum.

Photodiode : S1223-01 HAMAMATSU PHOTONICS K.K.

White Display : White Raster (gray scale level L63)

Black Display : Black Raster (gray scale level L0)