

SPECIFICATION FOR APPROVAL

()	Preliminary Specification
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(◆) Final Specification

Title 14.0"W HD TFT LCD

Customer	TOSHIBA
MODEL	

SUPPLIER	LG Display Co., Ltd.			
*MODEL	LP140WH1			
Suffix	TLA3			

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE					
Please return 1 copy for your confirmation with your signature and comments.						

APPROVED BY	SIGNATURE
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PREPARED BY	p
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C. H. Lee / Engineer	DHHA.
	CAN'
Products Engineeri LG Display Co.	•

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Dec.30. 2009	-	Preliminary Specification	0.0
0.1	Feb.18. 2010	13,14	Update optical Specification	0.1
0.2	Mar.17. 2010	6,7	Update electrical Characteristics	
		13	Update power sequence	
		21-23	Update LGD proposal for system cover design	
		25	Update Internal standards	
1.0	Mar.29. 2010	-	Final Specification	1.0

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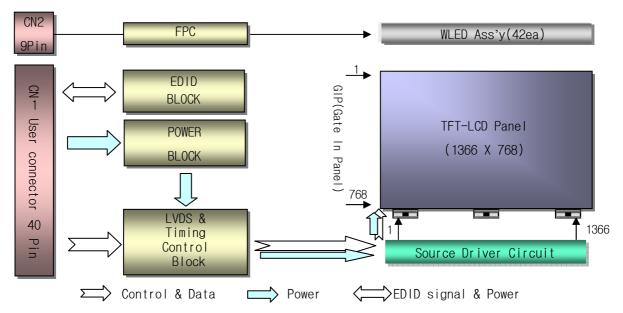


1. General Description

The LP140WH1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 14.0 inches diagonally measured active display area with HD resolution(768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP140WH1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP140WH1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP140WH1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	14.0 inches diagonal
Outline Dimension	$323.5(H, typ) \times 192.0(V, typ) \times 5.2(D,max)$ [mm]
Pixel Pitch	0.2265mm × 0.2265 mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	220 cd/m ² (Typ.5 point)
Power Consumption	Total 4.5 Watt(Typ.) @ LCM circuit 1.3 Watt (Typ.), B/L input 3.2 Watt(Typ.)
Weight	350g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer
RoHS Comply	Yes

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2. Absolute Maximum Ratings

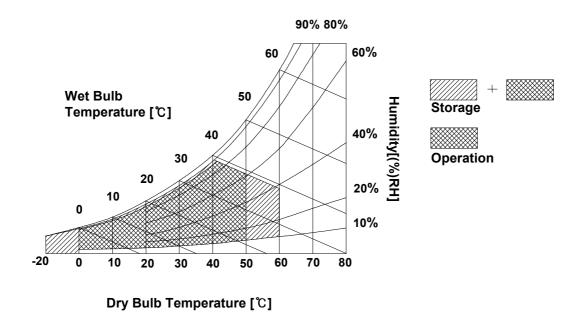
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
i didilictei	Symbol	Min	Max	Office	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Hst	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP140WH1 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

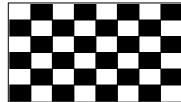
Parameter		Cumb al	Values			I I m i 4	Notes
		Symbol	Min	Тур	Max	Unit	Notes
LOGIC:							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc	-	400	468	mA	2
Power Consumption		Pcc	-	1.3	1.5	W	2
Power Supply Inrush Current		Icc_p	-	-	1500	mA	3
LVDS Impedance		ZLVDS	90	100	110	Ω	4
BACKLIGHT : (with LED Drive	er)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	5
LED Power Input Current		ILED	-	265	285	mA	6
LED Power Consumption		PLED	-	3.2	3.4	w	6
LED Power Inrush Current		ILED_P	-	-	-	mA	7
PWM Duty Ratio			5	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		Zpwm	20	40	60	k Ω	
PWM Frequency		FPWM	200	_	1000	Hz	10
PWM High Level Voltage		V _{PWM_H}	3.0	-	5.3	V	
PWM Low Level Voltage		V _{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zpwm	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	3.0	-	5.3	V	
LED_EN Low Voltage		VLED_EN_L	0	-	0.3	V	
Life Time			12,000	-	-	Hrs	11

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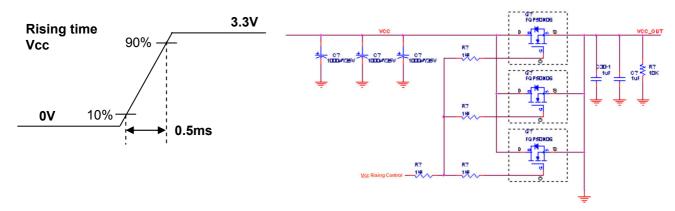


Note)

- 1. The measuring position is the connector of LCM and the test condition is under 25 °C, fv = 60Hz, Black pattern.
- 2. The specified Icc current and power consumption are under the Vcc = 3.3V, $25\,^{\circ}$ C, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.

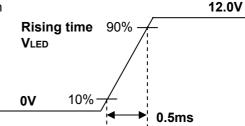


3. The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same the minimum of T1 at Power on sequence.



- 4. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25 $^{\circ}$ C.
- 6. The current and power consumption with LED Driver are under the V_{LED} = 12.0V , 25°C, Dimming of Max luminance whereas White pattern is displayed and fv is the frame frequency.
- 7. The below figures are the measuring VLED condition and the VLED control block LGD used.

VLED control block is same with Vcc control block.



- 8. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum. It may cause flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.

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3-2. Interface Connections

This LCD employs two interface connections, a 40 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model CABLINE-VS RECE ASS'Y manufactured by I-PEX.

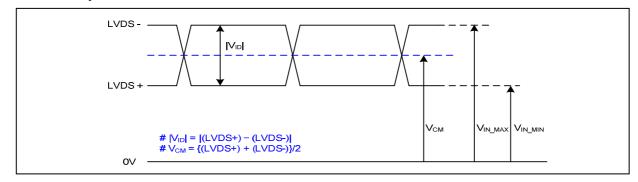
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No Connection	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips 1.1 LCD: SW, SW0624 (LCD Controller)
5	NC	No Connection	including LVDS Receiver
6	CIK EEDID	DDC Clock	1.2 System : THC63LVDF823A
7	DATA EEDID	DDC Data	or equivalent
8	Odd_R _{IN} 0-	Negative LVDS differential data input	* Pin to Pin compatible with LVDS
9	Odd_R _{IN} 0+	Positive LVDS differential data input	2. Connector
10	GND	Ground	2.1 LCD : CABLINE-VS RECE ASS'Y, I-PEX
11	Odd_R _{IN} 1-	Negative LVDS differential data input	or its compatibles 2.2 Mating: CABLINE-VS PLUG CABLE
12	Odd_R _{IN} 1+	Positive LVDS differential data input	ASS'Y or equivalent.
13	GND	Ground	2.3 Connector pin arrangement
14	Odd_R _{IN} 2-	Negative LVDS differential data input	
15	Odd_R _{IN} 2+	Positive LVDS differential data input	40 ПППП
16	GND	Ground	
17	Odd_CLKIN-	Negative LVDS differential clock input	
18	Odd_CLKIN+	Positive LVDS differential clock input	[LCD Module Rear View]
19	GND	Ground	[EOD Module Real view]
20	NC	No Connection	
21	NC	No Connection	
22	GND	Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	NC	No Connection	
35	PWM	PWM for luminance control	
36	LED_EN	Backlight On/Off Control	
37	NC	No Connection (Reserved)	
38	VLED	LED Power Supply 7V-21V	
39	VLED	LED Power Supply 7V-21V	
40	VLED	LED Power Supply 7V-21V	



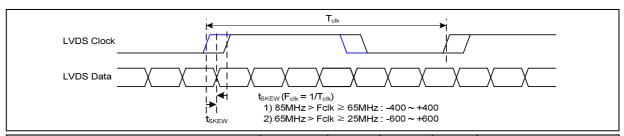
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

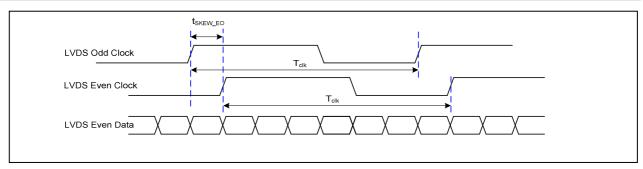
3-3-2. AC Specification



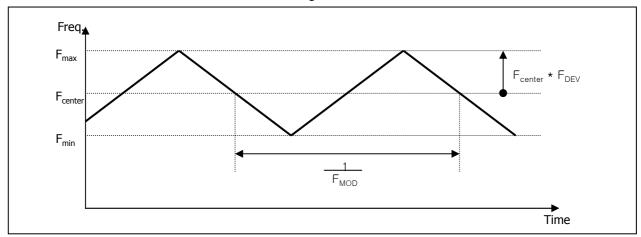
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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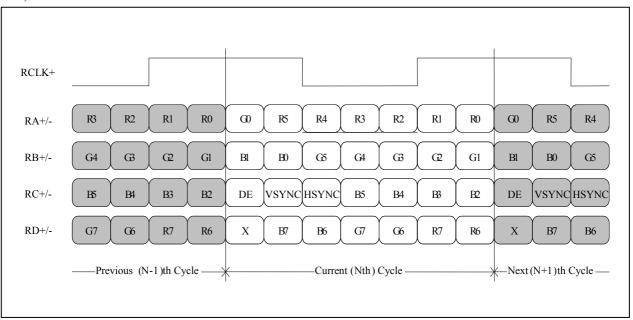
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Condition: VCC =3.3V

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Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 4. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	68.7	72.3	76.2	MHz	
	Period	t _{HP}	1470	1526	1586		
Hsync	Width	t _{wH}	23	32	40	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	779	790	801		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{wva}	768	768	768		
	Horizontal back porch	t _{HBP}	72	80	124	+CLK	
Data	Horizontal front porch	t _{HFP}	8	48	48	ICLK	
Enable	Vertical back porch	t _{VBP}	8	14	20	+HD	
	Vertical front porch	t _{VFP}	1	3	5	768 124 48 20 tHP	

3-5. Signal Timing Waveforms

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High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc DCLK t_{HP} Hsync **t**wha t_{HFP} t_{HBP} Data Enable Vsync t_{VFP} twva t_{VBP} Data Enable

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3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
,	30101	MSE	3					MSE	3				LSB		3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	В0
	Black	0	0			0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1		1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	. 0		0	0	1	.1	. 1			1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	. 1		
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	. 1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					••••• ••						 						 		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					· · · · ·						 						· · · · · ·		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	 1	 1	 1
<u> </u>	1																		



3-7. Power Sequence

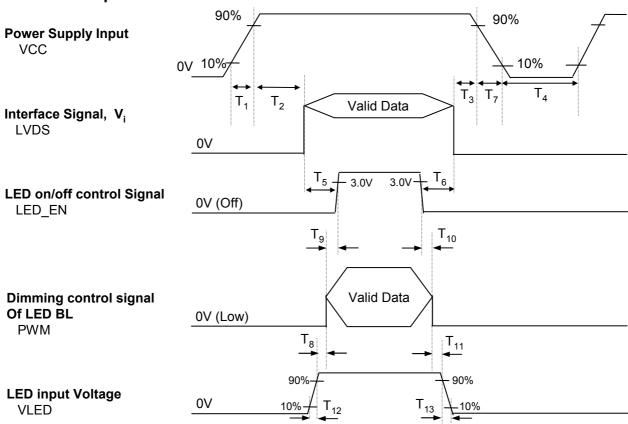


Table 6. POWER SEQUENCE TABLE

Logic		Value		Units	LED		Value		Units
Parameter	Min.	Тур.	Max.	Ullits	Parameter	Min.	Тур.	Max.	Ullits
T ₁	0.5	-	10	ms	T ₈	10	ı	-	ms
T ₂	0	ı	50	ms	T ₉	0	ı	-	ms
T ₃	0	-	50	ms	T ₁₀	0	ı	-	ms
T ₄	400	-	ı	ms	T ₁₁	10	ı	-	ms
T ₅	200	-	ı	ms	T ₁₂	0.5	ı	-	ms
T ₆	200	-	ı	ms	T ₁₃	0	ı	5000	ms
T ₇	3	-	10	ms					

Note)

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. LVDS Signal Timing Specifications"
- 3. LVDS, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of LVDS turn on.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to Φ 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.



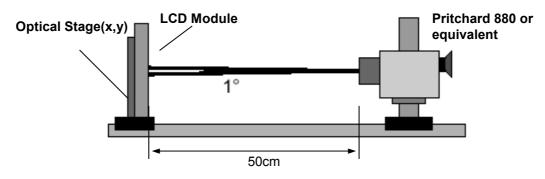


Table 7. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_V =60Hz, f_{CLK} = 72.3MHz, I_{LED} = 20 mA

Parameter	Symbol		Values		Units	Notes
Parameter	Symbol	Min	Тур	Max	Units	notes
Contrast Ratio	CR	400	-	-		1
Surface Luminance, white	L _{WH}	190	220	<u>-</u>	cd/m ²	2
Luminance Variation(13points)	δ_{WHITE}	60	70		%	3
Luminance Variation(5points)		70	80		%	
Response Time	Tr _R + Tr _D	-	16	25	ms	4
Color Coordinates	[]	
RED	RX	0.576	0.606	0.636	1	
	RY	0.324	0.354	0.384	[
GREEN	GX	0.304	0.334	0.364	[
	GY	0.569	0.599	0.629	[
BLUE	вх	0.127	0.157	0.187		
	BY	0.060	0.090	0.120		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle]	5
x axis, right(⊕=0°)	Θr	40	-	-	degree	
x axis, left (Φ=180°)	Θl	40	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	30	-	-	degree	
Gray Scale						6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH}$$
 = Average($L_1, L_2, \dots L_5$)

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13})}{\text{Minimum}(L_1, L_2, \dots L_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_{v} = 60$$
Hz

Gray Level	Luminance [%] (Typ)
L0	0
L7	1.90
L15	7.13
L23	15.21
L31	25.66
L39	39.08
L47	55.70
L55	79.01
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

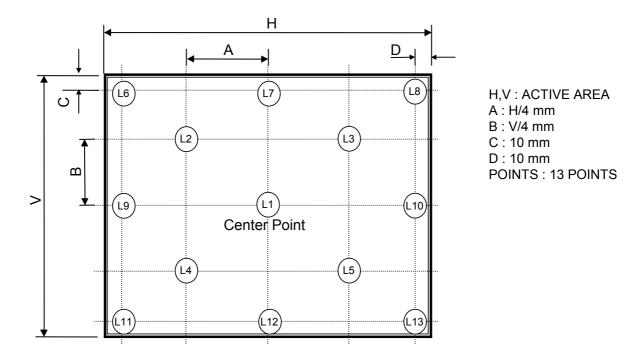
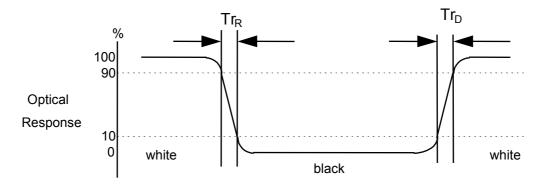


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



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5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP140WH1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

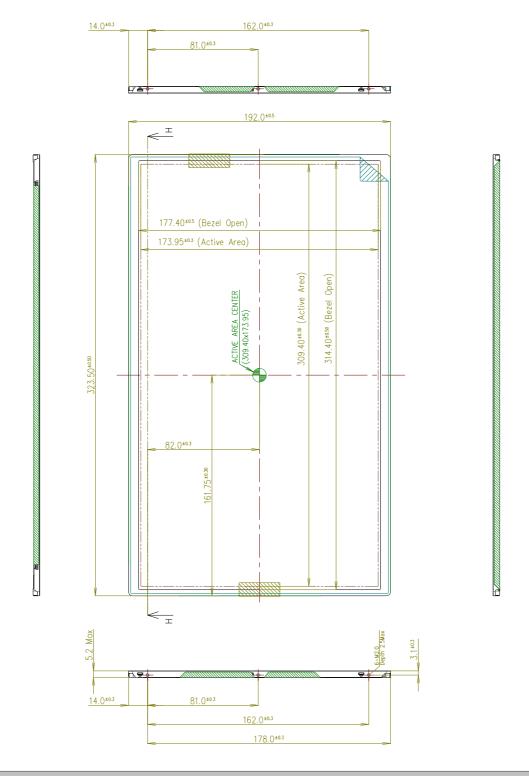
	Horizontal	323.5 ± 0.5mm					
Outline Dimension	Vertical	192.0 ± 0.5mm					
	Thickness	5.2mm (max)					
Bezel Area	Horizontal	314.4 ± 0.5 mm					
Bezel Area	Vertical	177.4 ± 0.5mm					
Active Display Area	Horizontal	309.40 mm					
Active Display Area	Vertical	173.95 mm					
Weight	350g (Max.)						
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer						

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<FRONT VIEW>

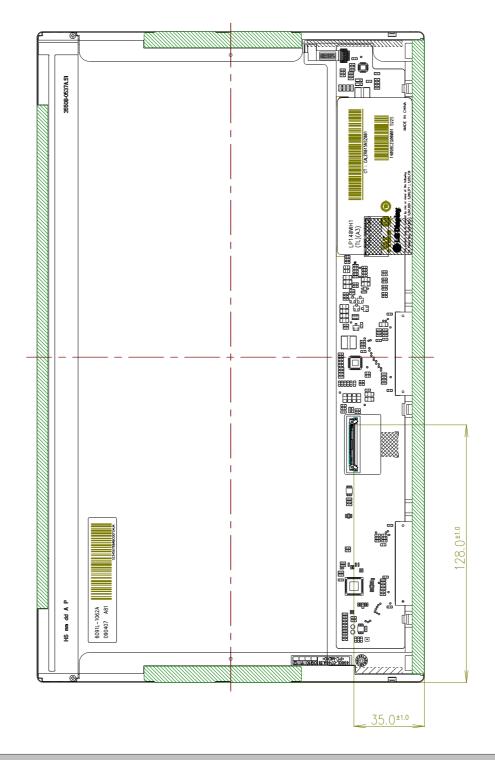
Note) Unit:[mm], General tolerance: \pm 0.5mm





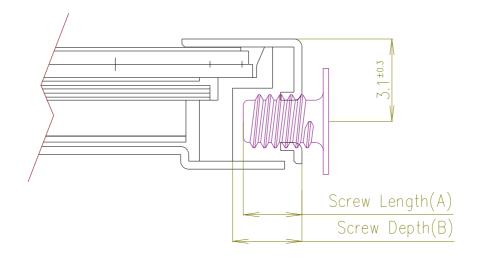
<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



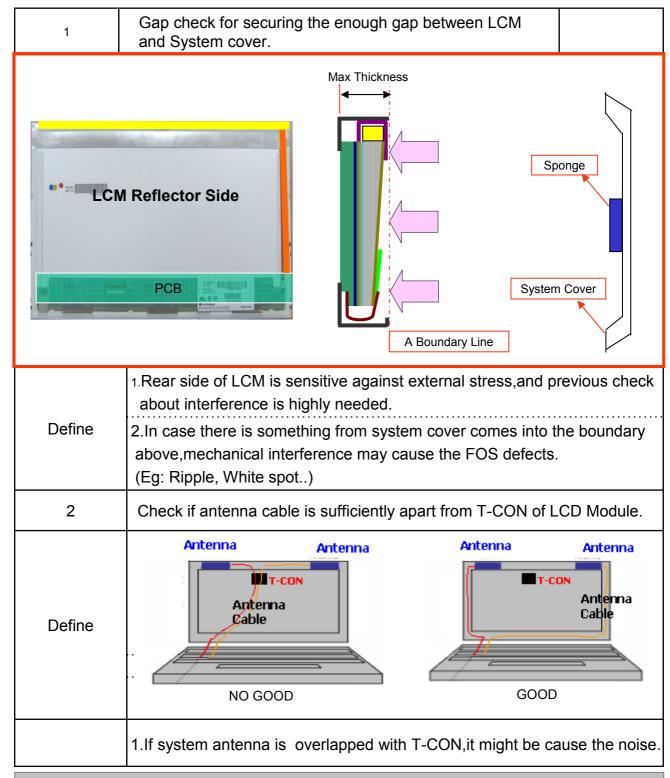
- * Mounting Screw Length (A) = 2.0(Min) / 2.5(Max)
- * Mounting Screw Hole Depth (B) = 2.5(Min)
- * Mounting hole location: 3.1(typ.)
- * Torque : 2.0 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.



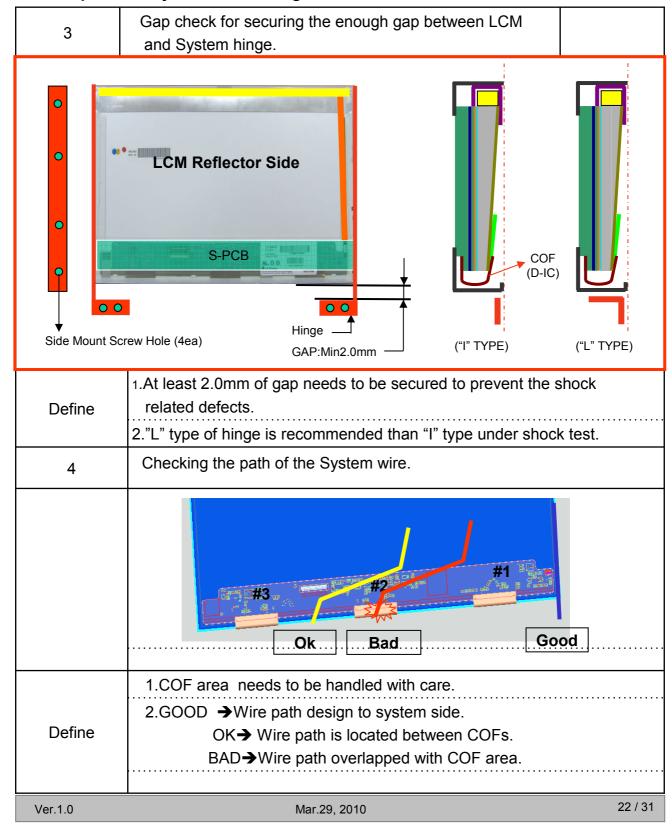
LGD Proposal for system cover design.(Appendix)



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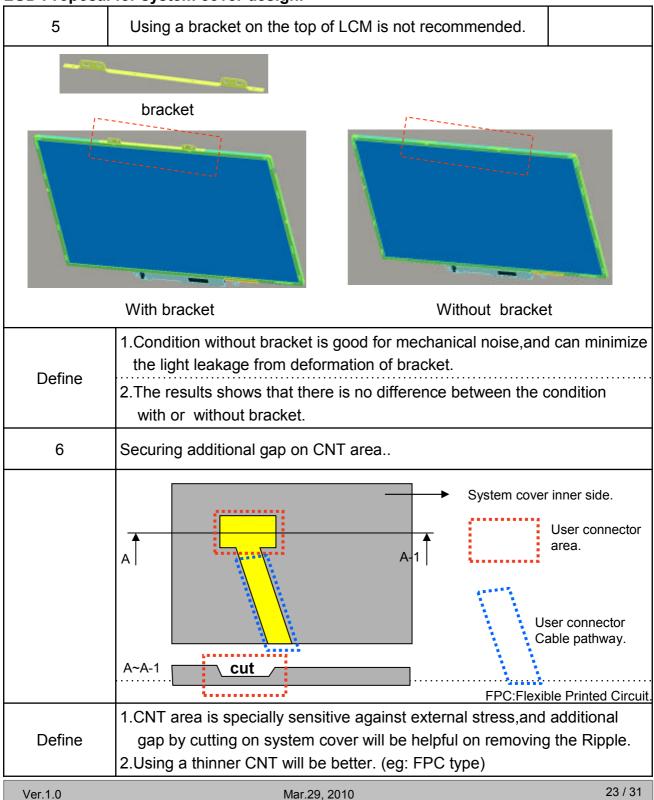


LGD Proposal for system cover design.





LGD Proposal for system cover design.





6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis					
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J		М
---------------------	--	---

 $A,B,C:SIZE(INCH) \\ D:YEAR$

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30 pcs

b) Box Size : 490 mm imes 390 mm imes 256 mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte	Byte	Field Name and Comments	Value	Value
	(Dec)	(Hex) 00	Header	(Hex) 00	(Bin) 00000000
Header	1	01	Header	FF	11111111
	2	02	Header	FF	11111111
	3	03	Header	FF	11111111
	4	04	Header	FF	11111111
	5	05	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
EDID	9	09	EISA manufacture code (Compressed ASC II)	E4	11100100
	10	0A	Panel Supplier Reserved - Product Code 029Bh	9B	10011011
	11	0B	(Hex. LSB first)	02	00000010
	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
roduct Version	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
odi ers	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
P	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
1	16	10	Week of Manufacture 0 weeks	00	00000000
Vendor / Product Version	17	11	Year of Manufacture 2009 years	13	00010011
Zer.	18	12	EDID structure version # = 1	01	00000001
	19	13	EDID revision #= 3	03	00000011
Š	20	14	Video input Definition = Digital signal	80	10000000
ay ete	21	15	Max H image size (Rounded cm) = 31 cm	1F	00011111
Display Parameters	22	16	Max V image size (Rounded cm) = 17 cm	11	00010001
Di	23	17	Display gamma = (gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma	78	01111000
b	24	18	Feature Support (no_DPMS, no_Active Off/Very Low Power, RGB color display, Timing BLK 1,no_GTF)	0A	00001010
S	25	19	Red/Green Low Bits (RxRy/GxGy)	69	01101001
Panel Color Coordinates	26	1A	Blue/White Low Bits (BxBy/WxWy)	45	01000101
dir.	27	1B	$Red X \qquad Rx = 0.606$	9B	10011011
100	28	1C	Red Y Ry =0.354	5A	01011010
Ö	29	1D	Green X Gx = 0.334	55	01010101
loi	30	1E	Green Y Gy = 0.599	99	10011001
ુ	31	1F	Blue X Bx = 0.157	28	00101000
ıel	32	20	Blue Y By = 0.09	17	00010111
bar.	33	21	White X Wx =0.313	50	01010000
	34	22	White Y Wy =0.329	54	01010100
Estabi ished Timin	35	23	Established timing 1 (00h if not used)	00	00000000
Estab ished Timin as	36	24	Established timing 2 (00h if not used) Manufesturaria timing (00h if not used)	00	00000000
, , ,	37	25	Manufacturer's timings (00h if not used) Standard timing ID1 (01h if not used)	00	00000000
Standard Timing ID	38	26 27	Standard timing ID1 (01h if not used) Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID1 (01h if not used) Standard timing ID2 (01h if not used)	01	0000001
	41	29	Standard timing ID2 (01h if not used) Standard timing ID2 (01h if not used)	01	0000001
	42	2.A	Standard timing ID3 (01h if not used)	01	00000001
	43	2B	Standard timing ID3 (01h if not used)	01	0000001
	44	2C	Standard timing ID4 (01h if not used)	01	0000001
	45	2D	Standard timing ID4 (01h if not used)	01	00000001
	46	2E	Standard timing ID5 (01h if not used)	01	00000001
	47	2F	Standard timing ID5 (01h if not used)	01	00000001
	48	30	Standard timing ID6 (01h if not used)	01	00000001
	49	31	Standard timing ID6 (01h if not used)	01	00000001
	50	32	Standard timing ID7 (01h if not used)	01	00000001
	51	33	Standard timing ID7 (01h if not used)	01	00000001
	52	34	Standard timing ID8 (01h if not used)	01	00000001
	53	35	Standard timing ID8 (01h if not used)	01	00000001

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #1	54	36	Pixel Clock/10,000 (LSB) 72.3 MHz @ 59.97Hz	3E	00111110
	55	37	Pixel Clock/10,000 (MSB)	1C	00011100
	56	38	Horizontal Active (lower 8 bits) 1366 Pixels	56	01010110
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 160 Pixels	A0	10100000
	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
	59	3B	Vertical Avtive 768 Lines	00	00000000
	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	16	00010110
oto	61	3D	Vertical Active: Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
riţ	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	00110000
esc	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	00100000
Ď	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 3 Lines: 5 Lines	35	00110101
ing	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
i.i.	66	42	Horizontal Image Size (mm) 310 mm	36	00110110
I	67	43	Vertical Image Size (mm) 174 mm	AE	10101110
	68	44	Horizontal Image Size / Vertical Image Size	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note : LSB is set to '1'	19	00011001
			if panel is DE-timing only. H/V can be ignored.		
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
_	76	4C	Flag	00	00000000
#2	77	4D	Descriptor Defined by manufacturer	00	00000000
or	78	4E	Descriptor Defined by manufacturer	00	00000000
ip.	79	4F	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000
De	81	51	Descriptor Defined by manufacturer	00	00000000
Su	82	52	Descriptor Defined by manufacturer	00	00000000
mi	83	53	Descriptor Defined by manufacturer	00	00000000
Ţ	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
Timing Descriptor #3	93	5D	Data Type Tag (ASCII String)	FE	11111110
	94	5E	Flag	00	00000000
	95	5F	ASCII String L	4C	01001100
	96	60	ASCII String G	47	01000111
	97	61	ASCII String	20	00100000
sci	98	62	ASCII String D	44	01000100
De	99	63	ASCII String i	69	01101001
Timing	100	64	ASCII String s	73	01110011
	101	65	ASCII String p	70	01110000
	102	66	ASCII String 1	6C	01101100
	103	67	ASCII String a	61	01100001
	104	68	ASCII String y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC \coprod code 0Ah, set remaining char = 20h)	0A	00001010
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah,set remaining char = 20h)	20	00100000
	107	6B	Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (ASCII String)	FE	11111110
	112	70	Flag	00	00000000
#	113	71	ASCII String L	4C	01001100
Timing Descriptor	114	72	ASCII String P	50	01010000
	115	73	ASCII String 1	31	00110001
	116	74	ASCII String 4	34	00110100
	117	75	ASCII String 0	30	00110000
	118	76	ASCII String W	57	01010111
	119	77	ASCII String H	48	01001000
	120	78	ASCII String 1	31	00110001
	121	79	ASCII String -	2D	00101101
	122	7A	ASCII String T	54	01010100
	123	7B	ASCII String L	4C	01001100
	124	7C	ASCII String A	41	01000001
	125	7D	ASCII String 3	33	00110011
Сћес	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
С	127	7F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	FF	11111111

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