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TV126WTM-NU0 Preliminary Product Specification Rev. P1

BEIJING BOE DISPLAY TECHNOLOGY

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REVISION HISTORY

()Preliminary Specification()Final Specification

REV.	ECN NO.	DESCRIPTION OF CHANGES	DATE	PREPARED
P0		Initial Release	Jul.14. 16'	Sun Peng
P1		TFT Pol增加APF	Sep.23. 16'	Sun Peng
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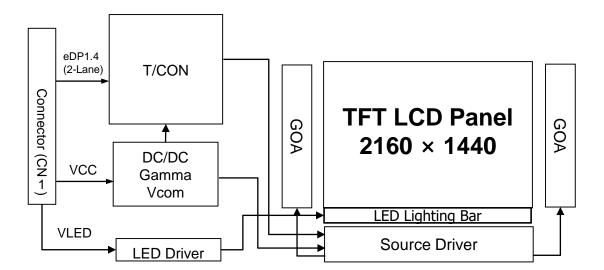


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1.0 GENERAL DESCRIPTION

1.1 Introduction

TV126WTM-NU0 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 12.6 inch diagonally measured active area with FHD+ resolutions (2160 horizontal by 1440 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors.



1.2 Features

- eDP 1.4 2Lane Interface support 5.4Gbps/lane
- High-speed response
- 8-bit color depth, display 16. 7M colors
- High luminance and contrast ratio and wide viewing angle
- RoHS/Halogen Free
- CABC/PSR/ sDRRS support

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1.3 Application

• 2 in 1 Notebook PC

1.4 General Specification

The followings are general specifications at the model TV126WTM-NU0

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	265.68(H) x 177.12(V)	mm	
Number of pixels	2160(H) ×1440(V)	pixels	
Pixel pitch	0.123 (H) ×0.123 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally Black		
Dimensional outline	271.28(H) x188.52(V) x1.95(D) TYP	mm	
Weight	163 (Max.)(Without Polaroid Protect Film)	g	
Surface Treatment	HC, 3H		
Back-light	Lower Down side ,1-LED Lighting Bar type		Note 1
	P _D : 1.1W (max)		
Power Consumption	P _{BL} : 2.3W (max)		Note 2
	Ptotal: 3.4W(max)		

Notes: 1. LED Lighting Bar (50EA Array, 5P10S)

2. P_{BL}=LED Quantity * VPIN×IPIN/ LED Driver Eff.

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2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

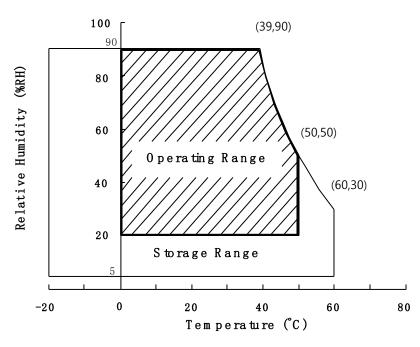
< Table 2. Absolute Maximum Ratings>

[VSS=GND=0V]

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V _{cc}	-0.3	3.3	4.0	V	Ta = 25 ℃
LED Input Current	ILED	100	120	230	mA	
LED Input Voltage	VLED	10	19	24	V	
Operating Temperature	T _{OP}	0	-	50	°C	1)
Storage Temperature	T _{ST}	-20	-	+60	°C	1)

Note: 1) Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C max. and no condensation of water.



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3.0 ELECTRICAL SPECIFICATIONS

3.1Electrical Specifications

< Table 3. Electrical specifications >

[Ta = 25 ± 2 °C]

Parameter		Min.	Тур.	Max.	Unit	Remarks	
Power Supply Voltage	V _{DD}	3.15	3.3	3.45	V	Noted	
Power Supply Current .	I _{DD}	-	305	-	mA	Note1	
In-Rush Current	I _{RUSH}	-	2.0	3.0	А	Note 2	
Permissible Input Ripple Voltage	V_{RF}	-	-	300	mV		
High Level Differential Input Threshold Voltage	V _{IH}	1.8	-	-	V		
Low Level Differential Input Threshold Voltage	V _{IL}	-	-	0.75	V	VDD=2.5V	
	P _D	-	1.0	1.1	W	Note 2	
Power Consumption	P _{BL}	-	2.15	2.3	W	Note 3	
	P _{total}	-	3.15	3.4	W		

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for 3.3V at 25°C.

- 2. Mosaic Pattern: 1.1(W) Max
- 3. Calculated value for reference (VLED × ILED / Efficiency)

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3.2 Backlight Unit

< Table 4. LED Driving guideline specifications >

Ta=25+/-2°C

	Parameter		Min.	Тур.	Max.	Unit	Remarks
LED Forward	Voltage	V_{F}	1	2.85	3.0	V	-
LED Forward	Current	I _F	-	13.5	14	mA	-
LED Power C	Consumption	P _{LED}	-	2.15	2.3	W	Note 1
LED Life-Tim	е	N/A	15,000	-	-	Hour	IF = 13.5mA
Power supply LED Driver	voltage for	V _{LED}	10	19	24	٧	-
EN Control	Backlight on	-	1.2	-	-	٧	-
Level	Backlight off	-	-	-	0.6	V	-
PWM Control	PWM High Level	-	1.2	1	-	٧	-
Level	PWM Low Level	-	ı	ı	0.6	V	-
PWM Control Frequency		F _{PWM}	200	-	25,000	Hz	-
Duty Ratio		-	1	-	100	%	Note3

Note1: Power supply voltage 24V for LED Driver

Calculator Value for reference IF \times VF \times 50/ efficiency = PLED

Note2: The LED Life-time define as the estimated time to 50% degradation of initial luminous.

Note3: 1% duty cycle is achievable for the frequency range from 200Hz to1KHz

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=20mA on condition of continuous operating at 25 ± 2 °C

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

Paran	ıeter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	T.,	r:	Θ_3	Θ_3		85	89	Deg.	
Viewing Angle		Iorizontal	Θ_9	CR > 10	80	85	89	Deg.	Note 1
range		Vertical	Θ_{12}	CR > 10	80	85	89	Deg.	Note 1
		verticai	Θ_6		80	85	89	Deg.	
Luminance Contra	st rati	io	CR		600	800	-		Note 2
Luminance of Wh	ite		Y_{w}		212.5	250	-	cd/m ²	Note 3
White luminance	5	i Point	ΔΥ5		80	-	-	%	NI-4- 4
uniformity	13	3 Point	ΔΥ13		66	-	-	%	Note 4
		White	W_x			0.305		-	
		white	I VV I -	$\Theta = 0^{\circ}$ (Center)	T. 0.03	0.325	Trum +0.02	-	
		Red	R_x	Normal		0.642		-	
Reproduction	l	Rea	R_y	Viewing Angle		0.362		-	
of color		Green	G_{x}		Тур0.03	0.312	Typ.+0.03	-	Note 5
		Green	G_y			0.638		-	
		-	B_x			0.152		-	
В		Blue	\mathbf{B}_{y}		·	0.072]	-	
	Ga	mut			67	72	-	%	
Respons (Rising+l			T_{RT}	$Ta=25^{\circ} C$ $\Theta=0^{\circ}$		30	35	ms	Note 6
Cross	Talk		СТ	$\Theta = 0$ °	-	-	2.0	%	Note 7

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Notes:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
- 2. Contrast measurements shall be made at viewing angle of Θ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state.

(see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

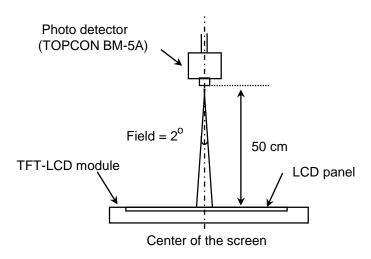
- 3. Center Luminance of white is defined as luminance values of center point across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the center point of the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : ΔY =Minimum Luminance of 5(or 13) points / Maximum Luminance of 5(or 13) points. (see FIGURE 2 and FIGURE 3).
- 5. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 5).

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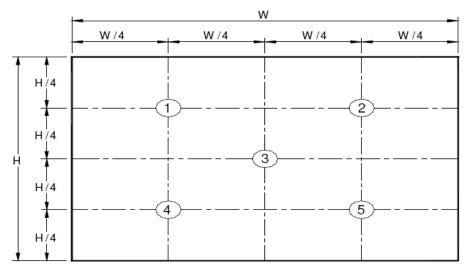
4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



Center Luminance of white is defined as luminance values of center points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the center point of the locations shown in FIGURE 2 for a total of the measurements per display.

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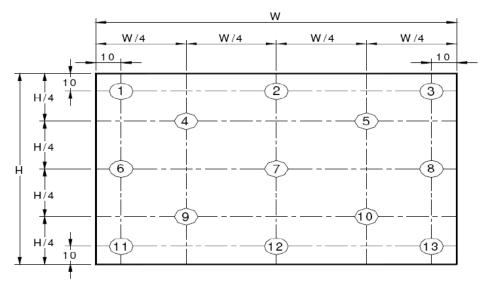
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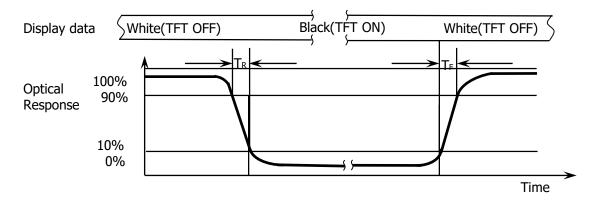
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Figure 3. Uniformity Measurement Locations (13 points)



The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5 = Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2) , <math>\Delta Y13 = Minimum Luminance of 13 points / Maximum Luminance of 13 points (see FIGURE 3).$

Figure 4. Response Time Testing

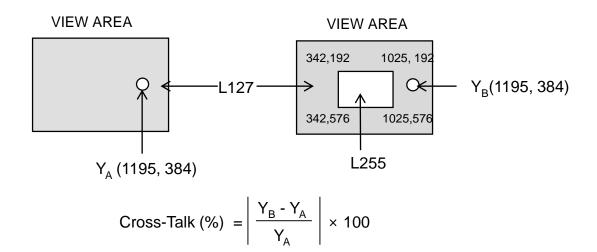


The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

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Figure 5. Cross Modulation Test Description



Where:

 Y_A = Initial luminance of measured area (cd/m²)

Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

5.1.1 LED Light Bar

< Table 1. LED Light Bar>

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Pin No	Symbol	Description
1	LED+	LED power supply
2	LED+	LED power supply
3	NC	NC
4	LED_FB1	LED-
5	LED_FB2	LED-
6	LED_FB3	LED-
7	LED_FB4	LED-
8	LED_FB5	LED-
9	NC	NC
	CONNECTOR	PF040-B09B-C09

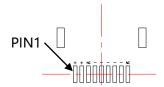


Figure 1. Top View of LED Bar Connector

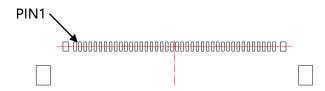


Figure 2. Back Side of Module

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Module Side Connector : I-PEX 20455-040E-66

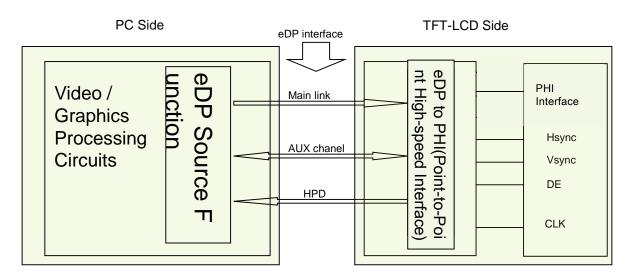
Pin No	Symbol	Function	Remark
1	DBC	Content Adaptive Brightness Control function	
2	H_GND	Ground	
3	NC	NC	
4	NC	NC	
5	H GND	Ground	
6	NC	NC	
7	NC	NC	
8	H_GND	Ground	
9	LANE1N	eDP RX channel 1 negative	
10	LANE1P	eDP RX channel 1 positive	
11	H GND	Ground	
12	LANE0N	eDP RX channel 0 negative	
13	LANE0P	eDP RX channel 0 positive	
14	H GND	Ground	
15	AUX CH P	eDP AUX CH positive	
16	AUX CH N	eDP AUX CH negative	
17	H GND	Ground	
18	VCC	Power Supply, 3.3V (typ.)	
19	VCC	Power Supply, 3.3V (typ.)	
20	VCC	Power Supply, 3.3V (typ.)	
21	VCC	Power Supply, 3.3V (typ.)	
22	BIST	Panel self test enable	
23	GND	LCD Ground	
24	GND	LCD Ground	
25	GND	LCD Ground	
26	GND	LCD Ground	
27	HPD	Hot plug detect output	
28	BL GND	LED Ground	
29	BL GND	LED Ground	
30	BL GND	LED Ground	
31	BL GND	LED Ground	
32	BL EN	LED enable pin	
33	BL PWM	System PWM Signal Input	
34	NC	No Connector	
35	NC	No Connector	
36	BL PWR	LED Power Supply 10V-24V	
37	BL PWR	LED Power Supply 10V-24V	
38	BL PWR	LED Power Supply 10V-24V	
39	BL PWR	LED Power Supply 10V-24V	
40	NC	No Connection	

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5.2 eDP Interface



Note. Transmitter: HX8879-D03 or equivalent.

Transmitter is not contained in Module.

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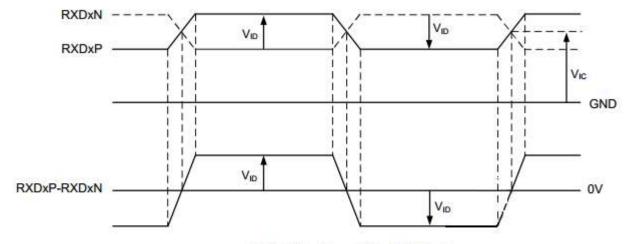
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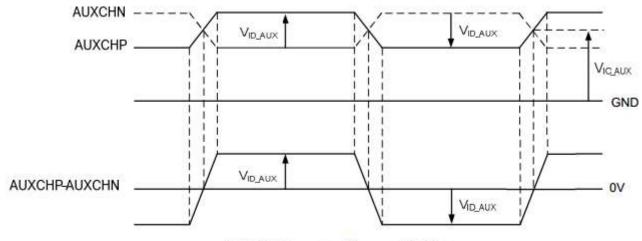
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5.3.eDP Input signal



Main Link VID and VIC definition



AUX CH VID AUX and VIC AUX definition

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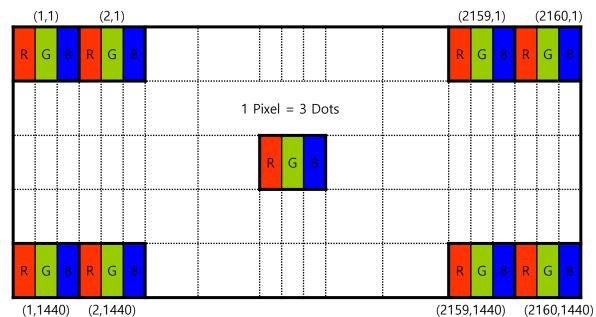
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5.4 Data Input Format



Display Position of Input Data (V-H)

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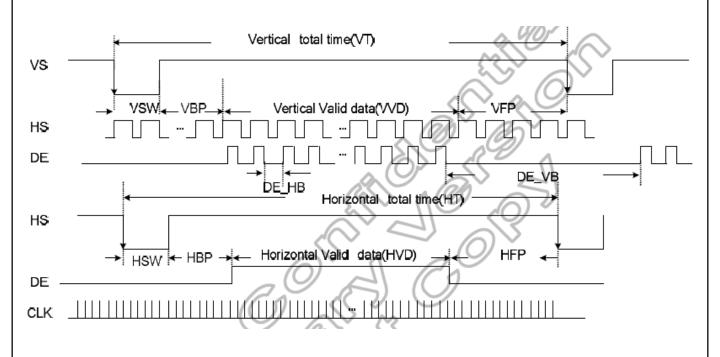
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6.0 SIGNAL TIMING SPECIFICATION

6.1 The TV126WTM-NU0 is operated by the DE only.

ITEM	Symbol		Min	Тур.	Max	Unit	Note
CLK	Period	t _{CLK}	-	4.82	-	ns	
CLK	Frequency	-	-	207.4	-	Mbps	
I lavas	Period	t _{HP}	-	2320	-	t _{CLK}	
Hsync	Frequency	f _H	-	139.2	-	KHz	
May mag	Period	t _{VP}	-	1490	-	t _{HP}	
Vsync	Frequency	f _V	-	89.4	-	KHz	
Horizontal Active	Valid	t _{HV}	-	2160	-	t _{CLK}	
Display Term	Total	t _{HP}	-	2320	-	t _{CLK}	
Vertical Active	Valid	t _{VV}	-	1440	-	t _{HP}	
Display Term	Total	t _{VP}	-	1490	-	t _{HP}	

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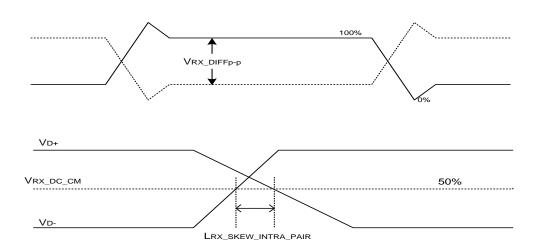
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6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 8.

<Table 8. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
Differential peak-to-peak input volt age at package pins	VRX-DIFFp-p	120	-	-	mV	
Rx input DC common mode Voltage	VRX_DC_CM	0	-	2.0	V	
Differential termination resistance	RRX-DIFF		100		Ω	
Rx short circuit current limit	IRX_SHORT			50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	60	ps	



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7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

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		_																							
Color & G	ray Scale	RED DATA R7 R6 R5 R4 R3 R2 R1 R0								GREEN DATA G7 G6 G5 G4 G3 G2 G1 G0												DA			
		+	_	-	_	_	-	_	_	-		_	_				-		_	_	-	_	B2		B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
L COLOIS	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
<u> </u>	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Δ					<u> </u>								<u> </u>								<u> </u>			
of RED	∇				,	ļ								<u> </u>								<u> </u>			
_	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
_	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
_	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	Δ	<u> </u>						<u></u>							1										
of GREEN	∇													ļ								<u> </u>			
<u> </u>	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
<u>_</u>	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
<u> </u>	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u> </u>	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	Δ					<u> </u>							•	<u> </u>				1							
OI BLUE	∇					ļ				↓							<u> </u>								
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
[Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Crox Sools	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
Gray Scale	Δ					<u> </u>								<u> </u>							_	<u> </u>			
of WHITE	∇													ļ								↓			
Ī	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
Ţ	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
l t	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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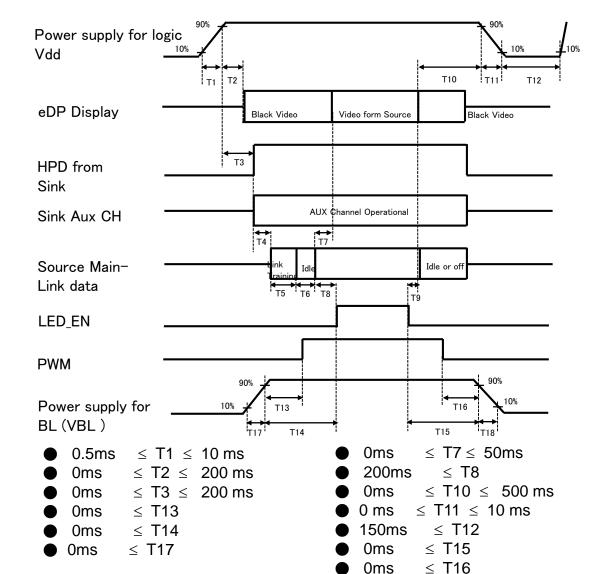
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8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off seq uence shall be as shown in below



Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

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0ms

0ms

≤ T18



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9.0 MECHANICAL CHARACTERISTICS

9.1 Dimensional Requirements

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	271.28(H) x188.52(V) x1.95(D) TYP	mm
Weight	163 (Max.)(Without Polaroid Protect Film)	gram
Active area	265.68(H) x 177.12(V)	mm
Pixel pitch	0.123(H) ×0.123(V)	mm
Number of pixels	$2160(H) \times 1440 (V) (1 \text{ pixel} = R + G + B \text{ dots})$	pixels
Back-light	Lower Down side ,1-LED Lighting Bar type	

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10.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

No	Test Item	Test Condition	Remark
1	High temperature storage	60C/240h	
2	Low temperature storage	-20C/240h	
3	High temperature/High humidity Storage	50C/80%RH/240h	_
4	High temperature operating	50C/240h	
5	Low temperature operating	0℃/240h	
6	Thermal Shock Storage	-20℃ (30 min)~ +60 ℃(30 min) , 100 cycles	

No	Other Test Item	Test Condition
1	Vibration test (non-operating)	1.5G, 10~500Hz,Half Sine X,Y,Z / Sweep rate : 1 hour
2	Shock test (non-operating)	220G, Half Sine Wave 2msec ±X,±Y,±Z Once for each direction
3	Package Drop test	Height: 60cm, 1 corner, 3 edges, 6 surfaces: 1 time for each direction
4	FPC Bending test	Bending degree is 180, bending 30 times and the bending radius is 1.0mm
5	FPC Insert/Remove test	30 time FPC insert/remove
6	ESD test (Component-LCD MDL)	【HM Air】150pF, 330Ω,±15KV 【HM Contact】150pF,330Ω,±8KV SPEC.: No abnormal display

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11.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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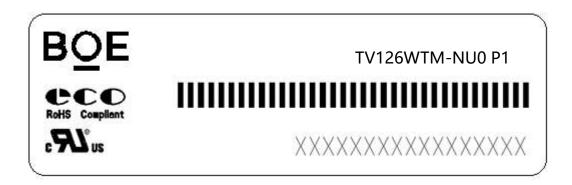
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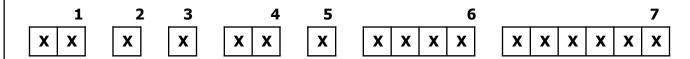
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12.0 PRODUCT SERIAL NUMBER



• Label Size : $80 \text{ mm (L)} \times 25 \text{ mm (W)}$



- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001: 01, 2002: 02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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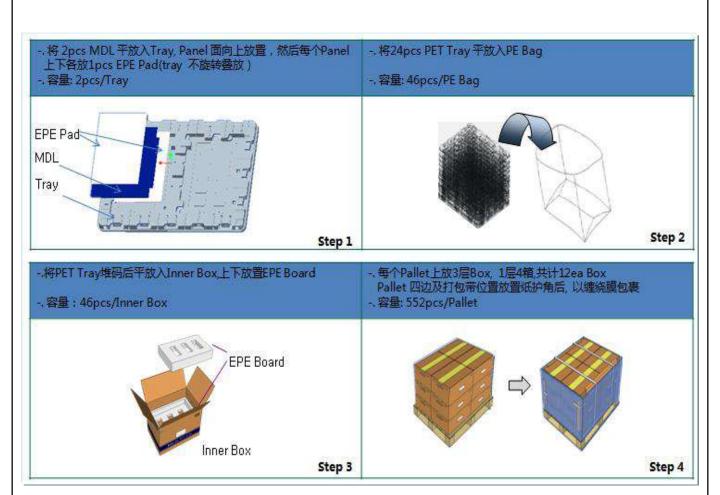
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13.0 Packing

13.1 Packing Order





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13.2 Packing Note

• Box Dimension : 550mm(W) × 470mm(L) × 296mm(H)

• Package Quantity in one Box : 46 pcs

Item	Specification			Remark
nem	Q'ty	Dimension(mm)	Weight (kg)	Remark
Panel	1	271.28(H)×188.52(V)×1.95(D)	0.163	-
Tray	1	490(L)×410(W)×18(H)	0.298	-
Inner Box	1	550(L)×470(W)×296(H)	1.5	-
Packing Box	46pcs/Box	550(L)×470(W)×296(H)	15.9	-
Pallet	1	1140(L)×980(W)×130(H)	18	-
Packing Pallet	12Box/Pallet	1140(H)×980(W)×1014(H)	223.4	-

13.3 Box label

• Label Size : $108 \text{ mm (L)} \times 56 \text{ mm (W)}$

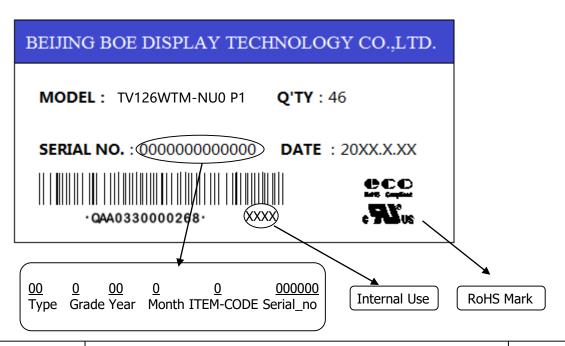
Contents

Model: TV126WTM-NU0

Q`ty: Module 46 Q`ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date



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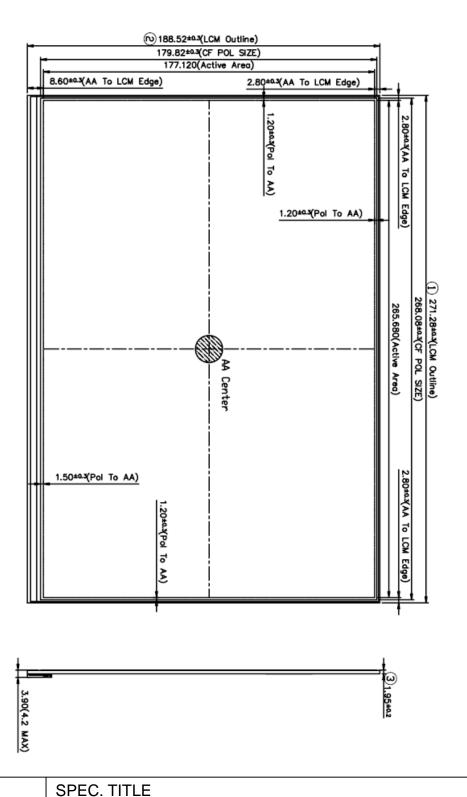
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Figure 5. TFT-LCD Module Outline Dimensions (Front view)

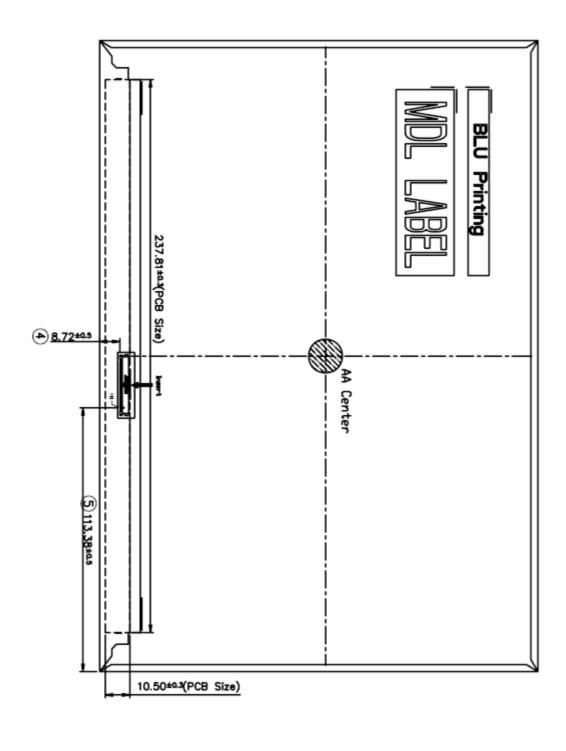


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Figure 6. TFT-LCD Module Outline Dimensions (Rear view)



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14.0 EDID Table

Function	Hex	Dec	crc	Input values.	Notes	
(HEX) 1 diletteri 00	00	0		0		
	FF	255		255		
	FF	255		255		
	FF	255		255	EDID Header	
Header	FF	255		255		
	FF	255		255		
	FF	255		255		
•	00	0		0		
an of a store a Name	09	9		DOE	ID DOE	
ID Manufacturer Name	E5	229		BOE	ID = BOE	
ID Product Code	EC	236		4770	ID = 1772	
	06	6		1772		
B 15 Froduct Code C D 32-bit serial No.	00	0				
	00	0				
	00	0				
	00	0				
k of manufacture	06	6		6		
r of Manufacture	1A	26		2016	Manufactured in 2016	
D Structure Ver.	01	1		1	EDID Ver 1.0	
DID revision #	04	4		4	EDID Rev. 0.4	
eo input definition	A5	165		-		
ax H image size	1B	27		27	27 cm (Approx)	
ax V image size	12	18		18	18 cm (Approx)	
isplay Gamma	78	120		2.2	Gamma curve = 2.2	
eature support	2	2			RGB display, Preferred Timming mode	
d/Green low bits	6D	109		-	Red / Green Low Bits	
e/White low bits	D0	208		-	Blue / White Low Bits	
Red x high bits	A4	164	657	0.642	Red (x) = 10100100 (0.642)	
Red y high bits	5C	92	370	0.362	Red (y) = 01011100 (0.362)	
reen x high bits	4F	79	319	0.312	Green (x) = 01001111 (0.312)	
reen y high bits	A3	163	653	0.638	Green (y) = 10100011 (0.638)	
lue x high bits	27	39	155	0.152	Blue (x) = 00100111 (0.152)	
Lue y high bits	12	18	73	0.072	Blue (y) = 00010010 (0.072)	
hite x high bits	4E	78	312	0.305	White (x) = 01001110 (0.305)	
hite y high bits	53	83	332	0.325	White (y) = 01010011 (0.325)	
ablished timing 1	00	0		-		
ablished timing 2	00	0		-		
ablished timing 3	00	0		-		
andord time: = #4	01	1			Notleed	
Standard timing #1	01	1			Not Used	
Standard timing #2	01	1			Notleed	
	01	1			Not Used	
Oten dend (C. 1. WO	01	1			Niedd	
Standard timing #3		1			Not Used	
andard timing #4	01	1			Not Used	
		01	ning #3 01 1 1 01 1	01 1 01 1 1 01 1	ning #3 01 1 01 1	

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Addres s (HEX)	Function	Hex	Dec	crc	Input value s.	Notes
2E	Standard timing #5	01	1			Not Used
2F		01	1			Not Osea
30	Standard timing #6	01	1			Not Used
31		01	1			Not osed
32	Standard timing #7	01	1			Not Used
33		01	1			1101.0000
34	Standard timing #8	01	1			Not Used
35		01	1		-	
36		04	4		207.4	207.408MHz Main clock
37	-	51	81		0.100	
38		70	112		2160	Hor Active = 2160
39	-	A0	160		160	Hor Blanking = 160
_	3A	80	128		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B	-	A0	160		1440	Ver Active = 1440
3C		32	50		50	Ver Blanking = 50
3D	<u> </u>	50	80		- 40	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E	Detailed timing/mo	30	48		48	Hor Sync Offset = 48
3F	nitor descriptor #1	20	32		32	H Sync Pulse Width = 32
40	descriptor#1	22	34		2	V sync Offset = 2 line
41	2 3 4 5 6	00	0		2	V Sync Pulse width : 2 line
42		0A	10		266	Horizontal Image Size = 266 mm (Low 8 bits)
43		<u>B1</u>	177		177	Vertical Image Size = 177 mm (Low 8 bits)
44		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0		0	Hor Border (pixels)
46		00	0		0	Vertical Border (Lines)
47		1A	26			Refer to right table
48 49	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	D0 40	208 64		165.9	165.9264MHz Main clock
4A		70	112		2160	Hor Active = 2160
4B		A0	160		160	Hor Blanking = 160
4C		80	128		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
4D		A0	160		1440	Ver Active = 1440
4E	•	32	50		50	Ver Blanking = 50
4F		50	80		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
50	Detailed timing/mo	30	48		48	Hor Sync Offset = 48
51	nitor descriptor #2	20	32		32	H Sync Pulse Width = 32
52		22	34		2	V sync Offset = 2 line
53		00	0		2	V Sync Pulse width : 2 line
54		0A	10		266	Horizontal Image Size = 266 mm (Low 8 bits)
55		B1	177		177	Vertical Image Size = 177 mm (Low 8 bits)
56		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Siz e
57		00	0		0	Hor Border (pixels)
58		00	0		0	Vertical Border (Lines)
59		1A	26			, ,

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Address (HEX)	Function	Hex	Dec	crc	Input values.	Notes
5A		00	0			
5B		00	0			
5C		00	0			ASCII Data Sting Tag
5D		FE	254			
5E		00	0			
5F		42	66		В	
60		4F	79		0	
61		45	69		Е	
62	Detailed timing/mo nitor descriptor #3	20	32			
63		42	66		В	
64		4A	74		J	
65		0A	10			Manufacture name : BOEBJ
66		20	32			
67		20	32			
68		20	32			
69		20	32			
6A		20	32			
6B		20	32			
6C		00	0			
6D		00	0			
6E		00	0			Product Name Tag (ASCII)
6F		FE	254			
70		00	0			
71		54	84		Т	
72		56	86		V	
73		31	49		1	
74	Detailed timing/mo	32	50		2	
75	nitor descriptor #4	36	54		6	
76		57	87		W	Model name : TV126WTM-NU0
77		54	84		Т	Model name : 1 V 126VV 1M-NOU
78		4D	77		М	
79		2D	45		-	
7A		4E	78		N	
7B		55	85		U	
7C		30	48		0	
7D		0A	10			
7E	Extension flag	00	0			
7F	Checksum	98	152	152	-	

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