

**TFT COLOR LCD MODULE**  
**NL8060BC26-13**

**26 cm (10.4 type), 800 × 600 pixels, 262144 colors,  
incorporated one lamp/edge-light type backlight**

**DESCRIPTION**

NL8060BC26-13 is a TFT (thin film transistor) active matrix color liquid crystal display (LCD) module comprising amorphous silicon TFT attached to each signal electrode, a driving circuit, and a backlight.

The 26 cm diagonal display area contains 800 × 600 pixels and can display 262144 color simultaneously.

By utilizing one lamp/edge-light type backlight, a very thin profile and low power consumption have been achieved.

**FEATURES**

- Thin and light weight
- Low power consumption
- Low reflection
- 6-bit digital RGB signals
- 3.3 V operation (5.0 V available)
- Incorporated edge light type backlight (One lamp, Inverter-less)
- Data enable function
- Three mounting holes (right-top, right-bottom and left-bottom)

**APPLICATIONS**

- Personal computer (PC), word processor
- Display terminals for control system
- Monitors for process controller



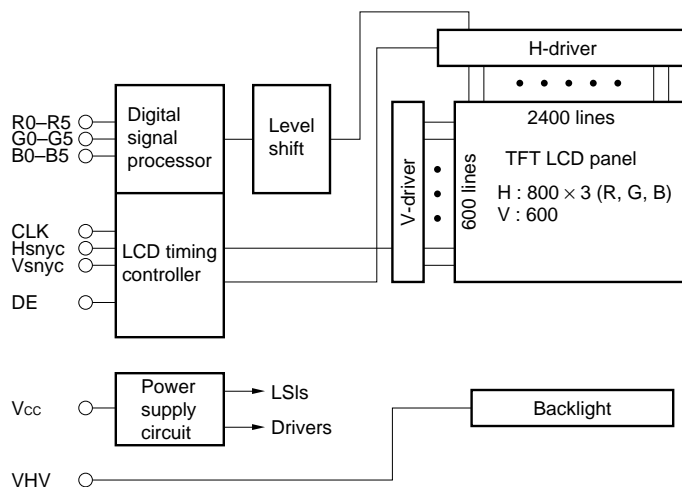
## STRUCTURE AND FUNCTIONS

A color TFT (thin film transistor) LCD module is comprised of a TFT liquid crystal panel structure, LSIs for driving the TFT array, and a backlight assembly. The TFT panel structure is created by sandwiching liquid crystal material in the narrow gap between a TFT array glass substrate and a color filter glass substrate. After the driver LSIs are connected to the panel, the backlight assembly is attached to the backside of the panel.

RGB (red, green, blue) data signals from a source system is modulated into a form suitable for active matrix addressing by the onboard signal processor and sent to the driver LSIs which in turn addresses the individual TFT cells.

Acting as an electro-optical switch, each TFT cell regulates light transmission from the backlight assembly when activated by the data source. By regulating the amount of light passing through the array of red, green, and blue dots, color images are created with clarity.

## BLOCK DIAGRAM



## OUTLINE OF CHARACTERISTICS (at room temperature)

Display area	211.2 (H) × 158.4 (V) mm
Drive system	a-Si TFT active matrix
Display colors	262144 colors
Number of pixels	800 × 600 pixels
Pixel arrangement	RGB vertical stripe
Pixel pitch	0.264 (H) × 0.264 (V) mm
Module size	242.0 (H) × 173.9 (V) × 7.0 typ. (D) mm
Weight	347 g (typ.)
Contrast ratio	150:1 (typ.)
Viewing angle (more than the contrast ratio of 10:1)	Horizontal : 50° (typ. left side, right side) Vertical : 20° (typ. up side), 40° (typ. down side)
Designed viewing direction	<ul style="list-style-type: none"> <li>• Wider viewing angle with contrast ratio : down side (6 o'clock)</li> <li>• Wider viewing angle without image reversal: up side (12 o'clock)</li> <li>• Optimum grayscale (<math>\gamma = 2.2</math>) : perpendicular</li> </ul>
Color gamut	42 % (typ. center, to NTSC)
Response time	40 ms (max.), "white" to "black"
Luminance	110 cd/m <sup>2</sup> (typ.) (Lamp current: 3.0 mArms)
Signal system	6-bit digital signals for each of RGB primary colors, synchronous signals (Hsync, Vsync), dot clock (CLK)
Supply voltage	3.3 V [5.0 V] (Logic, LCD driving)
Backlight	Edge light type, one cold cathode fluorescent lamp, inverter-less
Power consumption	2.8 W (typ. at 3.3 V, 110 cd/m <sup>2</sup> )

## GENERAL SPECIFICATIONS

Item	Specification	Unit
Module size	242.0 ± 0.5 (H) × 173.9 ± 0.5 (V) × 7.5 max.* (D)	mm
Display area	211.2 (H) × 158.4 (V)	mm
Number of pixels	800 (H) × 600 (V)	pixel
Dot pitch	0.088 (H) × 0.264 (V)	mm
Pixel pitch	0.264 (H) × 0.264 (V)	mm
Pixel arrangement	RGB (Red, Green, Blue) vertical stripe	
Display colors	262144	color
Weight	353 (max.)	g

**Note** No inverter for the backlight is integrated in the module.

\* D = 7.0 typ.

## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit	Remarks	
Supply voltage	V <sub>CC</sub>	−0.3 to 6.5	V	V <sub>I</sub> − V <sub>CC</sub> < 3.0	T <sub>a</sub> = 25 °C
Input voltage	V <sub>I</sub>	−0.3 to 6.5	V		
Storage temp.	T <sub>ST</sub>	−20 to 60	°C		
Operating temp.	T <sub>OP</sub>	0 to 50	°C	Module surface*	
Humidity	RH	≤ 95 % relative humidity	—	T <sub>a</sub> ≤ 40 °C	No condensation
		≤ 85 % relative humidity	—	40 < T <sub>a</sub> ≤ 50 °C	
		Absolute humidity shall not exceed T <sub>a</sub> = 50 °C, 85 % relative humidity level	—	T <sub>a</sub> > 50 °C	

\* Measured at the display area

## ELECTRICAL CHARACTERISTICS

## (1) Logic, LCD driving

T<sub>a</sub> = 25 °C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
Supply voltage	V <sub>CC</sub>	3.0 (4.75)	3.3 (5.0)	3.6 (5.25)	V	V <sub>CC</sub> = 3.3 V (V <sub>CC</sub> = 5.0 V)
Logic input "L" voltage	V <sub>IL</sub>	0	—	V <sub>CC</sub> × 0.3	V	CMOS Level
Logic input "H" voltage	V <sub>IH</sub>	V <sub>CC</sub> × 0.7	—	5.25	V	CMOS Level
Supply current	I <sub>CC</sub>	— —	*1 280 *1 (240)	*2 470 *2 (450)	mA	V <sub>CC</sub> = 3.3 V (V <sub>CC</sub> = 5.0 V)

\*1. Checker flag pattern (in EIAJ ED-2522)

\*2. Theoretical maximum current pattern

(2) Backlight

T<sub>a</sub> = 25 °C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
Lamp current	I <sub>L</sub>	2.0	3.0	4.0	mArms	80 cd/m <sup>2</sup> (TYP.): 2.0 mArms 110 cd/m <sup>2</sup> (TYP.): 3.0 mArms 120 cd/m <sup>2</sup> (TYP.): 4.0 mArms
Lamp voltage	V <sub>L</sub>	–	610	–	Vrms	I <sub>L</sub> = 3.0 mArms
Lamp turn on voltage	V <sub>s</sub>	1440	–	1600	Vrms	T <sub>a</sub> = 0 °C
		960	–	1600	Vrms	T <sub>a</sub> = 25 °C
Oscillator frequency	F <sub>t</sub>	50	–	95	kHz	<b>Note</b>

**Note** Recommended value of “F<sub>t</sub>”

- F<sub>t</sub> is within the specification.

and

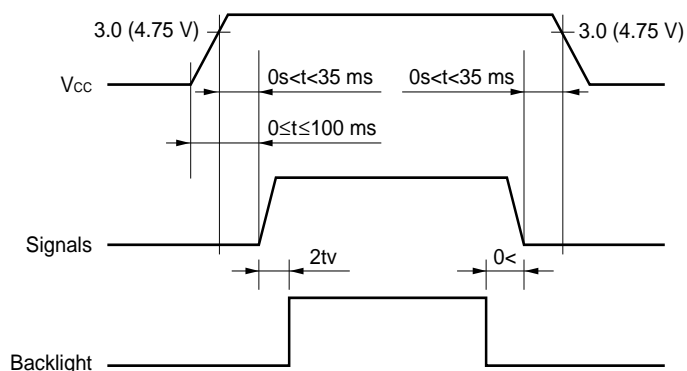
- $$F_t = \frac{1}{4t_h} \times (2n-1)$$

t<sub>h</sub> : Hsync period

n : a natural number (1, 2, 3, ....)

If F<sub>t</sub> is out of the recommended value, interference between F<sub>t</sub> frequency and Hsync frequency may cause beat on the display.

**SUPPLY VOLTAGE SEQUENCE**



- \*1 Apply VHV within the LCD operation period. When the backlight turns on before LCD operation or the LCD operation turns off before the backlight turns off, the display may momentarily become white.
- \*2 When the power is off, please keep whole signals (Hsync, Vsync, CLK, DE, data) low level or high impedance.

## INTERFACE PIN CONNECTION

### (1) Interface signals, power supply

Module side connector

CN1 ... DF9B-41P-1V (No. 1 to 41)

Supplier: HIROSE ELECTRIC CO., LTD.

Mating connector

DF9X-41S-1V (X ... A, B, C)

or

IL-310-T41S-VF

Supplier: Japan Aviation Electronics Industry limited (JAE)

note: Mating connectors are possible to use both parts.

Pin No.	Symbol	Function
1	GND	Ground <b>Note 3</b>
2	CLK	Dot clock
3	GND	Ground <b>Note 3</b>
4	Hsync	Horizontal sync.
5	Vsync	Vertical sync.
6	GND	Ground <b>Note 3</b>
7	GND	Ground <b>Note 3</b>
8	GND	Ground <b>Note 3</b>
9	R0	Red data (LSB)
10	R1	Red data
11	R2	Red data
12	GND	Ground <b>Note 3</b>
13	R3	Red data
14	R4	Red data
15	R5	Red data (MSB)
16	GND	Ground <b>Note 3</b>
17	GND	Ground <b>Note 3</b>
18	GND	Ground <b>Note 3</b>
19	G0	Green data (LSB)
20	G1	Green data
21	G2	Green data

Pin No.	Symbol	Function
22	GND	Ground <b>Note 3</b>
23	G3	Green data
24	G4	Green data
25	G5	Green data (MSB)
26	GND	Ground <b>Note 3</b>
27	GND	Ground <b>Note 3</b>
28	GND	Ground <b>Note 3</b>
29	B0	Blue data (LSB)
30	B1	Blue data
31	B2	Blue data
32	GND	Ground <b>Note 3</b>
33	B3	Blue data
34	B4	Blue data
35	B5	Blue data (MSB)
36	GND	Ground <b>Note 3</b>
37	DE	Data enable
38	V <sub>CC</sub>	Power supply <b>Note 1</b>
39	V <sub>CC</sub>	Power supply <b>Note 1</b>
40	V <sub>CC</sub>	Power supply <b>Note 1</b>
41	MODE	Timing mode select <b>Note 2</b>

LSB : Least Significant Bit

MSB : Most Significant Bit

**Notes** 1. V<sub>CC</sub>: All V<sub>CC</sub> terminals should be connected to 3.3 V [5.0 V].

2. MODE:  $\begin{cases} \text{H} & = \text{Fixed mode} \\ \text{L or open} & = \text{DE mode} \end{cases}$

3. GND is not connected to the frame of the LCD module.

### (2) Backlight

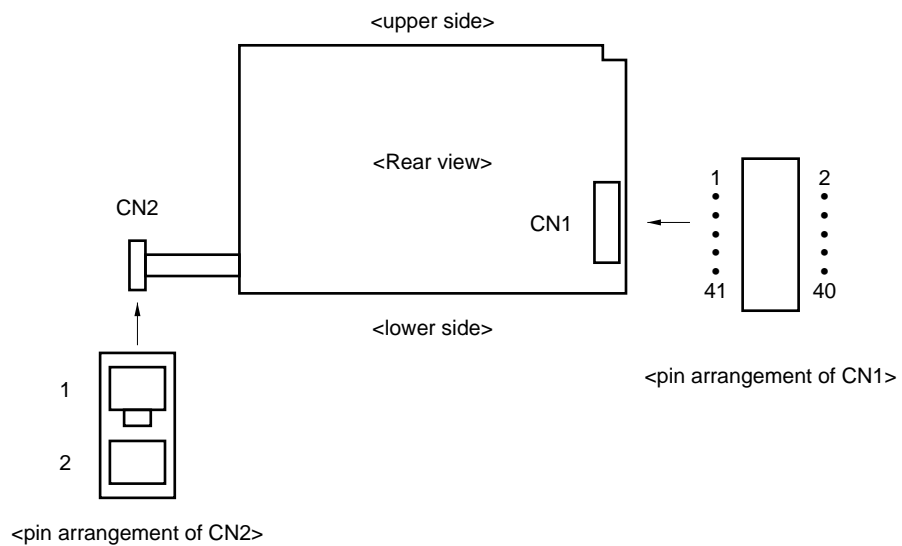
lamp side connector      mating connector

CN2 ... HV-2S-C1      HV-2P-HF

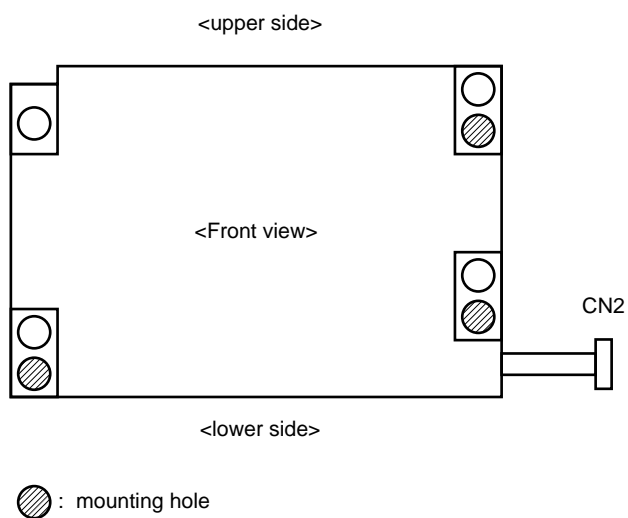
Supplier: Japan Aviation Electronics Industry  
Limited (JAE)

Pin No.	Symbol	Function
1	VHV	High voltage terminal
2	VHL	Low voltage terminal

(1) Connector location



**MOUNTING HOLE LOCATION**



## DISPLAY COLORS vs. INPUT DATA SIGNALS

Display colors		Data signal (0: Low level, 1: High level)																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark ↑ ↓ Bright	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark ↑ ↓ Bright	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0
Blue grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark ↑ ↓ Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

**Note** Colors are developed in combination with 6-bit signals (64 steps in grayscale) of each primary red, green, and blue color.

This process can result in up to 262144 ( $64 \times 64 \times 64$ ) colors.

## INPUT SIGNAL TIMING

## (1) Input signal specifications

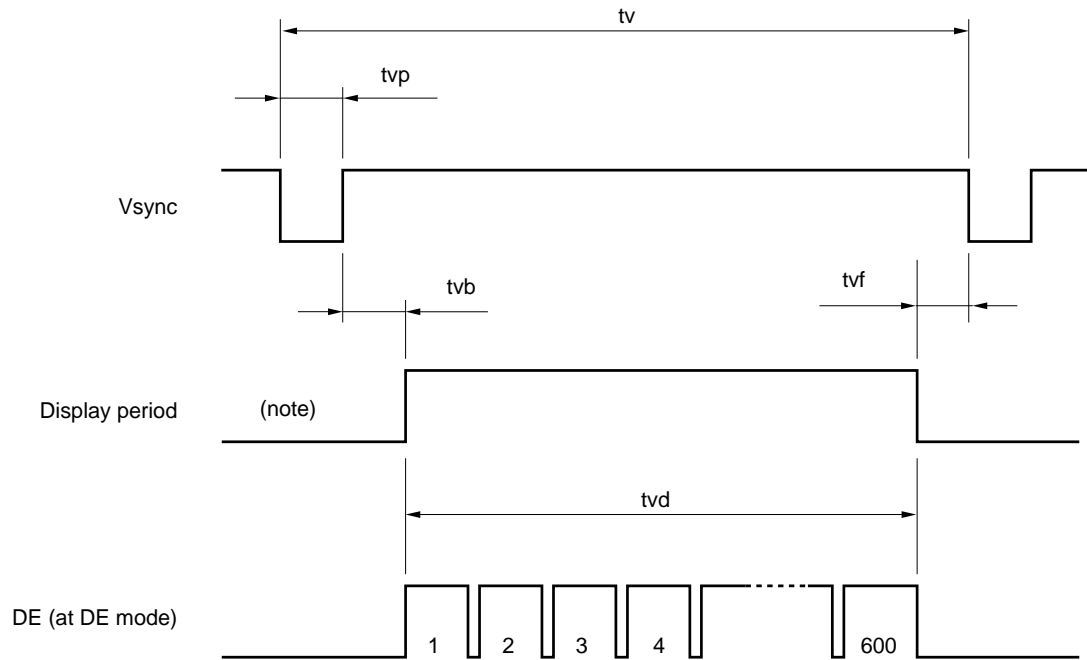
Parameter		Symbol	Min.	Typ.	Max.	Unit	Remarks
CLK	Frequency	1/tc	34.0	38.362	40.0	MHz	26.067 ns (TYP.)
	Duty	tch/tc	0.4	0.5	0.6	—	
	Rise, fall	tcrf	—	—	10	ns	
Hsync	Period	th	24.3	26.693	—	μs	37.463 kHz (TYP.)
			—	1024	—	CLK	
	Display period	thd	800			CLK	
	Front-porch	thf	24			CLK	fixed timing mode
			4	24	—	CLK	DE mode
	Pulse width	thp*	2	72	127	CLK	fixed timing mode
			2	72	127	CLK	DE mode
	Back-porch	thb*	73	128	198	CLK	fixed timing mode
			13	128	509	CLK	DE mode
	thp + thb*		200			CLK	fixed timing mode
			15	200	511	CLK	DE mode
	CLK-Hsync timing	thch	10	—	—	ns	
	Hsync-CLK timing	thcs	8	—	—	ns	
	Hsync-Vsync timing	tvhh	1	—	—	CLK	
	Vsync-Hsync timing	tvhs	15	—	—	ns	
	Rise, fall	thrf	—	—	10	ns	
	Vsync	Period	tv	16.1	16.683	17.2	ms
—				625	—	H	
Display period		tvd	600			H	
Front-porch		tvf	1			H	fixed timing mode
			1	1	—	H	DE mode
Pulse width		tvp*	1	2	23	H	fixed timing mode
			1	2	—	H	DE mode
Back-porch		tvb*	1	22	23	H	fixed timing mode
			1	22	—	H	DE mode
tvp + tvb*		24			H	fixed timing mode	
		2	24	24	H	DE mode	
Rise, fall		tvrf	—	—	10	ns	
DATA R0 - R5 G0 - G5 B0 - B5	CLK-DATA timing	tds	8	—	—	ns	
	DATA-CLK timing	tdh	10	—	—	ns	
	Rise, fall	tdrf	—	—	10	ns	
DE	DE-CLK timing	tes	8	—	—	ns	DE mode
	CLK-DE timing	teh	10	—	—	ns	
	Rise, fall	terf	—	—	10	ns	

All of parameters should be kept in the specified range.

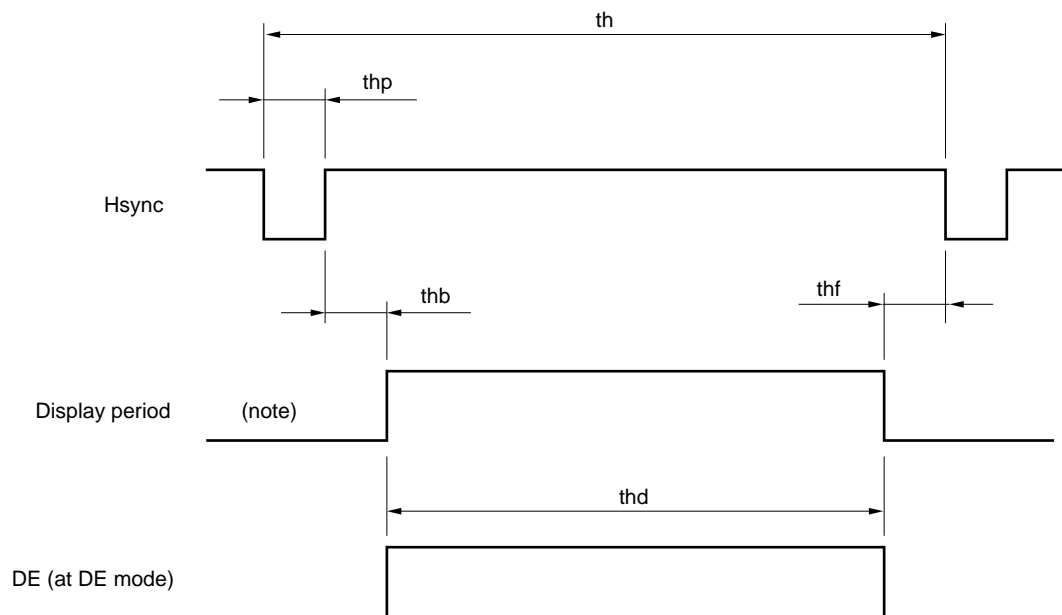


## (2) Definition of input signal timing

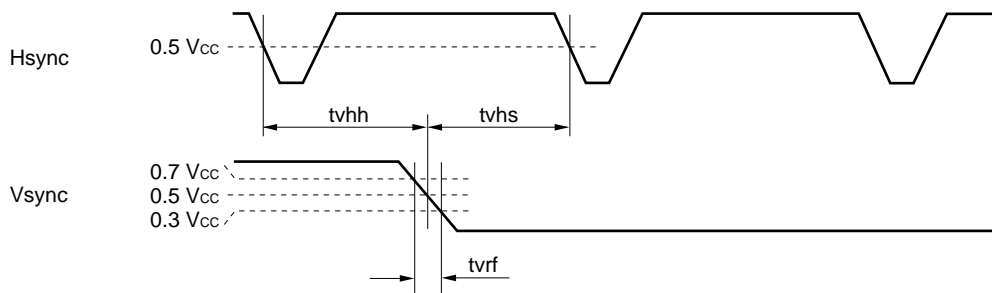
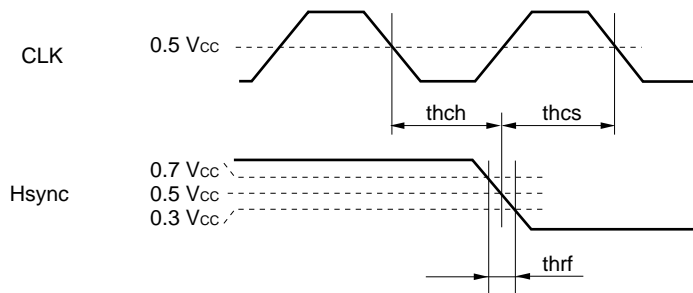
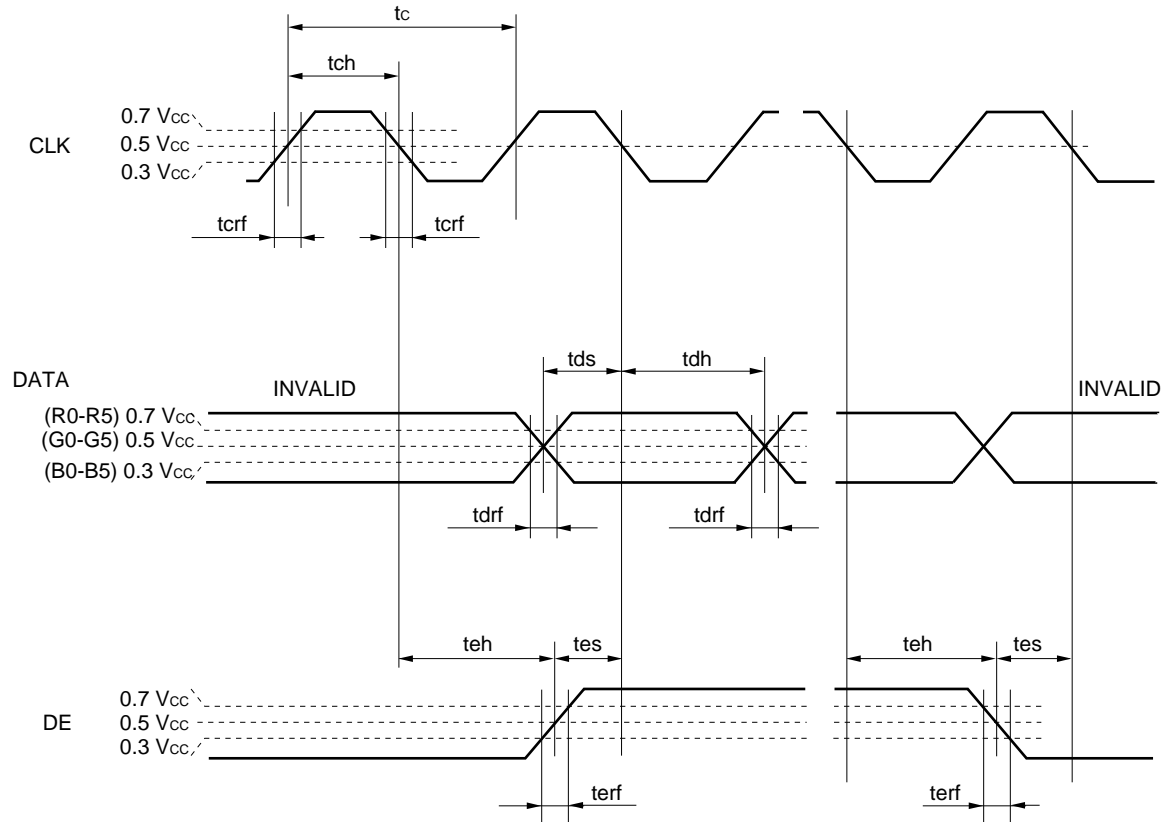
<Vertical>



<Horizontal>



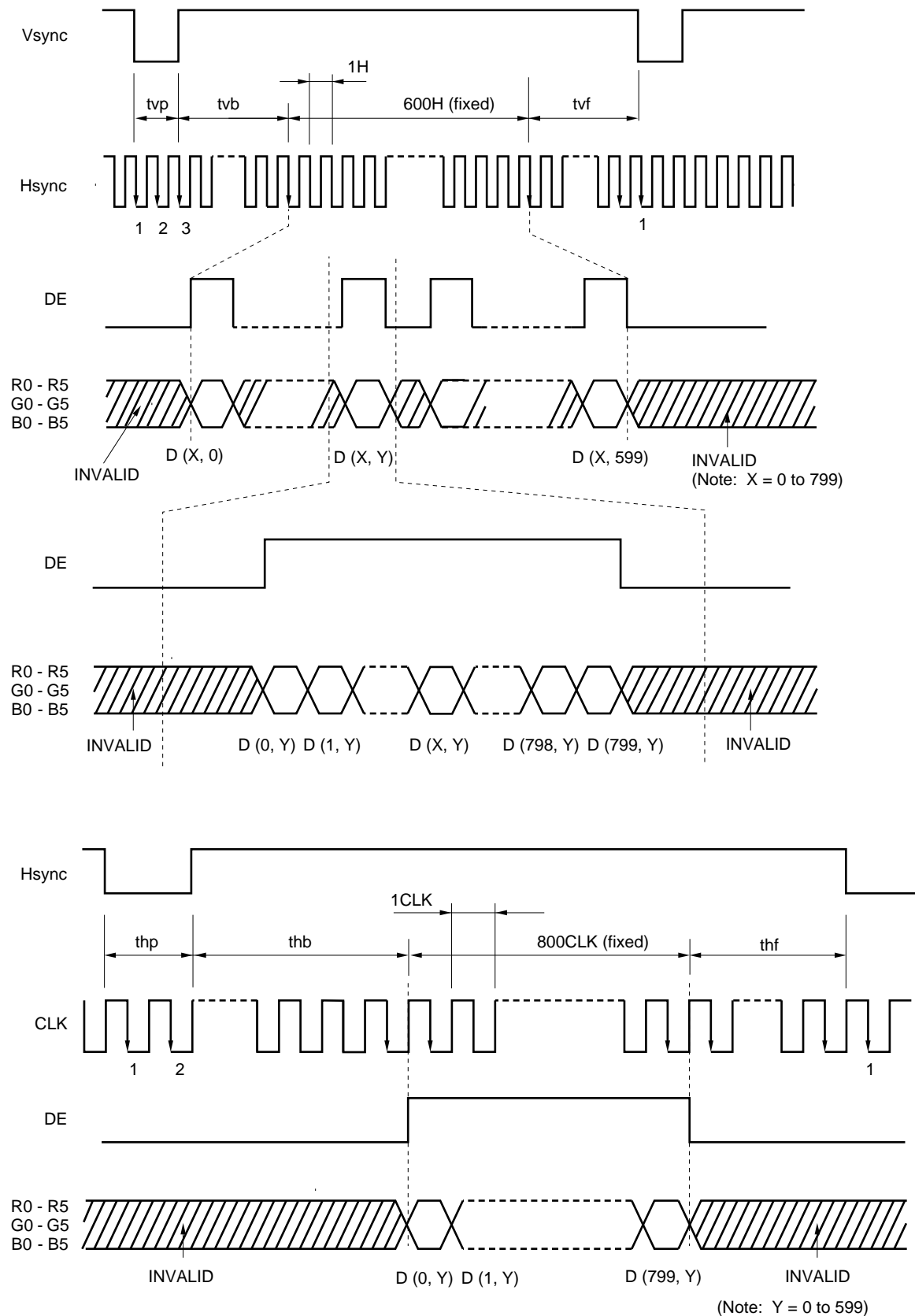
**Note** These do not exist as signals.



a) Fixed timing mode



b) DE mode



## (4) Display position

D (0, 0)	D (1, 0)	-----	D (X, 0)	-----	D (798, 0)	D (799, 0)
D (0, 1)	D (1, 1)	-----	D (X, 1)	-----	D (798, 1)	D (799, 1)
⋮	⋮	-----	⋮	-----	⋮	⋮
D (0, Y)	D (1, Y)	-----	D (X, Y)	-----	D (798, Y)	D (799, Y)
⋮	⋮	-----	⋮	-----	⋮	⋮
D (0, 598)	D (1, 598)	-----	D (X, 598)	-----	D (798, 598)	D (799, 598)
D (0, 599)	D (1, 599)	-----	D (X, 599)	-----	D (798, 599)	D (799, 599)

## OPTICAL CHARACTERISTICS

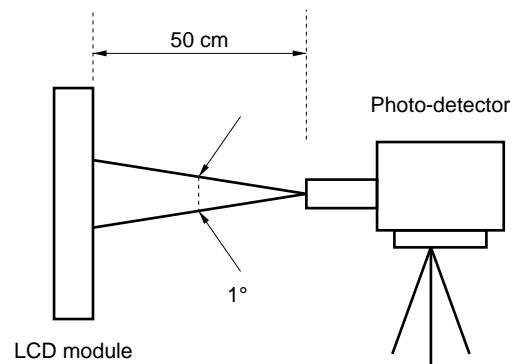
(T<sub>a</sub> = 25 °C, V<sub>CC</sub> = 3.3 V)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Contrast ratio	CR	Best contrast angle $\theta R = 0, \theta L = 0, \theta D = 5$ , White/black	—	400	—	—	<b>Note 1</b> <b>Note 4</b>
		$\gamma = 2.2$ viewing angle $\theta R = 0, \theta L = 0, \theta U = 0, \theta D = 0$ , White/Black, at center	80	150	—	—	
Luminance	Lvmax	White (IL = 3.0 mArms)	80	100	—	cd/m <sup>2</sup>	<b>Note 2</b>
Luminance uniformity	—	White	—	—	1.25	—	<b>Note 3</b>
Color gamut	C	At center, to NTSC	35	43	—	%	—
Response time	tpd	White to black	—	—	40	ms	<b>Note 5</b>
Viewing angle range	$\theta R$	CR > 10, $\theta U = 0, \theta D = 0$	—	50	—	deg.	<b>Note 4</b>
	$\theta L$		—	50	—	deg.	
	$\theta U$	CR > 10, $\theta R = 0, \theta L = 0$	—	20	—	deg.	
	$\theta D$		—	40	—	deg.	

**Notes 1.** The contrast ratio is calculated by using the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance with all pixels in "white"}}{\text{Luminance with all pixels in "black"}}$$

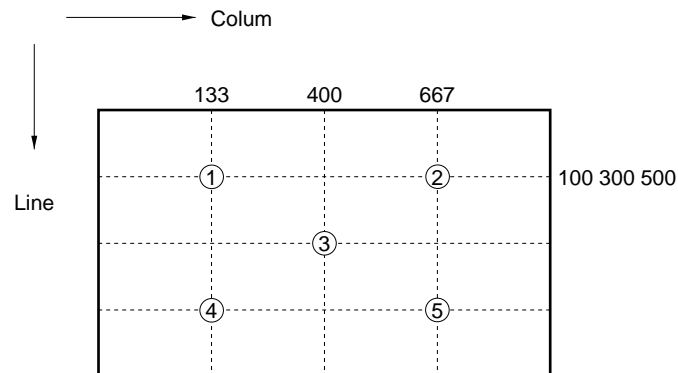
- 2.** The luminance is measured after 20 minutes from the module works, with all pixels in "white". The typical value is measured after luminance saturation.



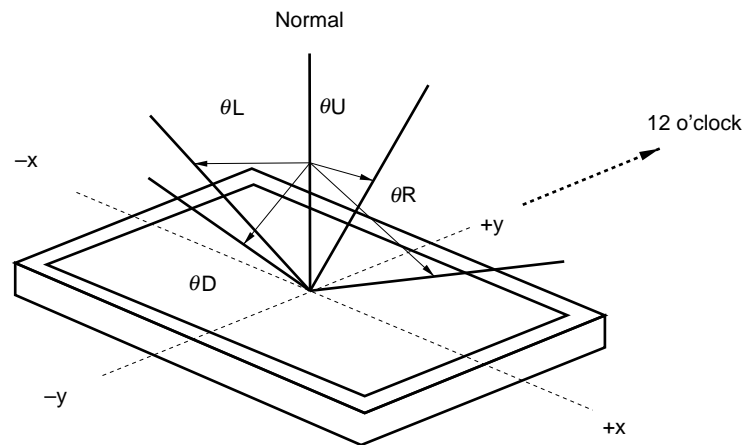
**Notes 3.** Luminance uniformity is calculated by using the following formula.

$$\text{Luminance uniformity} = \frac{\text{Maximum luminance}}{\text{Minimum luminance}}$$

The luminance is measured at near the five points shown below.

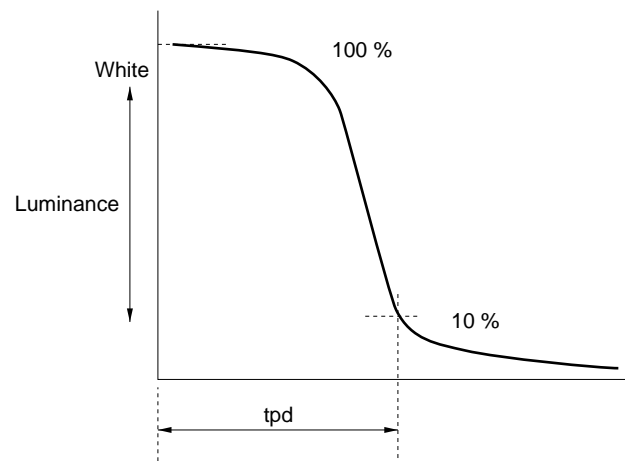


**4.** Definitions of viewing angle are as follows.




**5.** Definitions of response time is as follows.


Photo-detector output signal is measured when the luminance changes “white” to “black”. Response time is the time between 10 % and 100 % of the photo-detector output amplitude.




## GENERAL CAUTION

Next figures and sentence are very important, please understand these contents as follows.

 <b>CAUTION</b>	This figure is a mark that you will get hurt and/or the module will have damages when you make a mistake to operate.
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	This figure is a mark that you will get hurt when you make a mistake to operate
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### CAUTION

- (1) Caution when taking out the module
  - 1) Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
  - 1) As the electrostatic discharges may break the LCD module, handle the LCD module with care against electrostatic discharges. Peel a protection sheet off from the LCD panel surface as slowly as possible.
  - 2)  As the LCD panel and back-light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
  - 3) As the surface of polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
  - 4) Do not pull the interface connectors in or out while the LCD module is operating.
  - 5) Put the module display side down on a flat horizontal plane.
  - 6) Handle connectors and cables with care.
  - 7) The torque to mounting screw should never exceed 0.294 N•m (3 Kgf•cm).
- (3) Cautions for the operation
  - 1) When the module is operating, do not lose CLK, Hsync or Vsync signals. If any one of these signals is lost, the LCD panel would be damaged.
  - 2) Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
  - 1) Dew drop atmosphere should be avoided.
  - 2) Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
  - 3) This module uses cold cathode fluorescent lamp. Therefore, the life time of lamp becomes short conspicuously at low temperature.
- (5) Cautions for the module characteristics
  - 1) Do not apply fixed pattern data signal to the LCD module at product aging. Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
  - 1) Do not disassemble and/or re-assemble LCD module.
  - 2) Do not re-adjust variable resistor or switch etc.
  - 3) When returning the module for repair or etc., Please pack the module not to be broken.  
We recommend to use the original shipping packages.

Liquid Crystal Display has the following specific characteristics. These are not defects or malfunctions. The display condition of LCD module may be affected by the ambient temperature. The LCD module uses cold cathode tubes for backlighting. Optical characteristics, like luminance or uniformity, will change during time. Uneven brightness and/or small spots may be noticed depending on different display patterns.

Technical drawing of the HV-2S-C1 (JAE) module, showing top and side views with dimensions and labels.

**Top View Dimensions:**

- Overall width:  $242 \pm 0.5$
- Overall height:  $173.9 \pm 0.5$
- Module center offset (horizontal):  $158.1 \pm 0.3$
- Module center offset (vertical):  $146.5 \pm 0.3$
- Active area width:  $211.2 \times 158.4$
- Active area height:  $211.2 \times 158.4$
- Bezel opening:  $215.2 \times 162.4$
- Module center offset (horizontal):  $158.1 \pm 0.3$
- Module center offset (vertical):  $146.5 \pm 0.3$
- Active area width:  $211.2 \times 158.4$
- Active area height:  $211.2 \times 158.4$
- Bezel opening:  $215.2 \times 162.4$

**Side View Dimensions:**

- Module thickness:  $7.5 \text{ MAX}$
- Sealing tape thickness:  $0.5$
- Module center offset (horizontal):  $158.1 \pm 0.3$
- Module center offset (vertical):  $146.5 \pm 0.3$

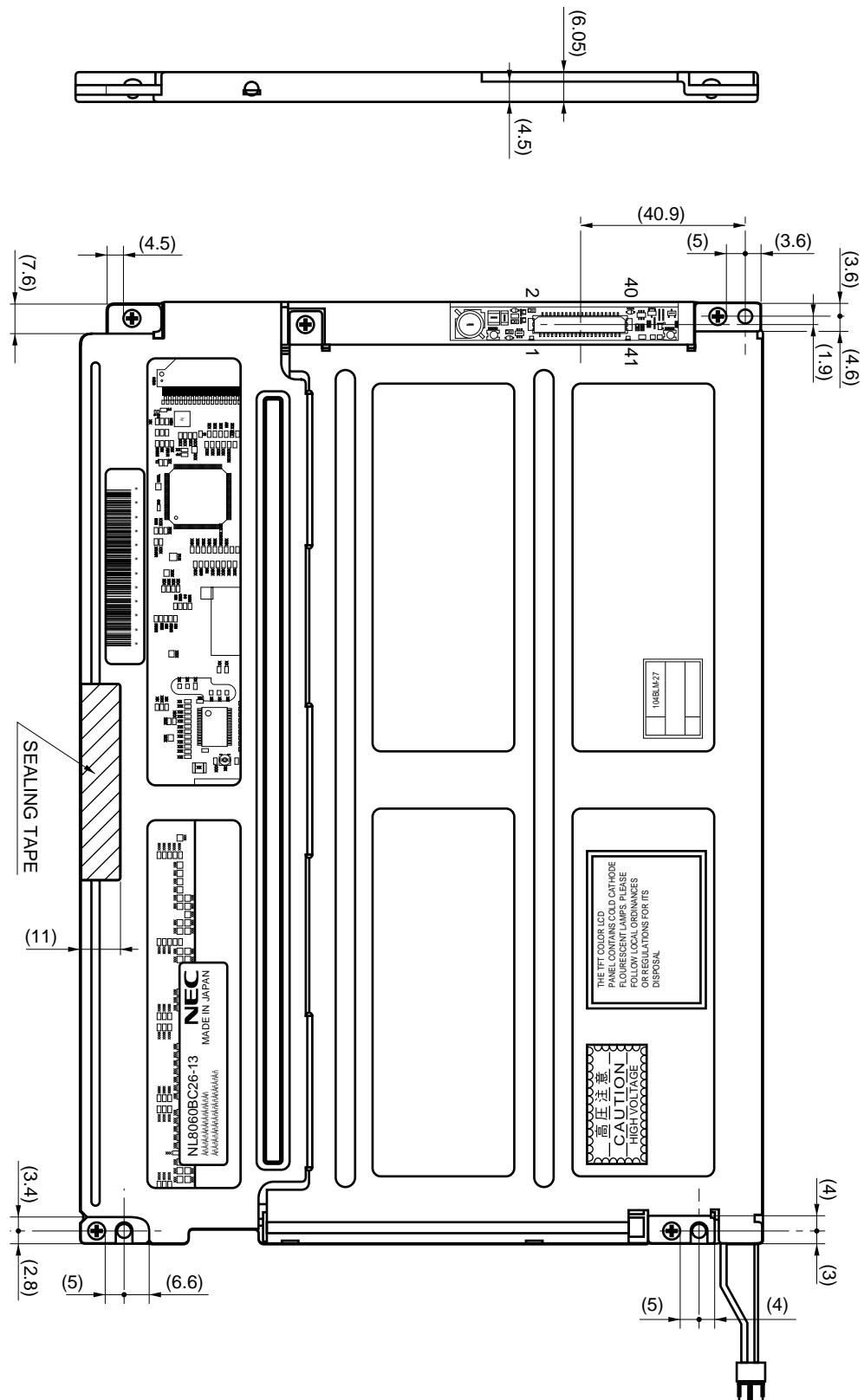
**Labels:**

- SEALING TAPE
- MODULE CENTER
- ACTIVE AREA CENTER
- BEZEL OPENING ( $215.2 \times 162.4$ )
- ACTIVE AREA ( $211.2 \times 158.4$ )
- 3- $\phi 3.4 \pm 0.2$
- 4-CPIMS  $\times M2$
- 1: HOT
- 2: COLD

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OUTLINE DRAWING: Rear View (Unit in mm)



[MEMO]

[MEMO]

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