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**TO :**

**Date : July. 10, 2007**

## **HannStar Product Specification**

**Model : HSD230WX01  
-A**

Accepted by:

Signature

Date

Proposed by: Technical Service Division

Signature

Date

- Note:
1. Please contact HannStar Display Corp., before designing your product based on this module specification.
  2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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## Record of Revisions

Rev.	Date	Description of change
1.0	July.10, 2007	<ul style="list-style-type: none"> <li>Specification for HSD230WX01-A was first issued</li> </ul>



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## 1.0 GENERAL DESCRIPTION

### 1.1 Introduction

HannStar Display model HSD230WX01-A is a color active matrix thin film transistor (TFT) liquid crystal display(LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 22.92-inch diagonally measured active display area with Wide XGA resolution (768 vertical by 1280 horizontal pixel array) and can display up to 16.7million colors.

### 1.2 Features

- 22.92" WXGA for TV application panel
- High brightness and wide view angle
- Fast response time & High color reproduction
- LVDS interface system.
- Long lamp lifetime.

### 1.3 Applications

- High Definition TV application
- Public Display
- Measuring Instrument
- New media equipment

### 1.4 General information

Item	Specification	Unit
Display area	499.20(H) x 299.52(V)	mm
Number of Pixel	1280(H) x 768(V)	pixels
Pixel pitch	0.39(H) x 0.39(V)	mm
Pixel arrangement	RGB Vertical stripe	
Display color	16,777,216	colors
Display mode	Normally Black	
Surface treatment	Anti-reflective, Hard-Coating(3H)	
Weight	3300	g
Back-light	12 CCFL	
Input signal	1-ch LVDS	

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### 1.5 Mechanical Information

Item		Min.	Typ.	Max.	Unit
Module Size	Horizontal(H)	—	539.0	—	Mm
	Vertical(V)	—	340.0	—	Mm
	Depth(D)	—	30.2	—	Mm
Weight (Without inverter)		—	3300	—	g

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## 2.0 ABSOLUTE MAXIMUM RATING

### 2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T <sub>STG</sub>	-20	60	°C	
Operating temperature	T <sub>OPR</sub>	0	50	°C	
Vibration(non-operating)	V <sub>nop</sub>	—	1.5	G	(1)
Shock(non-operating)	S <sub>nop</sub>	—	70	G	(2)
Storage humidity	H <sub>STG</sub>	10	90	%RH	(3)
Operating humidity	H <sub>OP</sub>	10	80	%RH	(3)
Low pressure(operating)	H <sub>LOP</sub>	697	—	hPa	(4)
Low pressure(non-operating)	H <sub>LNOP</sub>	116	—	hPa	(5)

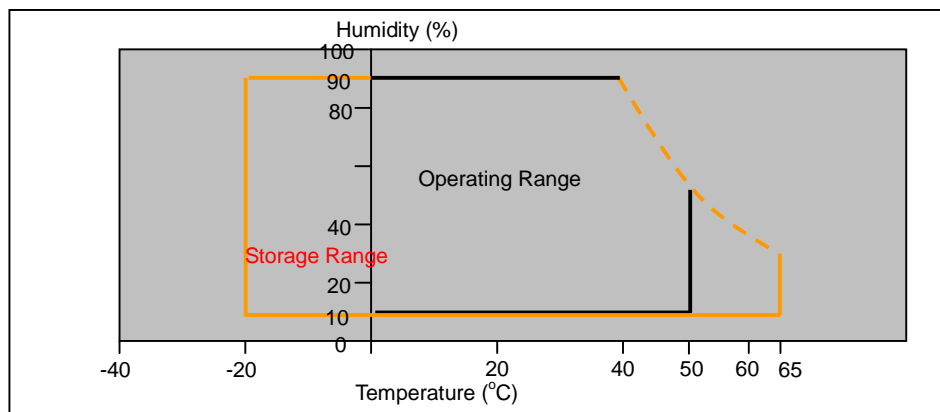
Note (1) 10-500-10Hz, sine wave, 30min/cycle, X/Y/Z each one cycle except for resonant frequency.

(2) 11ms/±X, ±Y, ±Z direction, half-sine wave.

(3) Max wet bulb temp = 39°C

(4) 2hours. (10000 feet)

(5) 24hours. (50000 feet)



## 2.2 Electrical Absolute Rating

### 2.2.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	V <sub>DD</sub>	-0.3	5.5	V	(1)
Logic input voltage	V <sub>IN</sub>	-0.3	3.6	V	(1)

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### 2.2.2 Back-Light Unit

Item	Symbol	Min.	Max.	Unit	Note
Lamp voltage	$V_L$	800	1000	$V_{rms}$	(1)
Lamp current	$I_L$	3	7.0	mA	(1)
Lamp frequency	$f_L$	25	100	kHz	(1)

Note (1) Permanent damage may occur to the LCD module if beyond this specification.  
Functional operation should be restricted to the conditions described under normally operating conditions.

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### 3.0 OPTICAL CHARACTERISTICS

#### 3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast		CR	$\Theta=0$ $\Phi=0$ Normal viewing angle	—	400	—		(1)(2)
Response time	Rising	T <sub>R</sub>		—	Tr+Tf=16	—	msec	(1)(3)
	Falling	T <sub>F</sub>		—		—		
luminance of white(center)		Y <sub>L</sub>		—	500	—	cd/m <sup>2</sup>	(1)(4)(5) ( I <sub>L</sub> =5.0mA )
Color chromaticity (CIE1931)	Red	R <sub>x</sub>		TBD	TBD	TBD		(1)(4)
		R <sub>y</sub>		TBD	TBD	TBD		
	Green	G <sub>x</sub>		TBD	TBD	TBD		
		G <sub>y</sub>		TBD	TBD	TBD		
	Blue	B <sub>x</sub>		TBD	TBD	TBD		
		B <sub>y</sub>		TBD	TBD	TBD		
	White	W <sub>x</sub>		—	0.28	—		
		W <sub>y</sub>		—	0.31	—		
Viewing angle	Hor.	Θ <sub>L</sub>	CR>10	—	85	—		
		Θ <sub>R</sub>		—	85	—		
	Ver.	Θ <sub>U</sub>		—	85	—		
		Θ <sub>D</sub>		—	85	—		
Brightness uniformity		B <sub>UNI</sub>		70	—	—	%	(6)



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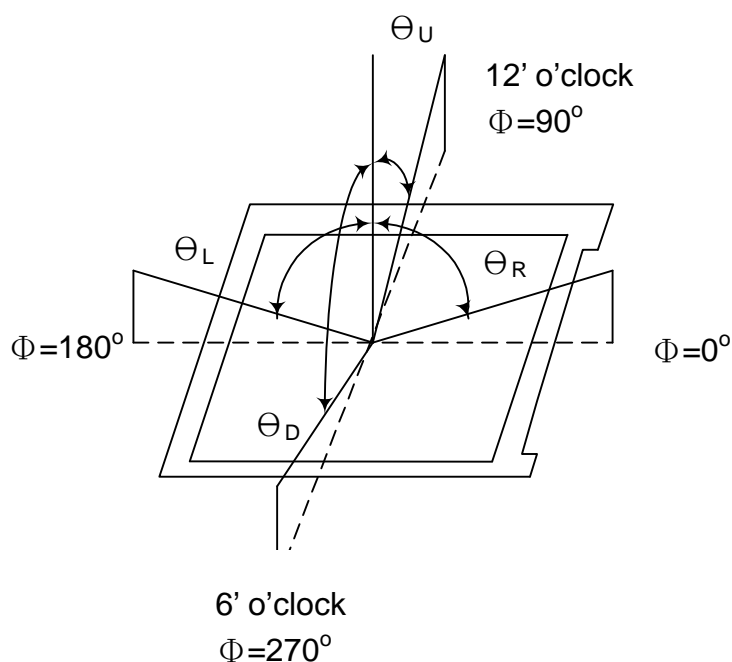
### 3.2 Measuring Condition

- Measuring surrounding : dark room
- Lamp current  $I_L$  :5.0mA, lamp freq.  $F_L$ = 60KHz
- $V_{DD}$ =+5.0V,  $f_V$ =60Hz,  $f_{DCLK}$ =65MHz
- Inverter Model:PLCD09231001(EMax)
- Surrounding temperature : 25°C
- 30min. warm-up time

### 3.3 Measuring Equipment

- LCD-7000 of Otsuka Electrics Corp., which utilized MCPD-7000 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 10~12mm

Note (1) Definition of Viewing Angle:



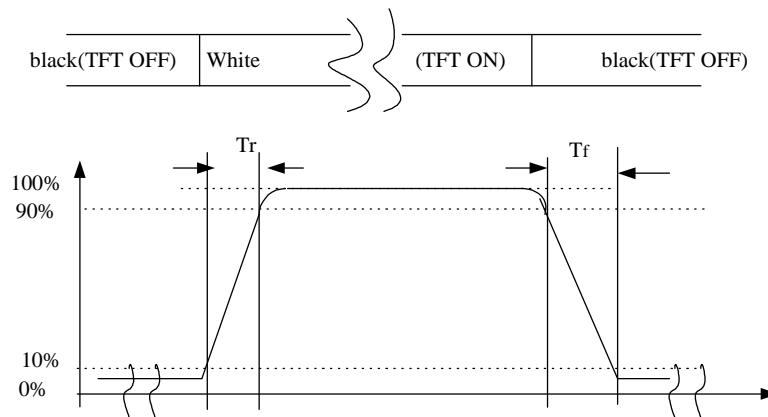
Note (2) Definition of Contrast Ratio(CR) :

Measured at the center point of panel

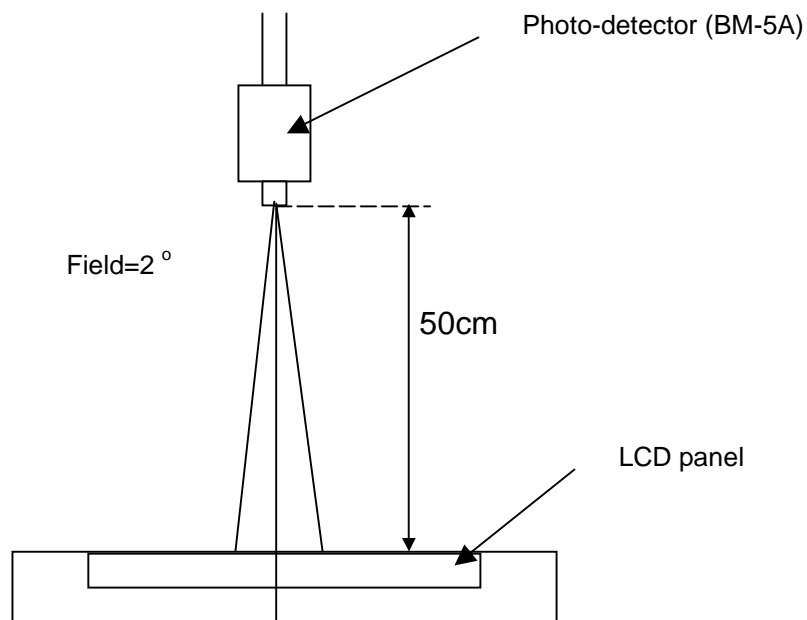
$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

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Note (3) Definition of Response Time: Sum of  $T_r$  and  $T_f$



Note (4) Optical Characteristic Measurement Setup



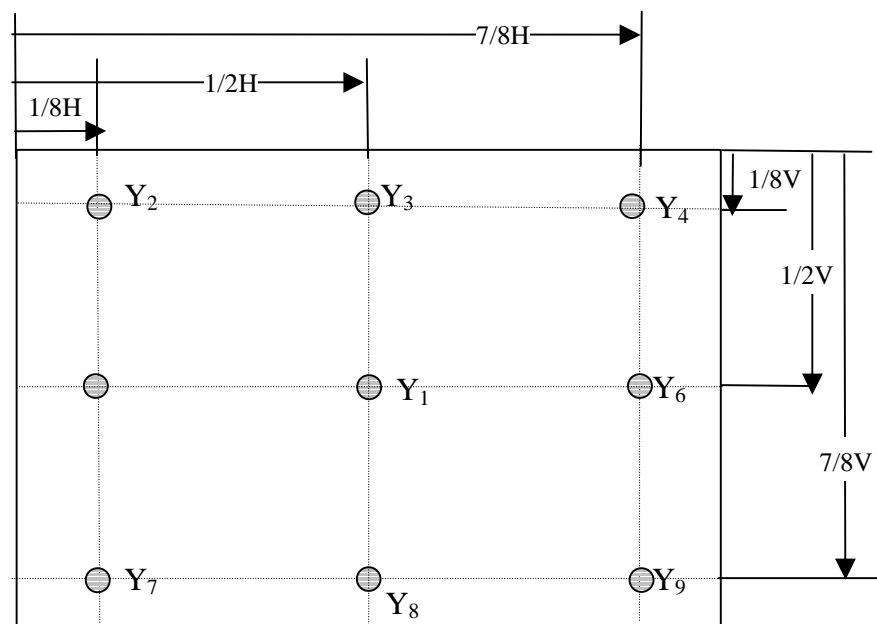
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Note (5) Definition of Luminance of White: measured the luminance of white at Y<sub>1</sub>

**Luminance of white = Y<sub>1</sub>**

Note (6) Definition of Brightness Uniformity

$$\text{Luminance uniformity} = \frac{(\text{Min Luminance of 9 points})}{(\text{Max Luminance of 9 points})} \times 100\%$$



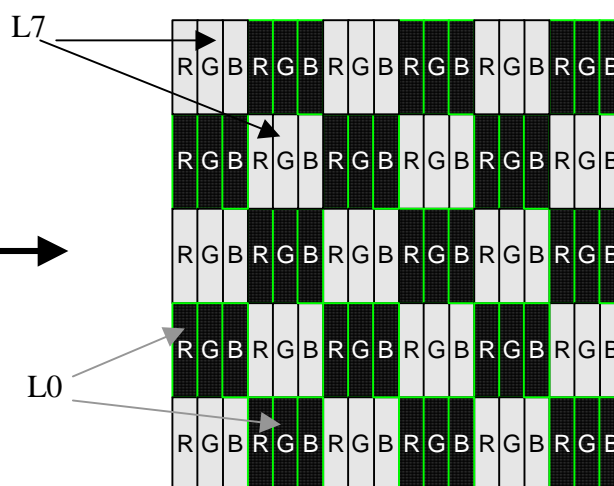
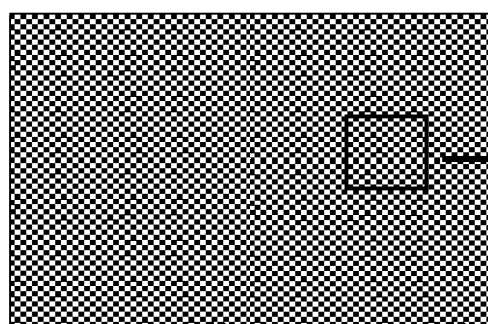
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## 4.0 ELECTRICAL CHARACTERISTICS

### 4.1 TFT LCD Module

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of power		V <sub>DD</sub>	4.5	5.0	5.5	V	
Differential input threshold voltage	High	V <sub>IH</sub>	—	—	100	mV	V <sub>CM</sub> =1.2 V
	Low	V <sub>IL</sub>	-100	—	—	mV	
Current of power supply	Mosaic	I <sub>DD</sub>	—	TBD	—	mA	(1)
Vsync frequency		f <sub>V</sub>	—	60	—	Hz	(2)
Hsync frequency		f <sub>H</sub>	—	48.4	—	kHz	
Main frequency		f <sub>DCLK</sub>		65.0		MHz	

Note (1) Mosaic : Dot checker image



Grey scale: L0~L256.

L0: Luminance with all pixels black.

L255: Luminance with all pixels white.

Note (2) When  $f_v$  is too low, a flicker may be occurred on the display.

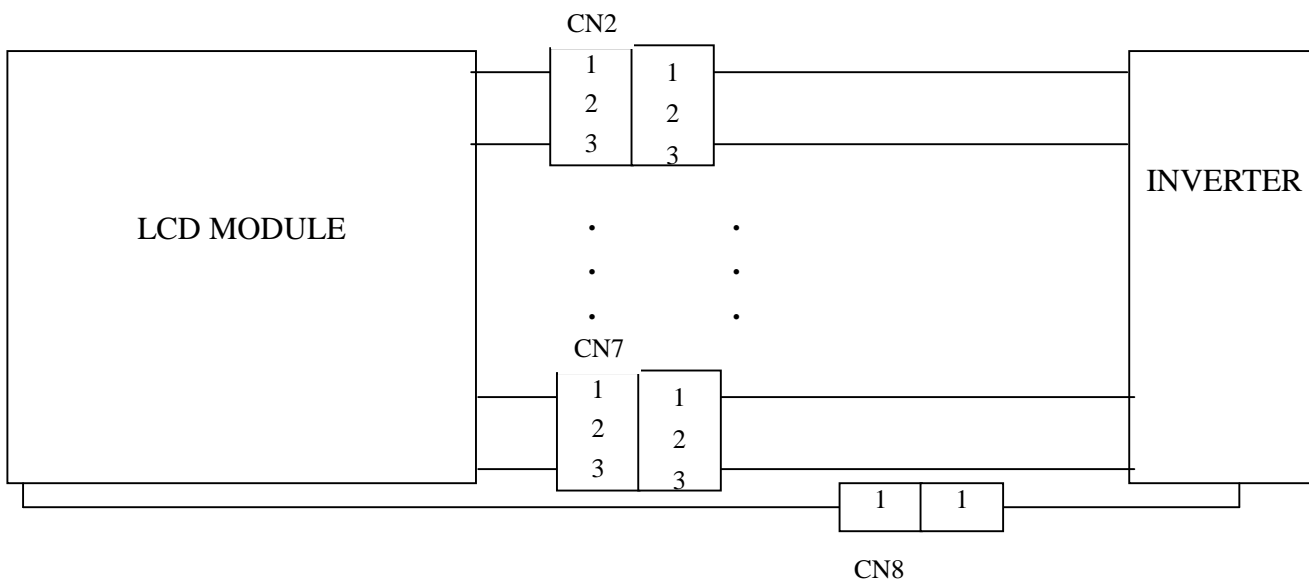
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## 4.2 Back-Light Unit

The back-light system is 12 CCFL(Cold Cathode Fluorescent Lamp).

The characteristics of the lamp are shown in the following tables.

INVERTER: PLCD09231001(EMax)(1)



Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp current	IL	3	5.0	7	mA(rms)	(1)
Lamp voltage	VL	800	900	1000	V(rms)	$I_L=5.0\text{mA}$
Frequency	fL	25	60	100	KHz	(2)
Lamp Operating life time	Hr		55000	--	Hour	(3)
Startup voltage	Vs	--		1400	V(rms)	at 25°C
				1600		at 0°C

Note (1) Lamp current is measured with current meter for high frequency as shown below. Specified valued are for a lamp.

Note (2) Lamp frequency may produce interference with horizontal synchronous Frequency and this may cause line flow on the display. Therefore lamp Frequency shall be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

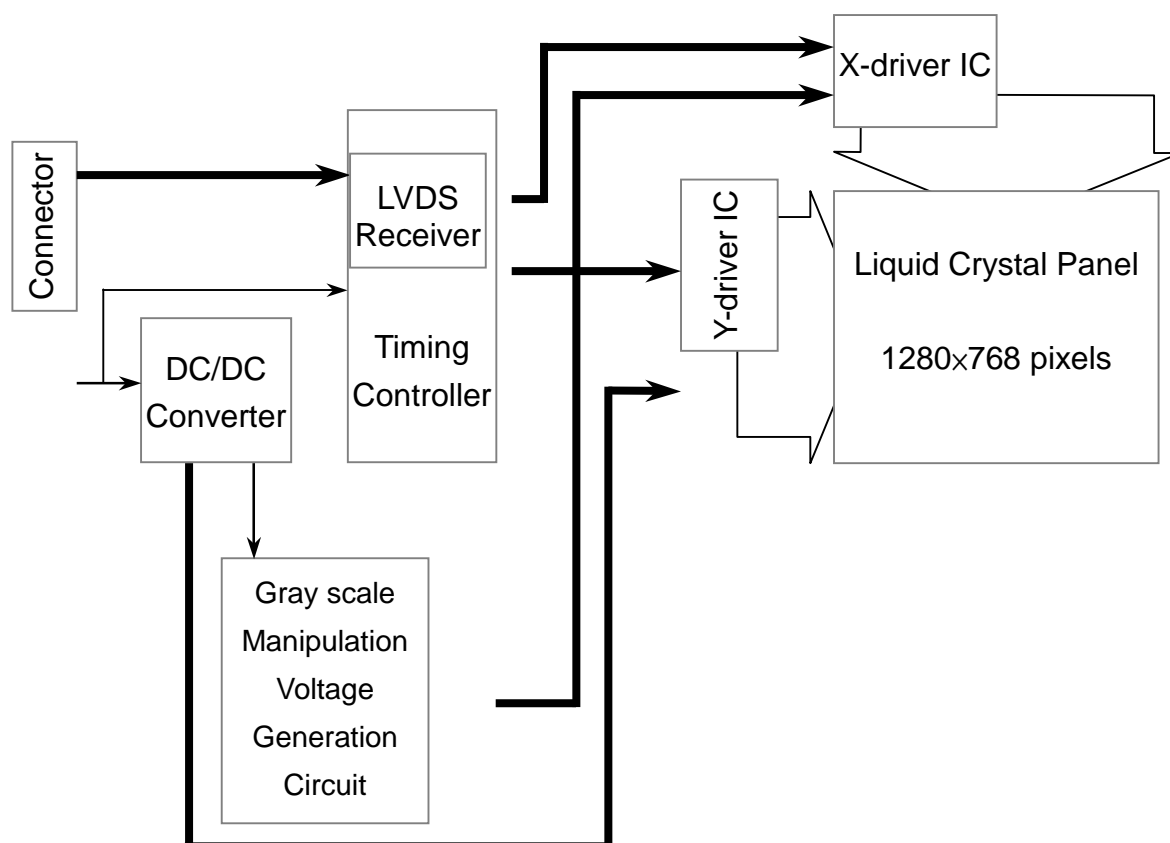
Note (3) Lamp operating Life time (Hr) can be defined as the time in which it Continues to operate under the condition:  $T_a=25\sim35^\circ\text{C}$ ,  $I_L=5.0\text{mA}(\text{typ})$  until One of the following events occurs:

1. When the brightness becomes 50%
2. At  $0^\circ\text{C}$ , the starting voltage of lamp must exceed the Max startup voltage.

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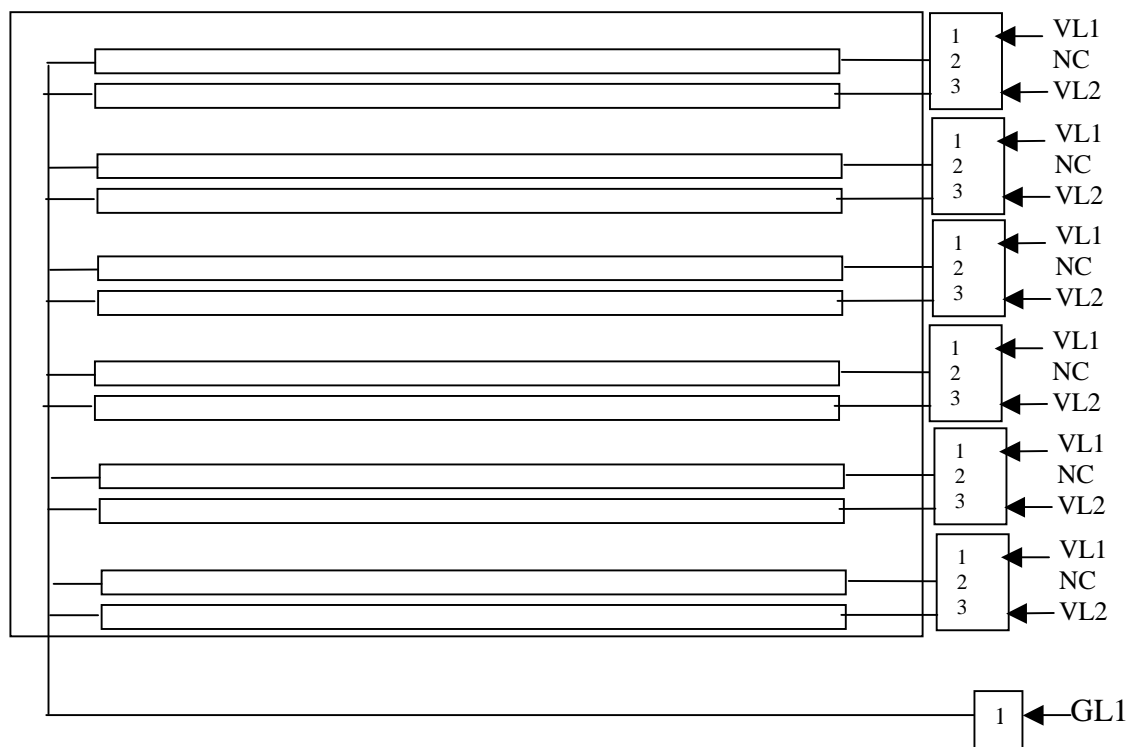
## 5.0 BLOCK DIAGRAM

### 5.1 TFT LCD Module



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## 5.2 Back Light Unit



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## 6.0 INTERFACE PIN CONNECTION

### 6.1 TFT LCD Module

Connector : HRS:DF14-20P-1.25H

Matching Socket : HRS:DF14-20P-1.25C

Pin No.	Symbol	Function	Polarity	Remark
1	VDD	Power Supply : +5.0V	—	
2	VDD	Power Supply : +5.0V	—	
3	GND	Ground	—	
4	GND	Ground	—	
5	IN0-	Transmission Data of Pixels 0	Negative	
6	IN0+	Transmission Data of Pixels 0	Positive	
7	GND	Ground		
8	IN1-	Transmission Data of Pixels 1	Negative	
9	IN1+	Transmission Data of Pixels 1	Positive	
10	GND	Ground		
11	IN2-	Transmission Data of Pixels 2	Negative	
12	IN2+	Transmission Data of Pixels 2	Positive	
13	GND	Ground	—	
14	CLK-	Sampling Clock	Negative	
15	CLK+	Sampling Clock	Positive	
16	GND	Ground		
17	IN3-	Transmission Data of Pixels 3	Negative	
18	IN3+	Transmission Data of Pixels 3	Positive	
19	GND	Ground		
20	GND	Ground		

### 6.2 Back-Light Unit

High Voltage connector:

CN2—CN7: BHR-03VS-1/Japan Solderless Terminal MFG Co., LTD

Terminal no.	Symbol	Function
1	VL1	CCFL power supply (high voltage)
2	GND	NC
3	VL2	CCFL power supply (high voltage)





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Low Voltage connector:

CN8: ZHR-02/Japan Solderless Terminal MFG Co., LTD

Terminal no.	Symbol	Function
1	GL1	CCFL power supply (low voltage)

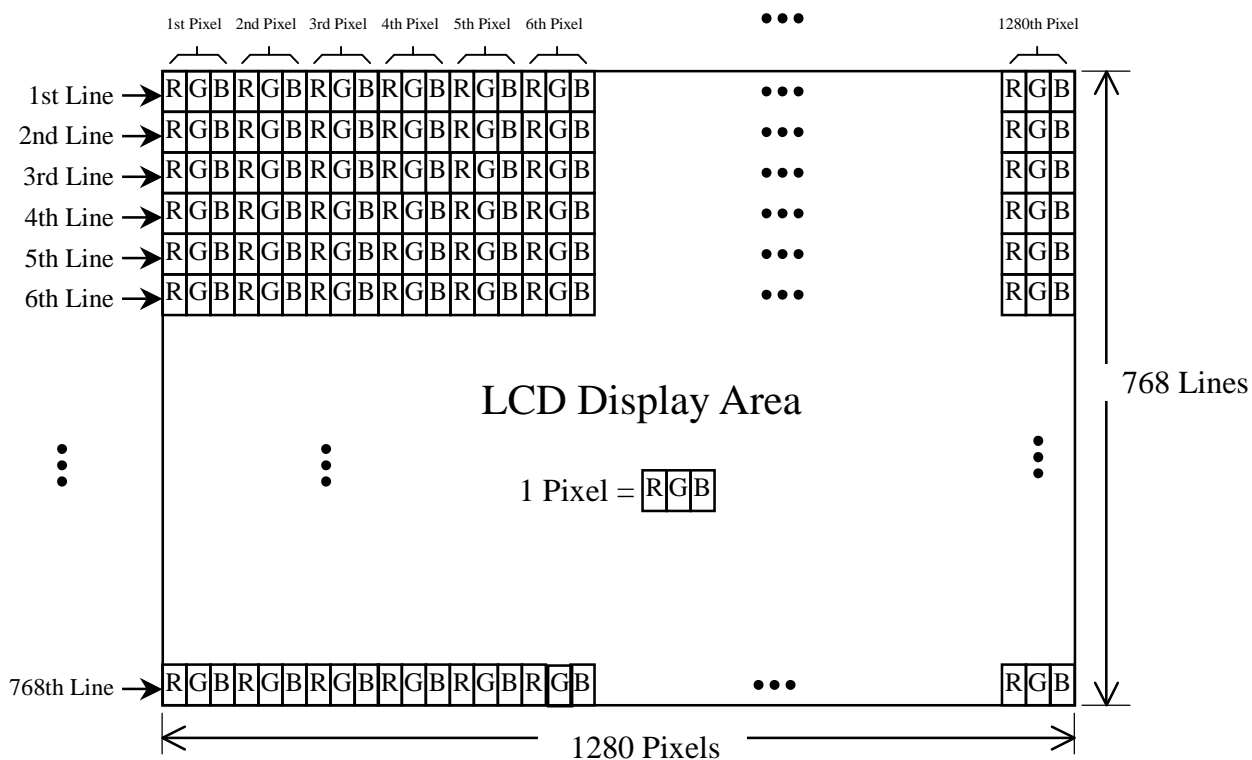
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### 6.3 Relationship Between Displayed Color and Input

	Display	MSB								LSB								MSB								LSB								Gray scale level
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0									
Basic color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-			
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	-			
	Green	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	-		
	Light Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-		
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-		
	Purple	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-		
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-		
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-		
Gray scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0		
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1		
		L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2		
		:								:								:								L3...L251								
		H	H	H	H	H	L	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252		
		H	H	H	H	H	H	L	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253	
	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254		
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255		
Gray scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0		
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1		
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2		
		:								:								:								L3...L251								
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	L	H	H	L	L	L	L	L	L	L	L252	
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	L	H	H	L	L	L	L	L	L	L253	
	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L254		
	Green	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Green L255		
Gray scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0		
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1		
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2		
		:								:								:								L3...L251								
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	L	H	H	L	L	L	L	L	L	L252		
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L253	
	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L254		
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L	L	L	L	L	L	Blue L255		
Gray scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0		
	Dark ↑ ↓ Light	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1		
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2		
		:								:								:								L3...L251								
		H	H	H	H	H	L	H	H	H	H	L	H	H	H	H	L	H	H	H	H	L	H	H	L	L	L	L	L	L	L	L252		
		H	H	H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	L	H	H	H	H	L	H	L	L	L	L	L	L	L	L253	
	H	H	H	H	H	H	H	L	H	H	H	H	H	L	H	H	H	H	H	L	H	H	L	L	L	L	L	L	L	L	L	L254		
	White	H	H	H	H	H	H	H	H	L	H	H	H	H	H	H	L	H	H	H	H	H	H	L	L	L	L	L	L	L	L	White L255		

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## 6.4 Pixel Format



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## 6.5 Recommended Transmitter to HSD230WX01-A Interface Assignment

### 8bit Transmitter

DS90CF386 or equivalent					
Input terminal no.	Input signal		Output signal	To HSD230WX01-A interface(CN1)	
Symbol	Symbol	Function	Symbol	Terminal	Symbol
Txin0	R0	RED Pixels Display Data	TOUT0-TOUT0+	No. 5 No. 6	IN0-IN0+
Txin1	R1	RED Pixels Display Data			
Txin2	R2	RED Pixels Display Data			
Txin3	R3	RED Pixels Display Data			
Txin4	R4	RED Pixels Display Data			
Txin6	R5	RED Pixels Display Data(MSB)			
Txin7	G0	RED Pixels Display Data			
Txin8	G1	RED Pixels Display Data	TOUT1-TOUT1+	NO.8 NO.9	IN1-IN1+
Txin9	G2	GREEN Pixels Display Data			
Txin12	G3	GREEN Pixels Display Data			
Txin13	G4	GREEN Pixels Display Data			
Txin14	G5	GREEN Pixels Display Data(MSB)			
Txin15	B0	BLUE Pixels Display Data			
Txin18	B1	BLUE Pixels Display Data			
Txin19	B2	BLUE Pixels Display Data	TOUT2-TOUT2+	NO.11 NO.12	IN2-IN2+
Txin20	B3	BLUE Pixels Display Data			
Txin21	B4	BLUE Pixels Display Data			
Txin22	B5	BLUE Pixels Display Data (MSB)			
Txin24	NC	Non Connection (open)			
Txin25	NC	Non Connection (open)			
Txin26	ENAB	Compound Synchronization Signal			
Txin27	R6	RED Pixels Display Data(LSB)	TOUT3-TOUT3+	NO.17 NO.18	IN3-IN3+
Txin5	R7	RED Pixels Display Data			
Txin10	G6	GREEN Pixels Display Data(LSB)			
Txin11	G7	GREEN Pixels Display Data			
Txin16	B6	BLUE Pixels Display Data (LSB)			
Txin17	B7	BLUE Pixels Display Data			
Txin23	NC	Non Connection (open)			
CLK IN	NCLK	DATA SAMPLING CLOCK	TCLK OUT-TCLK OUT+	NO. 14 NO. 15	CLK IN-CLK IN+

IN0	TIN7	TIN6	TIN4	TIN3	TIN2	TIN1	TIN0
	G0	R5	R4	R3	R2	R1	R0
IN1	TIN18	TIN15	TIN14	TIN13	TIN12	TIN9	TIN8
	B1	B0	G5	G4	G3	G2	G1
IN2	TIN26	TIN25	TIN24	TIN22	TIN21	TIN20	TIN19
	ENAB	NC	NC	B5	B4	B3	B2
IN3	TIN23	TIN17	TIN16	TIN11	TIN10	TIN5	TIN27
	NC	B7	B6	G7	G6	R7	R6

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## 7.0 INTERFACE TIMING <sup>1)2)3)4)5)6)</sup>

### 7.1 Timing Parameters (DE mode)

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Frame period	t1	772 $\times$ t3 —	806 $\times$ t3 16.7	1024 $\times$ t3 —	— ms	1) 5)
Vertical display term	t2	768 $\times$ t3 —	768 $\times$ t3 15.9	768 $\times$ t3 —	— ms	1)
One line Scanning time	t3	1300 $\times$ t5 —	1344 $\times$ t5 20.7	1688 $\times$ t5 —	— $\mu$ s	1) 5)
Horizontal display term	t4	1 $\times$ t5 —	1280 $\times$ t5 19.7		— $\mu$ s	1)
Clock period	t5		15.4	—	ns	5)

Note 1) Refer to TIMING CHART at page20, 21 and LVDS specification (DS90C385) by National Semiconductor Corporation.

Note 2) When ENAB is fixed to “H” level or “L” level after NCLK is supplied, the panel displays black with some flicker.

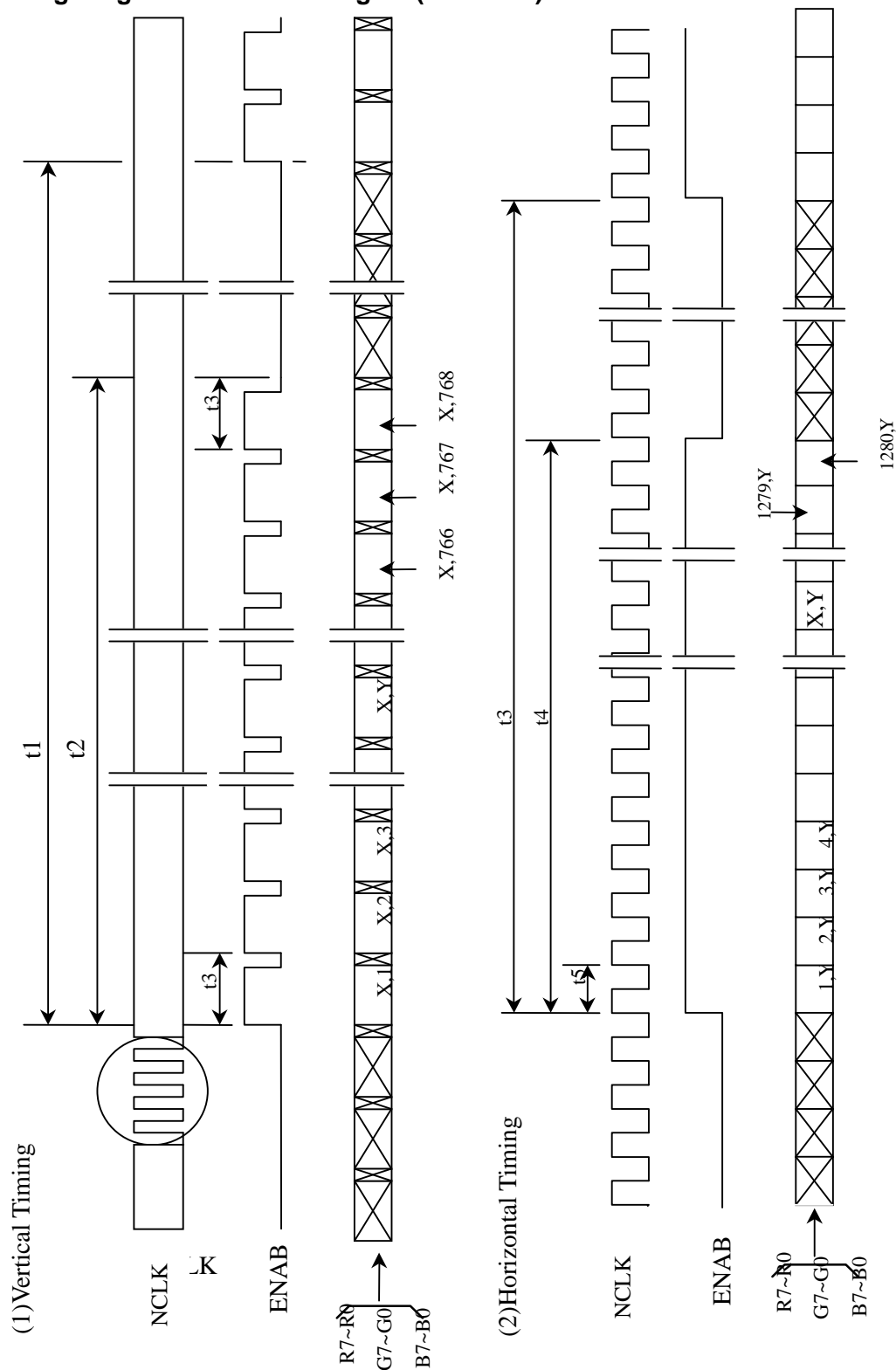
Note 3) If NCLK is fixed to “H” level or “L” level, for certain period while ENAB is supplied, the panel may be damaged.

Note 4) Do not make t1 and t3's fluctuate. If t1 or t3 is fluctuating, the panel displays black.

Note 5) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality. There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency).

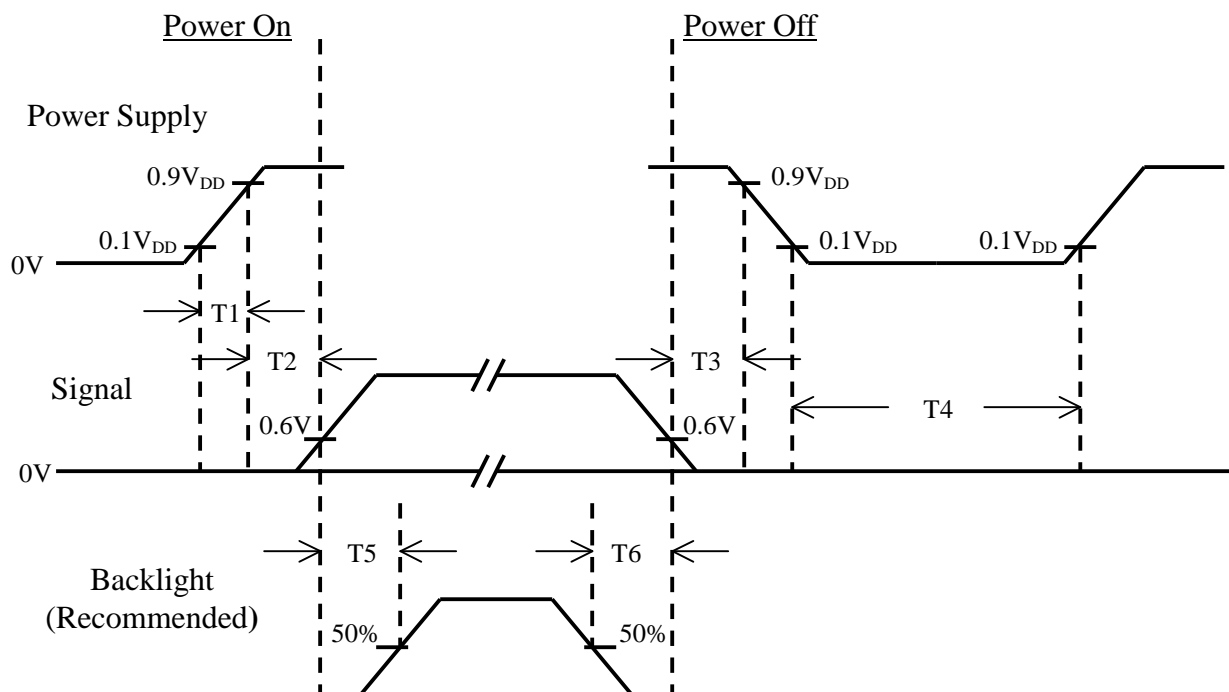
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## 7.2 Timing Diagram of Interface Signal (DE mode)



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### 7.3 Power ON/OFF Sequence



Item	Min.	Typ.	Max.	Unit	Remark
T1	0	—	10	msec	
T2	0	—	50	msec	
T3	0	—	50	msec	
T4	1	—	—	sec	
T5	200	—	—	msec	
T6	200	—	—	msec	

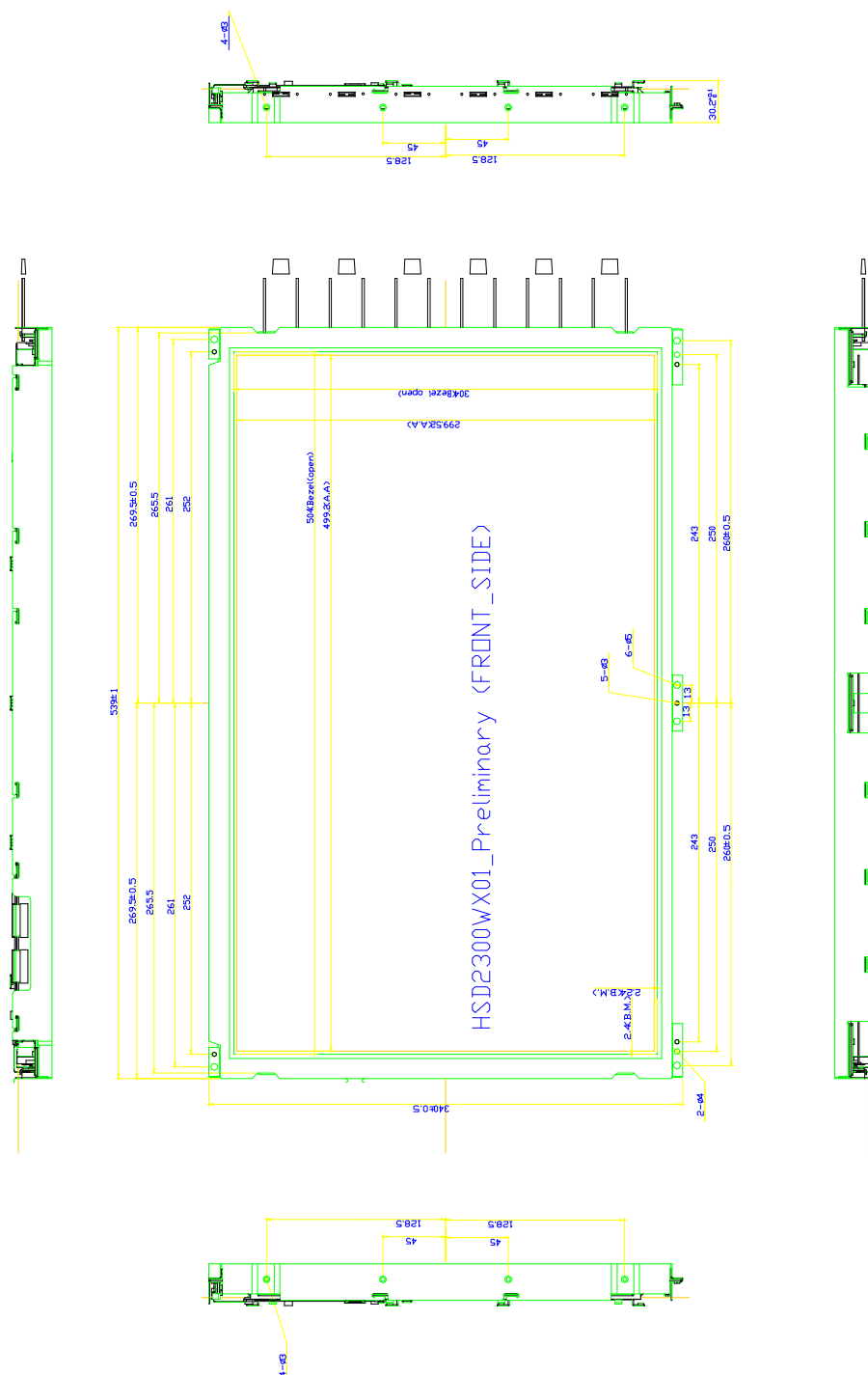
- (1) The supply voltage of the external system for the module input should be the same as the definition of V<sub>DD</sub>.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of V<sub>DD</sub> = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

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## 8.0 OUTLINE DIMENSION

### (1) Front view outline dimension

Unit: mm



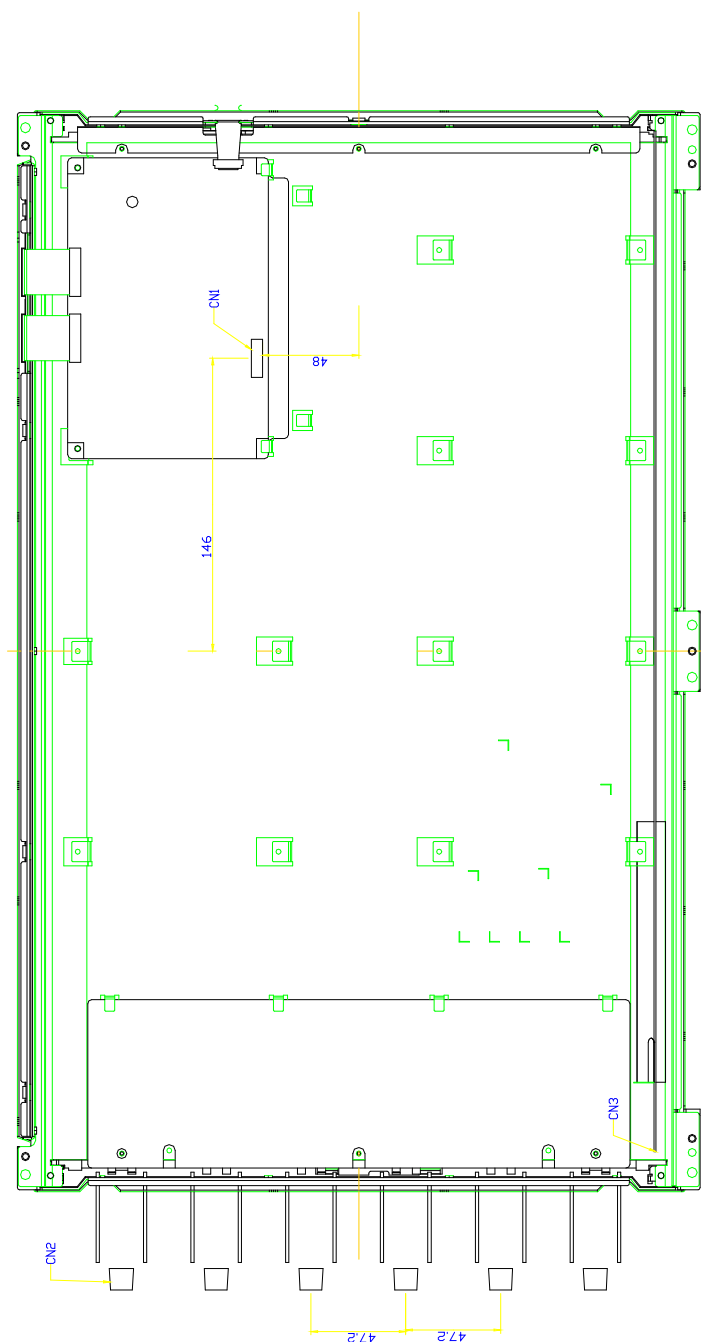


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## (2) Back view outline dimension

Unit: mm

HSD2300WX01\_Preliminary (BACK SIDE)



Note:  
CN1: HRS\DF14\_20P\_1.25H\*1  
CN2: lamp\_high\_connector\_BHR-03VS-1bhr\*6EA  
CN3: lamp\_low\_common\_connector\_ZHR-02\*1EA }

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## 9.0 LOT MARK

### 9.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----

code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location.

code 8: production year.

code 9: production month.

code 10,11,12,13,14,15: serial number.

#### Note (1) Production Year

Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

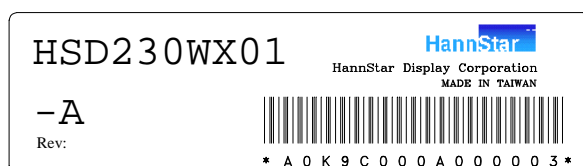
#### Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

### 9.2 Location of Lot Mark

(1) The label is attached to the backside of the LCD module.

(2) This is subject to change without prior notice.



Lot mark

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## 10.0 GENERAL PRECAUTION

### 10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

### 10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

### 10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

### 10.4 Electric Shock

- 10.4.1 Disconnect power supply before handling LCD module.
- 10.4.2 Do not pull or fold the CCFL cable.
- 10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

### 10.5 Absolute Maximum Ratings and Power Protection Circuit

- 10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 10.5.3 It's recommended to employ protection circuit for power supply.

### 10.6 Operation

- 10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 10.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 10.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

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10.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

10.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

#### 10.7 Mechanism

Please mount LCD module by using mouting holes arranged in four corners tightly.

#### 10.8 Static Electricity

10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

10.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

#### 10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 10.10 Disposal

When disposing LCD module, obey the local environmental regulations.