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TITLE: MV238QUM-N20
Product Specification
Rev. 0

Hefei Xinsheng Optoelectronics Technology Co.,LTD.

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S	TFT-LCD		2015.03.04	1 OF 30

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REVISION HISTORY

()preliminary specification

)Final specification

Revision No.	Page	Description of changes	Date	Prepared
Rev.0		Initial Release	2015.05.25	Li bin

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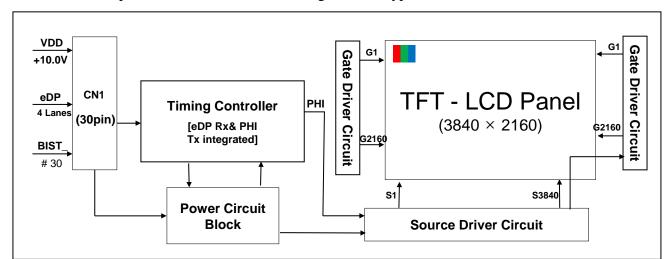
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1.0 GENERAL DESCRIPTION

1.1 Introduction

MV238QUM-N20 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 23.8 inch diagonally measured active area with UHD resolutions (3840 horizontal by 2160 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 1.07B colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- Reverse Type
- 4 lane eDP Interface with 5.4Gbps Link Rates
- High-speed response
- 10bit (8bit+H-FRC) color depth, display 1.07B colors
- Incorporated edge type back-light (LED)
- Compatible with sRGB 99% & NTSC75%
- High luminance and contrast ratio, low reflection and wide viewing angle
- RoHS/Halogen Free
- TCO 6.0, ES 6.0 compliant
- Gamma Correction

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1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model MV238QUM-N20.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	527.04 (H) × 296.46(V)	mm	
Number of pixels	$3840(H) \times 2160(V)$	pixels	
Pixel pitch	$0.13725(H) \times 0.13725(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	1.07B	colors	
Display mode	Normally Black		
Dimensional outline	$545.0 \text{ (H)} \times 323.4 \text{(V)} \times 12.5 \text{(D)} \text{ typ.}$	mm	Detail refer to dr awing
Weight	2300 (Typ.)	g	
Bezel width (L/R/U/D)	7.4/7.4/11.4/12.4	mm	
Surface Treatment	Haze 25%, 3H		
Back-light	Edge side, 1-LED Lighting Bar type		

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2.0 ABSOLUTE MAXIMUM RATINGS

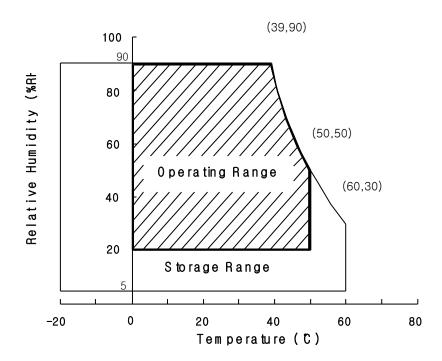
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

ſ	VSS =	GND:	=0V1
	1 00-	OI ID	-0 1

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.3	12.0	V	
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	V	Ta = 25 °C
Operating Temperature	T_{OP}	0	+50	$^{\circ}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}$	1)

Note : 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta = $25 \pm 2 \,^{\circ}$ C]

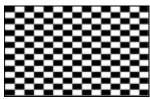
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	9	10.0	11	V	NI-4-1
Power Supply Current	I_{DD}	-	410	560	mA	Note1
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V _{RF}	-	-	400	mV	Note1,3
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	-	-	mV	
Differential input voltage	V _{ID}	100	-	600	mV	
Differential input common mode voltage	Vcm	0	-	2		V_{IH} =100mV, V_{IL} =-100mV
	P_{D}	-	3	6.8	W	
Power Consumption	P_{BL}	15.96	17.9	20.02	W	
	P _{total}	-	20.9	26.82	W	

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=10.0V, Frame rate=60Hz

Test Pattern of power supply current

a) Typ: Mosaic Patternb) Max: 1 line Inversion





- 2. Duration of rush current is about 2 ms and rising time of VDD is 1ms(min).
- 3. Ripple Voltage should be covered by Input voltage Spec.
- 4. Calculated value for reference (Input pins*VPIN ×IPIN) excluding inverter loss.

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3.2 Backlight Unit

< Table 4. LED Backlight Unit >

Parameter	Min.	Тур.	Max.	Unit	Remarks	
LED Light Bar Input Voltage Per Input Pin	VPIN	39.9	42.7	45.5	V	Duty 100%
LED Light Bar Input Current Per Input Pin	IPIN	100	105	110	mA	Note1,2,
LED Power Consumption	P_{BL}	15.96	17.9	20.02	W	Note 3
LED Life-Time	-	30,000	-		Hrs	Note 4

LED bar consists of 56LED packages, 4 strings (parallel) *14packages (serial) .

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 105mA

Note3: P_{BL}=4 Input pins*VPIN ×IPIN

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=120mA on condition of continuous operating at $25 \pm 2^{\circ}$ C

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and **TOPCONE PR730**) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 10.0V, Frame rate = 60Hz, Clock = 74.25MHz, I_{BL} = 260mA, Ta =25 \pm 2 °C] < Table 5. Module Optical >

Parame	ter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		85	89	-	Deg.	
Viewing Angle	Horizoiltai	Θ_9	CR > 10	85	89	-	Deg.	Note 1
range	Vertical	Θ_{12}	CR > 10	85	89	-	Deg.	Note 1
	vertical	Θ_6		85	89	1	Deg.	
Luminance Contrast	ratio	CR		700	1000			Note 2
Luminance of Whit	e	Y_{w}		240	300		cd/m ²	Note 3
White luminance un	iformity	ΔΥ		75	80		%	Note 4
	White	W_x		0.283	0.313	0.343	-	
	white	W_y	$\Theta = 0^{\circ}$ (Center)	0.299	0.329	0.359	-	
	Red	R_x	Normal	0.622	0.652	0.682	-	
Reproduction	Red	R_{y}	Viewing Angle	0.305	0.335	0.365	-	Note 5
of color	Green	G_x		0.252	0.282	0.312	-	Note 3
	Green	G_{y}		0.578	0.608	0.638	-	
	Dlue	$\mathbf{B}_{\mathbf{x}}$		0.115	0.145	0.175	-	
	Blue	Blue B _y		0.034	0.064	0.094	-	
Response Time	GTG	T_{g}			14	20	ms	Note 6
Cross Ta	alk	СТ		-	-	2.0	%	Note 7

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Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 9points / Maximum Luminance of 9points) * 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 5. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.

 Each time in below table is defined as appendix Figure 3 and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)"
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

5.1.1 LED Light Bar

-LED connector: 3708K-Q06N-00R manufactured by Entry

< Table 6. LED Light Bar>

Pin No	Symbol	Description		
1	IRLED1	LED current sense for string1		
2	IRLED2	LED current sense for string2		
3	VLED	LED power supply		
4	VLED	LED power supply		
5	IRLED3	LED current sense for string3		
6	IRLED4	LED current sense for string4		

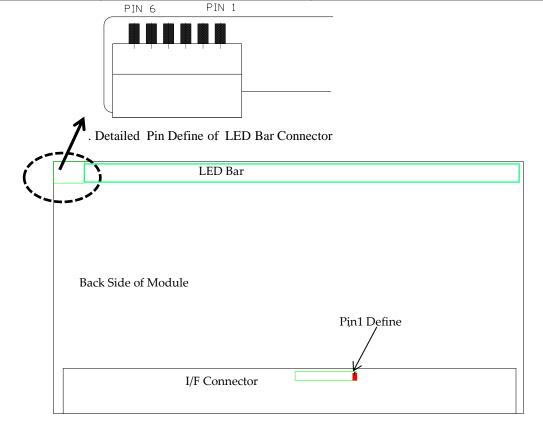


Figure 1. Back Side of Module

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Module Side Connector : UJU IS050-L30B-C10 or Equivalent

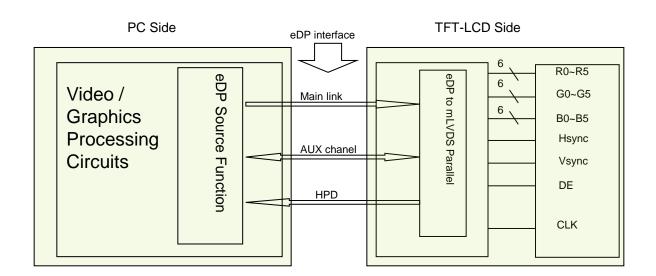
Pin No	Symbol	Function	Remark
1	VDD	Power Supply (10.0V)	
2	VDD	Power Supply (10.0V)	
3	VDD	Power Supply (10.0V)	
4	VDD	Power Supply (10.0V)	
5	VDD	Power Supply (10.0V)	
6	GND	Ground	
7	GND	Ground	
8	NC	No connection	
9	NC	No connection	
10	GND	Ground	
11	HPD	Hot Plug Detection Signal	
12	GND	Ground	
13	DAUXN	AUX_CHN Component Signal for Auxiliary Channel	
14	DAUXP	AUX_CHP True Signal for Auxiliary Channel	
15	GND	Ground	
16	DRX0P	Lane3P True Signal for Main Link 0	
17	DRX0N	Lane3N Component Signal for Main Link 0	
18	GND	Ground	
19	DRX1P	Lane3P True Signal for Main Link 1	
20	DRX1N	Lane3N Component Signal for Main Link 1	
21	GND	Ground	
22	DRX2P	Lane3P True Signal for Main Link 2	
23	DRX2N	Lane3N Component Signal for Main Link 2	
24	GND	Ground	
25	DRX3P	Lane3P True Signal for Main Link 3	
26	DRX3N	Lane3N Component Signal for Main Link 3	
27	GND	Ground	
28	GND	Ground	
29	NC	No connection	
30	BIST	BIST Function	BIST

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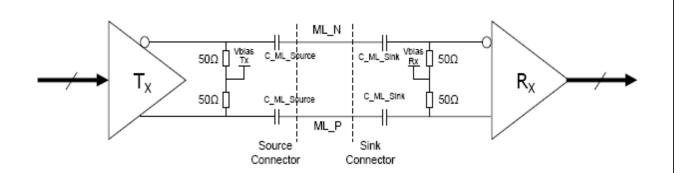
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5.2 eDP Interface



Note. Transmitter: Parade DP501or equivalent.

Transmitter is not contained in Module.



eDP Main Link differential pair

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6.0 SIGNAL TIMING SPECIFICATION

6.1 6.1 The MV238QUM-N20 is operated by the DE only.

	Item		Min	Тур	Max	Unit
Frequency		1/Tc	-	140	-	MHz
Clock	High Time	Tch	-	4/7Tc	-	
	Low Time	Tel	-	4/7Tc	-	
			-	2222	-	lines
F	rame Period	Tv	50	60	62	Hz
			20	16.7	16.13	ms
Vertica	al Display Period	Tvd	-	2160	-	lines
One line	e Scanning Period	Th	-	4200	-	clocks
Horizon	tal Display Period	Thd	-	3840	-	clocks

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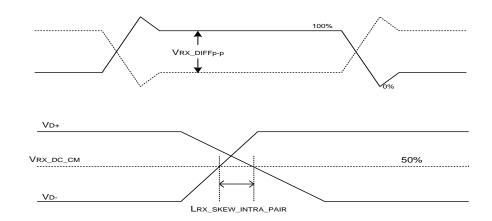
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6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 4.

<Table 4. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
Spread spectrum clock	ssc		0.5		%	
Differential peak-to-peak input volt age at package pins	VRX-DIFFp-p	100	0	1320	mV	
Rx input DC common mode voltage	VRX_DC_CM	ı	GND	-	V	
Differential termination resistance	RRX-DIFF	80	ı	100	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	1	-	20	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	150	ps	



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7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color % C	Smarr Capla	RED DATA					GREEN DATA								BLUE DATA										
Color & C	ray Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Colors	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\triangle					<u> </u>							,	<u> </u>								\uparrow			
of RED	∇					ļ							. ,	\downarrow								\downarrow			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
1	Δ		<u> </u>							<u> </u>						†									
of GREEN	∇				. ,	ļ				j								į į							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	\triangle				,					<u> </u>						<u> </u>									
OI BLUE	∇				. ,	ļ								\downarrow								\downarrow			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
- I	\triangle				,								,	<u> </u>								<u> </u>			
of WHITE	∇				,	ļ																↓ <u> </u>			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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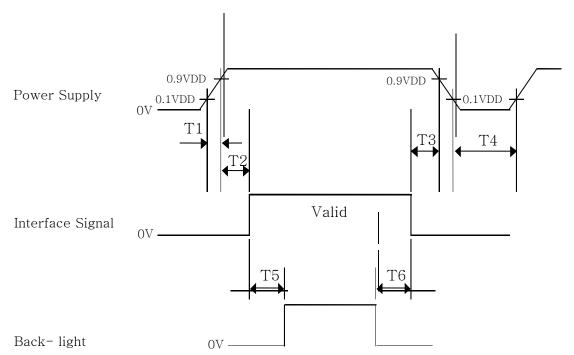
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8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- \bullet 0.5 ms \leq T1 \leq 10 ms
- \bullet 0 \leq T2 \leq 50 ms
- $0 \le T3 \le 50 \text{ ms}$
- $1 \sec \le T4$
- \bullet 200 ms \leq T5
- \bullet 200 ms \leq T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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9.0 MECHANICAL CHARACTERISTICS

9.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model MV238QUM-N20. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	$545.0 \text{ (H)} \times 323.4 \text{ (V)} \times 12.5 \text{(D)} \text{ typ.}$	mm
Weight	2300(typ.)	gram
Active area	527.04 (H) × 296.46 (V)	mm
Pixel pitch	$0.13725 \text{ (H)} \times 0.13725 \text{ (V)}$	mm
Number of pixels	$3840 \text{ (H)} \times 2160 \text{ (V) (1 pixel} = R + G + B \text{ dots)}$	pixels
Back-light	Edge side, 1-LED Lighting Bar type	

9.2 Mounting

See FIGURE 5. (shown in Appendix)

9.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

9.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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10.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

Table 6. Reliability Test Parameters >

No	Test Items		Conditions
1	High temperature storage test	$Ta = 60 ^{\circ}\text{C}, 240 ^{\circ}\text{h}$	nrs
2	Low temperature storage test	$Ta = -20 ^{\circ}\text{C}, 240$	hrs
3	High temperature & high humidity operation test	Ta = 50 °C, 80% I	RH, 240hrs
4	High temperature operation test	$Ta = 50 ^{\circ}\text{C}, 240\text{h}$	rs
5	Low temperature operation test	$Ta = 0^{\circ}C$, 240hrs	
6	Thermal shock	$Ta = -20 ^{\circ}\text{C} \leftrightarrow 60 ^{\circ}\text{C} (0.5 \text{ hr}), 100 \text{ cycle}$	
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	Random,10 ~ 300 Hz, 30 min/Axis 1.5 Grms X, Y, Z 30 min
		Gravity	50G
8	Shock test (non-operating)	Pulse width	11msec, sine wave
		Direction	$\pm X$, $\pm Y$, $\pm Z$ Once for each
9	Electro-static discharge test (non-operating)	Air : 150 pF Contact : 150 pF	F, 330Ω, 15 KV F, 330Ω, 8 KV

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11.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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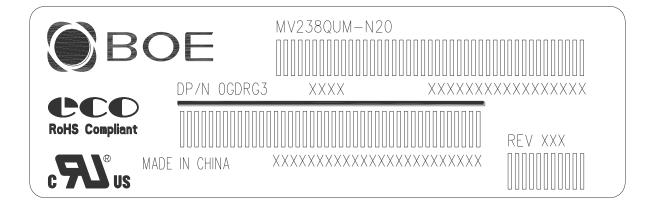
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12.0 PRODUCT SERIAL NUMBER



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- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001: 01, 2002: 02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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13.0 Packing 13.1 Packing Order

Put pad into the box

Place the modules bundled by packing bag in the box, 8pcs module per box, place a cover on the top of the box

















12ea box per pallet

After sealing the box, put the box on the pallet

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14.2 Packing Note

• Box Dimension : 315mm(W) × 633mm(L) × 429mm(H)

• Package Quantity in one Box: 8 pcs

14.3 Box label

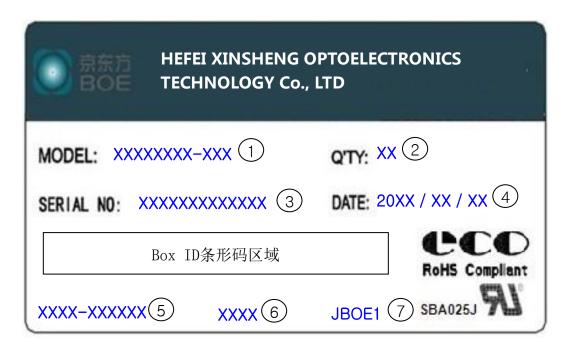
• Label Size : 110mm (L) × 55 mm (W)

Contents

Model: MV238QUM-N20 Q'ty: Module 8 Q'ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date



蓝色字体为后打印标识,说明如下:

- 1. FG-CODE
- 3. Box ID, 编码规则(CIM提供)
- 5. 产品物料号(客户端)(FAE提供)
- 7. 供应商代码

- 2. Box 产品数量
- 4. Box Packing 日期
- 6. FG-CODE 后四位

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15.0 APPENDIX

Figure 1. Measurement Set Up

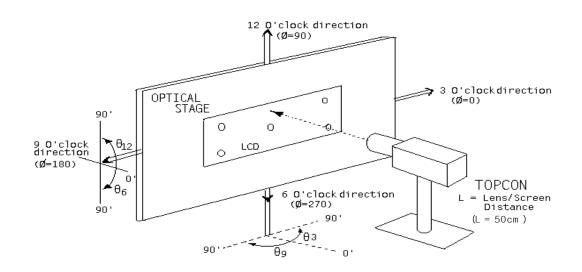
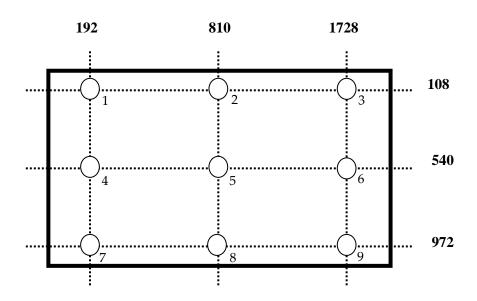


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



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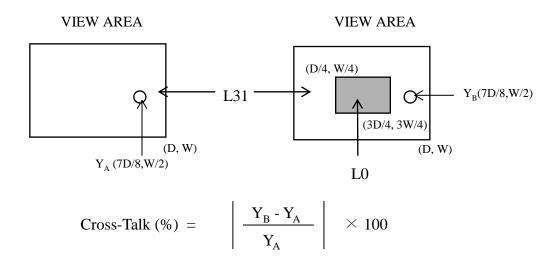
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Figure 3. Response Time Testing



Figure 4. Cross Modulation Test Description



Where: $Y_A = Initial luminance of measured area (cd/m²)$

 $Y_B =$ Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

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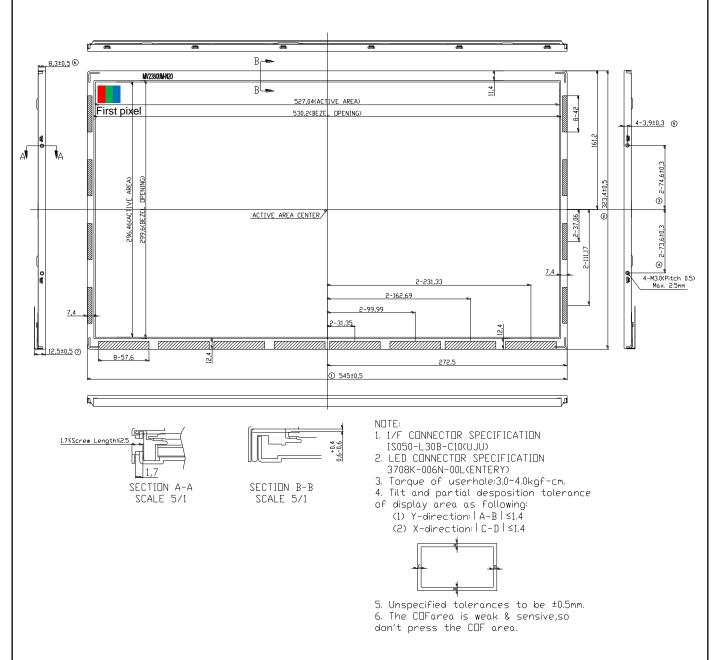
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Figure 5. TFT-LCD Module Outline Dimensions (Front view)



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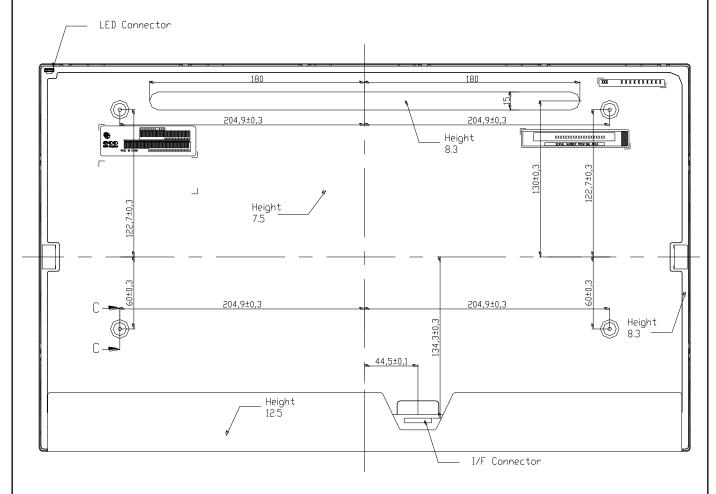
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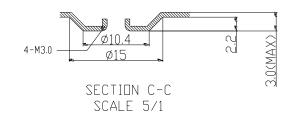
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Figure 6. TFT-LCD Module Outline Dimensions (Rear view)





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