



# **Product Information**

**SAMSUNG TFT-LCD** 

**MODEL NO.: LTN154AT01-001** 

LCD Development Group 1 (Mobile)

Samsung Electronics Co., LTD.



 Doc.No.
 LTN154AT01-001
 ISSUED DATE
 9/Aug/2007
 Page
 1 / 13

### **CONTENTS**

G	eneral Description	( 3	)
1.	Electrical Absolute Ratings	( 4	)
2.	Optical Characteristics	( 5	)
3.	Electrical Characteristics 3.1 TFT LCD Module 3.2 Backlight Unit	( 6	)
4.	Block Diagram 4.1 TFT LCD Module 4.2 Backlight Unit	( 7	)
5.	Input Terminal Pin Assignment 5.1 Input Signal & Power 5.2 Backlight Unit 5.3 Timing Diagrams of LVDS For Transmitting	( 8	)
6.	Interface Timing 6.1 Timing Parameters 6.2 Timing Diagrams of interface Signal 6.3 Power ON/OFF Sequence	( 10	))
7.	Outline Dimension	( 12	2)

#### **GENERAL DESCRIPTION**

#### **DESCRIPTION**

LTN154AT01-001 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight system. The resolution of a 15.4" contains 1280 x 800 pixels and can display up to 262,144 colors. 6 O'clock direction is the Optimum viewing angle.

#### **FEATURES**

- Thin and light weight
- High contrast ratio, high aperture structure
- Wide XGA (1280x800 pixels) resolution
- Fast Response Time
- Low power consumption
- Single CCFL
- DE (Data enable) only mode.
- 3.3V LVDS Interface
- On board EDID chip
- Pb-free product

#### **APPLICATIONS**

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

#### **GENERAL INFORMATION**

Item	Specification	Unit	Note
Display area	331.2(H) X 207.0(V) (15.4"diagonal)	mm	
Driver element	a-si TFT active matrix		
Display colors	262,144		
Number of pixel	1280 x 800 ( 16 : 10, Wide XGA )	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.25875(H) x 0.25875(V)	mm	
Display Mode	Normally white		
Surface treatment	Haze 0, Hard-Coating 3H		

<b>Doc.No.</b>   LTN154AT01-001   <b>ISSUED DATE</b>   9/Aug/2007   <b>Page</b>   3 / 13	Doc.No.	LTN154AT01-001	ISSUED DATE	9/Aug/2007	Page	3 / 13
--	---------	----------------	-------------	------------	------	--------

#### Mechanical Information

	Item	Min.	Тур.	Max.	Unit	Note
	Horizontal (H)	343.5	344.0	344.5	mm	
Module size	Vertical (V)	221.5	222.0	222.5	mm	
0.20	Depth (D)	-	6.1	6.5	mm	(1)
Weight		-	575	595	g	

Note (1) Measurement condition of outline dimension

. Equipment : Vernier Calipers . Push Force : 500g ·f (minimum)

#### 1. ELECTRICAL ABSOLUTE RATINGS

### (1) TFT LCD MODULE

 $V_{DD} = 3.3V$ ,  $V_{SS} = GND = 0V$ 

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>DD</sub>	V <sub>DD</sub> - 0.3	V <sub>DD</sub> + 0.3	V	(1)
Logic Input Voltage	V <sub>DD</sub>	V <sub>DD</sub> - 0.3	V <sub>DD</sub> + 0.3	V	(1)

Note (1) Within Ta (25  $\pm$  2 °C )

### (2) BACK-LIGHT UNIT

Ta = 25  $\pm$  2 °C

Item	Symbol	Min.	Max.	Unit	Note
Lamp Current	IL	3.0	6.5	mArms	(1)
Lamp frequency	F <sub>L</sub>	50	80	kHz	(1)

Note 1) Permanent damage to the device may occur if maximum values are exceeded Functional operation should be restricted to the conditions described under normal operating conditions.

Doc.No.	LTN154AT01-001	ISSUED DATE	9/Aug/2007	Page	4 / 13
---------	----------------	-------------	------------	------	--------

### 2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state.

Measuring equipment: TOPCON SR-3 and SR-3

\* Ta =  $25 \pm 2$  °C, VDD=3.3V, fv= 60Hz, fDCLK = **68.9**MHz, IL = 6.0 mA

Contrast Ratio (5 Points)  Response Time at Ta ( Rising + Falling )		Symbol	Condition	Min.	Тур.	Max	Unit
		CR		300	-	-	-
		T <sub>RT</sub>		-	8	12	msec
Average Lum of White (5		Y <sub>L</sub> ,AVE	Normal	187	220	-	cd/m²
	Dark	Rx	Viewing	0.565	0.595	0.625	
	Red	Ry	Angle $\phi = 0$ $\theta = 0$	0.310	0.340	0.370	
	Green	Gx		0.290	0.320	0.350	
Color	Green	G <sub>Y</sub>		0.520	0.550	0.580	
Chromaticity ( CIE )	Blue	Вх		0.125	0.155	0.185	-
	Dide	By		0.100	0.130	0.160	
	\\/hito	Wx		0.283	0.313	0.343	
	White	WY		0.299	0.329	0.359	
	Hor.	θι		40	45	-	
Viewing	пог.	θн	CB > 10	40	45	-	Dograda
Angle	Ver.	фн	CR ≥ 10	10	15	-	Degrees
		фL		25	30	-	
	13 Points White Variation			-	-	1.7	-

Doc.No. LTN154AT01-001 ISSUED DATE	)/Aug/2007 Page	5 / 13	
------------------------------------	-----------------	--------	--

### 3. ELECTRICAL CHARACTERISTICS

#### 3.1 TFT LCD MODULE

Ta= 25 ± 2°C

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of Powe	Voltage of Power Supply		3.0	3.3	3.6	V	
Differential Input	High	Vıн	-	-	+100	mV	Vcm = +1.2V
Voltage for LVDS Receiver Threshold	Low	Vıl	-100	-	-	mV	
Vsync Freque	ency	fv	50	60	-	Hz	
Hsync Frequency		fн	48.3	48.96	60	KHz	
Main Freque	ency	fdclk	63.5	68.9	85	MHz	
Rush Curre	Rush Current		•	-	1.5	Α	
	White		-	420	-	mA	
Current of Power Supply	Mosaic	l <sub>DD</sub>	-	480	-	mA	
	V. Stripe		-	590	650	mA	

#### 3.2 BACK-LIGHT UNIT

The backlight system is an edge-lighting type with a single CCFT ( Cold Cathode Fluorescent Tube ). The characteristics of a single lamp are shown in the following table.

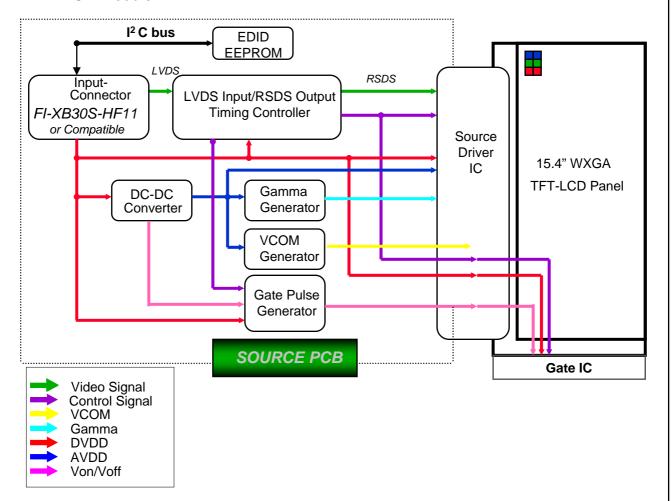
- INVERTER: SEM SIC 130T

Ta=  $25 \pm 2$  °C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp Current I∟		3.0	6.0	6.5	mArms	
Lamp Voltage	VL	-	705	-	Vrms	I∟=6.0mA
Frequency	f∟	50	60	80	KHz	
Power Consumption	P∟		4.2		W	I∟=6.0mA
Operating Life Time	Hr	10,000			Hour	
Startup Valtage	.,	-		1,180	Vrms	25°C
Startup Voltage	Vs		-	1,415	Vrms	0°C
Lamp startup time		-	-	1.0	sec	

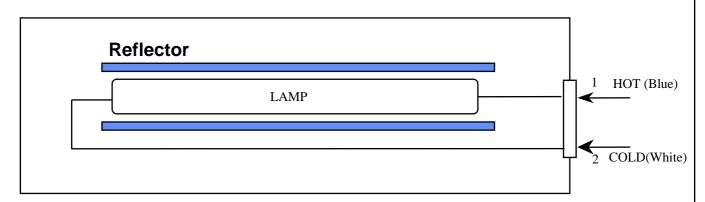
### 4. BLOCK DIAGRAM

#### 4.1 TFT LCD Module



**Product Information** 

#### 4.2 BACK-LIGHT UNIT



Note) The output of the inverter may change according to the material of the reflector.

Doc.No.	LTN154AT01-001	ISSUED DATE	9/Aug/2007	Page	7 / 13
---------	----------------	-------------	------------	------	--------

### 5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power (LVDS, Connector : JAE FI-XB30SRLZ-HF11 or compatible )

Mating Connector : JAE FI-X30M or compatible)

PIN NO	SYMBOL	FUNCTION	POLARITY	REMARK
1	VSS	Ground		
2	VDD	POWER SUPPLY +3.3V		
3	VDD	POWER SUPPLY +3.3V		
4	VEEDID	DDC 3.3V Power		
5	NC	No Connection		
6	CLKEDID	DDC Clock		
7	DATAEDID	DDC data		
8	RxIN0-	LVDS Differential Data INPUT (R0-R5,G0)	Negative	
9	RxIN0+	LVDS Differential Data INPUT (R0-R5,G0)	Positive	
10	VSS	Ground		
11	RxIN1-	LVDS Differential Data INPUT (G1-G5,B0-B1)	Negative	
12	RxIN1+	LVDS Differential Data INPUT (G1-G5,B0-B1)	Positive	
13	VSS	Ground		
14	RxIN2-	LVDS Differential Data INPUT (B2-B5,Sync,DE)	Negative	
15	RxIN2+	LVDS Differential Data INPUT (B2-B5,Sync,DE)	Positive	
16	VSS	Ground		
17	RxCLK-	LVDS Differential Data INPUT (Clock)	Negative	
18	RxCLK+	LVDS Differential Data INPUT (Clock)	Positive	
19	VSS	Ground		
20	NC	No Connection		
21	NC	No Connection		
22	NC	No Connection		
23	NC	No Connection		
24	NC	No Connection		
25	NC	No Connection		
26	NC	No Connection		
27	NC	No Connection		
28	NC	No Connection		
29	NC	No Connection		
30	NC	No Connection		

Doc.No. LTN154AT01-001 ISSUED DATE 9/Aug/20	007 <b>Page</b> 8 / 13
---	------------------------

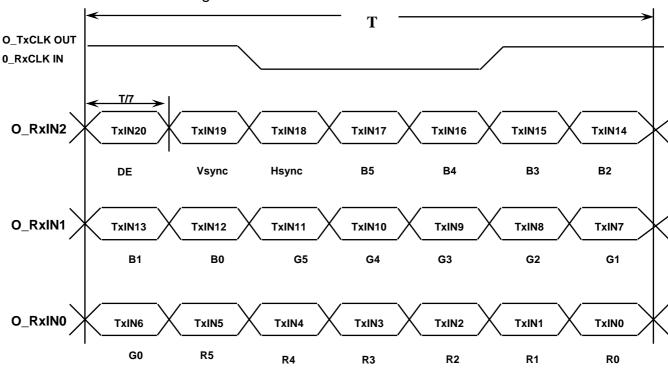
#### 5.2 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1 Mating Connector : SM02B-BHSS-1(JST)

Pin NO.	Symbol	Color	Function
1	НОТ	Pink	High Voltage
2	COLD	White	Low Voltage

### 5.3 Timing Diagrams of LVDS For Transmission

LVDS Receiver: Integrated T-CON



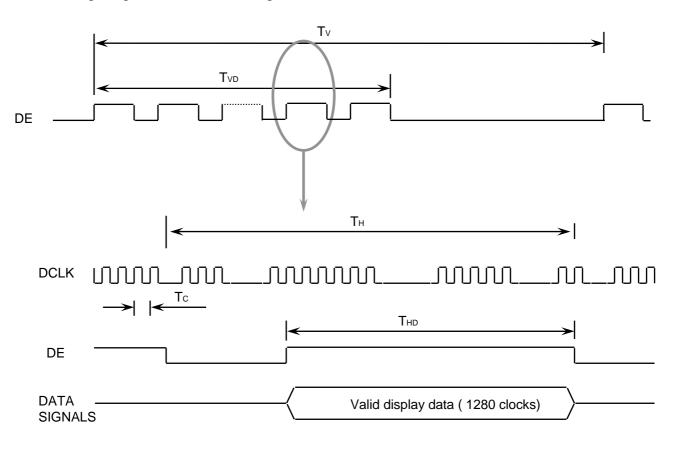
## **Product Information**

### 6. INTERFACE TIMING

### 6.1 Timing Parameters

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Frame Frequency	Cycle	TV	804	816	1000	Lines	-
Vertical Active Display Term	Display Period	TVD	-	800	-	Lines	-
One Line Scanning Time	Cycle	TH	1350	1408	1550	Clocks	-
Horizontal Active Display Term	Display Period	THD	-	1280	-	Clocks	-

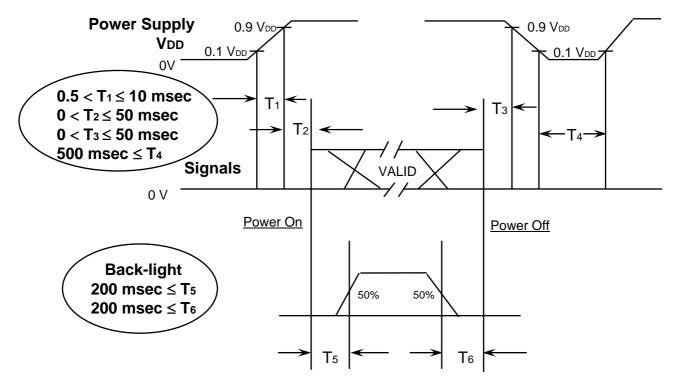
### 6.2 Timing diagrams of interface signal



Doc.No.	LTN154AT01-001	ISSUED DATE	9/Aug/2007	Page	10 / 13
---------	----------------	-------------	------------	------	---------

#### 6.3 Power ON/OFF Sequence

: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



### Power ON/OFF Sequence

T1: Vdd rising time from 10% to 90%

T2: The time from Vdd to valid data at power ON.

T3: The time from valid data off to Vdd off at power Off.

T4: Vdd off time for Windows restart

T5: The time from valid data to B/L enable at power ON.

T6: The time from valid data off to B/L disable at power Off.

#### NOTE.

- (1) The supply voltage of the external system for the module input should be the same as the definition of VDD.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

Doc.No.	TN154AT01-001	ISSUED DATE	9/Aug/2007	Page	11 / 13
---------	---------------	-------------	------------	------	---------

7. MECI	HANICAL OUTLINE	E DIMENSION		Product I	nform	ation
[ Ref	er to the next page ]					
Doc.No.	LTN154AT01-001	ISSUED DATE	9/Aug	/2007	Page	12 / 13

R.UE SPEC : MAX 3.0 Kg/-cm