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Specification of FUJITSU TFT-LCD module

NA19026-C081A

Approval
 Date : By :

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, FDTC shall not be liable against the customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

Specification No. : Tech Bes LCD-00225

Issue Date : Oct. 15, 2003

Issued by :



F. Yamada

Director

Design Dept.

LCD Products Div.

FUJITSU DISPLAY TECHNOLOGIES CORPORATION

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CDEF1

1. APPLICATIONS

This specification is applied to the 17.0 in. XGA-WIDE supported TFT-LCD module.

2. PRODUCT NAME AND MODEL NAME

2-1. Product Name : LCD Module

2-2. Model Name : NA19026-C081A

3. OVERVIEW

This LCD module has a TFT active matrix type liquid crystal panel 1280×768 pixels, and diagonal size of 43cm (17.0-inch). This module supports 1280×768 XGA-WIDE mode (Non-interlace).

This LCD has a XGA-WIDE-capable 1ch-LVDS interface and can display 16 million colors.

By timing control signal, images are displayed on the screen at given value under specified timing.

Control signals to this LCD module are ENAB (Data enable signal), DCLK signal and RGB data signal. Data signal is transmitted with single port. (8 bit for each R, G, B)

The power supply of this LCD module is +5V DC.

4. CONFIGURATION

This LCD module consists of a color TFT-LCD panel that mounted with TFT driver ICs, a cold-cathode fluorescent tube back-light, a LVDS interface PCB, a structure and a bezel. And these components are integrated in a LCD module.

Cold-cathode fluorescent tube and lamp holder are replaceable.

Fig.4-1 shows a block diagram of this LCD module.

[illegible]

6. ABSOLUTE MAXIMUM RATINGS

Table 6-1 shows the absolute maximum rating of this LCD module.

Table 6-1. Absolute Maximum Ratings

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{CC}	- 0.3	—	6.0	V
Input Voltage	V _{IN}	- 0.3	—	V _{CC} +0.3	V

7. RECOMMENDED OPERATING CONDITIONS

Table 7-1 shows the recommended operating conditions of this LCD module.

Table 7-1. Recommended Operating Conditions

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{CC}	4.75	5.0	5.25	V
Ripple Voltage (V _{cc})	V _{RP}	—	—	100	mVp-p

Note 1) Measurement Circuit

Based on Fig.8-1.

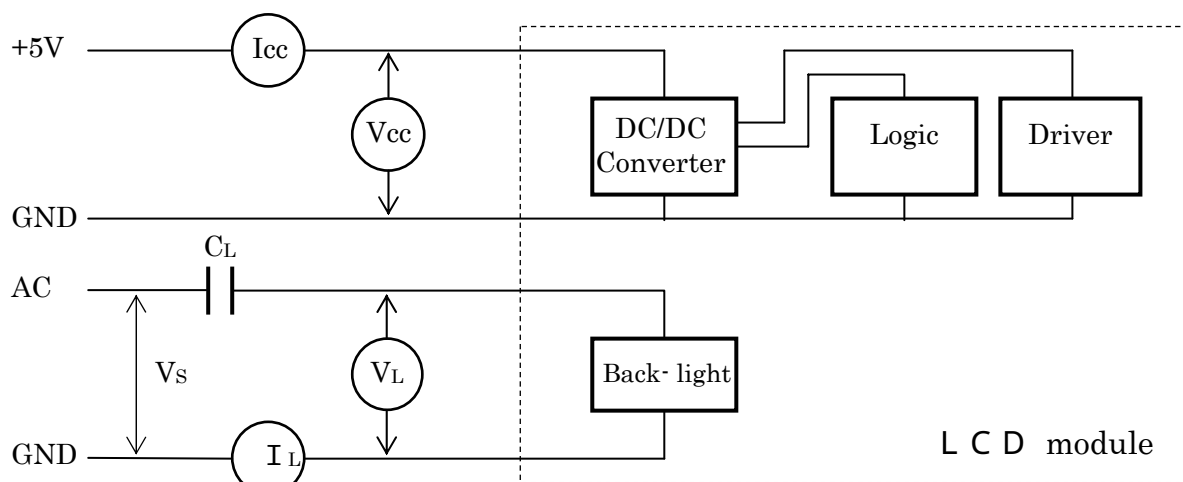


Fig.8-1. Measurement Circuit

Note 2) Equivalent Circuit

Based on Fig.8-2 (a), (b).

Input signals(LVDS-1ch)

RxCLK+
RxCLK-
Rx0+
Rx0-
Rx1+
Rx1-
Rx2+
Rx2-
Rx3+
Rx3-

Differential Input

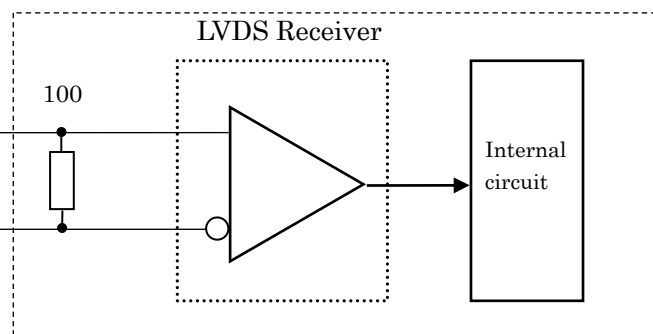


Fig.8-2 (a) Equivalent Circuit of Logic Signal Input

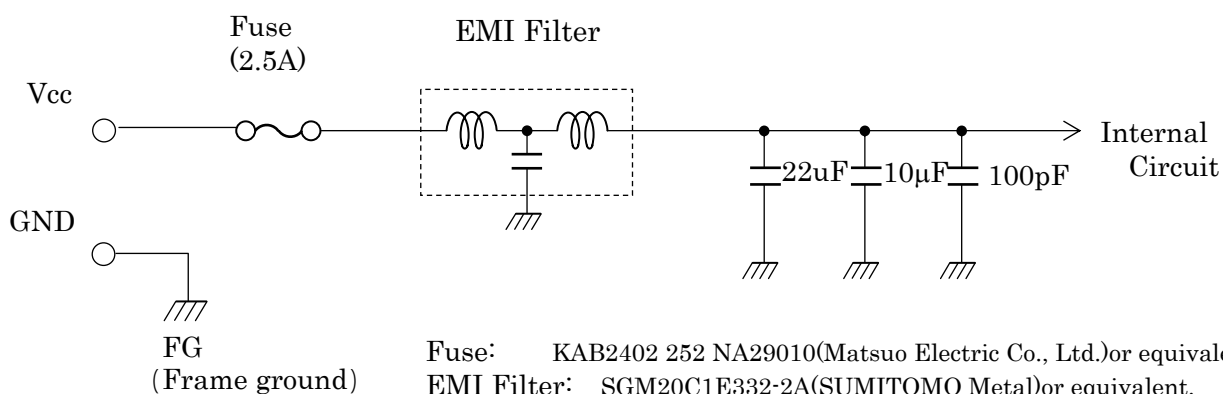


Fig.8-2 (b) Equivalent Circuit of Power Supply

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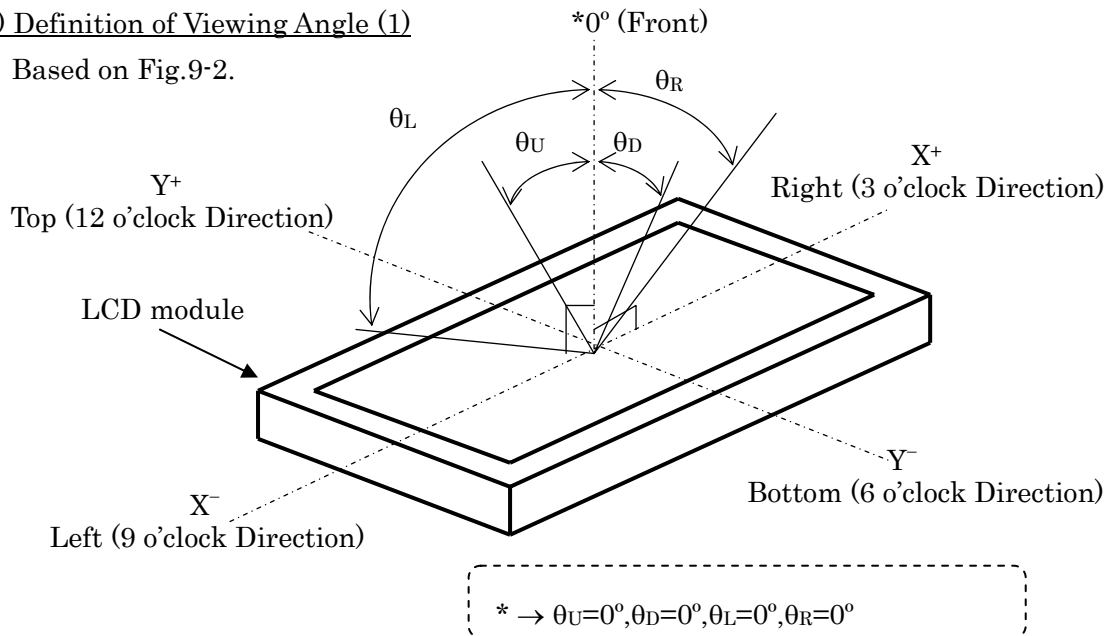
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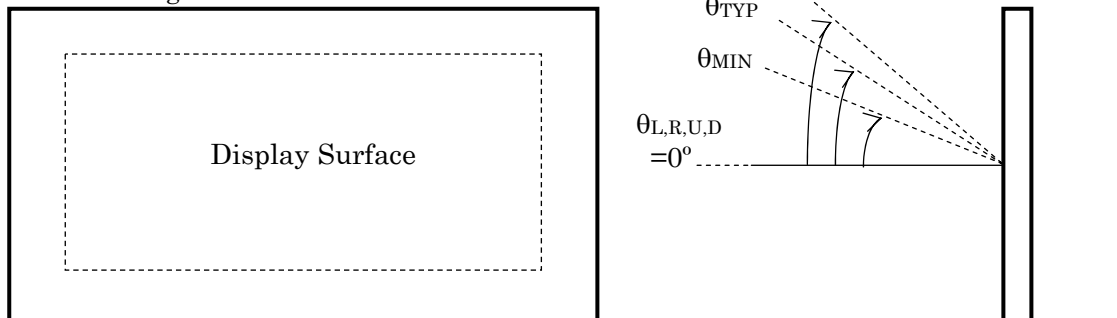
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9. OPTICAL SPECIFICATIONS										
Table 9-1 shows the optical specifications of this LCD module.										
Table 9-1. Optical Specifications Ta=25°C										
Item		Symbol	Condition		Specifications			Unit	Remark	
					MIN.	TYP.	MAX		Note	
Visual Angle	Horizontal	$\theta_{L,R}$	$CR \geq 10$	$\theta_{U,D}=0^\circ$	85	—	—	deg	(1)(2)	
	Vertical	$\theta_{U,D}$		$\theta_{L,R}=0^\circ$	85	—	—	deg	(3)(5)(6)	
Contrast Ratio		CR	$\theta_{L,R,U,D}=0^\circ$		350	600	—	—	White/Black (1)(2)(3)(5)	
Response Time(ON) (B W)		t_{on}	$\theta_{L,R,U,D}=0^\circ$	Ta=25°C	—	15	30	ms	(1)(4)(5)	
				Ta=0°C	—	50	100	ms		
Response Time(OFF) (W B)		t_{off}	$\theta_{L,R,U,D}=0^\circ$	Ta=25°C	—	10	25	ms		
				Ta=0°C	—	50	100	ms		
Brightness		I	$\theta_{L,R,U,D}=0^\circ$ V _{CC} =5V, I _L =10.5mA (at maximum brightness)		350	450	—	cd/m ²	White*1 (1)(5)	
Brightness Uniformity		ΔI			75	—	—	%		(1)(5)(7)
Chromaticity	W	X			0.283	0.313	0.343	—		(1)(5)
		Y			0.299	0.329	0.359	—		
	R G B	(x, y)			Red	0.65, 0.34 Typ.				
			Green	0.30, 0.59 Typ.						
Blue			0.15, 0.14 Typ.							
-curve					2.4 Typ.					
LCD Panel Type					TFT Color					
Display Mode					Normally Black VA					
Wide Viewing Angle Technology					MVA-Premium					
Optimum Viewing Angle					- (symmetry) (6)					
Display Color					16 million (each 6-bit+2-bit FRC)					
Color of non-display area					Black					
Surface Treatment					Anti-glare (Haze value:25%, 3H(at weight of 300g))					
(*1) Specified value is measured in 20~30 minutes after lighting on (LCD module single). A required value may not be achieved on condition that LCD module is built in the cabinet because of its radiation. All items without “Brightness Uniformity” are measured at the center of display board. (Note1) •CS-1000 (MINOLTA Co., Ltd.) , BM-5A(Topcon) or equivalent luminance colorimeter should be used for the measurement. Field=2°, L=500mm •The specified value of viewing angle, contrast, brightness, brightness uniformity and chromaticity are under the dark room condition (1lux or less).										
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Note 1) Definition of Viewing Angle (1)

Based on Fig.9-2.

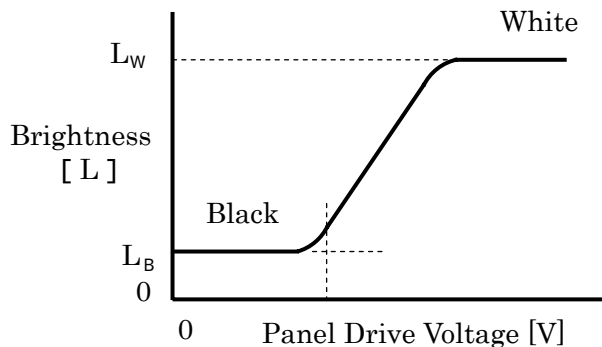
Fig.9-2. Definition of Viewing Angle (1)Note 2) Definition of Viewing Angle (2)

Based on Fig.9-3.

Fig.9-3. Definition of Viewing Angle (2)Note 3) Definition of Contrast Ratio (CR)

Determined by Formula (1) based on Fig.9-4.Voltage-Brightness Characteristics.

$$= \frac{L_W \text{ (Brightness at white)}}{L_B \text{ (Brightness at black)}} \quad \text{----- (1)}$$

Fig.9-4. Voltage-Brightness Characteristics

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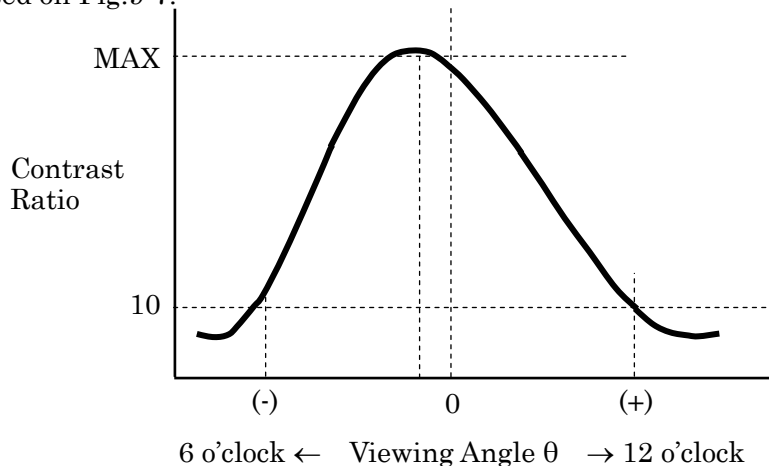
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<div>Note 4) Definition of Response Time</div> <div>Based on Fig.9-5.</div> <div><div><div><div>Drive signal of LCD panel</div><div>ON</div><div>OFF</div></div><div><div>Non-select status</div><div>Select status</div><div>Non-select status</div></div></div><div><div>Relative Brightness</div><div>100%</div><div>90%</div><div>White</div><div>0%</div><div>Black</div><div>10%</div><div>Black</div></div><div><div><div>t_{on}</div><div>ON Response Time</div></div><div><div>t_{off}</div><div>OFF Response Time</div></div></div></div>							
<div>Fig.9-5. Definition of Response Time</div>							
<div>Note 5) Contrast Ratio and Response Measurement System</div> <div>Based on Fig.9-6.</div> <div><div><div><div><div>Y⁺</div><div>X⁺</div><div>Z⁺</div><div>Z⁻</div><div>X⁻</div><div>Y⁻</div></div><div><div>θ</div><div>Brightness Meter or Luminance Colorimeter (with luminosity correction function)</div></div></div><div><div>Drive and Measurement System</div></div></div></div>							
<div>Fig.9-6. Contrast Ratio and Response Time Measurement System</div>							
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Fig.9-5. Definition of Response Time

Note 6) Definition of Optimum Viewing Angle

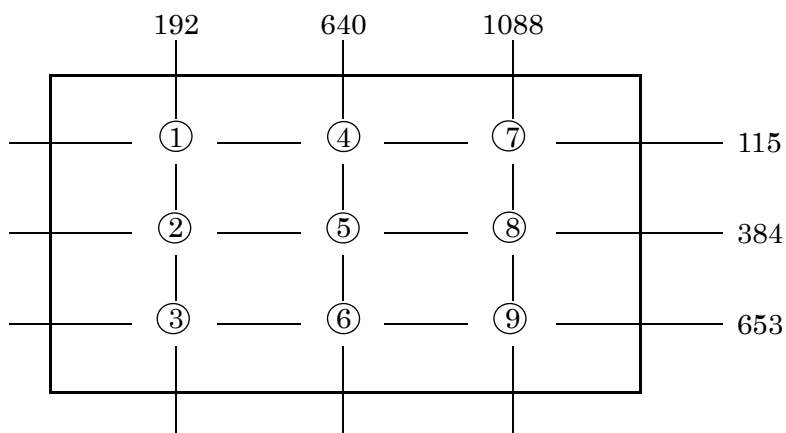
Based on Fig.9-7.

Fig.9-7. Definition of Viewing AngleNote 7) Definition of Brightness Uniformity

Brightness uniformity is defined by the following formula.

Brightness (I1~I9) are measured at the following 9 points (① ~ ⑨) on the display area shown in Fig.9-8.

$$\text{Brightness Uniformity } (\Delta L) = \frac{|\text{Min. In}|}{|\text{Max. In}|} \times 100 (\%), n=1 \text{ to } 9$$



Note) Each measurement point (① ~ ⑨) defines the center spot of Brightness Meter view.
The tolerance of measurement position is $\pm 3\text{mm}$.

Fig.9-8. Measurement Points

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10. INTERFACE SPECIFICATIONS

10-1-1. Signal descriptions

Table 10-1 shows the description and configuration of Interface signals (CN1).

Table 10-1-1. Interface signals (CN1)

Pin	Symbol	I/O	Function
1	VDD	-	+5V Power suply
2	VDD	-	+5V Power suply
3	GND	-	Ground
4	GND	-	Ground
5	RX0-	↓	LVDS Receiver Signal(-)
6	RX0+	↓	LVDS Receiver Signal(+)
7	GND	-	Ground
8	RX1-	↓	LVDS Receiver Signal(-)
9	RX1+	↓	LVDS Receiver Signal(+)
10	GND	-	Ground
11	RX2-	↓	LVDS Receiver Signal(-)
12	RX2+	↓	LVDS Receiver Signal(+)
13	GND	-	Ground
14	RXCLK-	↓	LVDS Receiver Clock Signal(-)
15	RXCLK+	↓	LVDS Receiver Clock Signal(+)
16	GND	-	Ground
17	RX3-	↓	LVDS Receiver Signal(-)
18	RX3+	↓	LVDS Receiver Signal(+)
19	GND	-	Ground
20	Data Mapping Select Input	I	Low : table 10-1-2 Open or Hi : table 10-1-3

Upper side

Interface connector

LCD Module
Rear side

20
1

Lower side

Connector : D14H-20P-1.25H(HIROSE)

User's connector : DF14-20S-1.25 (HIROSE)

Note)When using a interface connector other than the recommended one, a defect in the initial stage or a problem concerning long term reliabiling may occur.

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10-1-2. LVDS Data Mapping 1

Table 10-1-2 shows the LVDS data mapping 1.(DataMappingSelectInput = Low)

Table 10-1-2. LVDS Data Mapping 1

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R 0	TxOUT0- TxOUT0+	5 6	RX0- RX0+	27	RxOUT0	I R 0
52	TxIN1	R 1				29	RxOUT1	I R 1
54	TxIN2	R 2				30	RxOUT2	I R 2
55	TxIN3	R 3				32	RxOUT3	I R 3
56	TxIN4	R 4				33	RxOUT4	I R 4
2	TxIN5	R 7	TxOUT3- TxOUT3+	17 18	RX3- RX3+	34	RxOUT5	I R 7
3	TxIN6	R 5	TxOUT0- TxOUT0+	5 6	RX0- RX0+	35	RxOUT6	I R 5
4	TxIN7	G 0	TxOUT0- TxOUT0+	6	RX0+	37	RxOUT7	I G 0
6	TxIN8	G 1	TxOUT1- TxOUT1+	8 9	RX1- RX1+	38	RxOUT8	I G 1
7	TxIN9	G 2	TxOUT1- TxOUT1+	9	RX1+	39	RxOUT9	I G 2
8	TxIN10	G 6	TxOUT3- TxOUT3+	17 18	RX3- RX3+	41	RxOUT10	I G 6
10	TxIN11	G 7	TxOUT3- TxOUT3+	18	RX3+	42	RxOUT11	I G 7
11	TxIN12	G 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	43	RxOUT12	I G 3
12	TxIN13	G 4				45	RxOUT13	I G 4
14	TxIN14	G 5				46	RxOUT14	I G 5
15	TxIN15	B 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	47	RxOUT15	I B 0
16	TxIN16	B 6				49	RxOUT16	I B 6
18	TxIN17	B 7				50	RxOUT17	I B 7
19	TxIN18	B 1	TxOUT1- TxOUT1+	8 9	RX1- RX1+	51	RxOUT18	I B 1
20	TxIN19	B 2	TxOUT2- TxOUT2+	11 12	RX2- RX2+	53	RxOUT19	I B 2
22	TxIN20	B 3				54	RxOUT20	I B 3
23	TxIN21	B 4				55	RxOUT21	I B 4
24	TxIN22	B 5				1	RxOUT22	I B 5
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18	RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12	RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	E N A B
50	TxIN27	R 6	TxOUT3- TxOUT3+	17 18	RX3- RX3+	7	RxOUT27	I R 6
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15	RXCLK- RXCLK+	26	RxCLKOUT	D C L K

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10-1-3. LVDS Data Mapping 2

Table 10-1-3 shows the LVDS data mapping 2.(DataMappingSelectInput = Open or Hi)

Table 10-1-3. LVDS Data Mapping 2

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R 2	TxOUT0- TxOUT0+	5 6	RX0- RX0+	27	RxOUT0	IR 2
52	TxIN1	R 3				29	RxOUT1	IR 3
54	TxIN2	R 4				30	RxOUT2	IR 4
55	TxIN3	R 5				32	RxOUT3	IR 5
56	TxIN4	R 6				33	RxOUT4	IR 6
2	TxIN5	R 1	TxOUT3- TxOUT3+	17 18	RX3- RX3+	34	RxOUT5	IR 1
3	TxIN6	R 7	TxOUT0- TxOUT0+	5 6	RX0- RX0+	35	RxOUT6	IR 7
4	TxIN7	G 2				37	RxOUT7	IG 2
6	TxIN8	G 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	38	RxOUT8	IG 3
7	TxIN9	G 4				39	RxOUT9	IG 4
8	TxIN10	G 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	41	RxOUT10	IG 0
10	TxIN11	G 1				42	RxOUT11	IG 1
11	TxIN12	G 5	TxOUT1- TxOUT1+	8 9	RX1- RX1+	43	RxOUT12	IG 5
12	TxIN13	G 6				45	RxOUT13	IG 6
14	TxIN14	G 7				46	RxOUT14	IG 7
15	TxIN15	B 2				47	RxOUT15	IB 2
16	TxIN16	B 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	49	RxOUT16	IB 0
18	TxIN17	B 1				50	RxOUT17	IB 1
19	TxIN18	B 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	51	RxOUT18	IB 3
20	TxIN19	B 4	TxOUT2- TxOUT2+	11 12	RX2- RX2+	53	RxOUT19	IB 4
22	TxIN20	B 5				54	RxOUT20	IB 5
23	TxIN21	B 6				55	RxOUT21	IB 6
24	TxIN22	B 7				1	RxOUT22	IB 7
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18	RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12	RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	E N A B
50	TxIN27	R 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	7	RxOUT27	IR 0
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15	RXCLK- RXCLK+	26	RxCLKOUT	D C L K

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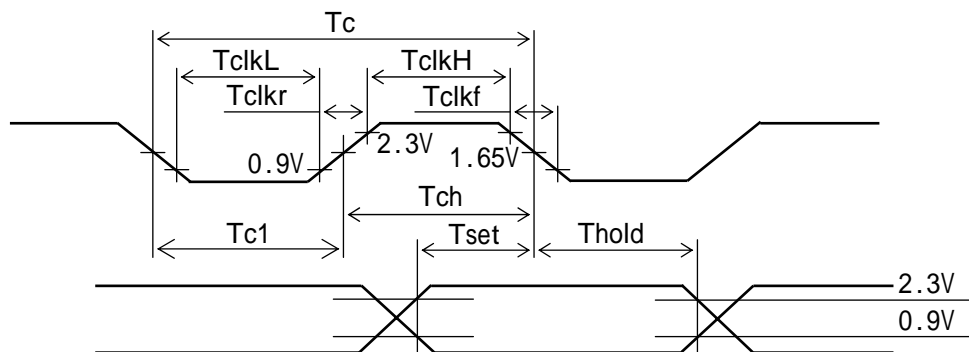
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(T=0~50°C, V_{cc}=5±0.25V)

***6)**The display quality is guaranteed by the Typ timing.

R0-7
G0-7
B0-7



B

R0-7
G0-7
B0-7

The diagram illustrates the timing of a clock signal and its delay. The top signal is a clock with shaded high and low periods. The bottom signal is a delayed version of the clock. A horizontal double-headed arrow labeled T_h spans the period between the first rising edges. Another horizontal double-headed arrow labeled T_{hd} spans the period between the first falling edges.

C

R0-7
G0-7
B0-7

The diagram illustrates the timing relationship between the data stream and the clock signal. The data stream is divided into blocks, with shaded blocks representing valid data. The clock signal is a periodic square wave. The time interval T_{vd} is the duration for which the data is valid, and T_v is the total time interval shown.

D

R0-7
G0-7
B0-7

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S0001 S0002 S0003 S0004 S0005 S0006 S0007 S0008									S3839 S3840			
C001	R 0001	G 0001	B 0001	R 0002	G 0002	B 0002	R 0003	G 0003			G 1280	B 1280
C768	R 0001	G 0001	B 0001	R 0002	G 0002	B 0002	R 0003	G 0003			G 1280	B 1280

Fig.10-4. Correspondence Data and Display Position

The sequence of input signals and On/Off of the power supply of this LCD module should be in the specification shown in Fig.10-5 to prevent latch-up of the driver ICs and DC driving of the LCD panel.

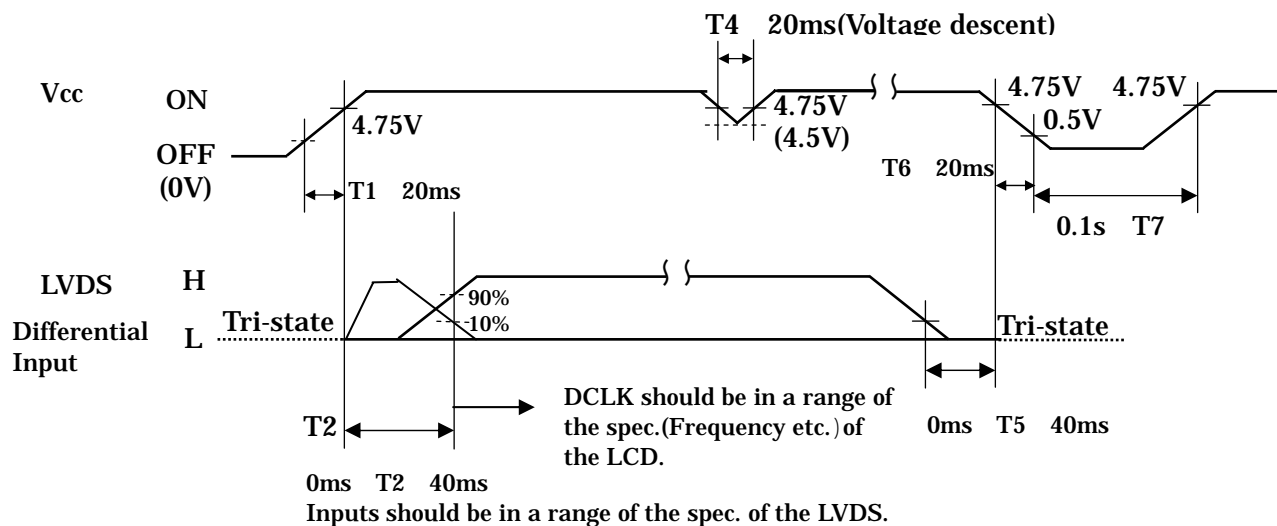


Fig.10-5. Power Supply Sequence

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11. BACKLIGHT SPECIFICATIONS									
A	11-1. Pin Configuration for Backlight Table 11-1(a) and 11-1(b) shows the description and pin assignment of the connectors (CN-A and B) for the Backlight of this LCD module.								
	Table 11-1(a) Pin Assignment of CN-A				Table11-1(b) Pin Assignment of CN-B				
	Pin	Signal	Function	Cable color	Pin	Signal	Function	Cable color	
	1	VL1	Power supply for CCFL 1	Red	1	VL3	Power supply for CCFL 3	Red	
	2	VL2	Power supply for CCFL 2	Red	2	VL4	Power supply for CCFL 4	Red	
	3	NC		-	3	NC		-	
B	4	GND	Ground (for VL1, 2)	White	4	GND	Ground (for VL3, 4)	White	B
<div style="display: flex; justify-content: space-between;"> <div> <p>Connector : Housing : BHR-04VS-1</p> <p style="margin-left: 20px;">: Contact : SBH-001T-P0.5</p> <p>User's Connector : Post with base : SM04(4.0)B-BHS-1-TB</p> <p>Supplier : Japan Solderless Terminal Trading Company LTD. (J.S.T.)</p> </div> </div>									
C	11-2. CCFL Supplier : SANKEN ELECTRIC CO., LTD Part No. SD26E3850E8350B3113000								
	11-3. Life The life of the backlight is a minimum of 25,000 hours at the following conditions.								
	(1) Working conditions Ambient temperature : 25 ± 5°C Tube current(I _L) : (10.5mA or less)								
D	(2) Definition of life Brightness becomes 50% or below 50% of the minimum brightness value shown in Table 9-1. The lamp cannot be lit by the breakdown voltage of 1600Vrms. Lamp is flashing.								
E	11-4. Lamp Assembly set (for replacement) Lamp Assembly set (with charge) is prepared for maintenance. This set consists of an upper lamp assembly and a lower lamp assembly. Type number : FLCL-21								
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1												2				3				4			
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13. ENVIRONMENTAL SPECIFICATIONS																							
Table 13-1 shows the environmental specifications.																							
Table 13-1. Environmental Specifications																							
Item		Condition										Remark											
Temperature		Operation		0~57℃ (Note1)								Temperature on surface of LCD panel (display area.)											
		Storage		-20~60℃																			
Humidity		Operation		20~85%RH								Maximum wet-bulb temperature should not exceed 29℃. No condensation.											
		Storage		5~85%RH																			
Vibration		Non-operation		10~500Hz, 1 cycle/20minute, 2G, 1.5mm max, 2hour each X, Y and Z directions								For single module without package.(Note2)											
Shock		Non-operation		30G, 6ms, 1time each ± X, ± Y and ± Z directions.																			
Note1: Temperature on surface of LCD panel should be under 57 .																							
Note2: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged.																							
Table 13-2. Shock Resistance Standard when Module is Packaged																							
Dropping location						Dropping height						Count											
A ~ J						60cm						1 time											
Fig.13-1. Direction to apply shock to package																							
												NA19026-C081A											
												Tech Bes LCD-00225										CUST.	
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1																							

1		2		3		4		
【NA19026-C081A】								
14. INDICATIONS								
This module has the following indications.								
A	(1) Product name		: LCD unit					
	(2) Model number		: NA19026-C081A					
	(3) Manufacturing number		: 3 Y 0 0 0 0 1					
						Serial number (To be reset every month on 1st.)		
B						Manufacturing month (Oct. = X, Nov. =Y, Dec. =Z)		
						Last digit of manufacturing year.		
(8) Caution when changing cold-cathode tubes and disposal method of them. (See Fig. 14-1)								
C	<div>· WHEN CHANGING COLD CATHODE FLUORESCENT LAMPS, FOLLOW OPERATING SPECIFICATIONS. ESPECIALLY BE CAREFUL ABOUT THE LAMP'S SIDE-EDGE. · 蛍光管の交換は作業仕様書に従って行って下さい。特に蛍光管ホルダ側面のエッジに気をつけて下さい。 · THIS TFT COLOR LCD CONTAINS COLD CATHODE FLUORESCENT LAMPS. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR ITS DISPOSAL. · 当該液晶ディスプレイユニットには蛍光管が組み込まれていますので、地方自治体の条例または規則に従って廃棄して下さい。</div>				<div>LCD unit NA19026-C081 A 2101234</div>			
	Fig.14-1				Fig.14-2 Product label (example)			
15. PACKAGING								
Separately specified in packaging specifications.								
D	15-1. Packaging specifications							
	(1) 5 LCD modules / 1 package.							
	(2) Weight: approximately 13kg / 1 package.							
	(3) Outline dimensions : 348mm(W) × 328(D) × 490mm(H)							
	15-2. Packaging method							
	Fig.15-2 (a),(b),(c),(d) show the packing method.							
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A

A

B

B

C

C

D

D

E

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DATE

Anti-Electric Bag

Tape

Protective sheet

Front side

Rear side

Front side

Fig.15-2(a) Packaging Method

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A

Holder(bottom)

LCD unit

A

B

B

C

Holder(upper)

C

D

D

Label(example)

型 格 (TYPE)	数 量 (QTY.)	5
図 番	版 数	01A
(DRWG. NO.)	(REV. NO.)	
9X00001		
9X00002		
9X00003		
9X00004		
9X00005		

バーコード

MADE IN JAPAN

- Taping
Upper : H or I method
Bottom : H method
- Upper and bottom holders should be anti-electrostatic type.

(490)

(348)

(328)

Fig.15-2(b) Packaging Method

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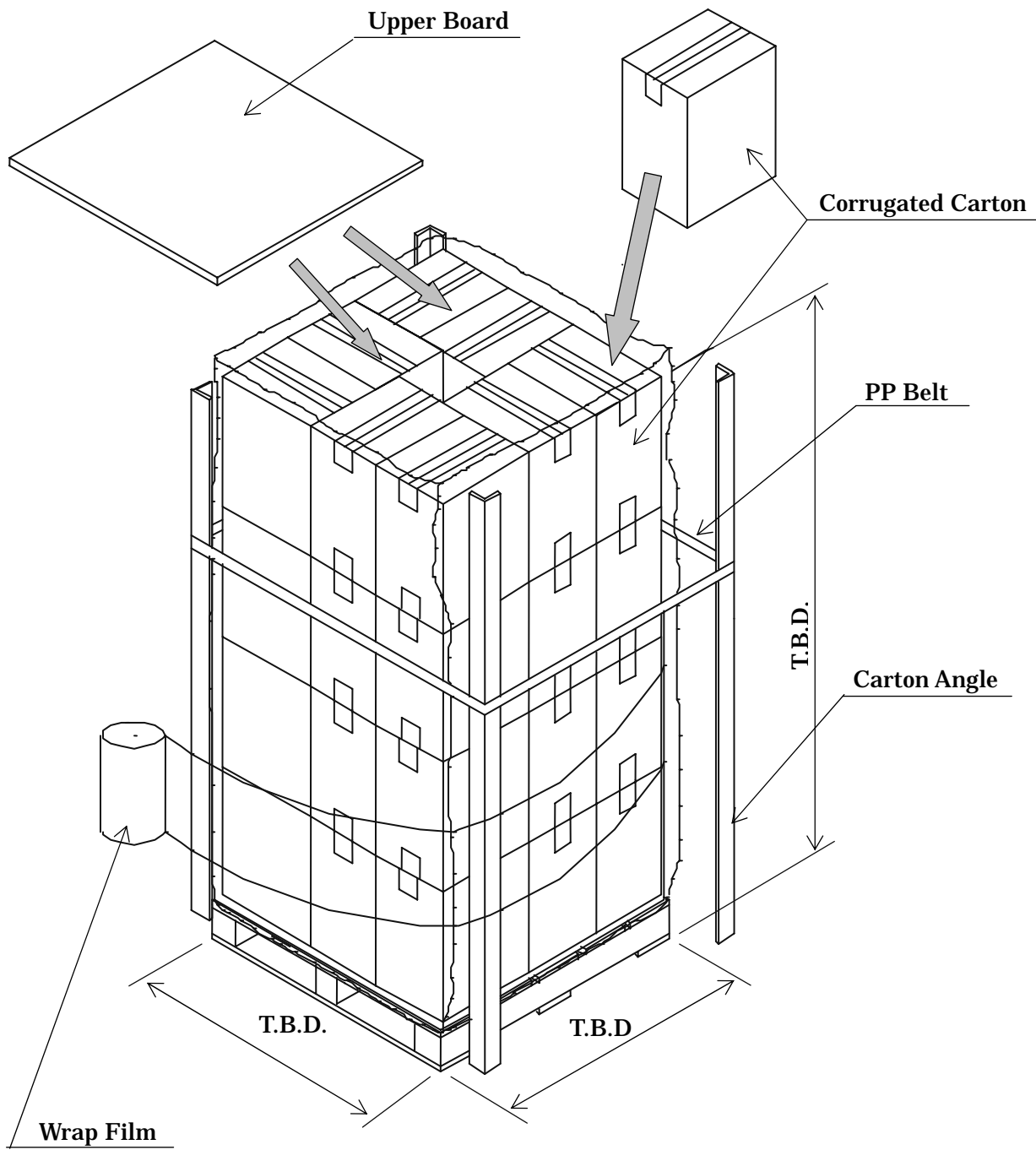
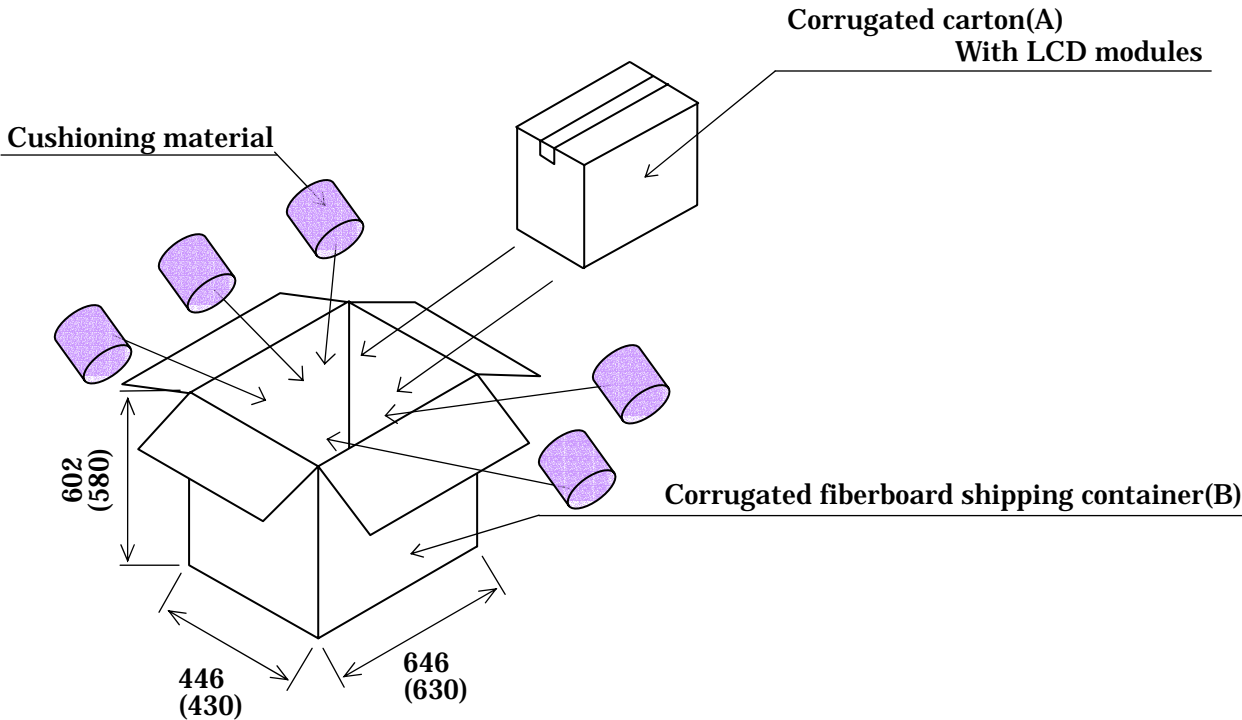


Fig.15-2(c) Packaging Method

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Note 1) The carton (A) should be placed in the middle of the container(B) with enough cushioning materials.

Figure.15-2(d) Packaging Method

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		1			2			3			4
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A	(5) Precautions on designing module mounting Do not press the display surface and bottom face of the LCD module. Display quality or reliability may be deteriorated if the installation of the LCD module is inappropriate and, as a result, excessive pressure is applied to the surface of LCD screen. Brightness uniformity or the reliability of CCFL may decrease if the pressure is applied to the backlight module.										
	Consider the module mounting design, so that twisting and bending do not occur to the LCD module. Excessive twisting and bending may damage display quality and reliability.										
	The power cable length between the LCD module and inverter should not be extended. Otherwise the backlight may not light or flickering may occur.										
	Do not make the power cable of the backlight clung to a metal plate, etc. Backlight frequency current for backlight driving may leak to the metal and desired brightness may not be assured.										
B	When Mounting LCD module with M4 screw(x4) should be screw up under 5.75kgf·cm torque.										
	(6) Storage method Do not store the LCD module in an atmosphere of organic solvent or corrosive gas. In an organic solvent atmosphere, the polarizer film discolors and display quality deteriorates. In a corrosive gas environment, various problems may occur.										
C	Store the LCD module in a Fujitsu package. At storing, Fujitsu packages can be stacked up to 4 boxes. The LCD module is in an anti-static bag. Keep the module in that status.										
	It is recommended that the storage environment should be humidity controlled, cool and dark. Recommended storage environment <ul style="list-style-type: none"> •Place :Dark (avoid direct sunlight) •Temperature : 10~35°C •Humidity : 50~60%RH 										
D	Note) If the module is left in an environment of 60 or more for a long period of time, optical characteristics may deteriorate.										
	(7) Storage method If the LCD panel is damaged, do not inhale or allow the liquid crystal to enter the mouth If the liquid crystal contacts the body or cloths, wash it off with soap immediately. Follow precautions for regular electronic components.										
E	Solder flux on the printed circuit board is harmless to the quality and reliability of LCD module. Fujitsu is practicing non-wash technology on module assembly process.										
F											

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