

SPECIFICATION FOR APPROVAL

() Preliminary Specification
() Final Specification

Title 15.0" SXGA+ TFT LCD

BUYER	HP(COMPAL)
MODEL	

SUPPLIER	LG.Philips LCD CO., Ltd.
*MODEL	LP150E01
SUFFIX	A2M2

*When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
	. <u></u>
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

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RECORDS OF REVISIONS

Revision No	Revision No	Page	DESCRIPTION
0.0	July. 18. 2001	- r age	First Draft
0.1	October.16.2001	18	Depth: 6.3 ? 0.3mm 6.5 ? 0.3mm
1.0	November.28.2001	19	Cover shield(G) CHANGE.(FRONT)
		20	Cover shield(S) CHANGE.(REAR)

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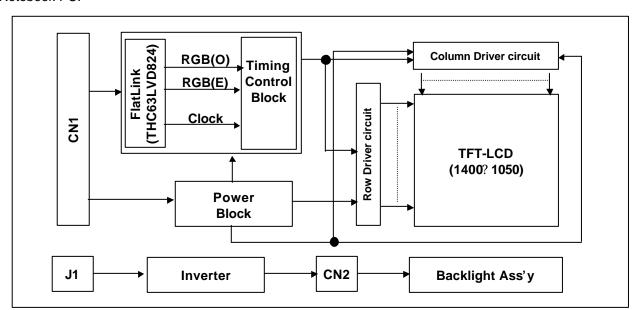


1. General Description

The LP150E01-A2 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.0 inches diagonally measured active display area with SXGA+ resolution(1050 vertical by 1400 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150E01-A2 has been designed to apply the interface method that enables low power, high speed, low EMI. Flat Link must be used as a LVDS(Low Voltage Differential Signaling) chip.

The LP150E01-A2 is intended to support applications where thin thickness, low power are critical factors and graphic display are important. In combination with the vertical arrangement of the sub-pixels, the LP150E01-A2 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active screen size	15.0 inches(38.10cm) diagonal
Outline Dimension	317.3(H) x 241.5(V) x 6.5(D) mm(Typ.)
Pixel Pitch	0.2175 mm x 0.2175 mm
Pixel format	1400 horiz. By 1050 vert. Pixels RGB stripes arrangement
Color depth	6-bit, 262,144 colors
Luminance, white	180 cd/m ² (Typ.)
Power Consumption	Total 5.75 Watt(Typ.) : 1.85W logic Typ. / 3.9 W B/L Typ.
Weight	615g(Typ.), 630g(Max.)
Display operating mode	Transmissive mode, normally white
Surface treatments	Hard coating(3H) Anti-glare treatment of the front polarizer



2. Absolute Maximum Ratings

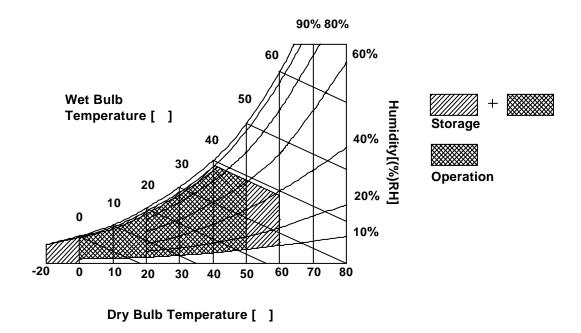
The following are maximum values which, if exceeded, may cause operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Deversates	aa.b. a.l	Values		l leite	Nata	
Parameter	symbol	Min.	Max.	Units	Notes	
Power Input Voltage	V _{cc}	-0.3	4.0	Vdc	At 25 ? 5 C	
Operating Temperature	T _{OP}	0	50	C	1	
Storage Temperature	T _{ST}	-20	60	C	1	
Operating Ambient Humidity	H _{OP}	10	90	%RH	1	
Storage Humidity	H _{ST}	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP150E01-A2 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Units	Notes
raiailletei	Symbol	Min.	Тур.	Max.	Ullits	Notes
MODULE: Power Supply Input Voltage Power Supply Input Current Differential Impedance Power Consumption Rush Current	V _{CC} I _{CC} Zm P _C I _{RUSH}	3.0 - 90 - -	3.3 0.560 100 1.85 1.5	3.6 0.810 110 2.67 2	Vdc A ohm Watts A	1 2 1 3
LAMP: Operating Voltage Operating Current Established Starting Voltage at 25 € at 0 € Operating Frequency Discharge Stabilization Time Power Consumption Life Time	V _{BL} I _{BL} V _S f _{BL} Ts P _{BL}	630 3.0 - - 45 - 10,000	650 6.0 - - 60 3.9	800 6.5 1130 1360 80 3 4.29	V _{RMS} mA V _{RMS} V _{RMS} kHz Minutes Watts Hrs	4 5 6 7 8 9

Note: The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

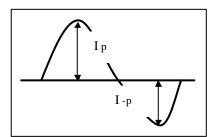
When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD Assembly should be operated in the same condition as installed in you instrument.

- 1. The specified current and power consumption are under the V_{CC} =3.3V, 25 C, f_V =60Hz condition whereas Mosaic pattern(Typ).,full black pattern(Max) is displayed and f_V is the frame frequency.
- 2. This impedance value is needed to proper display and measured from LVDS T_X to the mating connector.
- 3. The duration of rush current is about 20ms.
- 4. The variance of the voltage is ? 10%.
- 5. The voltage above V_S should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

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- 6. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform. (Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
 Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%. T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
- 8. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current.
- 9. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ? 2 °C.
- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 - It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $2 \pm 10\%$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

| I p - I p | / Irms * 100%

* Distortion rate

I p (or I p) / Irms

Do not attach a conducting tape to lamp connecting wire.

If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

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3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics and the other connector is used for the integral backlight system.

The electronics interface connector is a model FIXB30SR-HF11 manufactured by JAE.

The pin configuration for the connector is shown in the table below.

Table 3. MODULE CONNECTOR PIN CONFIGURATION(LVDS)

Pin Symbol Description Notes		П		TOR FIN CONFIGURATION(LVD3)
2 Vcc Power(3.3V) 3 Vcc Power for EDID 5 NC NC CLKEDID Clock for EDID 6 CLKEDID Data for EDID 7 DATA EDID 8 RA1- 10 GND 11 RB1- 12 RB1- 13 GND 14 RC1- 15 RC1+ 16 GND 17 RCLK1- 18 RCLK1- 19 GND 20 RA2- 21 RA2- 21 RA2- 22 GND 23 RB2- 24 RB2+ 25 GND 26 RC2- 27 RC2+ 28 GND 29 RCLK2- 20 RCLK2- 20 RCLK2- 20 GND 20 RCLK2- 21 Minus Signal of Even Channel 2 Ground 22 Power for EDID 23 Power for EDID 24 No Connection Clock for EDID Data for EDID Clock for EDID Data for EDID Or competition 2. Connector 2.1 LCD : FI-XB30SR-HF11(JAE) or compatible 2.2 Mating : Wire Type : FI-X30H(JAE) FPC Type : FI-X30H(JAE) Wire Type : FI-X30H(JAE) Wi	Pin	Symbol	Description	Notes
	2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29	VCC VCC VEDID NC CLKEDID DATA EDID RA1+ GND RB1- RB1+ GND RC1+ GND RCLK1- RCLK1+ GND RA2- RA2+ GND RB2- RB2- RB2- RB2- RC1- RC1+ GND RCLK1- RC	Power(3.3V) Power (3.3V) Power for EDID No Connection Clock for EDID Data for EDID Minus Signal of Odd Channel 0 Plus Signal of Odd Channel 1 Plus Signal of Odd Channel 1 Plus Signal of Odd Channel 1 Ground Minus Signal of Odd Channel 2 Plus Signal of Odd Channel 2 Plus Signal of Odd Channel 2 Ground Minus Signal of Odd Clk Channel Plus Signal of Odd Clk Channel Plus Signal of Odd Clk Channel Plus Signal of Even Channel 0 Plus Signal of Even Channel 0 Plus Signal of Even Channel 1 Plus Signal of Even Channel 1 Plus Signal of Even Channel 1 Ground Minus Signal of Even Channel 2 Plus Signal of Even Channel 2	1.1 LCD : LPZ4E122S6L (Thine) (THC63LVDF824A core + Timing Controller) 1.2 System : THC63LVDM823A (Thine) 2. Connector 2.1 LCD : FI-XB30SR-HF11(JAE) or compatible 2.2 Mating : Wire Type : FI-X30H(JAE) FPC Type : FI-X30M(JAE) 2.3 Connector Pin Arrangement 122930 Viewing on Display Side

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The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent.

The pin configuration for the connector is shown in the table below.

Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored pink. The low voltage side terminal is white

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3-3. Signal Timing Specifications

This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 5. Timing Table

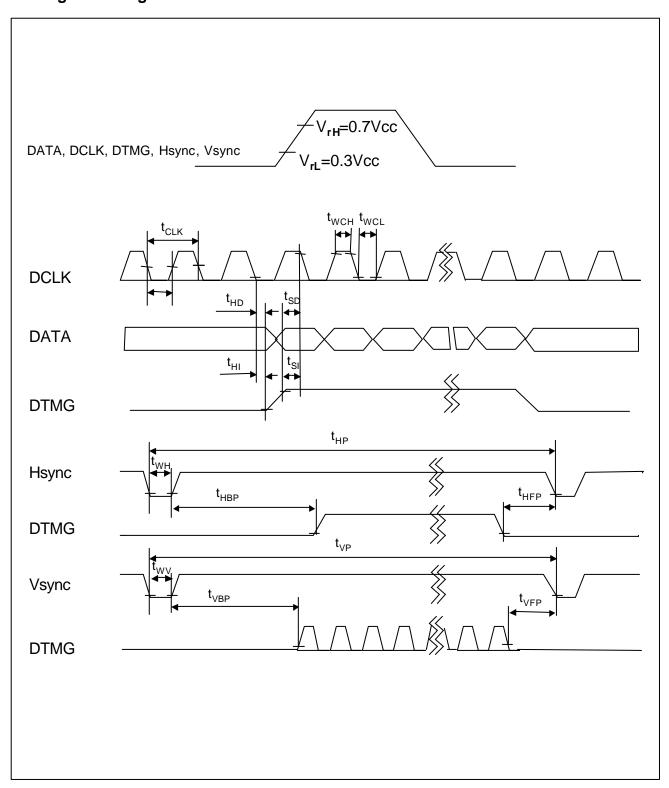
tclk: Dual Port Operating

	ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Dclk	Period	f _{CLK}	105	108	109	MHz	
Hsync	Period	t _{HP}	1464	1600	1696		
Tisyric	Width-Active	t _{wH}	16	-	-	t _{CLK}	
	Period	t _{VP}	1060	1100	1150	+	
Vsync	Width-Active	t _{wv}	2	3	-	t _{HP}	
	Frequency	f_{V}	-	60	-	Hz	
	Horizontal Back Porch	t _{HBP}	16	-	-		
D-7140	Horizontal Front Porch	t _{HFP}	16	-	-	t _{CLK}	
DTMG	Vertical Back Porch	t _{VBP}	3	-	-	t _{HP}	
	Vertical Front Porch	t _{VFP}	2	-	-	יור	

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3-4. Signal Timing Waveforms





3-5. Color Input Data Reference

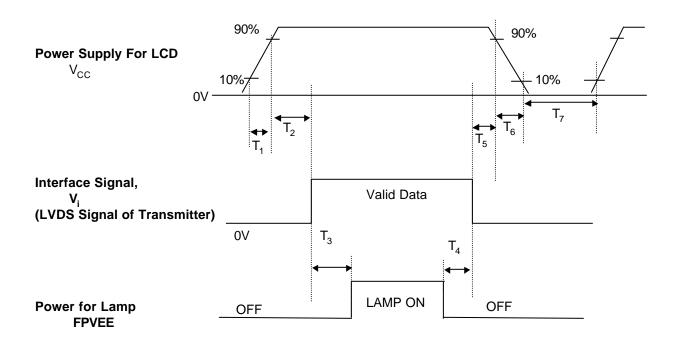
The brightness of each primary color(red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 6. COLOR DATA REFERENCE

									Inp	ut Co	lor D	ata							
	Color	MSE	3	Re	ed		LSB	MSI	3	Gre	een		LSB	MSE	3	Bl	ue		LSB
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	В0
Basic Colors	Black Red(63) Green(63) Blue(63) Cyan Magenta Yellow White	0 1 0 0 0 1 1	0 1 0 0 0 1 1	0 1 0 0 0 1 1	0 1 0 0 0 1 1	0 1 0 0 0 1 1	0 1 0 0 0 1 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0	0 0 1 0 1 0 1	0 0 1 0 1 0	0 0 1 0 1 0 1	0 0 0 1 1 1 0	0 0 0 1 1 1 0	0 0 0 1 1 1 0	0 0 0 1 1 1 0	0 0 0 1 1 1 0	0 0 0 1 1 1 0
Red	Red(00) Dark Red(01) Red(02) : Red(61) Red(62) Red(63) Bright	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 1 : 0 1	0 1 0 : 1 0	0 0 0 : 0 0	0 0 0 0 0	0 0 0 : 0 0	0 0 0 0 0	0 0 0 : 0 0	0 0 0 0 0	0 0 0 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0
Green	Green(00)Dark Green(01) Green(02) : Green(61) Green(62) Green(63)Bright	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 1 : 0 1	0 1 0 : 1 0	0 0 0 0 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0	0 0 0 : 0 0
Blue	Blue(00) Dark Blue(01) Blue(02) : Blue(61) Blue(62) Blue(63) Bright	0 0 0 : 0 0	0 0 0 0 0	0 0 0 : 0 0	0 0 0 0 0 0	0 0 0 : 0 0	0 0 0 0 0 0	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 0 : 1 1	0 0 1 : 0 1	0 1 0 : 1 0						



3-6. Power Sequence



Parameter		Values							
Parameter	Min.	Тур.	Max.	Units					
T ₁ T ₂ T ₃ T ₄ T ₅ T ₆ T ₇	- 0 200 200 0 - 400	- - - - -	10 50 - - 50 10 -	ms ms ms ms ms ms					

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD $V_{\rm CC}$ to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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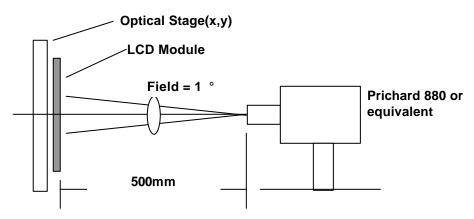


4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of ? and ? equal to 0 °.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method



Parameter	Symbol		Values		Units	Notes
raiailletei	Syllibol	Min.	Тур.	Max.	Ullits	Notes
Contrast Ratio	CR	150	200	-		1
Surface Luminance, white	L _{WH}	150	180	-	cd/m ²	2
Luminance Variation	? WHITE	-	1.25	1.45		3
Response Time Rise Time Decay Time	Tr Tr _R Tr _D	- -	15 30	30 50	ms	4
CIE Color Coordinates Red Green Blue White	xR yR xG yG xB yB xW yW	0.536 0.304 0.276 0.510 0.124 0.106 0.283 0.299	0.566 0.334 0.306 0.540 0.154 0.136 0.313 0.329	0.596 0.364 0.336 0.570 0.184 0.166 0.343 0.359		
Viewing Angle x axis, right(?=0) x axis, left (?=180) y axis, up (?=90) y axis, down (?=270)	?r ?l ?u ?d	40 40 10 30	- - -	- - - -	degree	5
Gray Sclae	-	-	-	-		6

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Contrast Ratio =

Product Specification

Notes: 1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels

Surface Luminance with all black pixels

- Surface luminance is the center point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2.
 When I_{BL}=6mA
- 3. The variation in surface luminance , ? WHITE is determined by measuring L_{ON} at each test position 1 through 5, and then dividing the maximum L_{ON} of 5 points luminance by minimum L_{ON} of 5 points luminance. For more information see FIG 2.

? WHITE = Maximum($L_{ON1}, L_{ON2}, \dots, L_{ON5}$) ? Minimum($L_{ON1}, L_{ON2}, \dots, L_{ON5}$)

- 4. Response time is the time required for the display to transition from to black(Rise Time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

Gray Level	Luminance(%)
LO	0.40
L7	0.90
L15	3.50
L23	10.5
L31	22.5
L39	36.5
L47	54.5
L55	82.5
L63	100

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FIG. 2 Luminance

<measuring point for luminance variation>

<measuring point for surface luminance>

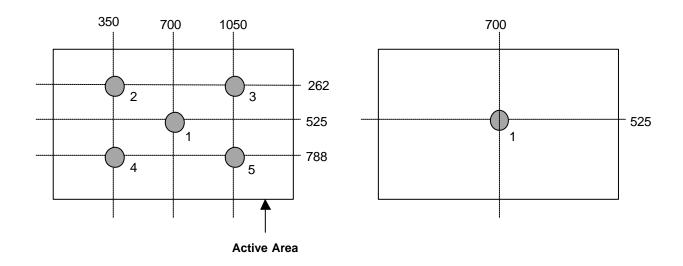
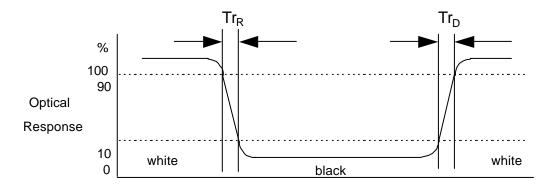


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

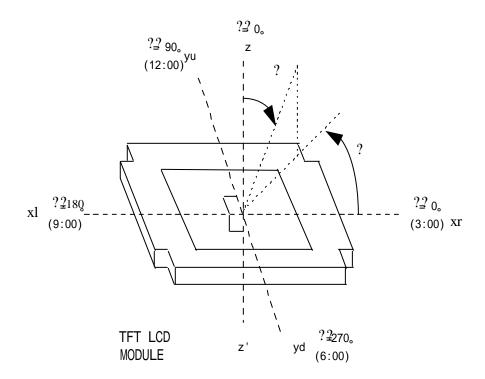


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FIG. 4 Viewing angle

<dimension of viewing angle range>



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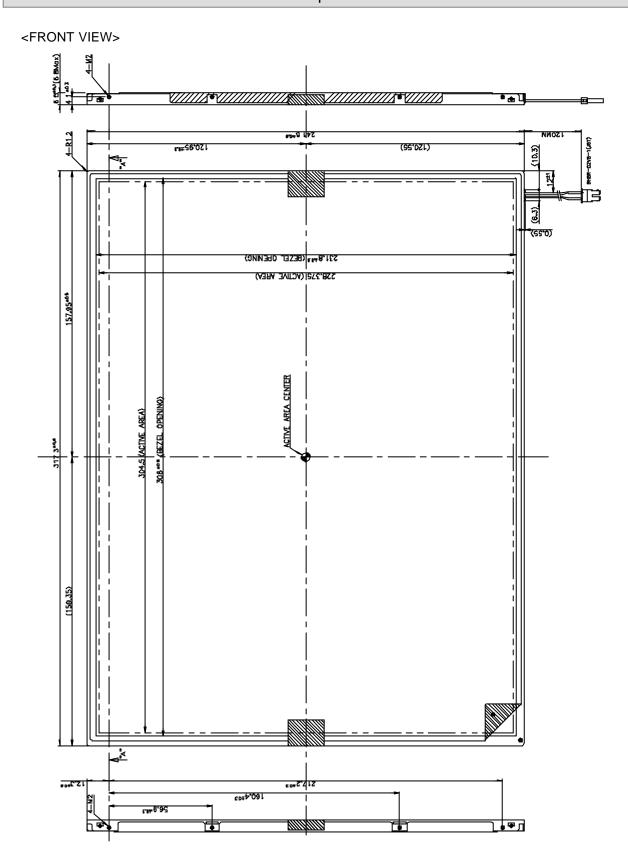
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP150E01-A2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	317.3 ? 0.5mm
Outside dimensions	Vertical	241.5 ? 0.5mm
	Depth	6.5 ? 0.3mm
Danalana	Horizontal	308 ? 0.5mm
Bezel area	Vertical	231.8 ? 0.5mm
A .: 1: 1	Horizontal	304.5mm
Active display area	Vertical	228.375mm
Weight(approximate)	615g(Typ.),	630g(Max.)
Surface Treatment	Hard coating(3H) Anti-glare treatment c	of the front polarizer

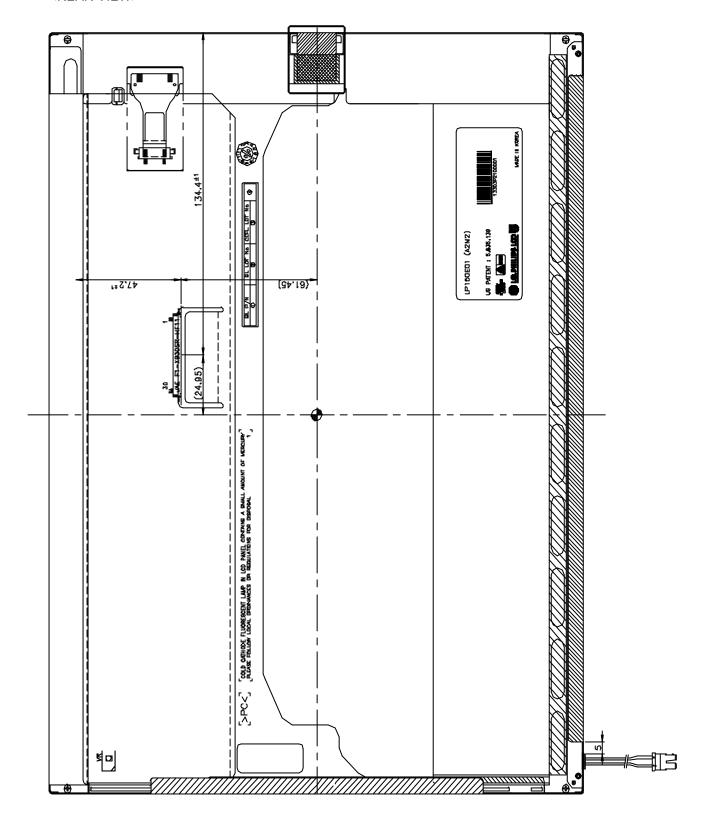
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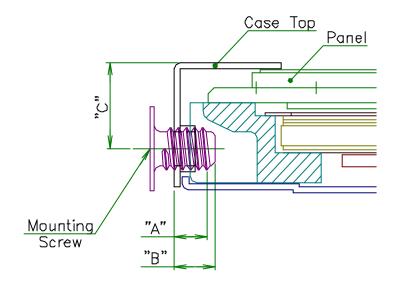


<REAR VIEW>





<DETAIL DESCRIPTION OF SIDE MOUNTING SCREW>



* Mounting Screw depth depth Min.: "A" =2.0 depth Max: "B" =2.5

* Mounting screw Dim. : "C" = 4.1(typ.)

*Torque: 2 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

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6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60 ℃ 240h
2	Low temperature storage test	Ta= -20 ℃ 240h
3	High temperature operation test	Ta= 50 ℃ 50%RH 240h
4	Low temperature operation test	Ta= 0 °C 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)
7	Altitude operating storage / shipment	0 - 10,000 feet(3048m) 0 - 40,000 feet(12,192m)

[{] Result Evaluation Criteria }

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There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

- a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.
 Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.
 Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A4: 1997+A11: 1997
 IEC 950 : 1991+A1: 1992+A2: 1993+A3: 1995+A4: 1996
 European Committee for Electrotechnical Standardization(CENELEC)
 EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization (CENELEC), 1998

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	E	F	G	Н	I	J	К	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE D : YEAR E : MONTH

F,G: PANEL CODE H: ASSEMBLY CODE I,J,K,L,M: SERIAL NO.

Note:

1. YEAR

YEAR	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

2. MONTH

MONTH	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

3. Serial No.

Serial No.	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999,, Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 10 pcs

b) Box Size : 360mm × 322mm × 391mm



9. PRECAUTIONS

Please pay attention to the following when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module.
 - And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach a transparent protective plate to the surface in order to protect the polarizer.

 Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not describe because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are determined to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm200$ mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 °C and 35 °C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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