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TITLE: HR236WU1-300
Product Specification
Rev.P0

# **BEIJING BOE Display TECHNOLOGY**

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S	TFT-LCD		2012.12.13	1 OF 30



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TET- I CD PRODUCT	Rev P0	Dec 13 12'

# **REVISION HISTORY**

REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
Rev.P0		Initial Release	Dec .13. 12'	F.WANG
	•	•	1	
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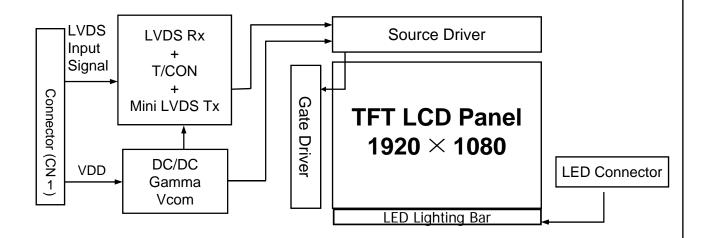


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## 1.0 GENERAL DESCRIPTION

#### 1.1 Introduction

HR236WU1-300 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 23.6 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



#### 1.2 Features

- LVDS Interface with 2 pixel / clock
- High-speed response
- 6-bit (Hi-FRC) color depth, display 16. 7M colors
- Incorporated edge type back-light (LED)
- sRGB
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS/Halogen Free
- TCO 5.0, E/S 5.0 compliant
- Gamma Correction

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## 1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

## 1.4 General Specification

The followings are general specifications at the model HR236WU1-300.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	521.28(H) × 293.22(V)	mm	
Number of pixels	1920(H) ×1080(V)	pixels	
Pixel pitch	$0.2715(H) \times 0.2715(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally Black		
Dimensional outline	$544.8(H) \times 320.5(V) \times 10.5(D)$ typ.	mm	
Weight	2515 (Typ.)	g	
Surface Treatment	Haze 25%, 3H		
Back-light	Lower edge side, 1-LED Lighting Bar type		Note 1
	P <sub>D</sub> : 5.5W (max)		
Power Consumption	P <sub>BL</sub> : 21.2W (max)		Note 2
	P <sub>total</sub> : 26.7 (max)		

Notes: 1. LED Lighting Bar (4\*input pins)

2. PLED=Input pins\* VPIN×IPIN

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## 2.0 ABSOLUTE MAXIMUM RATINGS

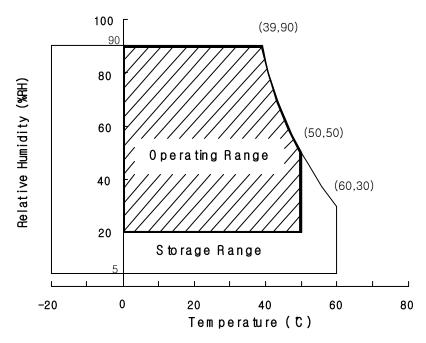
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	-0.3	6.0	V	
Logic Supply Voltage	V <sub>IN</sub>	VSS-0.3	V <sub>DD</sub> +0.3	V	Ta = 25 °C
LED Light Bar Current Per Input Pin	IPIN	-	140	mA	
LED Light Bar Voltage Per Input Pin	VPIN	-	44.2	V	
Operating Temperature	$T_{OP}$	0	+50	$^{\circ}$	1)
Storage Temperature	$T_{ST}$	-20	+60	$^{\circ}$	1)

Note : 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



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## 3.0 ELECTRICAL SPECIFICATIONS

## 3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta =  $25 \pm 2$  °C]

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	$V_{DD}$	4.5	5.0	5.5	V	Note1
Power Supply Current	$I_{DD}$	-	900	1100	mA	Note1
In-Rush Current	$I_{RUSH}$	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	$V_{RF}$	-	-	100	mV	$V_{DD} = 5.0V$
High Level Differential Input Threshold Voltage	$V_{IH}$	-	1	+300	mV	
Low Level Differential Input Threshold Voltage	$V_{\rm IL}$	-300	1	1	mV	
Differential input voltage	V <sub>ID</sub>	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		$V_{IH}$ =100mV, $V_{IL}$ =-100mV
	$P_{\mathrm{D}}$	-	4.5	5.5	W	
Power Consumption	$P_{BL}$	18.7	20.0	21.2	W	Note 3
	P <sub>total</sub>	-	24.5	26.7	W	

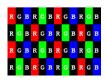
Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz

Clock frequency = 92.9 MHz. Test Pattern of power supply current

a) Typ: Color Test

b) Max: Skip Subpixel255



2. Duration of rush current is about 2 ms and rising time of VDD is 520  $\mu$ s  $\pm$  20 %

3. Calculated value for reference (Input pins\*VPIN  $\times$ IPIN) excluding inverter loss.

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## 3.2 Backlight Unit

< Table 4. LED Backlight Unit >

Parameter		Min.	Тур.	Max.	Unit	Remarks
LED Light Bar Input Voltage Per Input Pin	VPIN	39	41.6	44.2	V	Duty 100%
LED Light Bar Input Current Per Input Pin	IPIN	-	120	-	mA	Note1,2,
LED Power Consumption	$P_{BL}$	18.7	20.0	21.2	W	Note 3
LED Life-Time	1	30,000	-		Hrs	Note 4

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 120mA

Note3:  $P_{BL}$ =4 Input pins\*VPIN ×IPIN

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=120mA on condition of continuous operating at 25  $\pm 2$  °C

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## 4.0 OPTICAL SPECIFICATION

#### 4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance  $\leq 1$  lux and temperature =  $25\pm2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of  $\theta$  and  $\Phi$  equal to 0°. We refer to  $\theta_{\emptyset=0}$  (= $\theta_3$ ) as the 3 o'clock direction (the "right"),  $\theta_{\emptyset=90}$  (= $\theta_{12}$ ) as the 12 o'clock direction ("upward"),  $\theta_{\emptyset=180}$  (= $\theta_9$ ) as the 9 o'clock direction ("left") and  $\theta_{\emptyset=270}$  (= $\theta_6$ ) as the 6 o'clock direction ("bottom"). While scanning  $\theta$  and/or  $\emptyset$ , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V + /-10% at  $25^{\circ}$ C. Optimum viewing angle direction is 6 'clock.

## **4.2 Optical Specifications**

[VDD = 5.0V, Frame rate = 60Hz, Clock = 74.25MHz,  $I_{BL}$  = 480mA, Ta =25  $\pm$ 2 °C]

Parame	eter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	$\Theta_3$		75	89	-	Deg.	
Viewing Angle	нопиопа	$\Theta_9$	CR > 10	75	89	-	Deg.	Note 1
range	Vertical	$\Theta_{12}$	CR > 10	70	89	1	Deg.	Note 1
	vertical	$\Theta_6$		70	89	1	Deg.	
Luminance Contrast	ratio	CR		700	1000			Note 2
Luminance of Whit	e	$Y_{w}$		200	250		cd/m <sup>2</sup>	Note 3
White luminance un	iformity	ΔΥ		75	80		%	Note 4
	WILL	$W_x$		0.283	0.313	0.343	-	
	White	$W_y$	$\Theta = 0^{\circ}$	0.299	0.329	0.359	-	
	Red	$R_x$	(Center) Normal	0.615	0.645	0.675	-	
Reproduction	Red	$R_y$	Viewing Angle	0.310	0.340	0.370	-	Note 5
of color	Green	$G_x$		0.262	0.292	0.322	-	Note 3
	Green	$G_{y}$		0.604	0.634	0.664	-	
	Blue	$\mathbf{B}_{\mathbf{x}}$	7	0.118	0.148	0.178	-	
	Diue	$\mathbf{B}_{\mathrm{y}}$		0.030	0.060	0.090	-	
Response Time	GTG	$T_{g}$			14	20	ms	Note 6
Cross T	alk	СТ		-	-	2.0	%	Note 7

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## Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of  $\theta$ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster

Luminance when displaying a black raster

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as :  $\Delta Y = ($  Minimum Luminance of 9points / Maximum Luminance of 9points ) \* 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.

  Each time in below table is defined as Figure 3 and shall be measured by switching the input
  - signal for "any level of gray(bright)" and "any level of gray(dark)".
  - (See FIGURE 3 shown in Appendix).
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y<sub>A</sub>) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y<sub>B</sub>) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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## 5.0 INTERFACE CONNECTION.

## **5.1 Electrical Interface Connection**

#### 5.1.1 LED Light Bar

< Table 1. LED Light Bar>

Pin No	Symbol	Description	
1	IRLED1	LED current sense for string1	
2	IRLED2	LED current sense for string2	
3	VLED	LED power supply	
4	VLED	LED power supply	
5	IRLED3	LED current sense for string3	
6	IRLED4	LED current sense for string4	
7	CONNECTOR	3708K-Q06N-00R	

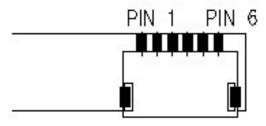


Figure 1. Top View of LED Bar Connector

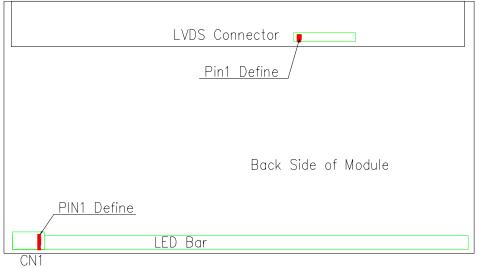


Figure 2. Back Side of Module

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## 5.0 INTERFACE CONNECTION.

## **5.1 Electrical Interface Connection**

• CN11 Module Side Connector : UJU IS100-L30R-C23or Equivalent User Side Connector : JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	RXO0-	Negative Transmission data of Pixel 0 (ODD)	
2	RXO0+	Positive Transmission data of Pixel 0 (ODD)	
3	RXO1-	Negative Transmission data of Pixel 1 (ODD)	
4	RXO1+	Positive Transmission data of Pixel 1 (ODD)	
5	RXO2-	Negative Transmission data of Pixel 2 (ODD)	
6	RXO2+	Positive Transmission data of Pixel 2 (ODD)	
7	GND	Power Ground	
8	RXOC-	Negative Transmission Clock (ODD)	
9	RXOC+	Positive Transmission Clock (ODD)	
10	RXO3-	Negative Transmission data of Pixel 3 (ODD)	
11	RXO3+	Positive Transmission data of Pixel 3 (ODD)	
12	RXE0-	Negative Transmission data of Pixel 0 (EVEN)	
13	RXE0+	Positive Transmission data of Pixel 0 (EVEN)	
14	GND	Power Ground	
15	RXE1-	Negative Transmission data of Pixel 1 (EVEN)	
16	RXE1+	Positive Transmission data of Pixel 1 (EVEN)	
17	GNG	Power Ground	
18	RXE2-	Negative Transmission data of Pixel 2 (EVEN)	
19	RXE2+	Positive Transmission data of Pixel 2 (EVEN)	
20	RXEC-	Negative Transmission Clock (EVEN)	
21	RXEC+	Positive Transmission Clock (EVEN)	
22	RXE3-	Negative Transmission data of Pixel 3 (EVEN)	
23	RXE3+	Positive Transmission data of Pixel 3 (EVEN)	
24	GND	Power Ground	Note 1
25	NC		
26	NC	No. Connection	
27	NC	]	
28	VDD		
29	VDD	Power Supply: +5V	
30	VDD	1	

Note 1: This pin should be connected with GND.

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# **5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent) 5.2.1 LVDS Interface**

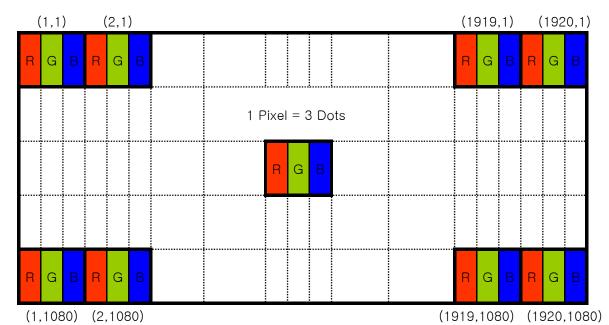
	Input	Trans	mitter	Interface		Intertace		HR236WU1-300 (CN11)	Remark
	Signal Pin 1		Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.			
	OR0	51							
	OR1	52							
	OR2	54	40	OLITO	DVO	1			
	OR3	55	48 47	OUT0- OUT0+	RXO0- RXO0+	1 2			
	OR4	56	.,	00101	10100	2			
	OR5	3							
	OG0	4							
	OG1	6							
	OG2	7							
	OG3	11	46	OLIT1	DVO1	2			
	OG4	12	46 45	OUT1- OUT1+	RXO1- RXO1+	3 4			
	OG5	14	73	00111					
	OB0	15							
L	OB1	19							
V	OB2	20							
D	OB3	22							
S	OB4	23	40	OLUTO	DVO	_			
	OB5	24	42 41	OUT2- OUT2+	RXO2- RXO2+	5 6			
	Hsync	27	71	00121	10.102	O			
	Vsync	28							
	DE	30							
	MCLK	31	40 39	CLK OUT- CLK OUT+	RXO CLK- RXO CLK+	8 9			
	OR6	50	_						
	OR7	2							
	OG6	8	20	OLUTA	RXO3-	10			
	OG7	10	38 37	OUT3- OUT3+	RXO3+	10 11			
	OB6	16	31	0013					
	OB7	18							
	RSVD	25							

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## **5.3 Data Input Format**



Display Position of Input Data (V-H)

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## **6.0 SIGNAL TIMING SPECIFICATION**

6.1 The HR236WU1-300 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	58.54	74.25	98	MHz
Clock	High Time	Tch	-	4/7Tc	-	
	Low Time		-	4/7Tc	-	
Frame Period			1115	1126	1136	lines
		Tv	50	60	75	Hz
			20	16.7	13.3	ms
Vertical Display Period		Tvd	1	1080	1	lines
One line Scanning Period		Th	1050	1100	1150	clocks
Horizontal Display Period		Thd	960	960	960	clocks

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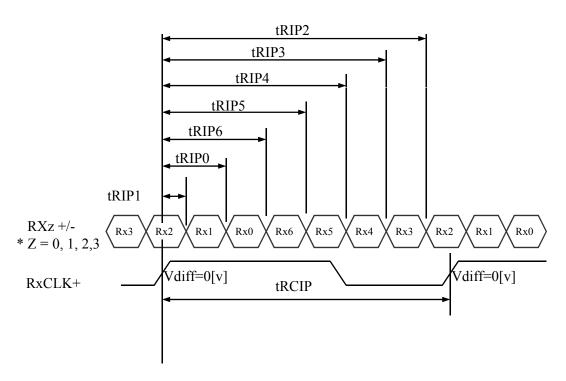
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## **6.2 LVDS Rx Interface Timing Parameter**

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.20	13.47	17.08	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	2 ×tRCIP/7+0.4	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	3 ×tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	$4 \times tRCIP/7+0.4$	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 × tRCIP/7	5 ×tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	$6 \times tRCIP/7-0.4$	6 × tRCIP/7	6 ×tRCIP/7+0.4	nsec	



\* Vdiff = (RXz+)-(RXz-),...,(RXCLK+)-(RXCLK-)

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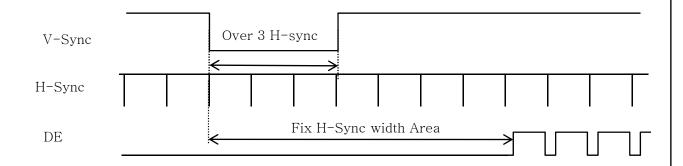
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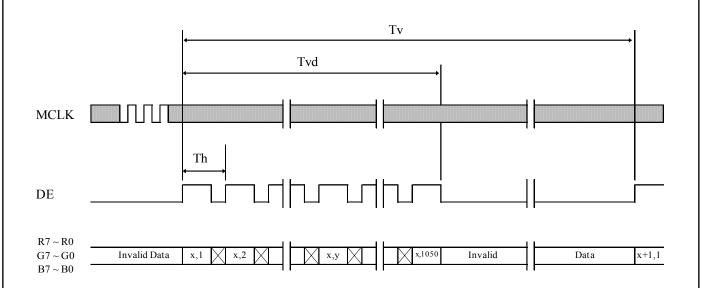
## 7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

## 7.1 Sync Timing Waveforms



- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

## 7.2 Vertical Timing Waveforms



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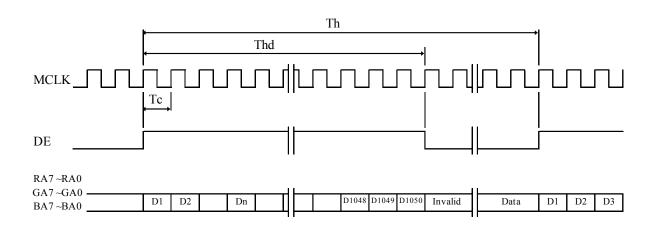
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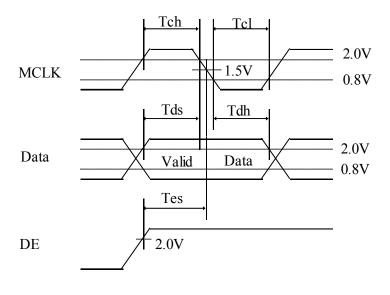
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## 7.3 Horizontal Timing Waveforms





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## 8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color & Gray Scale				RI	ED I	DAT	ГΑ			GREEN DATA BLUE DATA															
Colol & C	may Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Davis Calara	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\triangle$	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	$\triangle$				,	1							,	1							,	<u> </u>			
of RED	$\nabla$				,	ļ							,	ļ							,	$\downarrow$			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\nabla$	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\triangle$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	$\triangle$				,	1							,	1							,	1			
OI GREEN	$\nabla$				,								,	ļ							,	ļ			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	$\nabla$	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	Δ				,	1							,	<u> </u>							,	<u> </u>			
OIBLOL	$\nabla$				,								,								,				
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	$\nabla$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	$\triangle$	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
of WHITE	$\triangle$				•	1							,	<u> </u>								<u> </u>			
OI WHILE	$\nabla$				,								,								,	ļ			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	$\nabla$	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

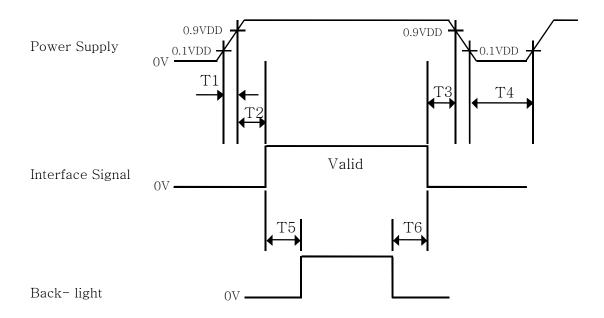
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## 9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- $\bullet$  0.5 ms  $\leq$  T1  $\leq$  10 ms
- $\bullet$  0  $\leq$  T2  $\leq$  50 ms
- $\bullet$  0  $\leq$  T3  $\leq$  50 ms
- $\bullet$  1 sec  $\leq$  T4
- $\bullet$  200 ms  $\leq$  T5
- $\bullet$  200 ms  $\leq$  T6

#### Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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## 10.0 MECHANICAL CHARACTERISTICS

## **10.1 Dimensional Requirements**

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HR236WU1-300. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	$544.8(H) \times 320.5(V) \times 10.5(D)$ typ	mm
Weight	2515(typ)	gram
Active area	521.28 (H) × 293.22 (V)	mm
Pixel pitch	$0.2715  (H) \times 0.2715  (V)$	mm
Number of pixels	$1920 \text{ (H)} \times 1080 \text{ (V) (1 pixel} = R + G + B \text{ dots)}$	pixels
Back-light	Lower edge side, 1-LED Lighting Bar type	

## 10.2 Mounting

No Mounting

## 10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

## 10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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## 11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below. <Table 6. Reliability Test Parameters >

No	Test Items	Conditions				
1	High temperature storage test	$Ta = 60  ^{\circ}\text{C}, 240  \text{h}$	nrs			
2	Low temperature storage test	$Ta = -20  ^{\circ}\text{C}, 240  ^{\circ}$	hrs			
3	High temperature & high humidity operation test	Ta = 50 °C, 80%F	RH, 240hrs			
4	High temperature operation test	$Ta = 50  ^{\circ}\text{C}, 240\text{hr}$	rs			
5	Low temperature operation test	$Ta = 0^{\circ}C$ , 240hrs				
6	Thermal shock	$Ta = -20 \text{ °C} \leftrightarrow 60$	°C (0.5 hr), 100 cycle			
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	Random,10 ~ 300 Hz, 30 min/Axis 1.5 Grms X, Y, Z 30 min			
		Gravity	50G			
8	Shock test (non-operating)	Pulse width	11msec, sine wave			
		Direction	$\pm X$ , $\pm Y$ , $\pm Z$ Once for each			
9	Electro-static discharge test (non-operating)	Air : 150 pF Contact : 150 pF	F, 330Ω, 15 KV F, 330Ω, 8 KV			

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#### 12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
  - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
  - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
  - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
  - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
  - Do not pull the interface connector in or out while the LCD module is operating.
  - Put the module display side down on a flat horizontal plane.
  - Handle connectors and cables with care.
- (3) Cautions for the operation
  - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
  - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
  - Dew drop atmosphere should be avoided.
  - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
  - Do not apply fixed pattern data signal to the LCD module at product aging.
  - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
  - Do not disassemble and/or re-assemble LCD module.
  - Do not re-adjust variable resistor or switch etc.
  - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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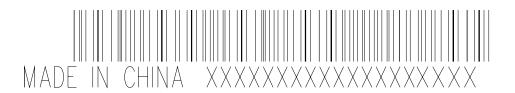
## 13.0 PRODUCT SERIAL NUMBER











- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001 : 01, 2002 : 02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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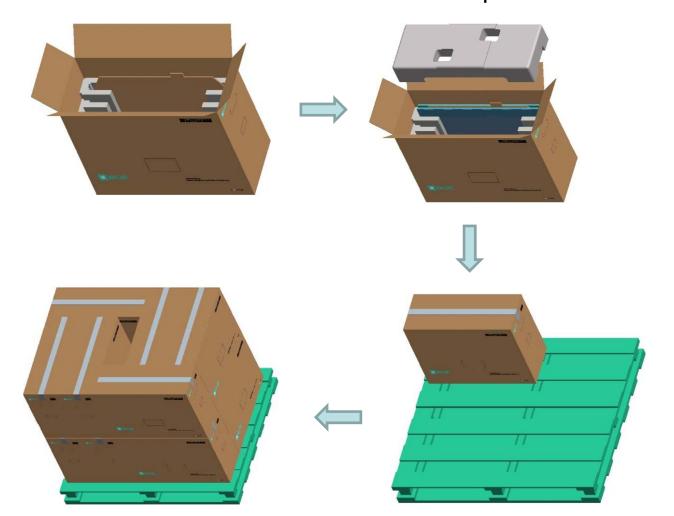
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## 14.0 Packing

## 14.1 Packing Order



Place the modules bundled by packing bag in the box, 8pcs module per box, place a cover on the top of the box



12ea box per pallet

After sealing the box, put the box on the pallet

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## 14.2 Packing Note

• Box Dimension :  $233\text{mm}(W) \times 613\text{mm}(L) \times 445\text{mm}(H)$ 

• Package Quantity in one Box: 8 pcs

#### 14.3 Box label

• Label Size : 108 mm (L) × 56 mm (W)

• Contents

Model: HR236WU1-300

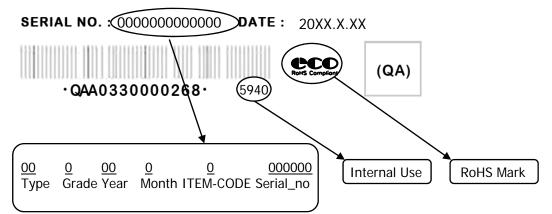
Q'ty: Module 8 Q'ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date



**MODEL**: HR236WU1-300 **Q'TY**: 8



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## 15.0 APPENDIX

Figure 1. Measurement Set Up

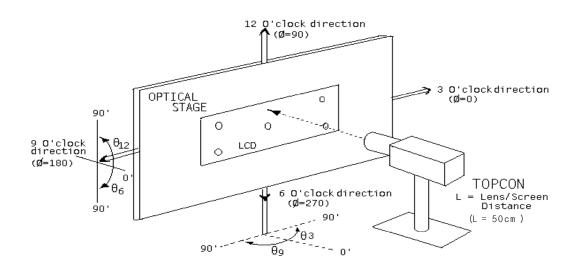
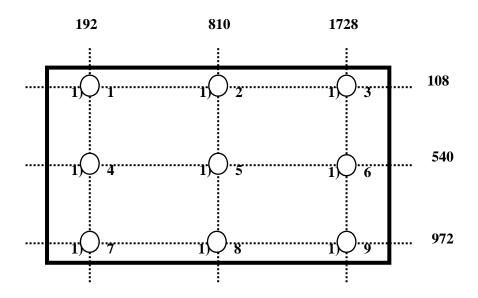


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



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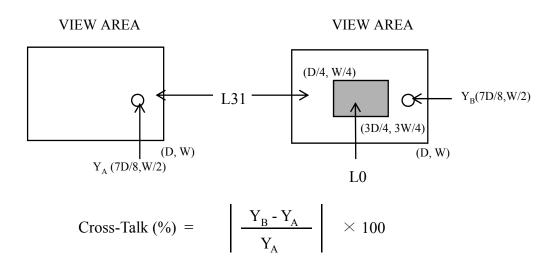
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Figure 3. Response Time Testing



**Figure 4. Cross Modulation Test Description** 



 $\begin{array}{ll} Where: & Y_A = Initial \ luminance \ of \ measured \ area \ (cd/m^2) \\ & Y_B = Subsequent \ luminance \ of \ measured \ area \ (cd/m^2) \\ The \ location \ measured \ will \ be \ exactly \ the \ same \ in \ both \ patterns \\ \end{array}$ 

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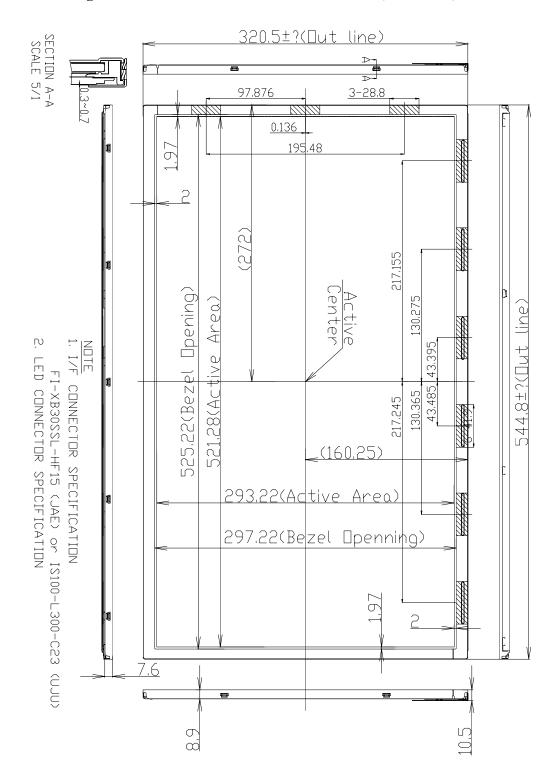
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**Figure 5. TFT-LCD Module Outline Dimensions (Front view)** 



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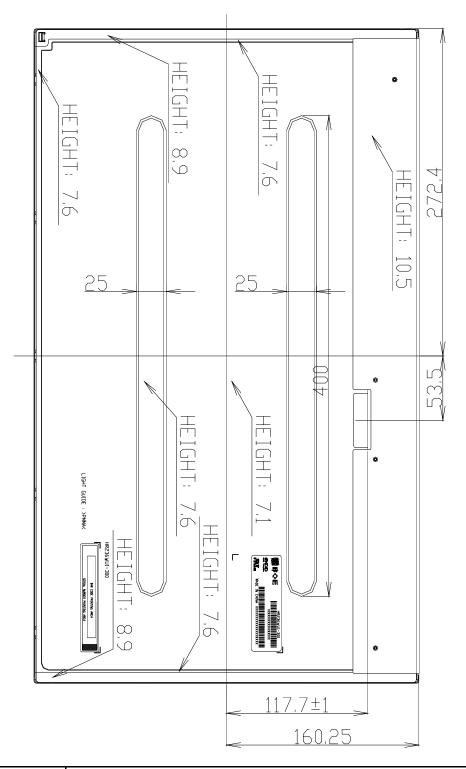
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Figure 6. TFT-LCD Module Outline Dimensions (Rear view)



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A4(210 X 297)