

To: Proview Electronics Co., Ltd.

APPROVAL SIGNATURE

SPECIFICATION

FOR

TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY

TFT-LCD MODULE

LTD141LA2S

SPECIFICATION No. : ______ LTD141LA2S-11

DATE OF ISSUE: 2003-04-15

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Sheet 1

Revision History

Date	Rev.	Sheet (New)	Item	Old	New	Reason
2003-04-15	11	New	-	-	-	-

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Specification No. Sheet 2

Caution and Handling Precaution

For your end user's safety, it is strongly advised that the items with " Σ " should be included in the instruction manual of the system which may be issued by your organization.

For Safety Warning

- (1) Toshiba Matsushita Display Technology's(TMD) Standard LCD modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
 - Since they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision System and Air traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.
- (2) DISCONNECT POWER SUPPLY before handling LCD module.
 - DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's (hereinafter called "FL") connector or cable in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.
- (3) Make sure to insert the module FL connector to the inverter connector in correct position.

Do not insert in irregular position.

If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.

If there is a possibility that the connector has been inserted incorrectly, please re-insert the connector only after you confirm the module and FL power is completely off.

DO NOT USE the mating FL connector which TMD does not specify.

Otherwise, TMD shall not be liable for any damages caused by the connector.



Caution

(1) DO NOT DISASSEMBLE OR MODIFY the module.

Sensitive parts inside LCD module may be damaged, and dusts or scratches may mar the displays.

TMD does not warrant the modules, if customer disassembled or modified them.

- *(2) DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT PERMIT this material to contact the skin, if LCD panel is broken and liquid crystal material spills out.
 - In the event of inadvertent contact, immediately rinse the mouth or eyes with adequate water. If this material should inadvertently contact the skin or clothing, wash immediately with alcohol and then rinse thoroughly with water.
- *(3) BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.
- (4) DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, ambient temperature, etc., otherwise LCD module may be damaged.
- (5) Suitable protection circuit should be applied for each system design.

DO NOT MODIFY the fuse used in the module. It may cause overheat and/or burning if dusts or metal particles are on the PCBs in the LCD module.

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(6) Be sure that power supply output from the system should be limited to smaller values than listed shown below. (For example Quick Arcing Fuse with listed ratings can be used.)

It is because this LCD module explained in this specification has a current limiter, with such function at power input line(s). But it may be some possibility of overheat and/or burning of LCD module and its peripheral devices before current limiter of the module when open-short test of the module is performed by using power supply higher than following recommended value.

Power supply	Recommended maximum output current of power supply	Recommended Fuse Rating (in case of using fuse for current limiter)	Built-in Fuse Rating (for reference)
V_{DD}	4.0 A	<u>1.25</u> A	<u>1.25</u> A

(7) Always comply with all applicable environmental regulations, when disposing of LCD.

For Designing the System

- (1) LCD module should be assembled to the system by using all mounting holes specified in this specification and with the specified screws.
- (2) Power supply lines should be designed as follows.
 - Power supplies should always be turned on before the input signals are supplied to LCD module, and the input signals should be disconnected before power supplies are turned off.
 - If the sequence does not satisfy specified conditions, it may cause miss-operation of the panel.
 - Refer to "2.4.2 Sequence of Power Supplies and Signals" for the detailed specification.
- (3) DO NOT GIVE high voltage to "Low Voltage" side of the FL.
 - For example, DO NOT USE a floating inverter which gives high voltage to "Low Voltage" side. That's because it has a possibility to burn or smoke around the FL.
- (4) Make sure to connect correctly high-voltage wire and low-voltage wire between FL tube and inverter unit.
- (5) Input FL starting voltage(V_{SFL}) should not be less than one second.
 - If it were less than one second, it may cause unstable operation of FL.
 - Please adjust inverter circuit parameters, such as capacitor, resistor, to assure the display quality is maintained.
 - There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency).
- (6) In case of severe environmental condition like outdoor usage, a proper transparent protective cover(lens) over LCD module is recommended to apply in order to prevent scratches, and invasion of dust, water, etc., from the system's window onto LCD module.
 - Ultra-violet ray cut filter is recommended to apply onto LCD module for outdoor operation. Strong ultra-violet ray may cause damage the panel.
- (7) Design the system not to display same pattern for a long time in order to prevent image sticking on the panel. Note that incorrect sequence of power supplies and input signals may cause the sticking on the panel, too.

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For Installation in Assembly

- (1) The C-MOS LSIs used in LCD module are very sensitive to ESD (Electro-static Discharge).
 - Ambient humidity of working area is recommended to be higher than 50%(RH).
 - Person handling LCD modules should be grounded with wrist band. Tools like soldering iron and screw driver, and working benches should be grounded.
 - The grounding should be done through a resistor of 0.5- $1M\Omega$ in order to prevent spark of ESD.
- (2) When remove protection film from LCD panel, peer off the film slowly (more than three seconds) from the edge of the panel, using a soft-pointed tweezers covered by teflon or adherent tape.
- (3) Reduce dust level in working area. Especially the level of metal particle should be decreased.
 - Use finger stalls or soft and dust-free gloves in order to keep clean appearance of LCD module when handled for incoming inspection and assembly.
- *(4) When LCD panel becomes dirty, wipe off the panel surface softly with absorbent cotton or another soft cloth.
 - If necessary, breathe upon the panel surface and then wipe off immediately and softly again.
 - If the dirt can not be wiped off, absorbent cotton wetted a little with normal-hexane or petroleum benzine can be used for wiping the panel.
 - Be careful not to spill this solvent into the inside of LCD module. Driver ICs and PCB area used inside LCD module may be damaged by the solvent.
- *(5) AVOID THE CONDENSATION OF WATER
 - Wipe off a spot or spots of water of mist and chemicals of mist on LCD panel softly with absorbent cotton or another cloth as soon as possible if happened, otherwise discoloration or stain may be caused. If water invade into LCD module, it may cause LCD module damages.
- *(6) Do not expose LCD module to the gas (which is not normally contained in the atmosphere), it may cause mis-operation or defects.
- *(7) DO NOT APPLY MECHANICAL FORCES.
 - Do not bend or twist LCD module even momentary when LCD module is installed an enclosure of the system. Bending or twisting LCD module may cause its damages.
 - Make sure to design the enclosure that bending/twisting forces are not applied to LCD module when it is installed in the system.
 - Refrain from strong mechanical shock like dropping from the working bench or knocking against hard object.
 - These may cause glass of the panel crack, damage of FL or other mis-operation.
- *(8) Refrain from excessive force like pushing the surface of LCD panel. This may cause damage of the panel or electrical parts on PCB.
- *(9) Do not put heavy object such as tools, books, etc., and do not pile up LCD modules.
 - Be careful not to touch surface of the polarizer laminated to the panel with any hard and sharp object. The polarizer is so soft that it can easily scratched, even the protect film covers it.
- (10) When inserting or disconnecting the connectors to LCD module, be sure not to apply force against PCB, nor connecting cables, otherwise internal connection of PCB and TAB drivers may be damaged.
 - Do not fasten screws while putting cables like those for interface or FL between LCD module and the enclosure.
 - Make sure to insert the module FL connector to the inverter connector in correct position.
 - If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.

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(11) Be careful not to pull the FL cables of the backlight in order to avoid mechanical damage in FL lamp and soldering area.

Be careful not to pull or not to hurt the FPC (Flexible Printed Circuit) cables.

- (12) Power supplies should always be turned off in assembling process.
 - Do not connect or disconnect the power cables and connectors with power applied to LCD module. This may cause damage of module circuit.
 - The signal should be applied after power are turned on. And the signal should be removed before power supplies are turned off. (Refer to "For Designing The System"(2).)
- (13) In case of LCD long period operation, discoloration of light guide or optical sheet will be happened due to ultra violet and heat from CCFL. As the result, there is possibility to have out of specification for the optical characteristics as "5.2".
 But this is a not irregular phenomenon. Moreover, CCFL also has the characteristic of color shift by long period operation.

For Transportation and Storage

- (1) Do not store LCD module in high temperature, especially in high humidity for a long time (approximately more than one month).
 - It is recommended to store LCD module where the temperature is in the range of 0 to 35 °C and the relative humidity is lower than 70%.
- (2) Store LCD module without exposure to direct sunlight or fluorescent lamps in order to prevent the module from strong ultra violet ray.
- *(3) Avoid condensation of water on LCD module, otherwise it may cause mis-operation or defects. Keep away LCD module from such ambient.
- (4) In case of transportation of storage after opening the original packing. LCD module are recommended to be repacked into the original packaging with the same method, especially with same kind of desiccant.

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1. Scope

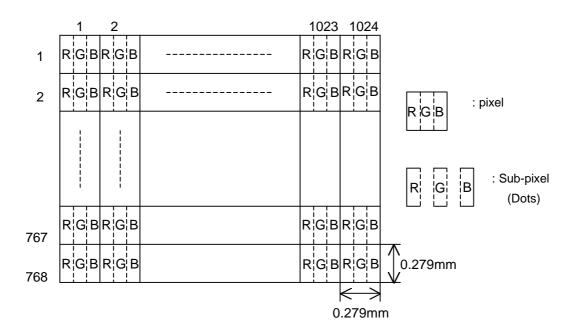
This specification is applicable to Toshiba's 35.8cm diagonal size TFT-LCD module "LTD141LA2S" designed for Personal Computer.

2. Product Specifications

2.1 General Specifications

Item	Specifications			
Display Mode	TN color(64 gray scales, 262,144 colors)			
	Transmissive type, Normally white			
Viewing Direction	6 o'clock (in direction of maximum contrast)			
Driving Method	TFT active matrix			
Input Signals	LVDS interface			
	RxOCLK+, RxOCLK-, RxOIN0+, RxOIN0-, RxOIN1+, RxOIN1-,			
	RxOIN2+, RxOIN2-			
Active Area	285.7 (W) × 214.3 (H) (mm)			
Bezel Opening	289.4 (W) × 218 (H) (mm)			
Number of Pixels	$1024 \ (W) \times 768 \ (H)^{-1}$			
Pixel Pitch	$0.279 (W) \times 0.279 (H) (mm)^{1}$			
Pixel Arrangement	RGB vertical stripes 1)			
Surface Treatment	Anti-Reflection and hard coat 3H on LCD surface			
Backlight	Single cold-cathode fluorescent lamp for sidelighting			
Dimensional Outline	299.5 (W) × 227.8 (H) × 5.5max. (D) (mm) (SPWG Style-B)			

Note 1)

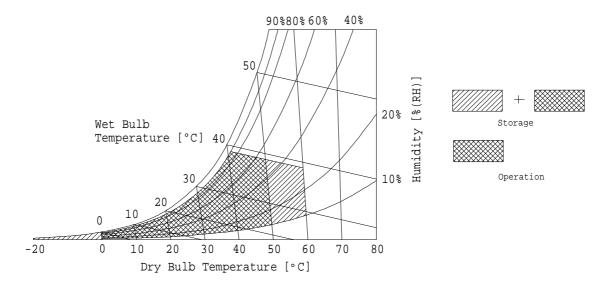


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2.2 Absolute Maximum Ratings 1)

Item	Symbol	Min.	Max.	Unit	Checked Terminal
Supply Voltage	$V_{ m DD}$	-0.3	+4.0	V	V _{DD} - GND
Input Voltage of Signals	V_{IN}	-0.3	V _{DD} +0.3	V	LVDS interface
Operating Ambient Temperature 2)	T_{OP}	0	50	°C	
Operating Ambient Humidity 2)	HOP	10	90	%(RH)	
Storage Temperature 2)	$T_{ m STG}$	-20	+60	°C	
Storage Humidity 2)	H_{STG}	10	90	%(RH)	
Operating Temperature for Panel 3)	-	0	+60	°C	

- Note 1) Do not exceed the maximum rating values under the worst probable conditions taking into account the supply voltage variation, input voltage variation, variation in part constants, and ambient temperature and so on. Otherwise the module may be damaged.
- Note 2) Wet bulb temperature should be 39°C Max, and no condensation of water. See figure below.
- Note 3) The surface temperature caused by self heat radiation of cell itself is specified on this item.
- Note 4) Refer to 2.4.5



2.3 Mechanical Specifications

2.3.1 Weight

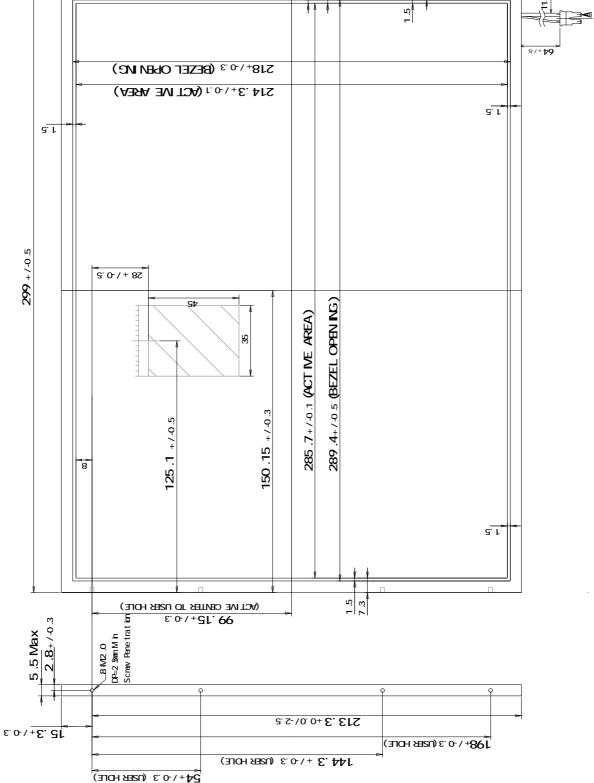
LCD Module : 440g (Max.)

Unit: mm

2.3.2 Dimensional Outline

(front figure)

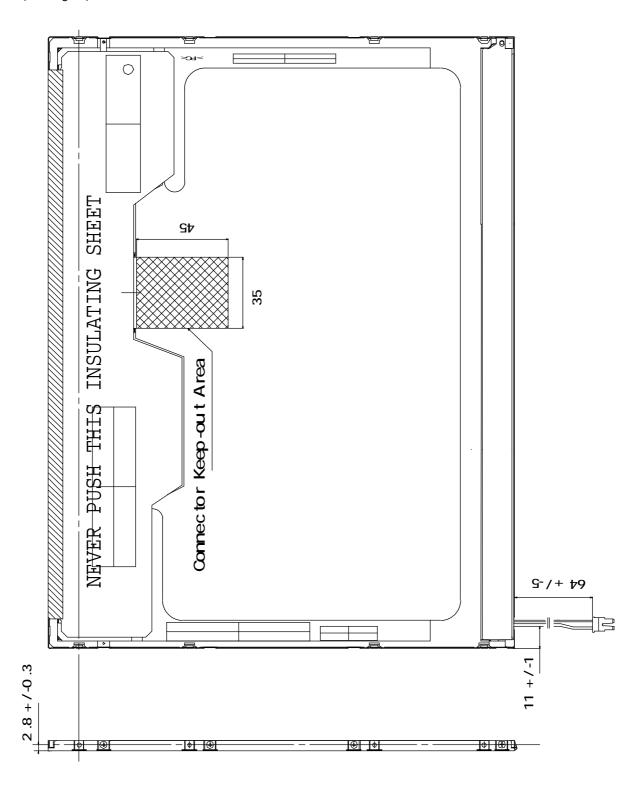
Standard Tolerance: ±0.5 SPWG Style B Dimension 8. **TSS**



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 $\mbox{Unit:mm} \\ \mbox{Standard Tolerance:} \pm 0.5 \\ \mbox{}$

(back figure)

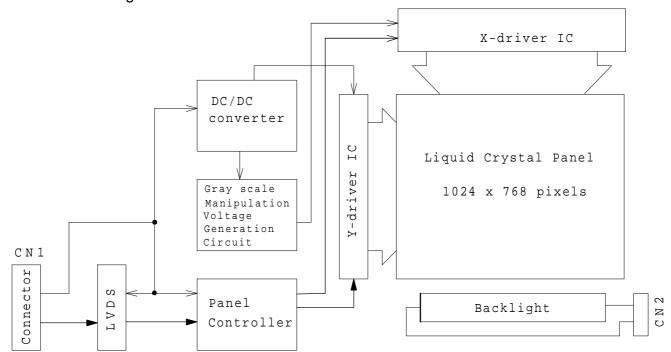


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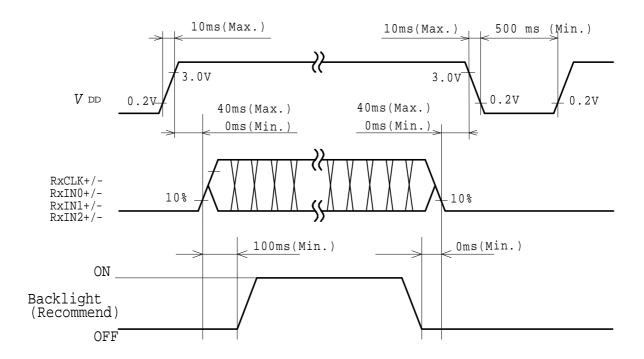
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2.4 Electrical Specifications

2.4.1 Circuit Diagram



2.4.2 Sequence of Power Supplies and Signals

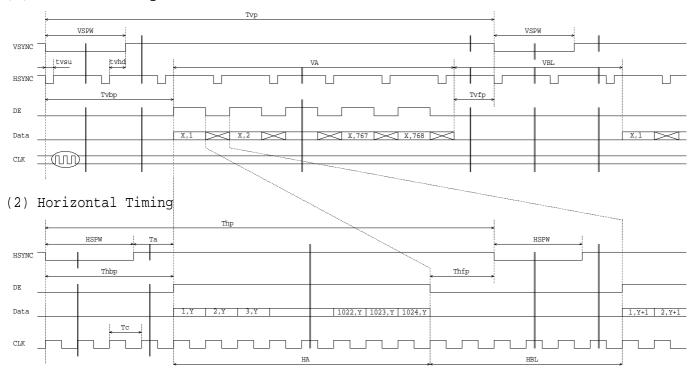


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2.4.3 Timing Chart

(1) Vertical Timing



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2.4.4 Timing Specifications 1) 2) 3) 4) 5) 6) 7) 8)

Item	Symbol	min.	typ.	max.	unit
Horizontal Scanning Term	<i>T</i> hp	1319	1344	1462	Tc
		-	20.68	-	us
H-sync Pulse Width	HSPW	12	-	140	Tc
Horizontal Front Porch	<i>t</i> hfp	4	-	136	Tc
Horizontal Back Porch	ta	4	-	136	Tc
Horizontal Blanking Term	HBL	295	320	438	Tc
Horizontal Display Term	HA	1024	1024	1024	Tc
Frame Period	<i>T</i> vp	778	806	860	<i>T</i> hp
		•	16.67	17.24	ms
V-sync Pulse Width	VSPW	2	-	7	<i>T</i> hp
V-sync Set Up Time (to H-sync)	<i>t</i> vsu	8	-	14	Tc
V-sync Hold Time	<i>t</i> vhd	8	-	-	-
Vertical Front Porch	<i>t</i> vfp	2	-	8	-
Vertical Back Porch	<i>T</i> vbp	8	-	14	-
Vertical Blanking Term	VBL	10	38	92	<i>T</i> hp
Vertical Display Term	VA	768	768	768	<i>T</i> hp
DE Pulse Width	HA	1024	1024	1024	Tc
Clock Period	Tc	15.000	15.384	-	ns

Note 1) Refer to "Timing Chart" and LVDS (THC63LVDF84A) specifications by THine Electronic, Inc..

Note 2) If ENAB is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.

Note 3) If NCLK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.

Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.

Note5) Do not make tv, tvdh and tvds fluctuate.

If tv, tvdh, and tvds are fluctuate, the panel displays black.

Note6) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note7) NCLK count of each Horizontal Scanning Time should be always the same.

V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer)

Frame period should be always the same.

Note 8) Please keep below equations.

VBL = Tvfp + Tvbp

HSPW = HBL - Thfp - ta

Thbp = HSPW + ta

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2.4.5 Interface Connector

CN1 INPUT SIGNAL

Connector: FI-XB30S*-HF** or FI-X30S-HF** / JAE

Mating Connector; Wire Type: FI-X30H(Housing), FI-XC3-1-15000(Contact)

FPC Type: FI-X30M or FI-X30MR,

Coax Type: FI-X30C or FI-X30C2(Housing), FI-X30CH-7000(Shell)

Terminal No.	Symbol	Function
1	V _{SS}	GND
2	$V_{ m DD}$	POWER SUPPLY : +3.3V
3	V_{DD}	POWER SUPPLY : +3.3V
4	NC	Non-Connection
5	NC	Non-Connection
6	NC	Non-Connection
7	NC	Non-Connection
8	RxIN0-	Negative LVDS differential data input, [R0-R5, G0]
9	RxIN0+	Positive LVDS differential data input, [R0-R5, G0]
10	$V_{\rm SS}$	GND
11	RxIN1-	Negative LVDS differential data input, [G1-G5, B0-B1]
12	RxIN1+	Positive LVDS differential data input, [G1-G5, B0-B1]
13	$V_{\rm SS}$	GND
14	RxIN2-	Negative LVDS differential data input, [B2-B5, HS, VS, DE]
15	RxIN2+	Positive LVDS differential data input, [B2-B5, HS, VS, DE]
16	V _{SS}	GND
17	RxCLKIN-	Negative LVDS differential clock input
18	RxCLKIN+	Positive LVDS differential clock input
19	V _{SS}	GND
20	RxIN0-	NC
21	RxIN0+	NC
22	V _{SS}	GND
23	RxIN1-	NC
24	RxIN1+	NC
25	V _{SS}	GND
26	RxIN2-	NC
27	RxIN2+	NC
28	V _{SS}	GND
29	RxCLKIN-	NC
30	RxCLKIN+	NC

Note 1) Please connect NC pin to nothing. Don't connect it to grand nor to other signal input.

Please connect GND to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) 262,144 colors are displayed by the combinations of 18 bits data. (See next page)

CN2 INVERTER INPUT SIGNAL

Connector: BHSR-02VS-1/JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Mating Connector: SM02B-BHS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Terminal No.	Symbol	Function
1	V FLH	CCFL Power Supply (high voltage)
2	V FLL	CCFL Power Supply (low voltage)

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RECOMMENDED TRANSMITTER (DS90CF363) TO LTD141LA2S INTERFACE ASSIGNMENT

Case1 : 6bit Transmitter

Input Terminal No.		(Gr	Input Signal aphics controller output signal)	Output Signal	_	D141LA2S ace(CN1)
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol
TIN0	44	R0	Red Pixels Display Data (LSB)			,
TIN1	45	R1	Red Pixels Display Data			
TIN2	47	R2	Red Pixels Display Data	TOUT0-	No. 8	RxIN0-
TIN3	48	R3	Red Pixels Display Data	TOUT0+	No. 9	RxIN0+
TIN4	1	R4	Red Pixels Display Data			
TIN5	3	R5	Red Pixels Display Data (MSB)			
TIN6	4	G0	Green Pixels Display Data (LSB)			
TIN7	6	G1	Green Pixels Display Data			RxIN1- RxIN1+
TIN8	7	G2	Green Pixels Display Data		No.11 No.12	
TIN9	9	G3	Green Pixels Display Data	TOUT1-		
TIN10	10	G4	Green Pixels Display Data	TOUT1+		
TIN11	12	G5	Green Pixels Display Data (MSB)			
TIN12	13	B0	Blue Pixels Display Data (LSB)			I
TIN13	15	B1	Blue Pixels Display Data			
TIN14	16	B2	Blue Pixels Display Data			
TIN15	18	В3	Blue Pixels Display Data			
TIN16	19	B4	Blue Pixels Display Data	TOUT2-	No.14	RxIN2-
TIN17	20	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.15	RxIN2+
TIN18	22	HSYNC	Horizontal Synchronized Signal			
TIN19	23	VSYNC	Vertical Synchronized Signal			
TIN20	25	DE	Data Enable			
TCLK IN	26	NCLK	Data Sampling Clock	TCLK OUT-	No.17	RxCLKIN -
				TCLK OUT+	No.18	RxCLKIN +

RXOINO TIN6	TIN5	TIN4	TIN3	TIN2	TIN1	TINO
G0	R5	R4	R3	R2	R1	R0
RXOIN1 TIN13	TIN12	X TIN11	TIN10	TIN9	X TIN8	TIN7
B1	В0	G5	G4	G3	G2	G1
RxOIN2 TIN20	TIN19	X TIN18	TIN17	X TIN16	TIN15	TIN14
DE	VSYNC	HSYNC	B5	В4	В3	B2

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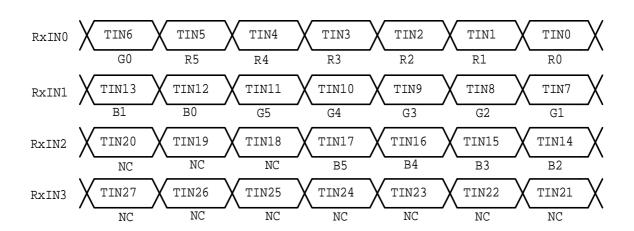
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RECOMMENDED TRANSMITTER (DS90CF383) TO LTD141LA2S INTERFACE ASSIGNMENT

Case2: 8bit Transmitter

Input Terminal No.		(Gr	Input Signal aphics controller output signal)	Output Signal		To LTD141LA2S Interface(CN1)	
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol	
TIN0	51	R0	Red Pixels Display Data (LSB)			- ,	
TIN1	52	R1	Red Pixels Display Data				
TIN2	54	R2	Red Pixels Display Data	T1OUT0-	No.8	RxIN0-	
TIN3	55	R3	Red Pixels Display Data	T1OUT0+	No.9	RxIN0+	
TIN4	56	R4	Red Pixels Display Data				
TIN5	3	R5	Red Pixels Display Data (MSB)				
TIN6	4	G0	Green Pixels Display Data (LSB)				
TIN7	6	G1	Green Pixels Display Data				
TIN8	7	G2	Green Pixels Display Data				
TIN9	11	G3	Green Pixels Display Data	T1OUT1-	No.11	RxIN1-	
TIN10	12	G4	Green Pixels Display Data	T1OUT1+	No.12	RxIN1+	
TIN11	14	G5	Green Pixels Display Data (MSB)				
TIN12	15	B0	Blue Pixels Display Data (LSB)				
TIN13	19	B1	Blue Pixels Display Data				
TIN14	20	B2	Blue Pixels Display Data				
TIN15	22	B3	Blue Pixels Display Data				
TIN16	23	B4	Blue Pixels Display Data	T1OUT2-	No.14	RxIN2- RxIN2+	
TIN17	24	B5	Blue Pixels Display Data (MSB)	T1OUT2+	No.15		
TIN18	27	HSYNC	Horizontal Synchronized Signal				
TIN19	28	VSYNC	Vertical Synchronized Signal				
TIN20	30	DE	Data Enable				
TIN21	50	NC	Non Connection (open)				
TIN22	2	NC	Non Connection (open)				
TIN23	8	NC	Non Connection (open)	T1OUT3-	-	-	
TIN24	10	NC	Non Connection (open)	T1OUT3+	-	-	
TIN25	16	NC	Non Connection (open)				
TIN26	18	NC	Non Connection (open)				
TIN27	25	NC	Non Connection (open)				
TCLK IN	31	NCLK	Data Sampling Clock	T1CLK OUT- T1CLK OUT+	No.17 No.18	RxCLKIN - RxCLKIN +	



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2.4.6 Colors Combination Table

	Display	R5 R4 R3 R2 R1 R0 G5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0	Gray ScaleLevel
	Black		-
	Blue		-
	Green		-
Basic	Light Blue		-
Color	Red	H H H H H H L L L L L L L L L L L L L	-
	Purple	H H H H H H L L L L L H H H H H H	-
	Yellow	H H H H H H H H H H H L L L L L L	-
	White	<u> </u>	-
	Black		L 0
_			L 1
Gray	Dark		L 2
Scale of	<u> </u>		L3
Red	\downarrow		L60
	Light	<u> </u>	L61
		H H H H H L L L L L L L L L L L L L	L62
	Red	H H H H H H L L L L L L L L L L L L L L	Red L63
	Black		L O
			L 1
Gray	Dark		L 2
Scale of	\uparrow	: : : :	L3
Green	\downarrow		L60
	Light		L61
			L62
	Green		Green L63
	Black		L 0
	Didok		L 1
Gray	Dark		L 2
Scale of	1		L3
Blue	\downarrow		L60
	Light		
	· ·		L61 L62
	Blue		Blue L63
	Black		L 0
	DIACK		L 0
Gray	Dark		L 2
Scale of	Daik ↑		L3
White &	<u> </u>		L3 L60
Black	↓ Light	·	
2.301	Ligit	<u> </u>	L61
		<u> </u>	L62
	White	<u> </u>	White L63

Note1 L: Low level voltage, H: High level voltage

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3. Recommended Operating Conditions 1) 2) 3) 10)

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks
Supply Voltage 4)	$V_{ m DD}$	3.0	3.3	3.6	V	
Differential Input Voltage 2) 4)	V_{ID}	0.1	-	0.6	V	
Common Mode Input Voltage 5)	V _{CM}	1.0	-	2.4 -1/2x V _{ID}	V	
FL Input Current 6) 7) 8)	<i>I</i> _{FL}	2.0	6.0	6.5	mA(rms)	
FL Driving Voltage 6)	V_{FL}	630	680	730	V(rms)	I_{FL} =6.0mA(rms)
FL Driving Frequency 6) 10)	f_{FL}	40	50	60	kHz	
FL Starting Voltage 6) 9)	$V_{ m SFL}$	1500	-	1800	V(rms)	0°C

- Note 1) The module should be always operated within these ranges. The "Typ." shows the recommendable value.
- Note 2) Recommended LVDS transmitter: THC63LVDM83A (made by Thine Electronic, Inc.)

 LVDS receiver included in this module is THC63LVDF84A (made by Thine Electronic, Inc.).

 Refer to LVDS specifications.
- Note 3) Checked Pin Terminal: V_{DD}, GND (0V)
- Note 4) Checked Pin Terminal: IN0+/-,IN1+/-,IN2+/-,CLK+/-, GND (0V)

Measure: $|V_{IN0+} - V_{IN0-}|, |V_{IN1+} - V_{IN1-}|,$ $|V_{IN2+} - V_{IN2-}|, |V_{CLK+} - V_{CLK-}|$

Note 5) Checked Pin Terminal: IN0+/-,IN1+/-,IN2+/-,CLK+/-, GND (0V)

Measure: $1/2 \times (V_{IN0+} + V_{IN0-}), 1/2 \times (V_{IN1+} + V_{IN1-}),$

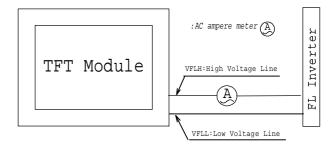
$$1/2 \times (V_{IN2+} + V_{IN2-}), 1/2 \times (V_{CLK+} + V_{CLK-})$$

- Note 6) Checked Pin Terminal: V_{FLH} V_{FLL}
- Note 7) If FL input current (I_{FL}) is higher than typical value(6.0mA(rms)), then FL lifetime becomes shorter.
- Note 8) Measuring Method of IFL.
- Note 9) Input FL starting voltage ($V_{\rm SFL}$) should not be less than one second.

If it were less than one second, it may cause unstable operation of FL.

Note 10) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above recommended operating conditions and timing specifications shown in 2.4.4.



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4. Electrical Characteristics

4.1 Test Conditions

Ambient Temperature : T_a 25±5°C Ambient Humidity : H_a 65±20%(RH)

Supply Voltage : V_{DD} 3.3V

Input Signal : Refer typical value in "2.4.4 Timing Specifications".

FL Input Current : I_{FL} =6.0mA(rms) FL Driving Frequency : f_{FL} =50kHz

4.2 Specifications

Item	Symbol	Min.	Typ. ¹⁾	Max.	Unit	Remark
Current Consumption	I _{DD}	-	210	260	mA	

Note 1) The Typical value of I_{DD} is measured in the following pattern.

1. White

2. Yellow

3. Purple

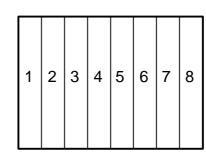
4. Red

5. Light Blue

6. Green

7. Blue

8. Black



5. Optical Characteristics

5.1 Test Conditions

It is same as 4.1

The measuring method is shown in 11.

5.2 Optical Specifications 1)

Item Sym		Symbol	Conditions		Specifica	Specifications			Remark
					MIn.	Тур	Max.		
Viewing Angle)	θ	<i>CR</i> >=10	φ = 180°	10	-	-	0	
				φ = 0°	30	-	-	0	
				φ = 90°	40	-	-	0	
				ф= -90°	40	-	-	0	
Contrast Ratio)	CR	θ =0°, φ=0°		300	-	-	-	
Response Tim	ne	$t_{\rm ON} + t_{\rm OFF}$	θ =0°, φ=0°		-	-	35	ms	
Luminance	Luminance		θ =0°, ϕ =0° Gray Scale		150	170	-	cd/m ²	I_{FL} =6.0mA(rms)
			Level=L63 (W	hite)					
Chromaticity	Red	X R	Gray Scale Le	vel:L63	0.54	0.60	0.66	-	
		y _R	θ =0°, ϕ =0°		0.27	0.33	0.39	-	
	Green	X G	Ditto		0.24	0.30	0.36	-	
		<i>y</i> _G			0.48	0.54	0.60	-	
	Blue	X B	Ditto		0.09	0.15	0.21	-	
		<i>y</i> B			0.07	0.13	0.19	-	
	White	\boldsymbol{x}_{W}	Ditto		0.25	0.31	0.37	-	
		Уw			0.27	0.33	0.39	-	

Note 1): Refer to "11. Measuring Method".

Note 2): The above test limit must be applied for initial use. Characteristics will be shifted by long period operation, but it is not irregular phenomena. Theoretically brightness characteristics will be decreased due to CCFL degradation and color shift due to optical components change.

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6.Quality

6.1 Inspection AQL

Total of Major Defects : AQL 0.65 % Total of Minor Defects : AQL 1.5 %

Sampling Method : ANSI/ ASQC Z1.4 (level II)

6.2 Test Conditions

1) Ambient Temperature : 25±5°C 2) Ambient Humidity : 65±20%(RH)

3) Illumination : Approximately 500 lx under the fluorescent lamp

4) Viewing Distance : Approximately 0.3m by the eyes of the inspector from the module

5) Inspection Angle : $\theta=0^{\circ}$, $\phi=0^{\circ}$

6.3 Dimensional Outline

The products shall conform to the dimensions specified in 2.3.2.

Definition of Major and Minor defects are as follows.

Item	Description	Class
Important Dimensions	ns Dimensional outline, Dimensional between N	
	the mounting holes(hinge)	
Others	Dimensions specified in this specifications	Minor

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6.4 Appearance Test

6.4.1 Test Conditions

1) Condition : Non-operating, operating (Pattern : L63 white raster) Same as 6.2

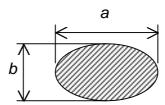
6.4.2 Specifications

Item		Description							
PCB Appearance	Patte	ern peeling snapping, ele		Major					
	Repa	Repair portion on PCB is not covered by epoxy resign							
Soldering	Cold	solder joint, lead move	when pulled			Minor			
Bezel, Frame,	Disti	nct stain, rust or scratch	•			Major			
Connectors									
Black and White						Minor			
Spots/Lines ¹⁾²⁾		Line Width(mm)	Length(mm)	Acceptable count				
•		<i>W</i> <=0.10			neglect				
		0.10< <i>W</i> <=0.15		ın	<i>n</i> <=8				
		0.15< <i>W</i> <=0.20	L<=10		<i>n</i> <=2				
		0.20< <i>W</i>	-		2)				
		Average diameter	(mm)	Aco	ceptable count/side				
		D<=0.20			neglect				
		0.20< <i>D</i> <=0.5	50		<i>n</i> <=5				
	0.50<		1.50		n<=2				
		1.50< <i>D</i>			0				
		0.50< <i>D</i> <=1.5 1.50< <i>D</i>	U						

Note 1) Inspection area should be within active area.

Note 2) Dusts which are bigger not less than 0.20mm (0.2 < W) shall be judged by "Average Diameter".

Average Diameter D = (a+b)/2 (mm)



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6.5 Display Quality

6.5.1 Test Conditions

1) Inspection Area : Within active area

2) Driving Condition: Same as test conditions shown in 4.1 and 6.2

3) Test Pattern : White display pattern (gray scale level L63) and black display pattern (gray scale level L0)

red display pattern (gray scale level L63), green display pattern (gray scale level L63) and

blue display pattern (gray scale level L63)

6.5.2 Specifications 4)

Item	Description / Specifications	Class
Function	No display, Malfunction	Major
Display Quality 1)2)3)	Missing line	Major
	Missing Sub-Pixels	Minor
	1) Bright defects : 15pcs. maximum	
	2) Dark defects : 15pcs. maximum	
	3) Total sub-pixel defects : 20pcs. maximum	
	Various uniformity (mura) : neglect	-
	Inconspicuous flicker, crosstalk, Newton's ring and other defects : neglect	-
Black and White	Inconspicuous defects : neglect	-
Spots/line		
Backlight	Missing (Non-operating)	Major

Note 1) Defects of both color filter and black matrix are counted as bright or dark defects.

Inspection area should be within the active area.

Note 2) Bright defect means a bright spot(sub-pixel) on the display pattern of gray scale L0.

Dark defect means a dark spot(sub-pixel) on the display pattern of gray scale L63.

Note 3) Bright spot which can not be found by using 5%ND-Filter shall not be counted as a defect.

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6.6 Reliability Test

6.6.1 Test Conditions

- 1) The module should be driven and inspected under normal test conditions.
- 2) The module should not have condensation of water (moisture) on the module.
- 3) The module should be inspected after two or more hours storage in normal conditions (15 35°C,45 65%(RH)).
- 4) A module shall be used only for one test.

6.6.2 Specifications

The module shall have no failure in the following reliability test items.

Test Item	Test Conditions	Result
High Temperature Operation 1)	50°C 192 h	3p/3p OK
High Temperature Storage 2)	60°C 192 h	3p/3p OK
High Temperature and	50°C 80% 192 h	3p/3p OK
High Humidity operation 1)		
Low Temperature Operation 1)	0°C 192 h	3p/3p OK
Low Temperature Storage 2)	-20°C 192 h	3p/3p OK
Temperature Shock 2)	-20°C ⇔ 60°C	3p/3p OK
	0.5h 0.5h	
	50 cycles	
Mechanical Vibration 2)	10 - 200 - 10Hz sweep/cycle,	3p/3p OK
	1.5×9.8m/s ² constant,	
	X.Y.Z each direction, 0.5h each	
Mechanical Shock 2)	50×9.8m/s ² , 20ms,	3p/3p OK
	±X, ±Y, ±Z each direction,	
	one time each	

Note 1) Operating

Note 2) Non-Operating

Definitions of failure for judgment shall be as follows:

- 1) Function of the module should be maintained.
- 2) Current consumption should be smaller than the specified value.
- 3) Appearance and display quality should not have distinguished degradation.
- 4) Luminance should be larger than 50% of the minimum value specified in 5.2.

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MADE IN JAPAN

LTD141LA2S

Rev. Z

6.7 Labels

(1) Product Label

Serial number : $\triangle \triangle$ \triangleq 3A Z 00001

① ② ③ ④ ⊗

①: Module type code

②: Manufacturing code

C, K, R

③: Lot code 3 A

(1) (2)

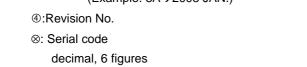
(1):Year code-end of the A.D.

(2):Month code-alphabet → Jan. : A - Dec. : L

(Example: 3A→2003 JAN.)

Bar code: CODE-39 High-density

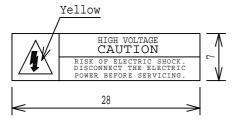
•There are two type of label according to country of origin.

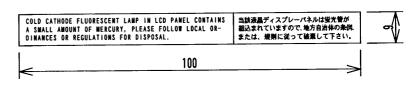


(2) Caution Labels

• High Voltage

Disposal of CCFL





Unit: mm

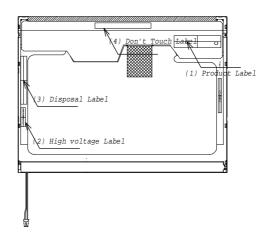
(3) Label Locations

(1): Product Label

(2): High Voltage Label

(3): Disposal Label

(4): CAUTION Label(Don't Touch)



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7. Lifetime

7.1 Module (except lamp)

MTTF (Mean Time To Failure): 50,000 h

(This value is not assurance time but inference value by following conditions.)

Conditions : Ambient temperature : 25±5°C (No wind)

Ambient humidity : 65%(RH)

7.2 Lamp

7.2.1 Test Conditions

Ambient temperature : 25±5°C (No wind)

Lamp current : 6.0mA(rms)

Lighting condition : continuous lighting

Driving frequency : 50kHz

7.2.2 Specifications

MTBF: 10,000 h

Definitions of failure for judgment shall be as follows.

- 1) LCD luminance becomes half of the minimum value specified in 5.2.
- 2) Lamp doesn't light normally.

Note 1): In case of LCD long period operation, discoloration of light guide or optical sheet will be happened due to ultra violet and heat from CCFL. As the result, there is possibility to have out of specification for the optical characteristics as "5.2".

But this is a not irregular phenomena. Moreover, CCFL also has the characteristic of color shift by long period operation.

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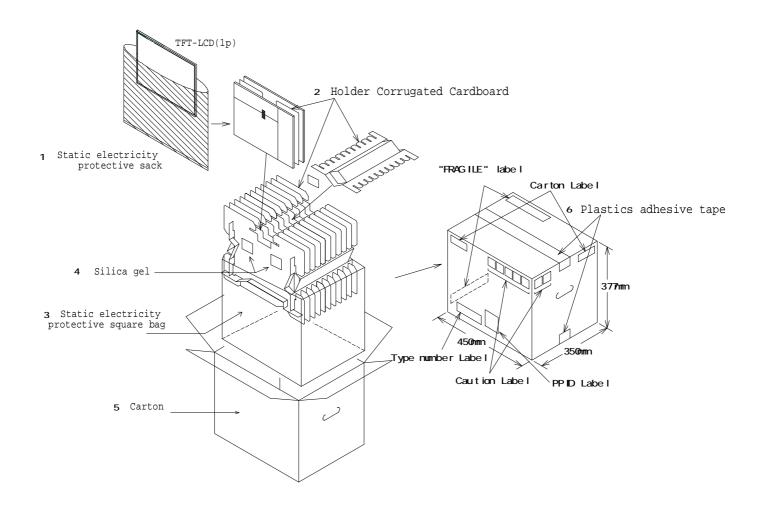
8. Packaging

8.1 Carton (internal package)

(1) Packaging Form

Corrugated cardboard box

(2) Packaging Method



Note 1) Total weight : (Approx.) 9.5 kg

Note 2) Acceptable number of piling: 8 sets

(3) Packaging Material

Number	Quantity	Description
1	20	Static electricity protective sack
2	1 set	Holder (inner box)
3	1	Static electric protective square bag
4	3	Silicagel (50g x 3)
5	1 set	Carton
6		Plastic adhesive tape

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9. Warranty

Finish of warranty term is arrival at your factory. (except defect which is clearly responsible for Toshiba Matsushita Display Technology)

10. Regulation

The set (which our LCD module is assembled into) to conform the regulations below, take measures in set side. Toshiba Matsushita Display Technology is not liable for the regulations to the complete set, nor can guarantee our LCD module conform the regulation by itself.

a) Examples of EMI Regulations

FCC : PART15 CLASS B

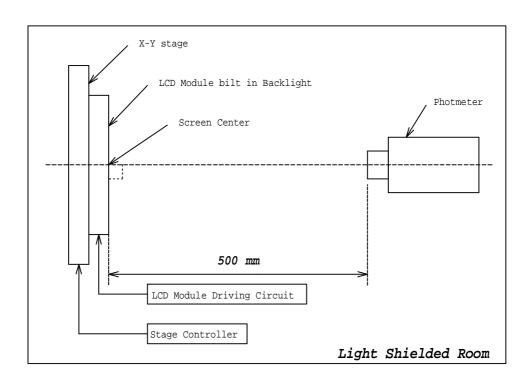
VCCI : CLASS B CISPR : CLASS B

b) Examples of Safety Regulations

IEC 950 UL 1950

11. Measuring Method

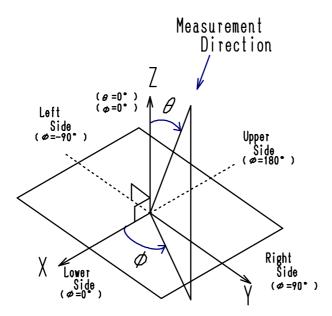
11.1 Measuring System



- (1) The measurement point is the center of the active area except for the measurement of Luminance Uniformity.
- (2) Photometer: BM-5A / BM-7 TOPCON (Aperture 2deg.)

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(3) Definition of ϕ and θ :

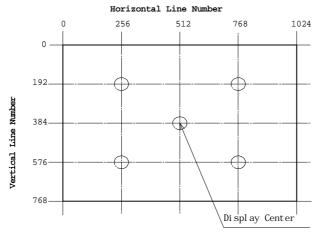


11.2 Measuring Methods

(1) Luminance:

The luminance of the center on a white raster (gray scale level L63) shall be measured.

Measurement shall be executed 30 minutes after the lamp is lit up.



(2) Contrast Ratio:

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

L63 : Luminance on the white raster (gray scale level L63)

L 0 : Luminance on the black raster (gray scale level L0)

(3) Viewing Angle

Viewing angle is defined as the angles(θ , ϕ), in which specified contrast ratio can be obtained. (Refer to 11.1(3) for the axes.)

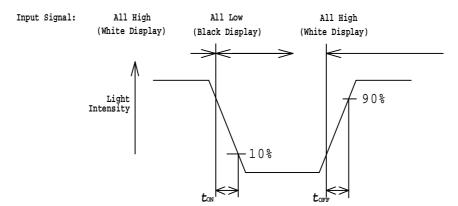
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(4) Chromaticity:

The values(x,y) of chromaticity coordinates should be measured for the White, Red, Green and Blue Raster(gray scale level L63) each with a photometer.

(5) Response Time:

The response time (t_{ON} , t_{OFF}) is measured with a photo detector (photodiode) which measures the light intensity of the pixels.



 t_{ON} : Turn on time is the time for a photo detector output waveform to go from maximum value to 10% of its maximum.

 t_{OFF} : Turn off time is the time for a photo detector output waveform to go from zero to 90% of its maximum.

Photodiode : S1223-01 HAMAMATSU PHOTONICS K.K.

White Display : White Raster (gray scale level L63) Black Display : Black Raster (gray scale level L0)

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