



Customer: DATE: 15.Feb.2006

**SAMSUNG TFT-LCD** 

**MODEL: LTI400W2-L01** 

Any Modification of Specification is not allowed without SEC's Permission.

NOTE:			

Customer's A	Customer's Approval		AP K
SIGNATURE	DATE		PF
			Jeoi

APPROVAED BY Kyunghuam Ko	DATE 15.Feb.2006
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Samsung Electronics Co., LTD.



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# \* Revision History

Date	Rev. No	Page	Summary
Feb 15, 2006	000	all	First issued

## **General Description**

### Description

LTI400W2-L01 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1366 x 768 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and High Definition TV (HDTV).

#### **Features**

- RoHS compliance (Pb-free)
- High contrast ratio, high aperture ratio, fast response time
- PVA(Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- Wide UXGA (1366 x 768 pixels) resolution (16:9)
- Low Power consumption
- Direct Type 20 CCFTs(Cold Cathode Fluorescent Tube)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

#### **General Information**

Items	Specification	Unit	Note
Module Size	952.0(H <sub>TYP</sub> ) x 551.0(V <sub>TYP</sub> )	mm	±1.0mm
Wioduic Oize	52.0(D <sub>MAX</sub> )	111111	
Weight	12,000(Max.)	g	
Pixel Pitch	0.648(H) x 0.216(W)*3	mm	
Active Display Area	885.168(H) x 497.664(V)	mm	
Surface Treatment	Haze 44%, Hard-coating (3H)		
Display Colors	8 bit - 16.7M	colors	
Number of Pixels	1366 x 768	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	500 (Typ.)	cd/m²	

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## 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note	
Power Suppl	$V_{DD}$	GND-0.5	6.5	V	(1)	
Storage temperature		T <sub>STG</sub>	-20	60		(2)
Glass surface Center		T <sub>OPR</sub>	0	50		(2) (5)
temperature (Operation)	T. Uniformity	Т	-	10		(2),(5)
Shock ( non - operating )		S <sub>nop</sub>	-	50	G	(3)
Vibration ( non	Vibration ( non - operating )			1.5	G	(4)

Note (1) Ta=  $25 \pm 2$  °C

- (2) Temperature and relative humidity range are shown in the figure below.
  - a. 90 % RH Max. (Ta 39 °C)
  - b. Maximum wet-bulb temperature at 39 °C or less. (Ta 39 °C)
  - c. No condensation
- (3) 11ms, sine wave, one time for ±X, ±Y, ±Z axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

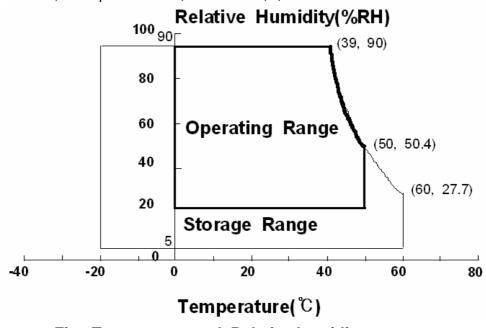
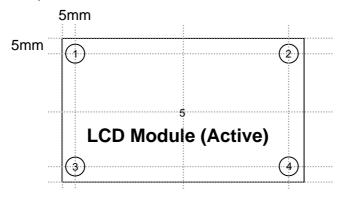


Fig. Temperature and Relative humidity range

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#### (5) Definition of test point



T should be less than 10  $(T = |T_{OPR} - T_{MAX}|)$ 

 $T_{OPR}$ : Temperature of the center of the glass surface (Test point 5)

T1 $\sim$  T4 : Temperature of each edge of the glass surface  $T_{MAX}$  : The highest temperature of the glass surface

## 2. Application information for I.D. (Information Display)

- Generally large-sized LCD modules are designed for TV applications. Accordingly, a long-term display like in PID application, can cause uneven display including image retention. To optimize module's lifetime and function, several operating usages are required.
- 1. Normal operating condition
- Temperature: 20  $\pm$  15
- Humidity: 65 ± 20 %
- Display pattern: dynamic pattern

Note) Long-term static display can cause image retention.

- 2. Operating usages under abnormal condition1)
  - a. Ambient condition
  - Well-ventilated place is recommended to set up ID system.
- b. Power and screen save
- Periodical power-off or screen save2) is needed after long-term display.
- 3. Operating usages to protect against image retention due to long-term static display
- a. Suitable operating time: under 18 hours a day.
- b. Static information display recommended to use with moving image.
- Cycling display between 5 minutes' information display and 10 seconds' moving image.
- c. Background and character (image) color change
- Use different colors for background and character, respectively.
- Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when ID is used according to operating usages.

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## 3. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent. Measuring equipment: TOPCON BM-7,SPECTRORADIOMETER SR-3

(Ta = 25  $\pm$  2°C, VDD=5V, fv= 60Hz,  $f_{DCLK}$ =80MHz,  $I_L$  = 6.0mArms)

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast I (Center of s		C/R		700	1000	-		(3) SR-3
_	Rising	Tr		-	8	12		4-1
Response Time	Falling	Tf		-	6	10	msec	(5) BM-7
	G-to-G	Tg		-	8	-		2111 7
Luminance of (Center of s		Y <sub>L</sub>	Normal θ <b>L,R</b> =0	400	500	-	cd/m <sup>2</sup>	(6) SR-3
	Dad	Rx	$\theta \mathbf{U}, \mathbf{D} = 0$		0.646			
	Red	Ry	Viewing		0.330			
	0	Gx	Angle	TYP. -0.03	0.280	TYP. +0.03		
Color	Green	Gy			0.601			(7),(8)
Chromaticity (CIE 1931)	Blue	Bx			0.144			SR-3
	Blue	Ву			0.060			
	White	Wx			0.280			
	vvriite	Wy			0.290			
Color Ga	mut	-		-	72	-	%	(7) SR-3
Color Temp	erature	-		-	10000	-	К	(7) SR-3
	Hor.	$\theta_{L}$		75	89	-		
Viewing	пот.	$\theta_{R}$	C/R 10	75	89	-	Dograd	(8)
Angle	Ver.	$\theta_{\sf U}$	C/K 10	75	89	-	Degree	SR-3
	ver.	$\theta_{D}$		75	89	-		
Brightness U (9 Poin		B <sub>uni</sub>		-	-	25	%	(4) SR-3

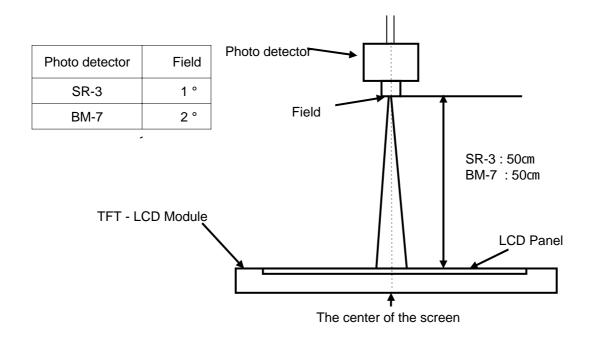
### Note (1) Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

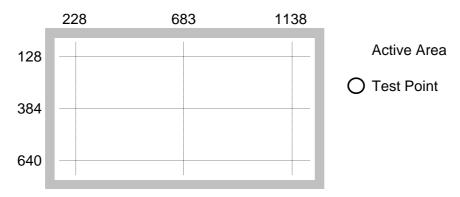
Single lamp current: 6.0mA

Environment condition: Ta = 25 ± 2 °C

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Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point of the panel

$$C/R = \frac{G \max}{G \min}$$

Gmax: Luminance with all pixels white Gmin: Luminance with all pixels black

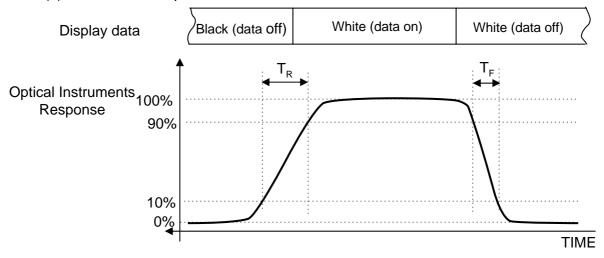
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Note (4) Definition of 9 points brightness uniformity

$$Buni = 100*\frac{(B \max - B \min)}{B \max}$$

Bmax : Maximum brightness Bmin : Minimum brightness

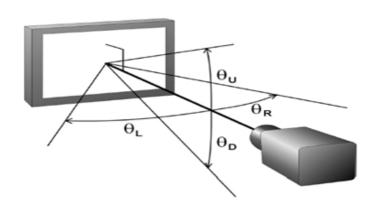
Note (5) Definition of Response time: Sum of Tr, Tf



Note (6) Definition of Luminance of White: Luminance of white at center point

Note (7) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point

Note (8) Definition of Viewing Angle : Viewing angle range (C/R 10)



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## 4. Electrical Characteristics

#### 4.1 TFT LCD Module

The connector for display data & timing signal should be connected.

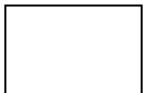
 $Ta = 25^{\circ}C \pm 2^{\circ}C$ 

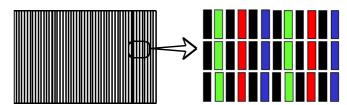
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of	Power Supply	V <sub>DD</sub>	4.5	5.0	5.5	V	(1)
Current	(a) Black		-	1400	-	mA	
of Power	(b) White	I <sub>DD</sub>	-	1600	-	mA	(2),(3)
Supply	(c) N-Pattern		-	1800	2000	mA	
Vsync Free	Vsync Frequency		-	60	-	Hz	
Hsync Frequency		f <sub>H</sub>	43	50	-	kHz	
Main Frequency		f <sub>DCLK</sub>	60	80	85	MHz	
Rush Current		I <sub>RUSH</sub>	-	-	4	А	(4)

Note (1) The ripple voltage should be controlled under 10% of  $\rm V_{\rm DD}.$ 

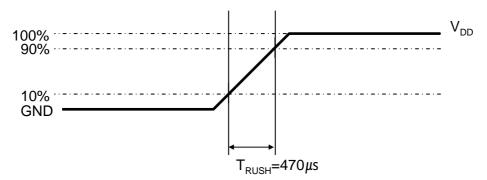
- (2)  $f_V=60Hz$ ,  $f_{DCLK}=80MHz$ ,  $V_{DD}=5.0V$ , DC Current. (3) Power dissipation check pattern (LCD Module only)
- a) Black Pattern
- b) White Pattern







### (4) Measurement Conditions



Rush Current  $I_{RUSH}$  can be measured when  $T_{RUSH}$  is 470  $\mu s$ .

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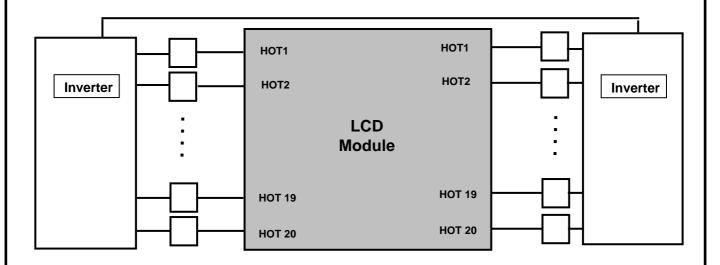
## 4.2 Back Light Unit

The back light unit contains 20 direct-lighting type CCFTs (Cold Cathode Fluorescent Tube). The characteristics of lamps are shown in the following tables.

 $Ta=25 \pm 2^{\circ}C$ 

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp Current	IL	4.0	5.5	7.0	mArms	
Lamp Voltage	V <sub>L</sub>	-	1540	-	Vrms	
Operating Life Time	Hr	50,000	ı	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value. [Operating condition:  $Ta = 25 \pm 2$ , IL = 6.0 mArms(typ.), For single lamp only.]

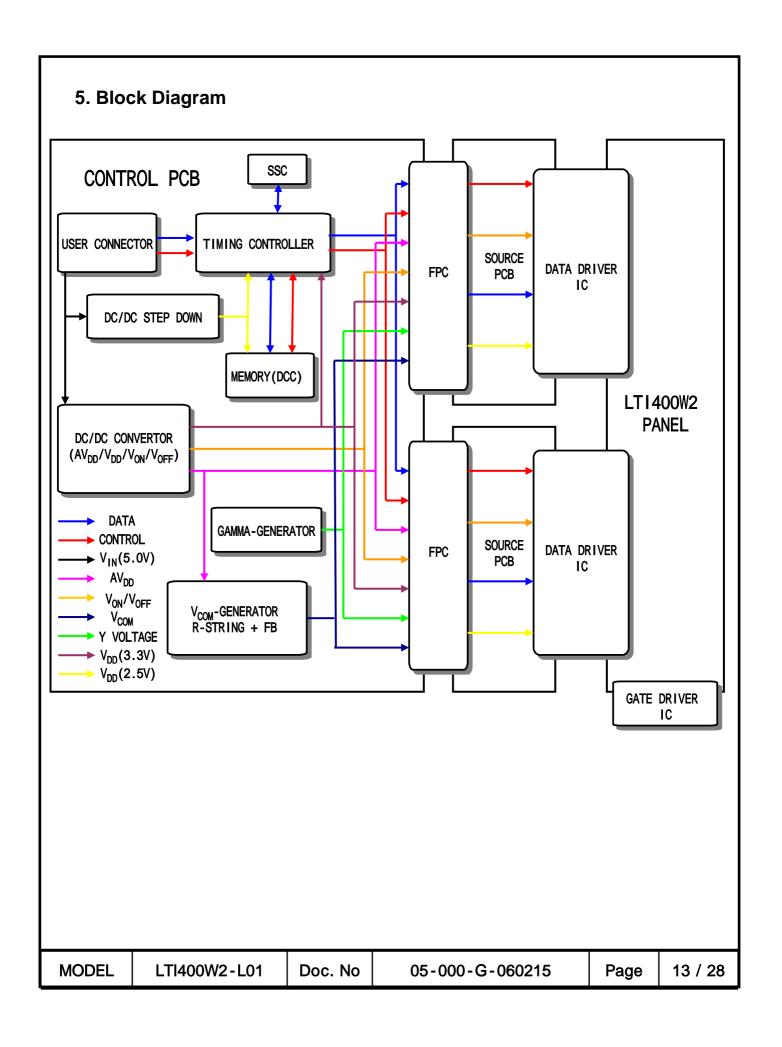


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# 4.3 Inverter Input Condition & Specification

Itomo	Cumbal	Conditions	Sp	ecificatio	ns	Lloit	Note
Items	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Input Voltage	Vin	-	21.6	24	26.4	V	Ta=25±2 °C
Input Current	I <sub>RUSH</sub>	Vin=24.0V Vdim=3.3V	-	-	8	А	Initial Turn-on
Lamp Current	l <sub>o</sub>	Vdim=3.3 V	6.0	6.5	7.0	mArms	-
Frequency	$F_{LAMP}$	Vin=24.0 V	55	60	65	kHz	-
Backlight	ON	Vin=24.0 V	2.4	-	5.25	V	
On/Off	OFF	Vin=24.0 V	0	-	0.8	V	-
Dimming	\ <u>\</u>	Max Lum	3.3	-	-		
Control	V <sub>DIM</sub>	Min. Lum	-	-	0	V	-

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## 6. Input Terminal Pin Assignment

## 6.1. Input Signal & Power

PIN No.	Description	PIN No.	Description
1	No Connection	16	GND
2	No Connection	17	RxIN3-
3	No Connection	18	RxIN3+
4	GND	19	GND
5	RxIN0-	20	No Connection
6	RxIN0+	21	LVDS OPTION (Note 1)
7	GND	22	No Connection
8	RxIN1-	23	GND
9	RxIN1+	24	GND
10	GND	25	GND
11	RxIN2-	26	Vin
12	RxIN2+	27	Vin
13	GND	28	Vin
14	RxCLK-	29	Vin
15	RxCLK+	30	Vin

Note No Connection: This PINS are only used for SAMSUNG internal using.

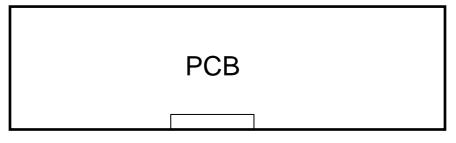
: LOW (GND) JEIDA LVDS format

Connector: FI-E30S (JAE)

SEQUENCE : On = VDD(T1) LVDS Option Interface Signal(T2)

OFF = Interface Signal(T3) LVDS Option VDD

## Note(1) Pin number starts from Right side



Pin No. 1 Pin No. 30

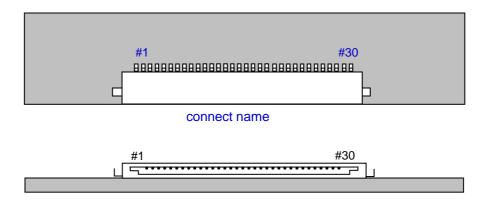


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

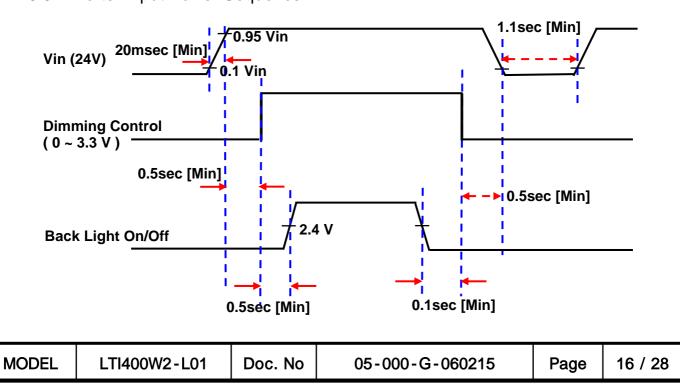
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## 6.2. Inverter Input Pin Configuration

Connector: 20022WR-14(P)(YEON-HO)

Pin No.	Pin Configuration(FUNCTION)
1	24 V
2	24 V
3	24 V
4	24 V
5	24 V
6	GND
7	GND
8	GND
9	GND
10	GND
11	No Connection * ERROR DETECTION (NORMAL : GND / ABNORMAL : 5V)
12	Backlight On /Off [ON: 2.4 - 5.25 V, OFF: 0 - 0.8 V]
13	Dimming Control [0V:Min, 3.3V:Max]
14	PWM Dimming Control

## 6.3. Inverter Input Power Sequence



# 6.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA & Normal)

		LVDS pin		JEIDA -DATA	VESA -D	АТА	
		TxIN/RxOU <sup>-</sup>	Γ0	R2	R0		
		TxIN/RxOU	Γ1	R3	R1		
		TxIN/RxOU	Γ2	R4	R2		
TxO	UT/RxIN0	TxIN/RxOU	Г3	R5	R3		
		TxIN/RxOU <sup>-</sup>	Γ4	R6	R4		
		TxIN/RxOU	Γ6	R7 R5			
		TxIN/RxOU	Γ7	G2	G0		
		TxIN/RxOU	Γ8	<b>G</b> 3	G1		
TxIN/Rx		TxIN/RxOU	Г9	G4	G2		
		TxIN/RxOUT12		G5	G3		
TxO	UT/RxIN1	TxIN/RxOUT13		G6	G4		
		TxIN/RxOUT	14	<b>G</b> 7	G5		
		TxIN/RxOUT	15	B2	В0		
		TxIN/RxOUT	18	В3	B1		
		TxIN/RxOUT	19	B4	B2		
		TxIN/RxOUT	20	B5	В3		
		TxIN/RxOUT	21	В6	B4		
TxO	UT/RxIN2	TxIN/RxOUT	22	В7	B5		
		TxIN/RxOUT	24	HSYNC	C HSYNC		
		TxIN/RxOUT	25	VSYNC	VSYN	0	
		TxIN/RxOUT	26	DEN	DEN		
		TxIN/RxOUT	27	R0	R6		
		TxIN/RxOU	Γ5	R1	R7		
		TxIN/RxOUT	10	G0	G0 G6		
TxO	OUT/RxIN3	TxIN/RxOUT	11	G1	G7		
		TxIN/RxOUT	16	В0	B6		
		TxIN/RxOUT	17	B1 B7			
		TxIN/RxOUT	23	RESERVED	RESERV	RESERVED	
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# 6.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

												D/	ATA S	SIGNA	٩L											GRAY
COLOR	DISPLAY (8bit)				RE	D							GRE	EN							BL	UE				SCALE LEVEL
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	ВО	B1	B2	ВЗ	В4	B5	В6	В7	LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
BASIC	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE			:	:	:						:		:	:					:				:			R3~
OF RED		• •	:	:	:						:		:	:					:				:			R252
1125	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R252
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0 R252
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R252
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~
OF GREEN		:	:	:	:		:			:	:		:	:				:	:	:		:	:			G252
	LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G252
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G252
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G252
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
GRAY SCALE		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE		:	:	:	:	:	:			:	:	:	:		:				:	:	:		:			B252
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1 1 1 1 1 B252					
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B252
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B252

Note) Definition of Gray:

Rn: Red Gray, Gn: Green Gray, Bn: Blue Gray (n = Gray level) Input Signal: 0 = Low level voltage, 1 = High level voltage

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# 7. Interface Timing

## 7.1 Timing Parameters (DE only mode)

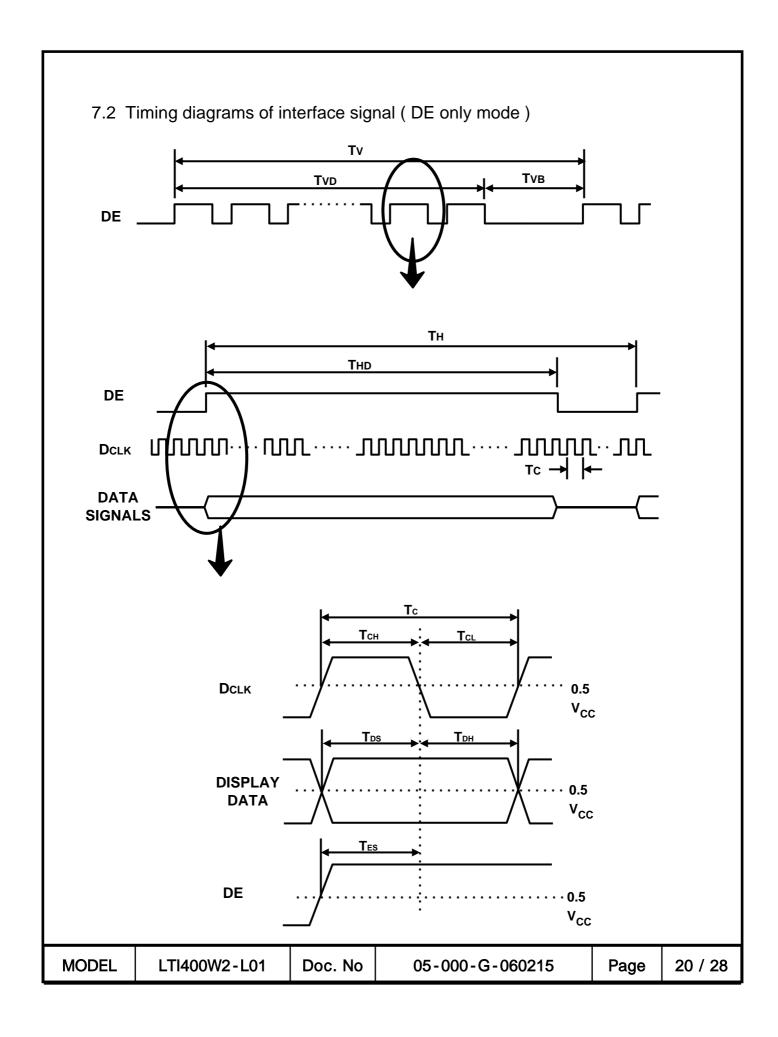
SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock		1/T <sub>C</sub>	60	80	85	MHz	-
Hsync	Frequency	F <sub>H</sub>	43	50	-	KHz	-
Vsync		$F_{V}$	-	60	-	Hz	-
Vertical Display Term	Active Display Period	T <sub>VD</sub>	-	768	-	Lines	-
	Vertical Total	T <sub>VB</sub>	773	838	1500	Lines	-
Horizontal	Active Display Period	T <sub>HD</sub>	-	1366	-	Clock s	-
Display Term	Horizontal Total	T <sub>H</sub>	1576	1600	2048	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point: TTL control signal and CLK at LVDS Tx input terminal in system

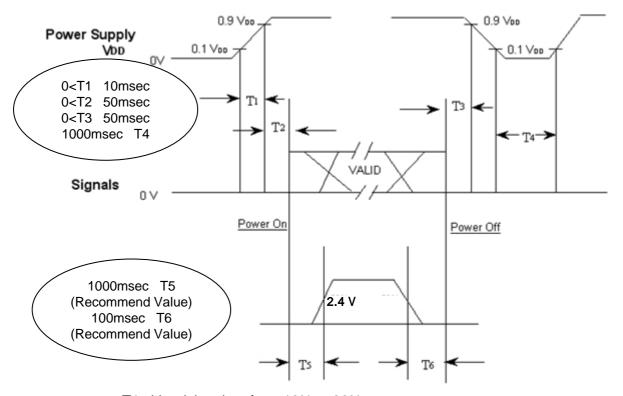
(2) Internal V<sub>DD</sub> = 3.3V

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## 7.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1: V<sub>DD</sub> rising time from 10% to 90%

T2 : The time from  $V_{DD}$  to valid data at power ON.

T3 : The time from valid data off to  $V_{\rm DD}$  off at power Off.

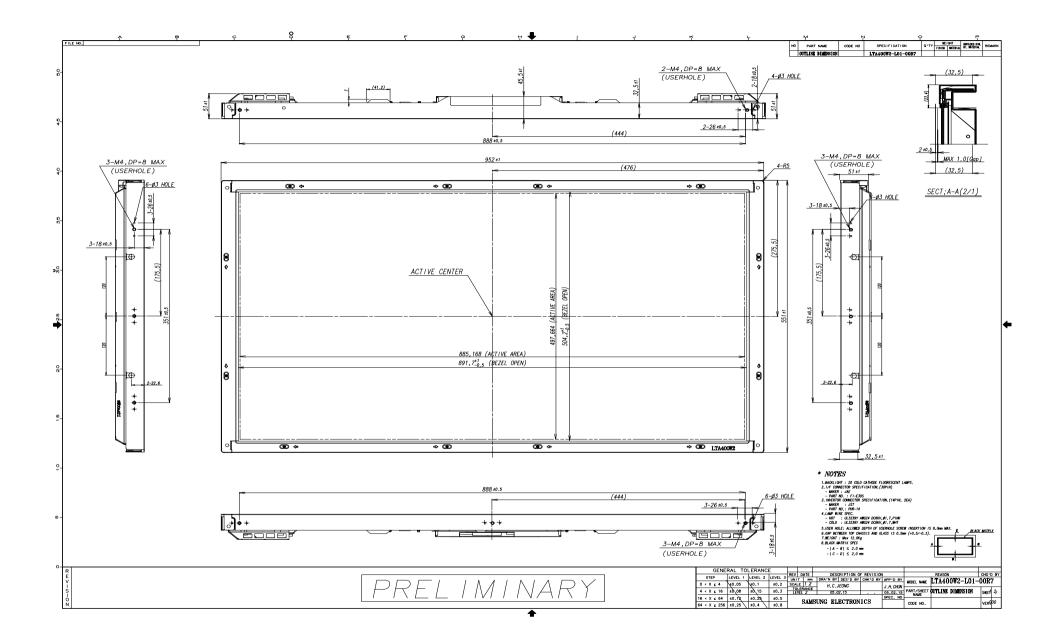
T4: V<sub>DD</sub> off time for Windows restart

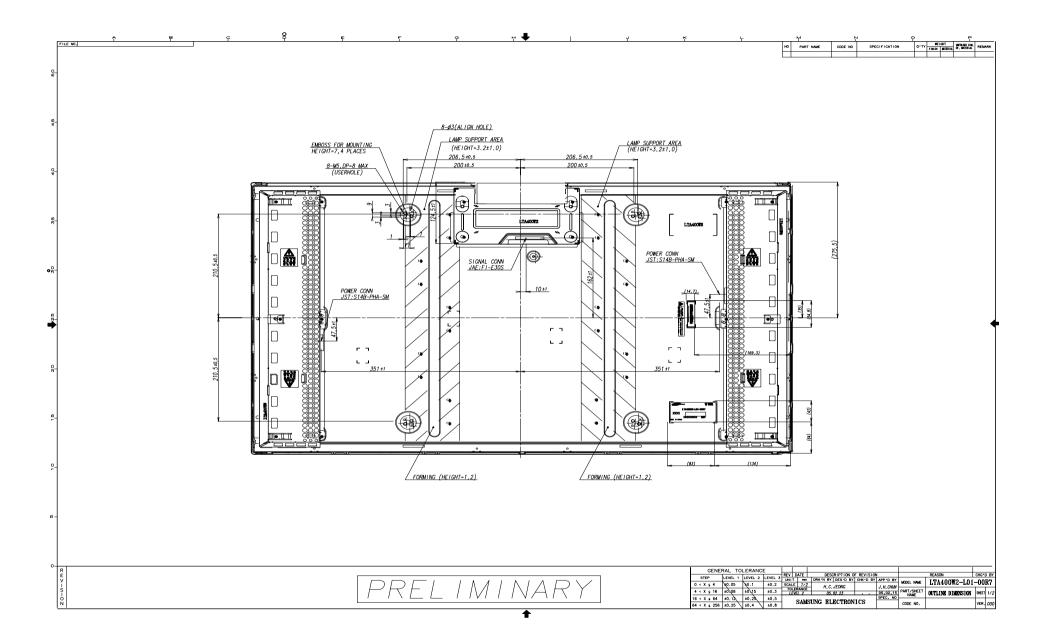
T5: The time from valid data to B/L enable at power ON.

T6: The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V<sub>DD</sub>.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V<sub>DD</sub> = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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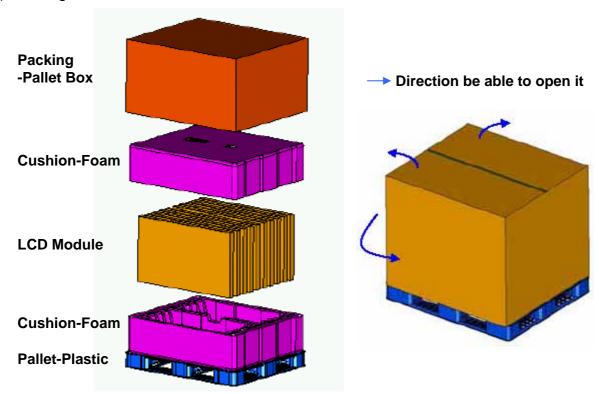


## 9. PACKING

## 9.1 CARTON (Internal Package)

(1) Packing Form
Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



## 9.2 Packing Specification

Item	Specification	Remark
LCD Packing	10ea / (Packing- Pallet Box)	<ol> <li>1. 120 Kg / LCD (10ea)</li> <li>2. 7 Kg / Cushion-pallet (2ea)</li> <li>3. 6.7 Kg / Packing-Pallet Box (1ea)</li> <li>4. Cushion-pallet Material : EPS</li> <li>5. Packing-Pallet Box Material : DW4</li> </ol>
Pallet	1Box / Pallet	1. Pallet weight = 8kg 2. 8Kg/Pallet
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x609mm(height)
Total Pallet Weight	141.7kg	Pallet(8kg) + Module(1*10=120) + Cushion(up+botton=7kg) + Pallet-BOX(6.7kg)

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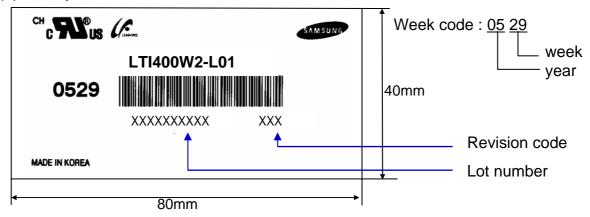
#### 10. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

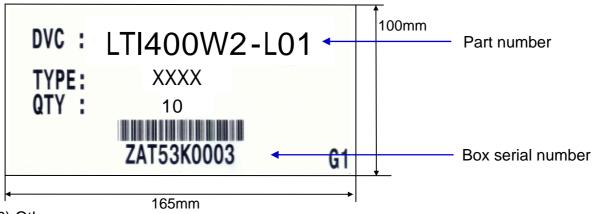
(1) Parts number: LTI400W2-L01

(2) Revision: Three letters

(4) Nameplate Indication



(5) Packing box attach



(6) Others

 After service part Lamps cannot be replaced because of the narrow bezel structure.

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#### 11. General Precautions

## 11.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.

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#### 11.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

## 11.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

#### 11.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.

Normal condition is defined as below:

Temperature: 20±15Humidity: 55±20%

- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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#### 11.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
  - Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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