(   ) Preliminary Specification
(V) Final Specification

Module	24.5" Color TFT-LCD
Model Name	M250HTN01.1

Customer	Date

Approved by

Note: This Specification is subject to change without notice.

Approved by	Date

Prepared by   Date

AU Optronics corporation

Contents

Record of Revision

Version	Date	Page	Old description	New Description	Remark
2.0	2016/1/29	All		New release	
0.1	2016/3/30	All	Document version 2.0	Document version 0.1	
		5	**2.1 Display Characteristics** — Power Consumption (LCD Module + Backlight unit) [Watt] TBD (Typ.) LCD module: PDD (Typ.)=TBD @ Black pattern, Fv=60Hz. Backlight unit: P_BLU (Typ.) =9.52 @Is=50mA	**2.1 Display Characteristics** — Power Consumption (LCD Module + Backlight unit) [Watt] TBD (Typ.) LCD module: PDD (Typ.)=TBD @ Black pattern, Fv=60Hz. Backlight unit: P_BLU (Typ.) =11.02 @Is=50mA	
		7	**2.3 Optical Characteristics** — The optical characteristics are measured on the following test condition. **Test Condition:** 1. Equipment setup: Please refer to *Note 2-2*. 2. Panel Lighting time: 30 minutes 3. VDD=5.0V, Fv=TBDHz, Is=TBDmA, Ta=25°C	**2.3 Optical Characteristics** — The optical characteristics are measured on the following test condition. **Test Condition:** 1. Equipment setup: Please refer to *Note 2-2*. 2. Panel Lighting time: 30 minutes 3. VDD=5.0V, Fv=TBDHz, Is=50mA, Ta=25°C	
		23	**4.1 Block Diagram** — The following shows the block diagram of the 24.5 inch Backlight Unit. And it includes 68 pcs LED in the LED light bar. (4 strings and 17 pcs LED of one string)	**4.1 Block Diagram** — The following shows the block diagram of the 24.5 inch Backlight Unit. And it includes 76 pcs LED in the LED light bar. (4 strings and 19 pcs LED of one string)	
0.1	2016/3/30	27	**4.3.1 Absolute Maximum Rating** (see table below)	**4.3.1 Absolute Maximum Rating** (see table below)	
		27	**4.3.2 Recommended Operating Condition** (see table below)	**4.3.2 Recommended Operating Condition** (see table below)	
		28	**Note 4-1, a** — a. Vr LED chip forward voltage, V_r (Min.)=2.5V, V_r(Typ.)=2.8V, V_r(Max.)=3.1V	**Note 4-1, a** — a. Vr LED chip forward voltage, V_r (Min.)=2.7V, V_r(Typ.)=2.9V, V_r(Max.)=3.1V	
1.0	2016/5/25	All		New Release	
1.1	2016/6/27	20	**3.4.5 Input Timing Specification** (see table below)	**3.4.5 Input Timing Specification** (see table below)	
1.2	2016/8/5	32	**7. Mechanical Characteristics**	**7. Mechanical Characteristics**	

Page 27 — 4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

Old (Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	120	[mA]	100% duty ratio
			300	[mA]	Duty ratio ≤ 10% Pulse time=10 ms

New (Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	90	[mA]	100% duty ratio
			150	[mA]	Duty ratio ≤ 10% Pulse time=10 ms

Page 27 — 4.3.2 Recommended Operating Condition

Old

Symbol	Description	Min	Typ	Max	Unit	Remark
Is	LED String Current		50	55	[mA]	100% duty ratio of LED chip, Note 4-6
Vs	LED String Voltage	51.3	55.1	58.9	[Volt]	Is=50mA @ 100% duty ratio; Note 4-1, Note 4-5, Note 4-7
ΔVs	Maximum Vs Voltage Deviation of light bar			3.8	[Volt]	Is=50mA @ 100% duty ratio; Note 4-2
P_BLU	LED Light Bar Power Consumption		11.02	11.78	[Watt]	Note 4-3
LT_LED	LED Life Time	30,000			[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	110% Vsmax			[Volt]	Note 4-5

New

Symbol	Description	Min	Typ	Max	Unit	Remark
Is	LED String Current		50	55	[mA]	100% duty ratio of LED chip, Note 4-6
Vs	LED String Voltage	42.5	47.6	52.7	[Volt]	Is=50mA @ 100% duty ratio; Note 4-1, Note 4-5, Note 4-7
ΔVs	Maximum Vs Voltage Deviation of light bar			3.4	[Volt]	Is=50mA @ 100% duty ratio; Note 4-2
P_BLU	LED Light Bar Power Consumption		9.52	10.54	[Watt]	Note 4-3
LT_LED	LED Life Time	30,000			[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	110% Vsmax			[Volt]	Note 4-5

Page 20 — 3.4.5 Input Timing Specification

Old

Symbol	Description		Min	Typ	Max	Unit	Remark
Tv	Vertical Section	Period	1094	1130	2906	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	1826	Th	
Fv		Frequency	30	60	76	Hz	
Th	Horizontal Section	Period	1034	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	32.8	67.8	87.2	KHz	Note 3-3
Tclk	LVDS Clock	Period	29.5	14.0	11.1	ns	1/Fclk
Fclk		Frequency	33.9	71.2	90.2	MHz	Note 3-4

New

Symbol	Description		Min	Typ	Max	Unit	Remark
Tv	Vertical Section	Period	1094	1130	2906	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	1826	Th	
Fv		Frequency	30	60	76	Hz	
Th	Horizontal Section	Period	1034	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	32.8	67.8	91.4	KHz	Note 3-3
Tclk	LVDS Clock	Period	10.6	14.0	29.5	ns	1/Fclk
Fclk		Frequency	33.9	71.2	94.5	MHz	Note 3-4

Page 32 — 7. Mechanical Characteristics

Old

```
NOTE:
1.TOLERANCE WITHOUT NOTICED TO BE 0.5MM.
2.DISPLAY AREA POSITION TOLERANCE ARE AS FOLLOWING:
 (1)Ia-IbI<=1.4mm
 (2)Ic-dI<=1.4mm
```

New

```
NOTE:
1.TOLERANCE WITHOUT NOTICED TO BE 0.5MM.
2.DISPLAY AREA POSITION TOLERANCE ARE AS FOLLOWING:
 (1)Ia-IbI<=1.0mm
 (2)Ic-dI<=1.0mm
```

1.3	2016/10/12	6	2.1 Display Characteristics – Weight 2086 +/- 80 grams	2.1 Display Characteristics – Weight 2090 +/- 80 grams	
		14	3.2.1 Connector Type *(connector table)*	3.2.1 Connector Type *(connector table)*	

3.2.1 Connector Type (left)

TFT-LCD Connector	Manufacturer	P-TWO	STM	
	Part Number	187034-3009	MSCKT2407P30HB	
Mating Connector	Manufacturer	JAE		
	Part Number	FI-X30HL (Locked Type)		

3.2.1 Connector Type (right)

TFT-LCD Connector	Manufacturer	P-TWO	STM	
	Part Number	187034-3009	MSFKT7437P30HF	
Mating Connector	Manufacturer	JAE		
	Part Number	FI-X30HL (Locked Type)		

1 Handling Precautions

1) Since polarizer is easily damaged, do not touch or press the surface of polorizer with hand.

2) Be sure to turn off power supply when inserting or disconnecting from input connector.

3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.

4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.

6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.

7) Do not open or modify the Module Assembly.

8) Do not press the reflector sheet at the back of the module to any directions.

9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.

10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.

11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.

12) Please avoid touching COF position while you are doing mechanical design.

13) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.

14) Do not apply the same pattern for a long time, it will enhance relevant defect.

2 General Description

This specification applies to the 24.5 inch wide Color a-Si TFT-LCD Module M250HTN01.1
The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 8-bits data input). The input interface is dual channel LVDS and this module doesn't contain an driver board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25℃ condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	622.28 (24.5")
Active Area	[mm]	543.744 (H) x 302.616 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	283.2 (per one triad) ×280.2
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	TN Mode, Normally White
White Luminance (Center)	[cd/m²]	250 (Typ.)
Contrast Ratio	-	1000 (Typ.)
Color Gamut	-	NTSC 72%
Response Time	[msec]	5 (Typ., on/off)
Power Consumption (LCD Module + Backligh unit)	[Watt]	14.92 (Typ.) LCD module : PDD (Typ.)=3.9 @ Black pattern,Fv=60Hz Backlight unit : P_{BLU} (Typ.) =11.02 @Is=50mA
Weight	[Grams]	2090 +/- 80
Outline Dimension	[mm]	556(H) × 323.2(V) × 11.9(D) Typ.
Electrical Interface	-	Dual channel LVDS
Support Color	-	16.7M colors (RGB 8-bits data input)
Surface Treatment	-	Anti-Glare, 3H
Temperature Range Operating Storage (Shipping)	 [℃] [℃]	 0 to +50 -20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 7.0 Compliance

2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	*Note 2-1*
TGS	Glass surface temperature (operation)	0	+65	[°C]	*Note 2-1* *Function judged only*
HOP	Operation Humidity	5	90	[%RH]	*Note 2-1*
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

1. 90% RH Max (Ta ≦39℃)
2. Max wet-bulb temperature at 39℃ or less. (Ta ≦39℃)
3. No condensation
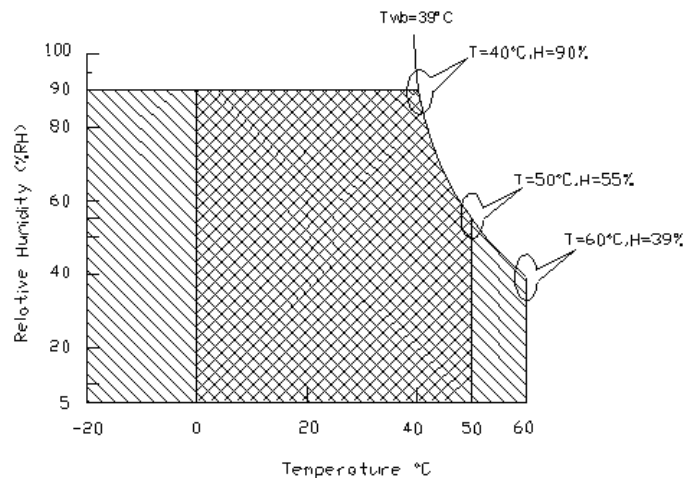
Operating Range   Storage Range
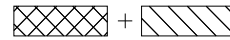
2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

Test Condition:
1. Equipment setup: Please refer to **Note 2-2**.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=60Hz, Is=50mA, Ta=25℃

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
L_W	White Luminance (Center of screen)		200	250	-	[cd/m2]	**Note 2-2** By SR-3
L_{uni}	Luminance Uniformity (9 points)		75	80	-	[%]	**Note 2-3** By SR-3
CR	Contrast Ratio (Center of screen)		600	1000	-	-	**Note 2-4** By SR-3
$θ_R$	Horizontal Viewing Angle (CR=10)	Right	75	85	-	[degree]	**Note 2-5** By SR-3
$θ_L$		Left	75	85	-		
$Φ_H$	Vertical Viewing Angle (CR=10)	Up	70	80	-		
$Φ_L$		Down	70	80	-		
$θ_R$	Horizontal Viewing Angle (CR=5)	Right	75	88	-		
$θ_L$		Left	75	88	-		
$Φ_H$	Vertical Viewing Angle (CR=5)	Up	70	85	-		
$Φ_L$		Down	70	85	-		
T_R	Response Time	Rising Time	-	3.8	5.5	[msec]	**Note 2-6** By TRD-100
T_F		Falling Time	-	1.2	2.5		
-		Rising + Falling	-	5	8		
R_x	Color Coordinates (CIE 1931)	Red x	0.624	0.654	0.684	-	By SR-3
R_y		Red y	0.303	0.333	0.363		
G_x		Green x	0.294	0.324	0.354		
G_y		Green y	0.595	0.625	0.655		
B_x		Blue x	0.127	0.157	0.187		
B_y		Blue y	0.045	0.075	0.105		
W_x		White x	0.283	0.313	0.343		
W_y		White y	0.299	0.329	0.359		
	NTSC coverage ratio			72		[%]	By SR-3
CT	Crosstalk		-	-	1.5	[%]	**Note 2-7** By SR-3
F_{dB}	Flicker (Center of screen)		-	-	-20	[dB]	**Note 2-8** By SR-3

Note 2-2: Equipment setup :

Photo detector (SR-3, TRD-100)

Measured distance (50cm)

LCD Panel

TFT-LCD Module

Center of the screen
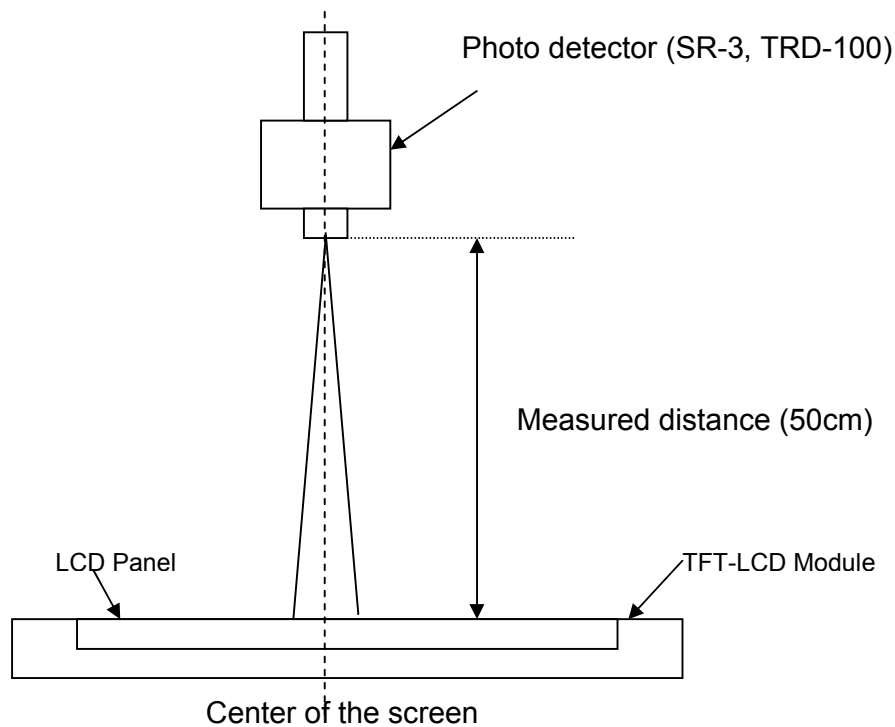
Note 2-3: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1} \sim \text{P9)}}{\text{Maximum Luminance of 9 Points (P1} \sim \text{P9)}}$$

a.Test pattern: White Pattern
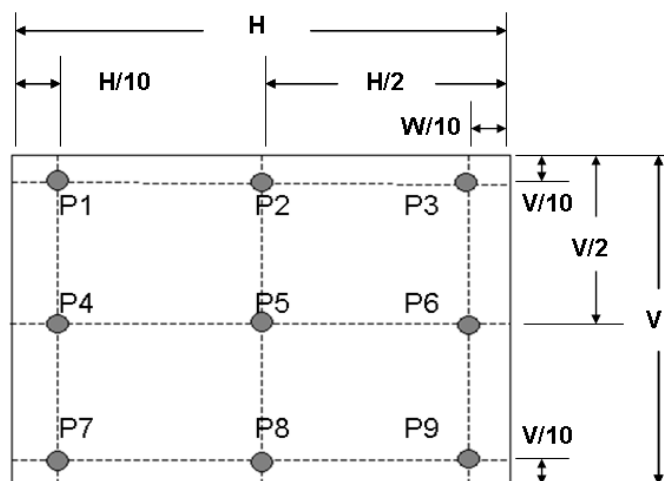
Note 2-4: Contrast Ratio Measurement

Definition:

$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

a. Measured position: Center of screen (P5) & perpendicular to the screen (θ=Φ=0°)

Note 2-5: Viewing angle measurement

Definition: The angle at which the contrast ratio is greater than 10 & 5 .

a. Horizontal view angle: Divide to left & right (θ_L & θ_R)

Vertical view angle: Divide to up & down (Φ_H &Φ_L)
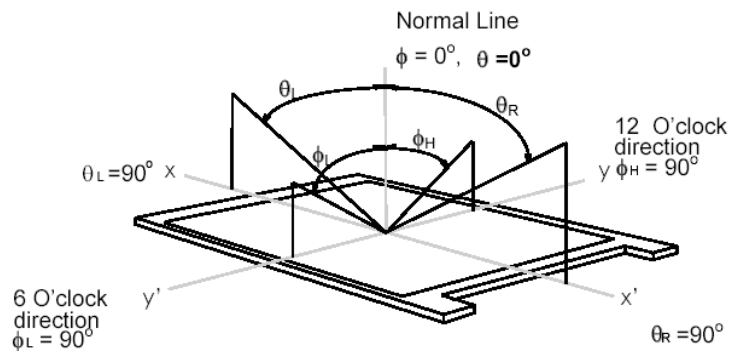
Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Black" to "White" (rising time, T_R), and from "White" to "Black" (falling time, T_F), respectively. The response time is interval between the 10% and 90% of optical response. (*Black & White color definition: Please refer section 3.4.3)*
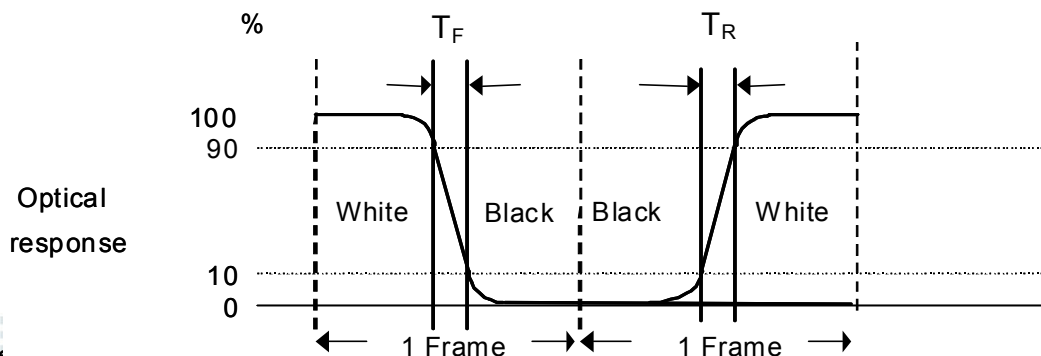
Note 2-7: Crosstalk measurement

Definition:

$CT = Max. (CT_H, CT_V);$

Where

a. Maximum Horizontal Crosstalk :

   $CT_H = Max. (| Y_{BL} - Y_{AL} | / Y_{AL} \times 100\%, | Y_{BR} - Y_{AR} | / Y_{AR} \times 100\%);$

   Maximum Vertical Crosstalk:

   $CT_V = Max. (| Y_{BU} - Y_{AU} | / Y_{AU} \times 100\%, | Y_{BD} - Y_{AD} | / Y_{AD} \times 100\%);$

b. $Y_{AU}, Y_{AD}, Y_{AL}, Y_{AR}$ = Luminance of measured location without Black pattern

   $Y_{BU}, Y_{BD}, Y_{BL}, Y_{BR}$ = Luminance of measured location with Black pattern
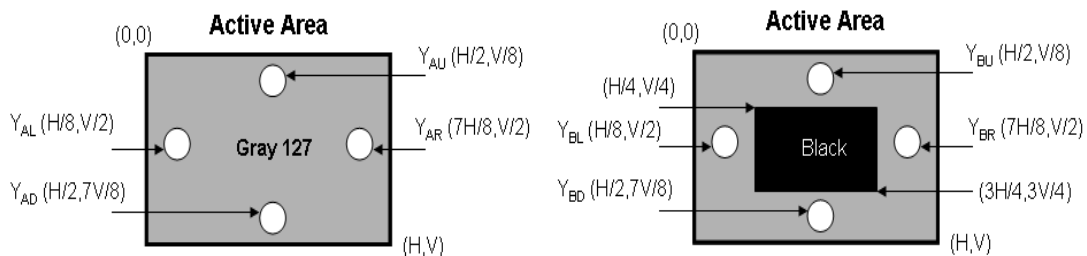
Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.

R: Red, G: Green, B:Blue
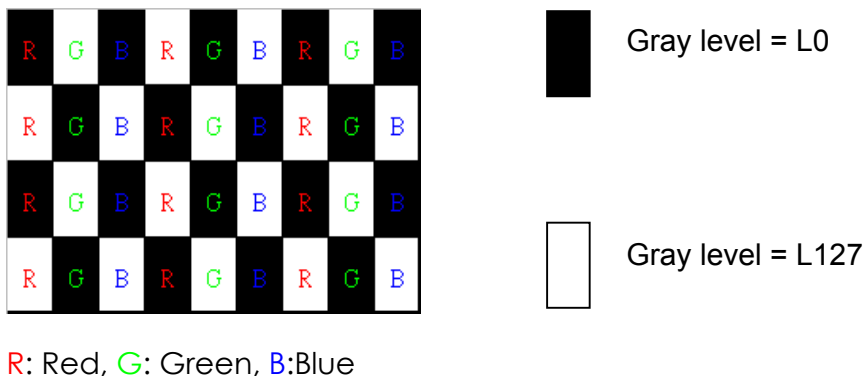
b. Measured position: Center of screen (P5) & perpendicular to the screen (θ=Φ=0°)

2.4 Mechanical Characteristics

Symbol	Description	Min.	Max.	Unit	Remark
P$_{bc}$	Backside Compression	2.5	-	[Kgf]	**Note 2-9**

Note 2-9: Test Method:

The point is at a distance from right-downside 25mm x 25mm defined as the Start Point of Measure Points, and the point is at a distance 25mm from left-side & around 10mm from PCB defined as the End Point.

Align 50mm x 50mm block from Start Point on the Bezel Back, and the corners of each block are Measure Points.

If the distance from the last block to each side of the End Point ≧ 25mm, add other blocks to make sure that most area of Bezel Back can be measured.
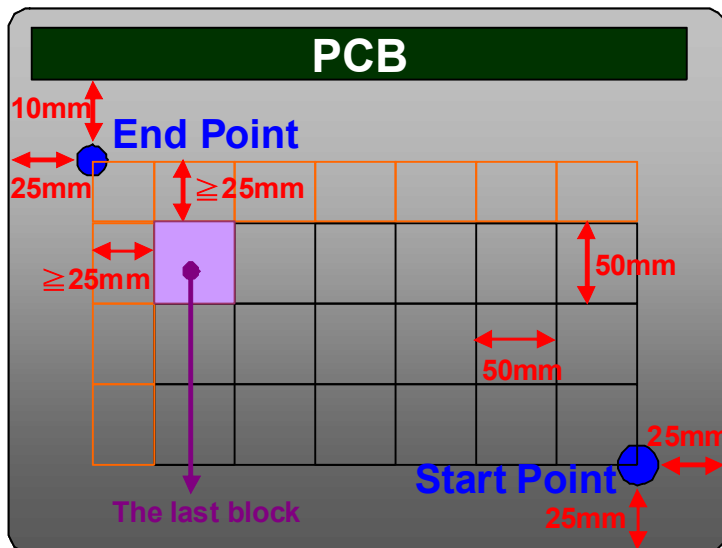
3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 24.5 inch Color TFT-LCD Module
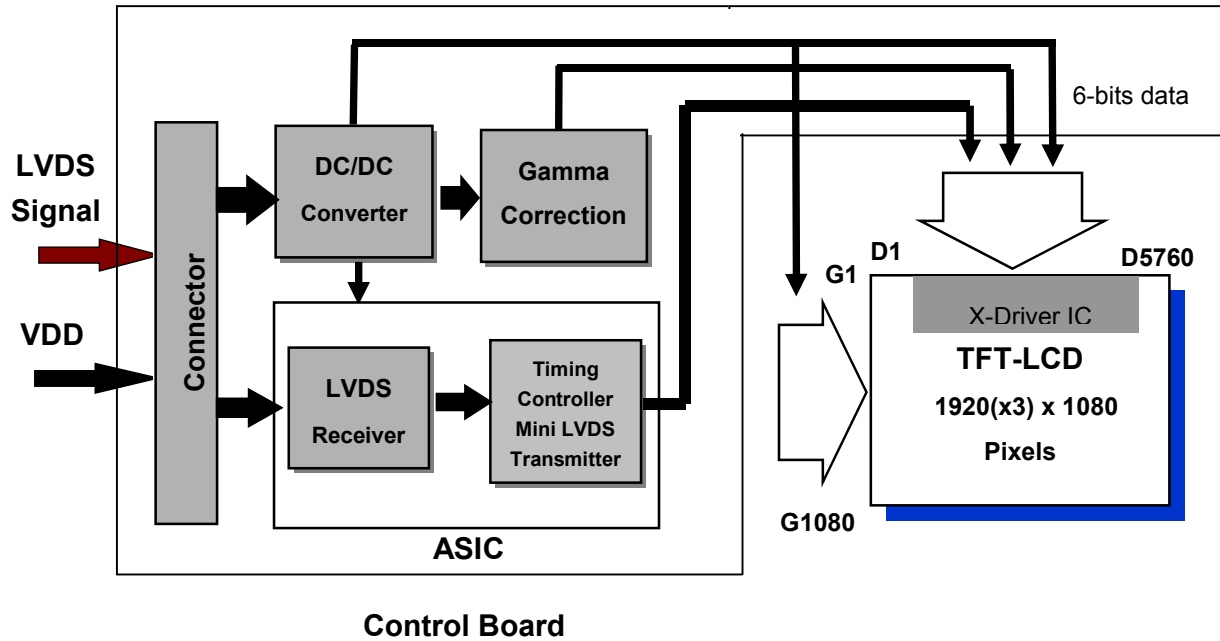
3.2 Interface Connection

3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM
	Part Number	187034-3009	MSBKT2407P30HB
Mating Connector	Manufacturer	JAE	
	Part Number	FI-X30HL (Locked Type)	

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	NC	No connection (for AUO test only. Do not	
25	NC	No connection (for AUO test only. Do not	
26	NC	No connection (for AUO test only. Do not	

27	NC	No connection (for AUO test only, Do not	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	

SHIPPING LABEL

BLU LABEL

Pin 30   Pin 1
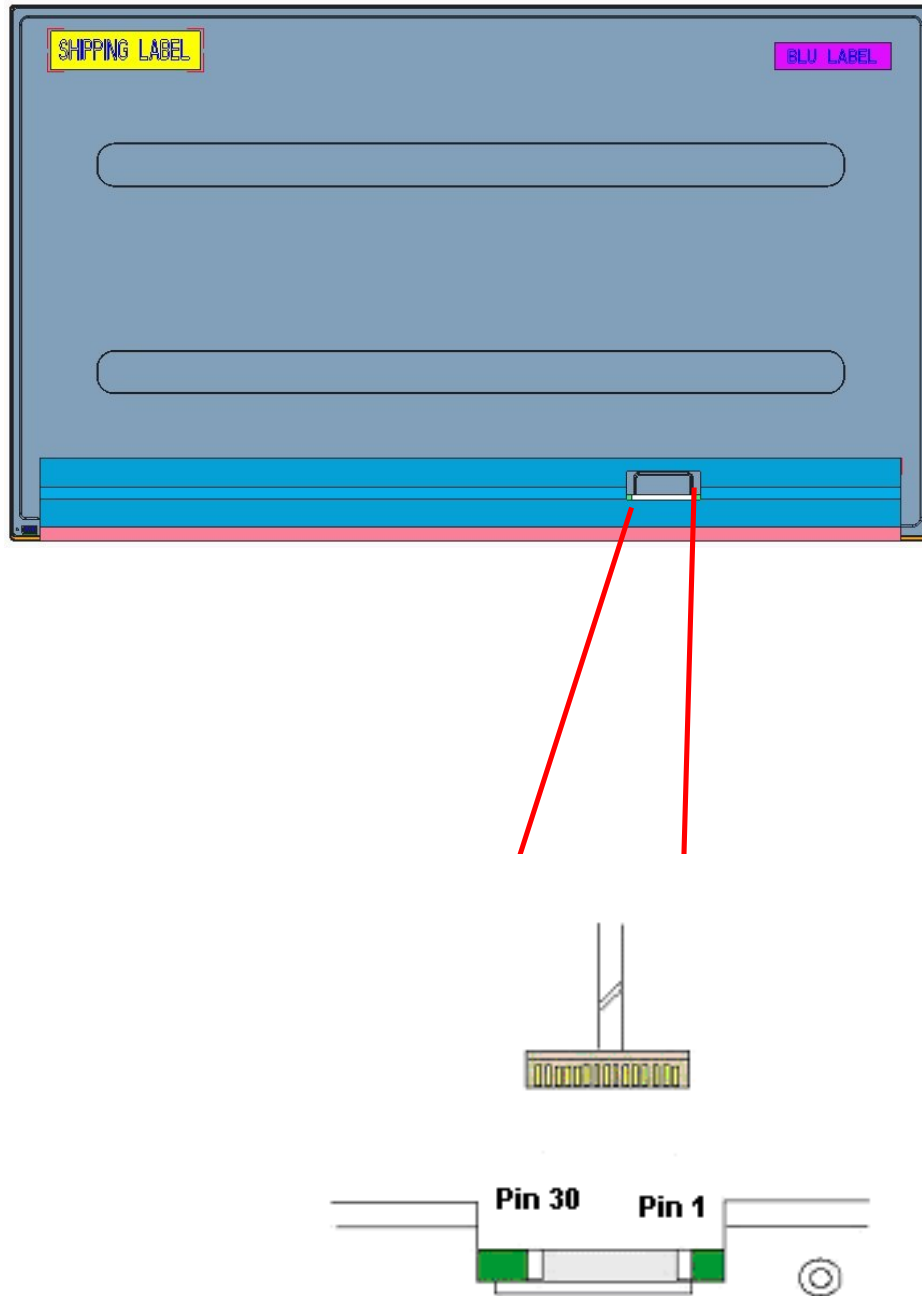
3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25℃

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.8	1.0	[A]	VDD= 5.0V, Black Pattern, Fv=60Hz
			1.0	1.2	[A]	VDD= 5.0V, Black Pattern, Fv=75Hz
PDD	VDD Power Consumption	-	3.9	5.0	[Watt]	VDD= 5.0V, Black Pattern, Fv=60Hz
			5.0	6.0	[Watt]	VDD= 5.0V, Black Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3	[A]	**Note 3-1**
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, Black Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

Test circuit:
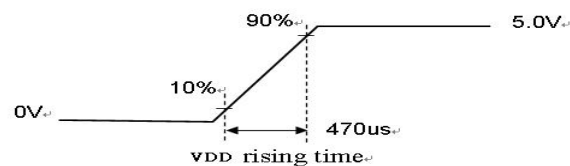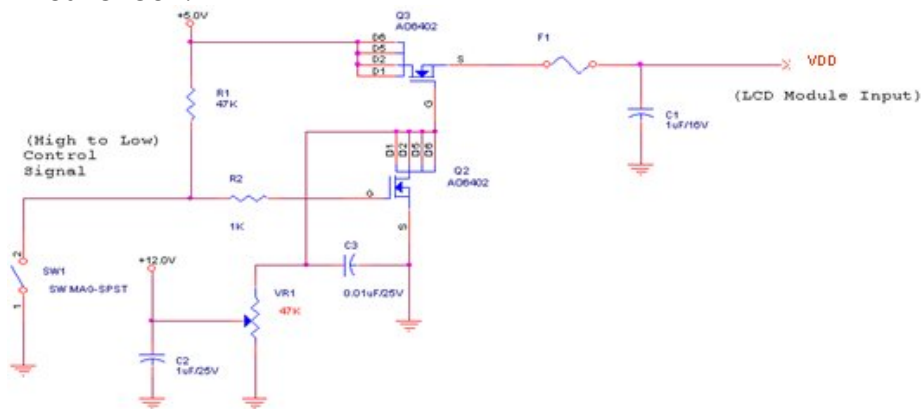
The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format
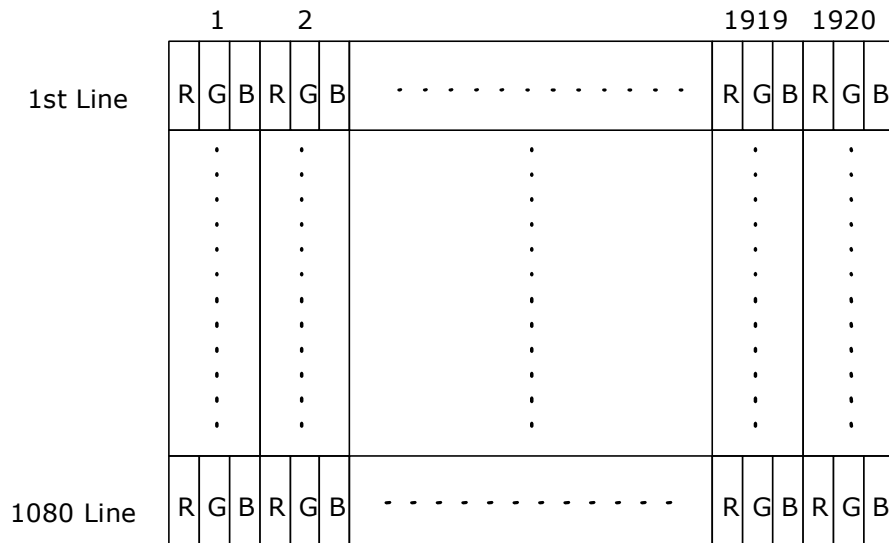
3.4.2 LVDS Data Format
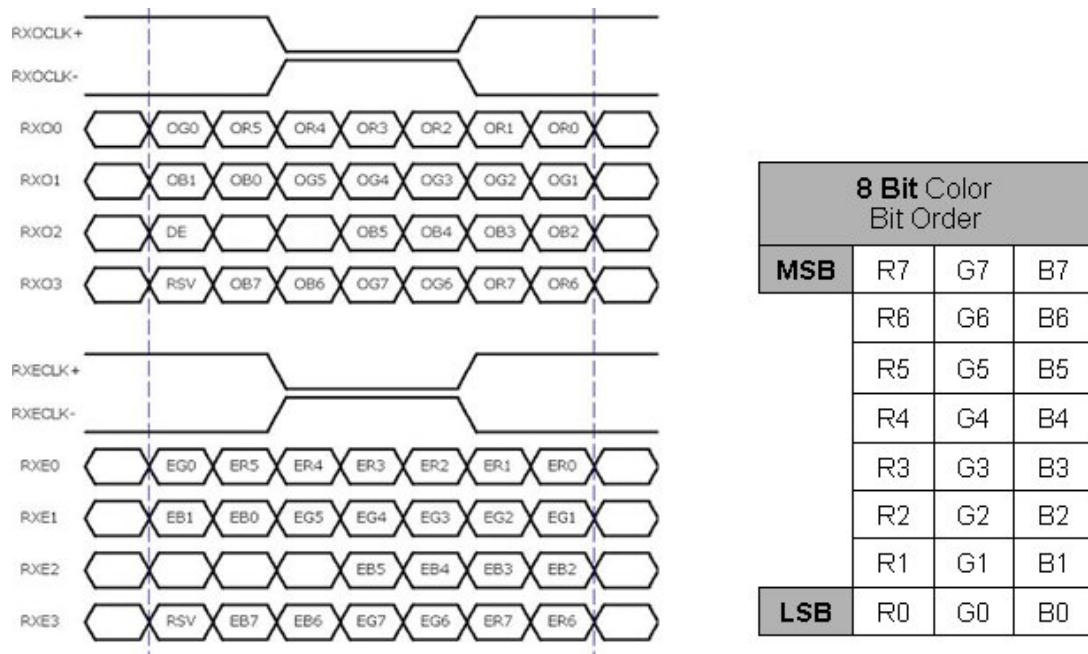
Note 3-2:

a. O = "Odd Pixel Data"   E = "Even Pixel Data"

b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								Remark
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	V_{CM} = 1.2V
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	V_{CM} = 1.2V
$\lvert V_{ID} \rvert$	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH}-V_{TL} = 200mV

LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.
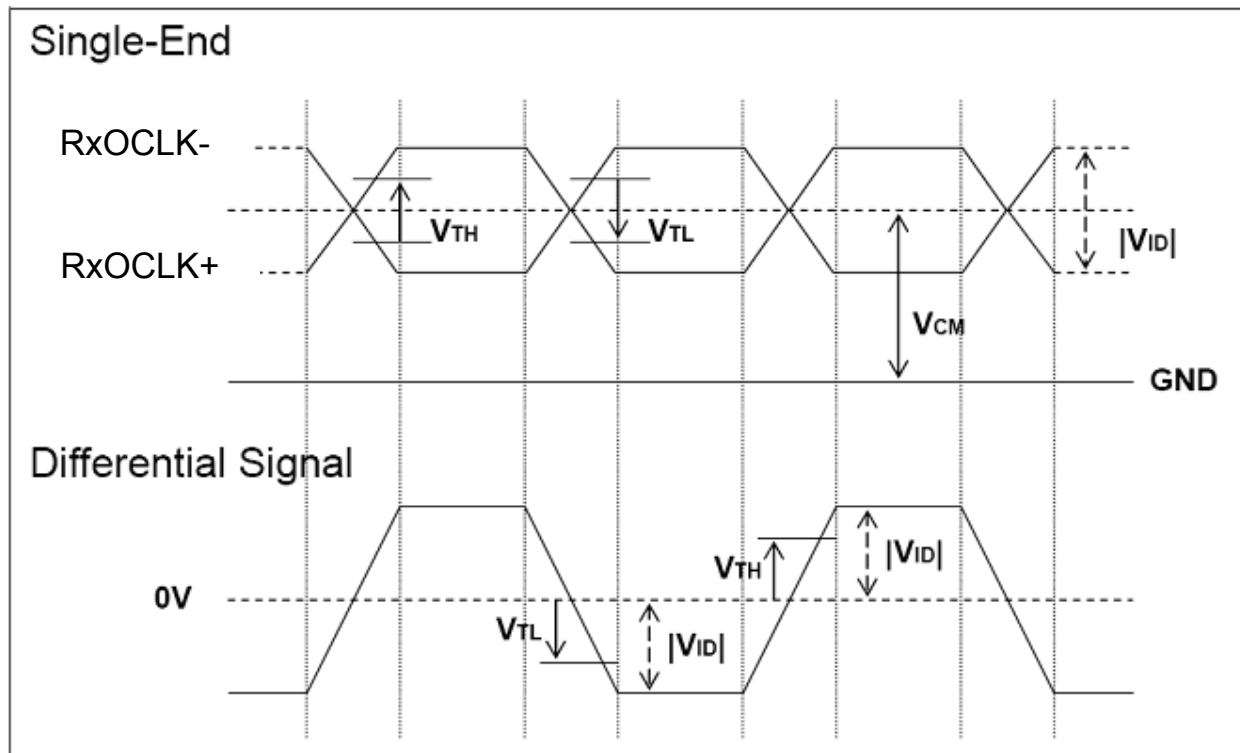
b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	

Freq

F_{max}

F_{clk}

F_{min}

$F_{clk} * F_{DEV}$

$$\frac{1}{F_{MOD}}$$

Time

Fclk: LVDS Clock Frequency< Spread Spectrum>
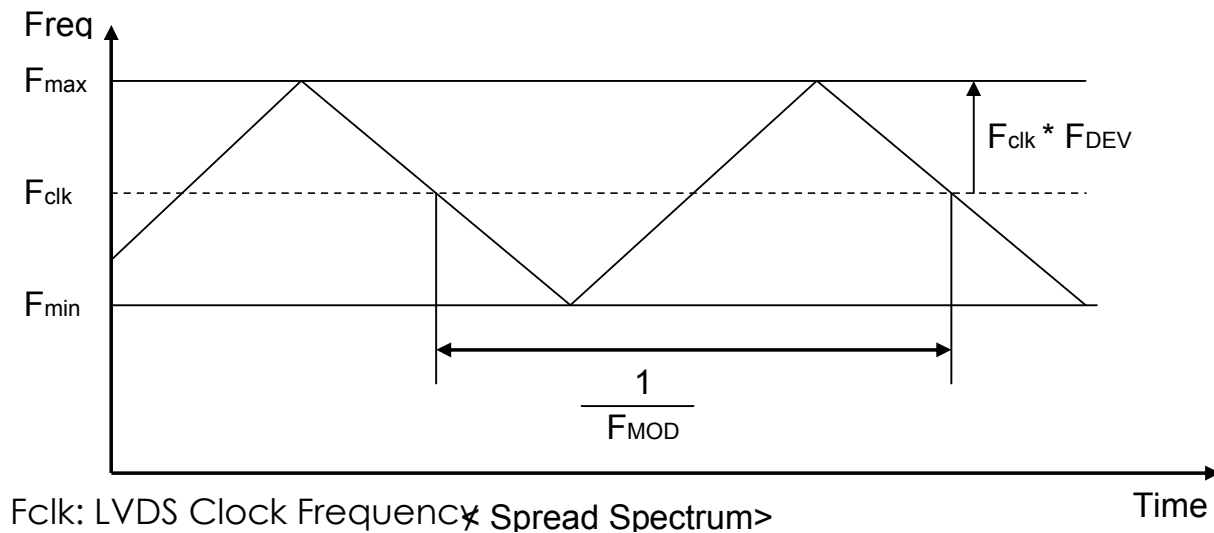
3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1094	1130	2906	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	1826	Th	
Fv		Frequency	30	60	76	Hz	
Th	Horizontal Section	Period	1034	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	32.8	67.8	91.4	KHz	*Note 3-3*
Tclk	LVDS Clock	Period	10.6	14.0	29.5	ns	1/Fclk
Fclk		Frequency	33.9	71.2	94.5	MHz	*Note 3-4*

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

> Fh (Min.) = Fclk (Min.) / Th (Min.);
> Fh (Typ.) = Fclk (Typ.) / Th (Typ.);
> Fh (Max.)= Fclk (Max.) / Th (Min.);

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

> Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);
> Fclk (min) < Fv x Th x Tv < Fclk (max)

3.4.6 Input Timing Diagram
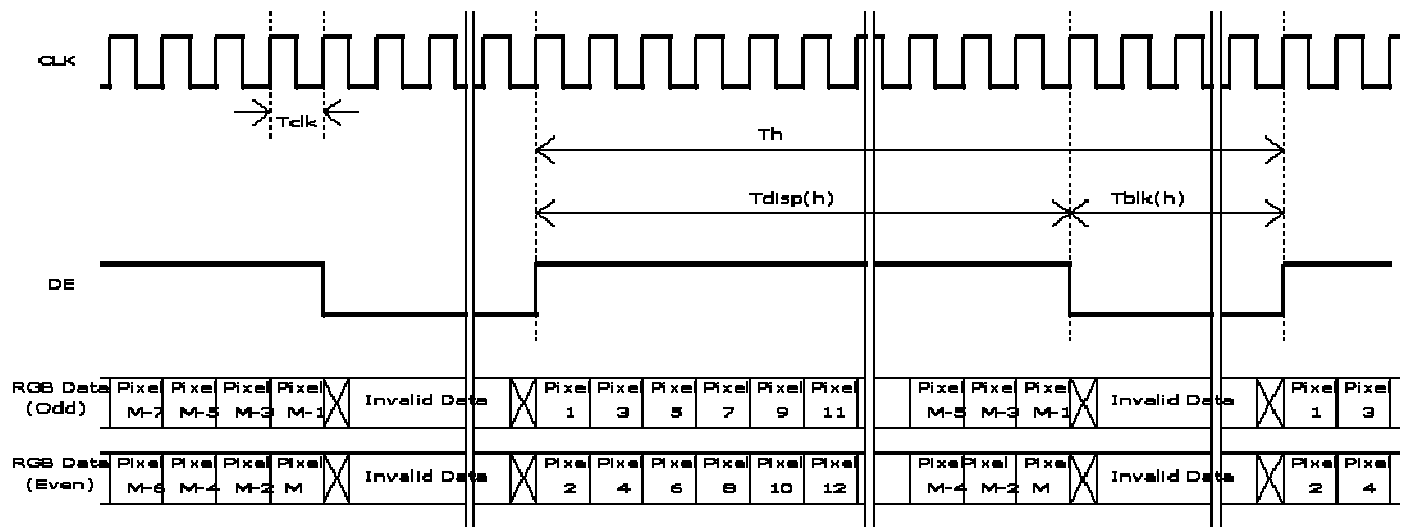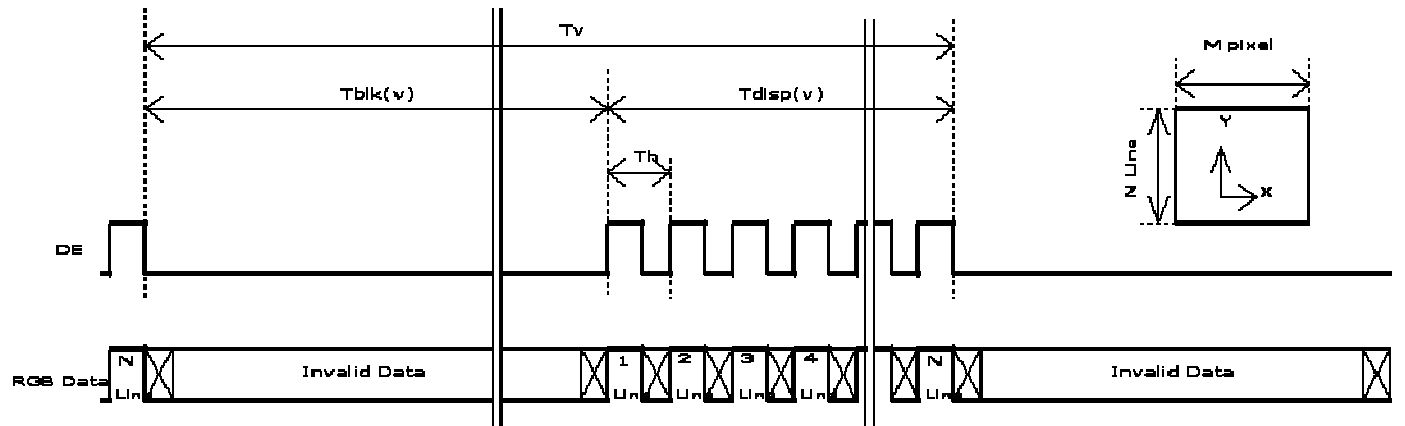
3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.
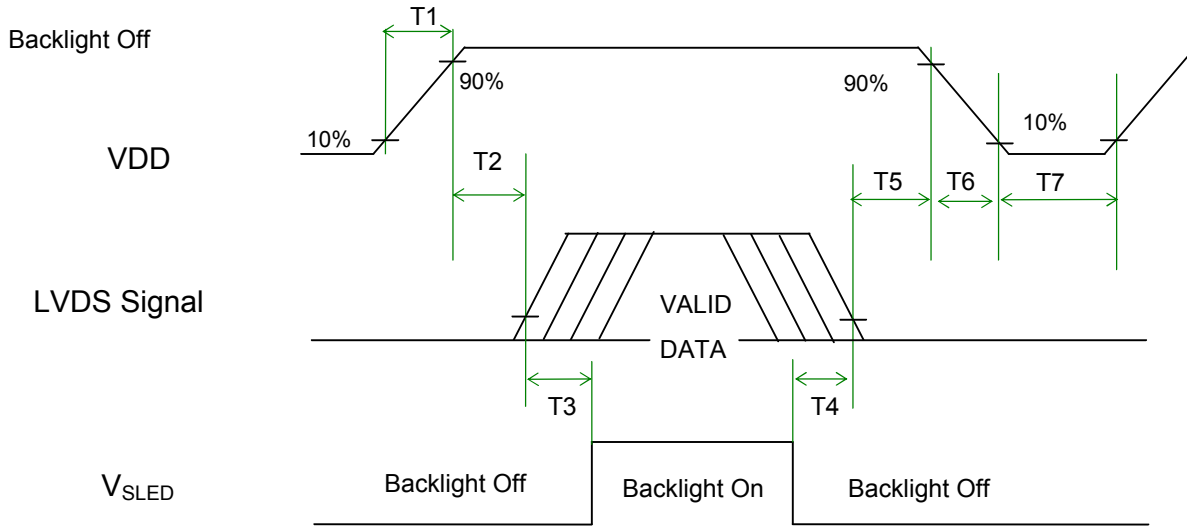
Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	*Note 3-5* *Note 3-6*
T6	0		200	[ms]	*Note 3-6* *Note 3-7*
T7	1000	-	-	[ms]	

Note 3-5 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.
Note 3-6 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.
Note 3-7 : Voltage of VDO must decay smoothly after power-off. (customer system decide this value)

4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 24.5 inch Backlight Unit. And it includes 76 pcs LED in the LED light bar. (4 strings and 19 pcs LED of one string).
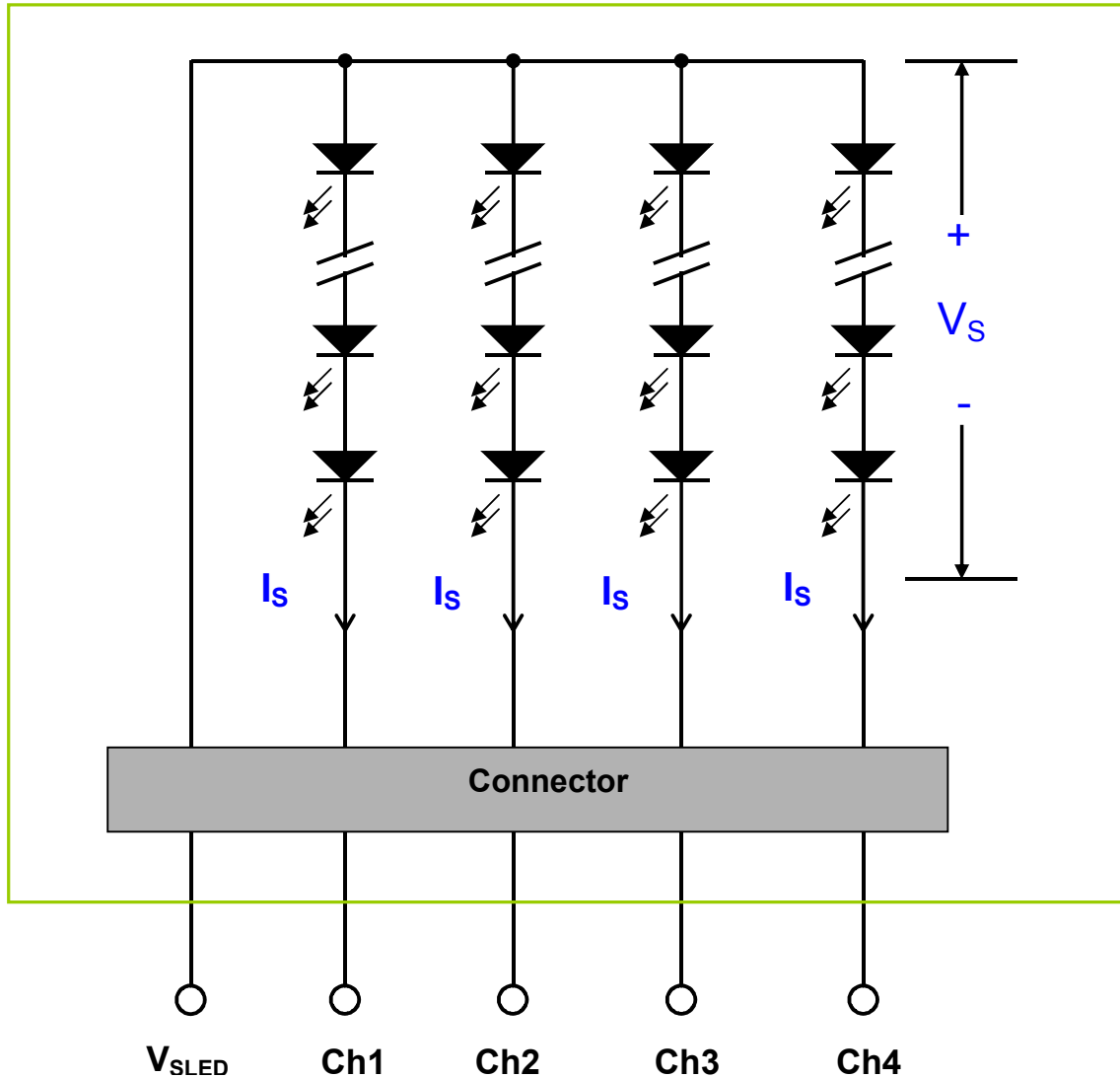
4.2 Interface Connection

4.2.1 Connector Type

Backlight Connector	Manufacturer	ENTERY
	Part Number	3709K-Q06C-04L
Mating Connector	Manufacturer	ENTERY
	Part Number	H112K-P06N-00B (Non-Locking type) H112K-P06N-13B (Locking type)

Backlight Connector dimension:

HxVxD=7.7x4.35x4, Pitch=1.0(unit=mm),use 6 pin connector
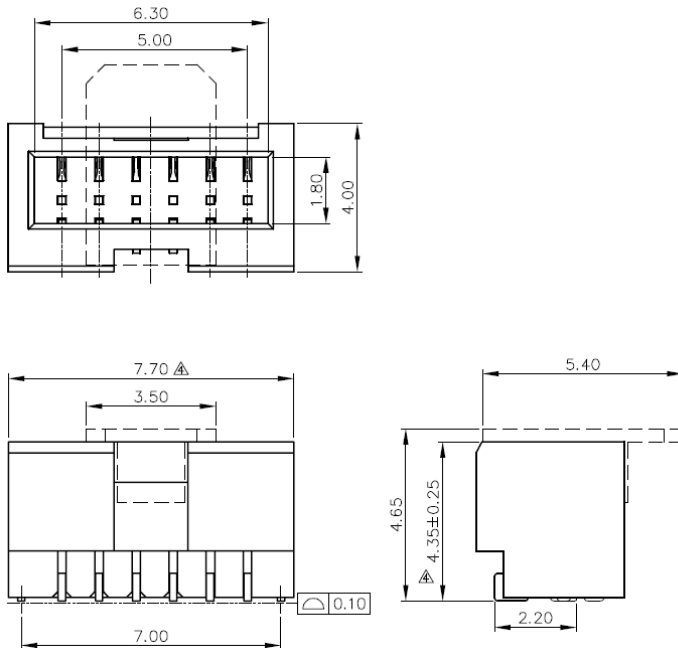
Mating Connector dimension:
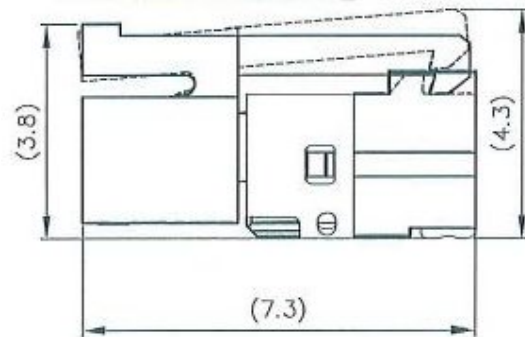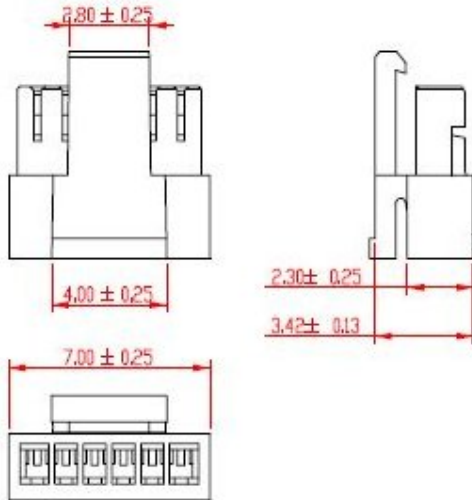
4.2.2 Connector Pin Assignment

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V$_{SLED}$	LED Power Supply Voltage Input Terminal	
4	V$_{SLED}$	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	
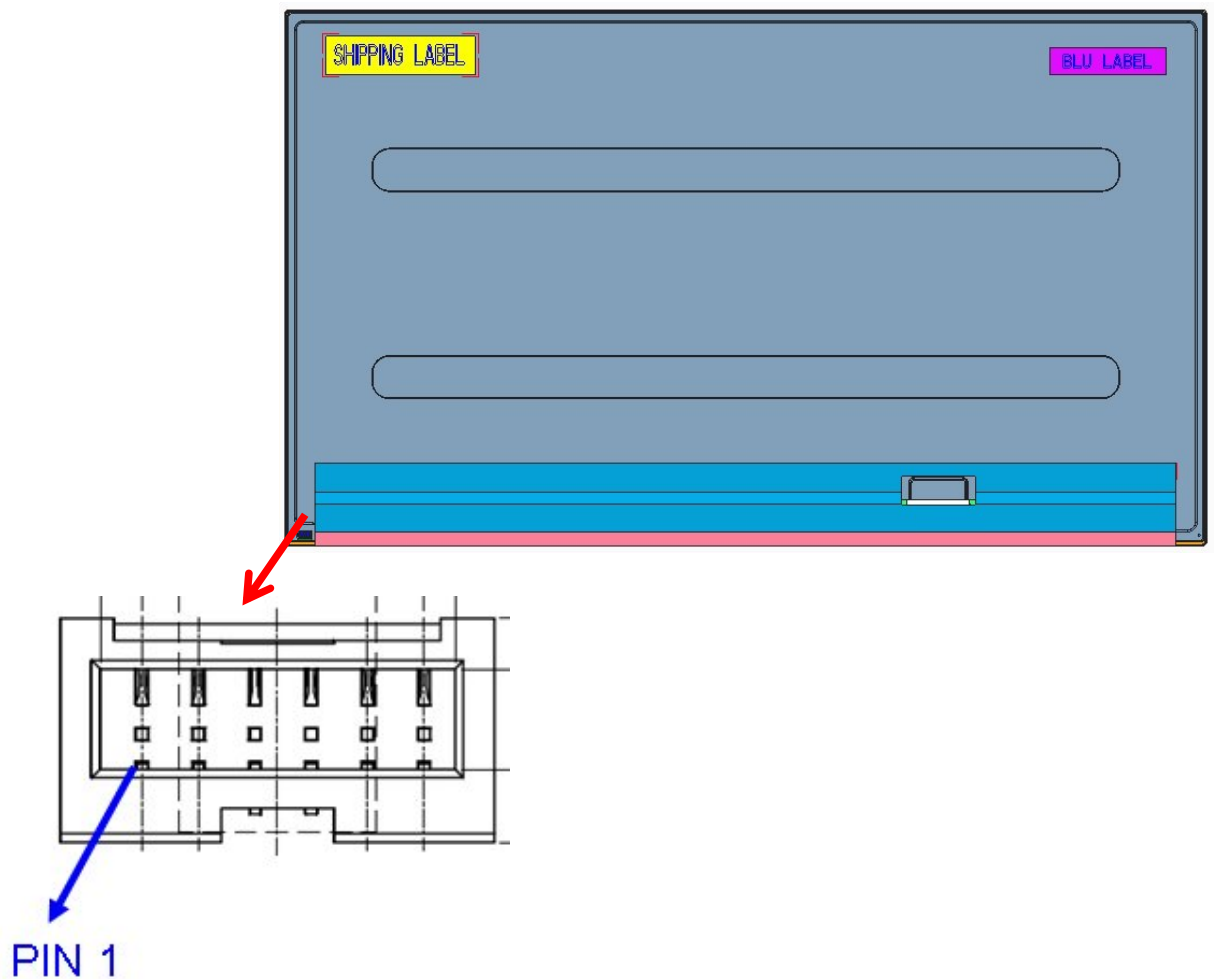
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25℃)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	90	[mA]	100% duty ratio
			150	[mA]	*Duty ratio ≦ 10% Pulse time=10 ms*
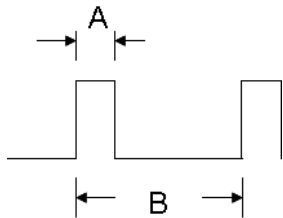
Duty ratio= (A / B) X 100% ;   (A: Pulse time, B: Period)

4.3.2 Recommended Operating Condition

(Ta=25℃)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	50	55	[mA]	100% duty ratio of LED chip, *Note 4-6*
Vs	LED String Voltage	51.3	55.1	58.9	[Volt]	Is=50mA @ 100% duty ratio; *Note 4-1, Note 4-5* *Note 4-7*
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	3.8	[Volt]	Is=50mA @ 100% duty ratio; *Note 4-2*
P$_{BLU}$	LED Light Bar Power Consumption	-	11.02	11.78	[Watt]	*Note 4-3*
LT$_{LED}$	LED Life Time	30,000	-	-	[Hour]	*Note 4-4*
OVP	Over Voltage Protection in system board	110% Vsmax	-	-	[Volt]	*Note 4-5*

Note 4-1: V_S (Typ.) = V_F (Typ.) X LED No. (one string);

a. V_F: LED chip forward voltage, V_F (Min.)=2.7V, V_F(Typ.)=2.9V, V_F(Max.)=3.1V

b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective V_F (Min.) & V_F(Max.);

Note 4-2: ΔV_S (Max.) = ΔV_F X LED No. (one string);

a. ΔV_F: LED chip forward voltage deviation; (0.2 V , each Bin of LED V_F)

Note 4-3: P_{BLU} (Typ.) = Vs (Typ.) X Is (Typ.) X 4 ; (4 is total String No. of LED Light bar)

P_{BLU} (Max.) = Vs (Max.) X Is (Typ.) X 4 ;

Note 4-4: Definition of life time:

a. Brightness of LED becomes to 50% of its original value

b. Test condition: Is = 50mA and 25℃ (Room Temperature)

Note 4-5: Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

Note 4-6: AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

Note 4-7: Ensure that the LED light bar is not subjected either forward or reverse voltage while monitor set is on standby mode or not in use.

5 Reliability Test

AUO reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50℃, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50℃, 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0℃, 300hours	
High Temperature Storage (HTS)	Ta= 60℃, 300hours	
Low Temperature Storage (LTS)	Ta= -20℃, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Thermal Shock Test (TST)	-20℃/30min, 60℃/30min, 100 cycles	*Note 5-1*
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 15KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	*Note 5-2*
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft	

Note 5-1: a. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test.

        b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed

        No data lost

        Self-recoverable

        No hardware failures.

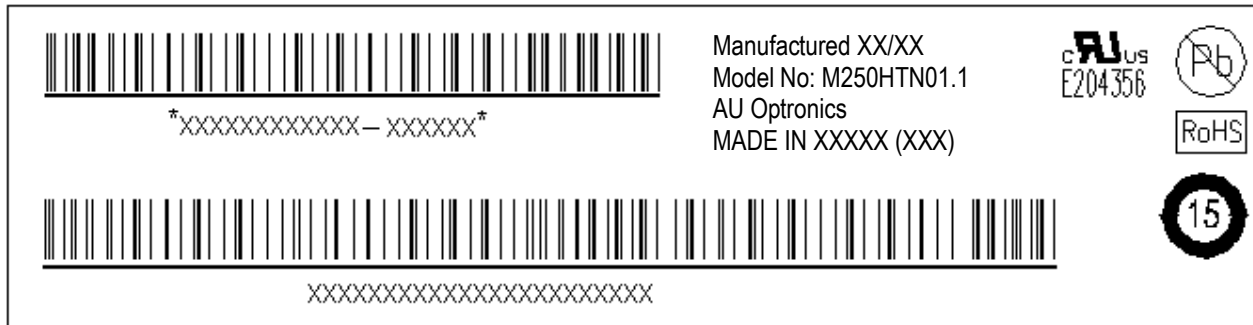**Note 5-3:** Result Evaluation Criteria:

        TFT-LCD panels test should take place after gradually cooling enough at room temperature

        In the normal application, there should be no particular problems that may affect the

display function.

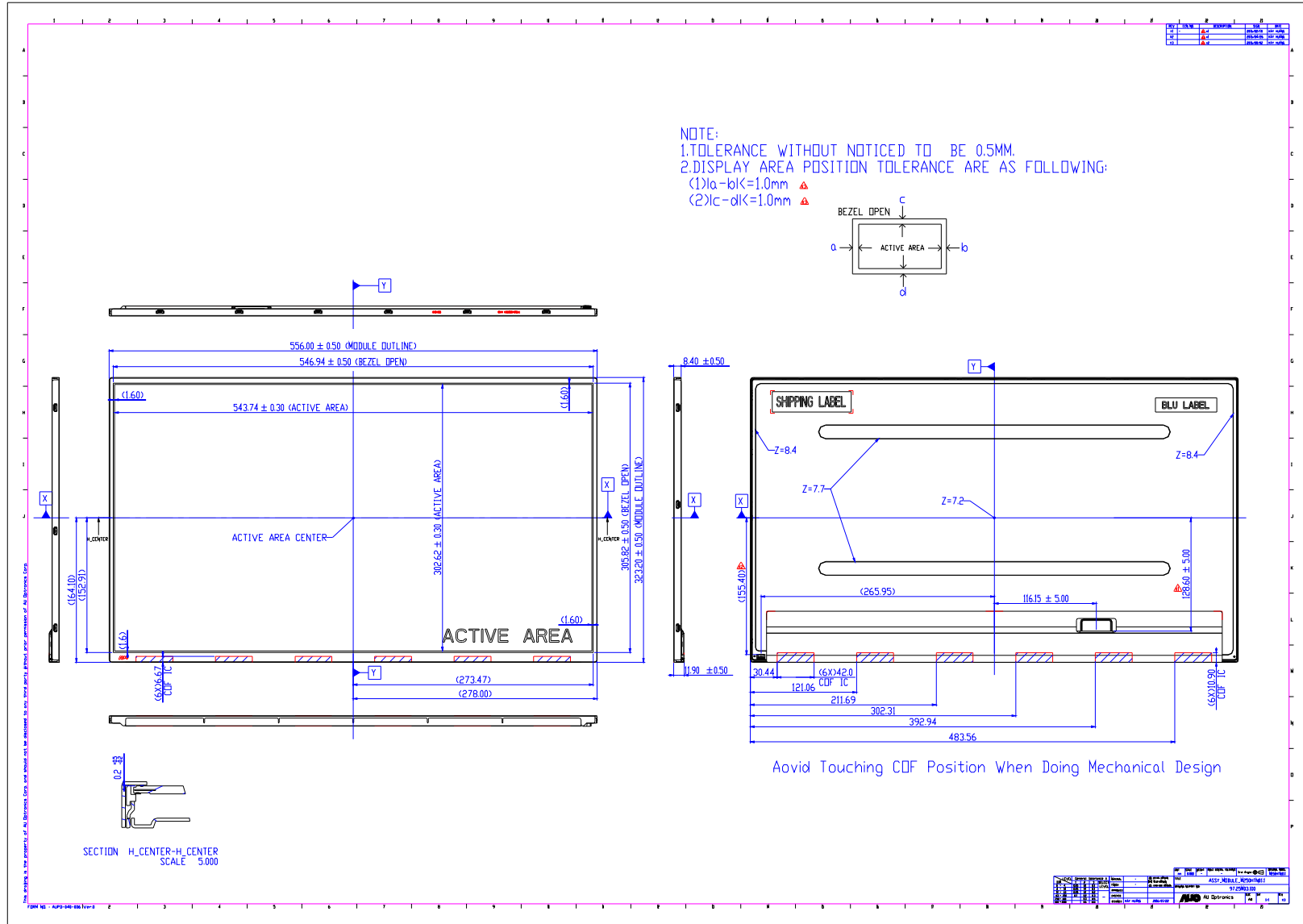## 6 Shipping Label

The label is on the panel as shown below:

Note 6-1: For Pb Free products, AUO will add for identification.

Note 6-2: For RoHS compatible products, AUO will add for identification.

Note 6-3: For China RoHS compatible products, AUO will add for identification.
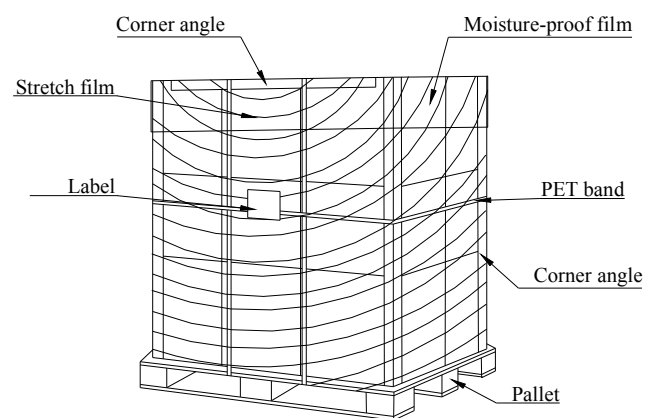
Note 6-4: The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

7 Mechanical Characteristics

Ver 1.3

8 Packing Specification

8.1 Packing Flow

Module

Paste protect film

Paste lamp wire

1pcs Module/ESD

Top cover

12 pcs/carton

Paper Cushion

Cushion

Carton

Put top cover

" H " Tape

Corner angle

Moisture-proof film

Stretch film

Label

PET band

Corner angle
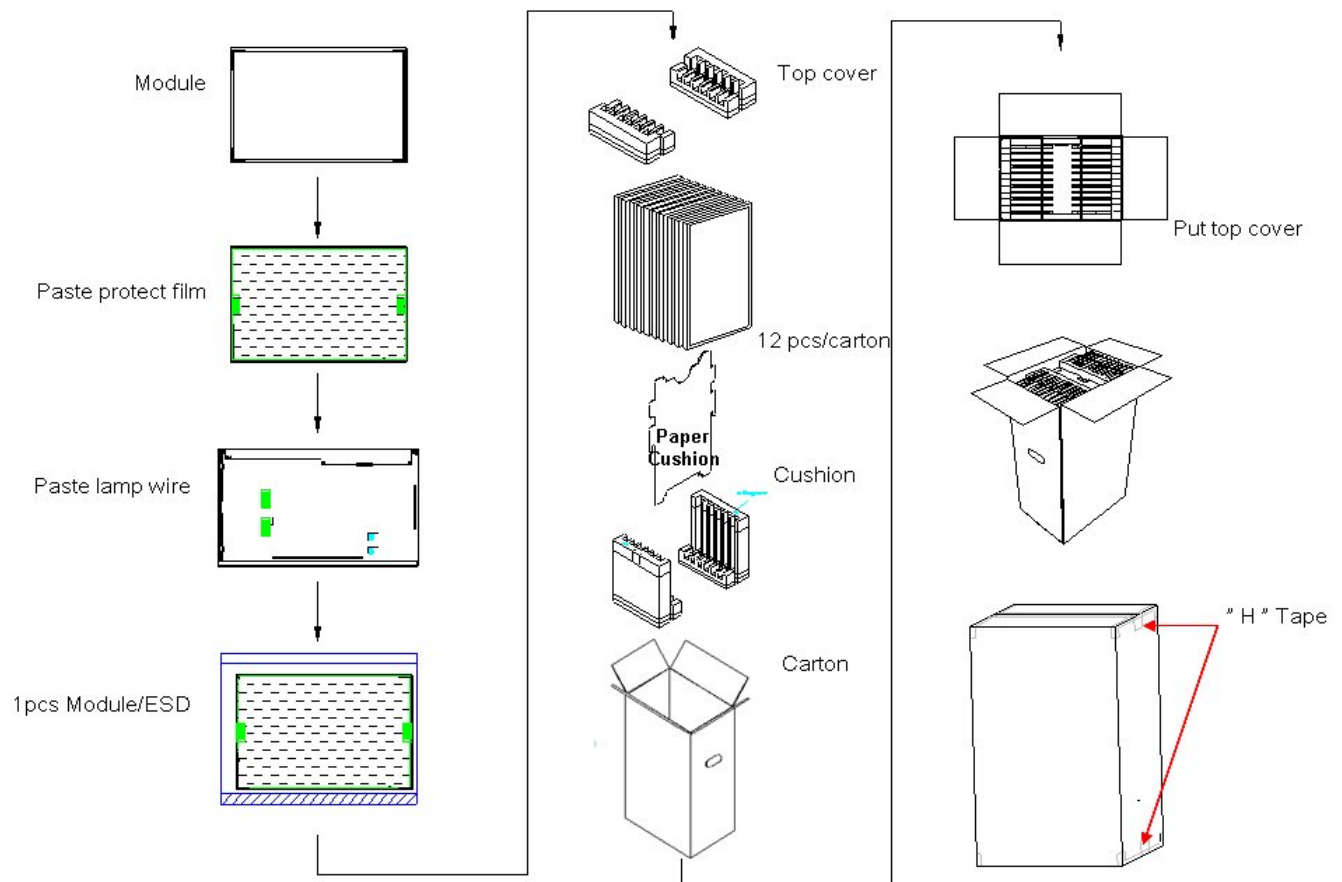
Pallet

8.2 Pallet and shipment information

Item	Specification			Remark
	Q'ty	Dimension	Weight(kg)	
Panel	1	556(H) × 323.2(V) × 11.9(D)mm	2.086	
Cushion	1		0.5	
Box	1	411(L)mm x 280(W)mm x 651(H)mm	1.6	without Panel & cushion
Packing Box	12 pcs/Box	411(L)mm x 280(W)mm x 651(H)mm	27.132	with panel & cushion
Pallet	1	1150(L)mm x 840(W)mm x 132(H)mm	12.00	
Pallet after Packing	16 boxes/pallet	1150(L)mm x 840(W)mm x 1434(H)mm	446.112	

M250HTN01.1

Ver 1.3