

M238HVN01.0

AU OPTRONICS CORPORATION

(Preliminary	Specification

(V) Final Specification

Module	23.8" Color TFT-LCD
Model Name	M238HVN01.0

Customer Date	Approved by Date
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Note: This Specification is subject to change without notice.	AU Optronics corporation



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Record of Revision

Version	Date	Page	Old description	New Description	Remark
0.0	2014/10/28	all	Draft version		
0.1	2014/12/29	P5		TIEAS	
0.1	2014/12/29	P7	2.3 Optical Characteristics	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	
0.1	2014/12/29	P14	3.3.2 Recommended Operating Condition	3.3.2 Recommended Operating Condition State Description	
1.0	2015/1/13	P5	2.1 Display Characteristics	2.1 Display Characteristics Control Contr	



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1 Handling Precautions

- 1) Since polarizer is easily damaged, do not touch or press the surface of polorizer with hand.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
- 10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
- 11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
- 12) Please avoid touching COF position while you are doing mechanical design.
- 13) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- 14) Do not apply the same pattern for a long time, it will enhance relevant defect.

2 General Description

This specification applies to the 23.8 inch wide Color a-Si TFT-LCD Module M238HVN01.0.

The display supports the Full HD - $1920(H) \times 1080(V)$ screen format and 16.7M colors (8-bit RGB data input). The input interface is Dual channel LVDS and this module doesn't contain an driver board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	(23.8")
Active Area	[mm]	527.04(H) x 296.46(V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	274.5 (per one triad) ×274.5
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	VA Mode, Normally Black
White Luminance (Center)	$[cd/m^2]$	250 (Typ.)
Contrast Ratio	-	3000 (Typ.)
Response Time	[msec]	20 (Typ., G/G)
Power Consumption	[Watt]	19.3 (Typ.)
(LCD Module + Backligh unit)		LCD module : PDD (Typ.)=5 @ White pattern,Fv=60Hz
		Backlight unit : P _{BLU} (Typ.) =14.3 @Is=70mA
Weight	[Grams]	2280+/-80
Outline Dimension	[mm]	543.0(H) x 317.4(V) x 11.2(D) (Typ.)
Electrical Interface	-	Dual channel LVDS, 8-bit RGB data input
Support Color	-	16.7M colors
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		0 to +50
Operating	[°C]	-20 to +60
Storage (Shipping)	[°C]	
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 6.0 Compliance

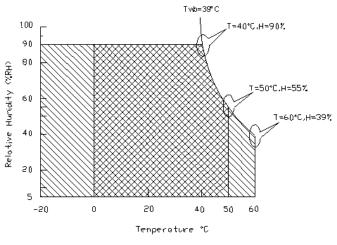
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

			~		
Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[°C]	Note 2-1 Function judged only
НОР	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

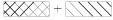
- 1. 90% RH Max (Ta $\leq 39^{\circ}$ C)
- 2. Max wet-bulb temperature at 39°C or less. (Ta \leq 39°C)
- 3. No condensation



Operating Range



Storage Range



2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

Test Condition:

1. Equipment setup: Please refer to *Note 2-2*.

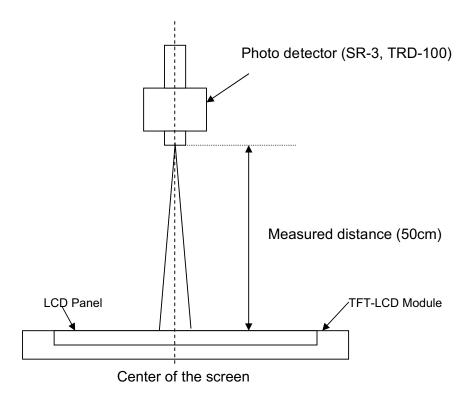
2. Panel Lighting time: 30 minutes

3. VDD=5.0V, Fv=60Hz,Is=70mA,Ta=25°C

Symbol	Description		Min.	Тур.	Max.	Unit	Remark
$L_{\rm w}$	White Luminance (Cente	er of screen)	200	250	ı	[cd/m2]	Note 2-2 By SR-3
L _{uni}	Luminance Uniformity	75	80	1	[%]	Note 2-3 By SR-3	
CR	Contrast Ratio (Center	of screen)	1800	3000	ı	-	Note 2-4 By SR-3
θ_{R}	Horizontal Viewing Angle	Right	75	89	-		
$\theta_{ m L}$	(CR=10)	Left	75	89	-		Note 2-5 By SR-3
Φ_{H}	Vertical Viewing Angle	Up	75	89	ı		
$\Phi_{ m L}$	(CR=10)	Down	75	89	-	[degree]	
θ_{R}	Horizontal Viewing Angle (CR=5)	Right	75	89	-		
$\theta_{ m L}$		Left	75	89	1		
Φ_{H}	Vertical Viewing Angle	Up	75	89	-		
$\Phi_{ m L}$	(CR=5)	Down	75	89	ı		
Тстс	Response Time	Gray to Gray	-	20	-	[msec]	Note 2-6 By TRD-100
R _x		Red x	0.624	0.654	0.684		
R_y		Red y	0.304	0.334	0.364		
G_{x}		Green x	0.290	0.320	0.350		
G_{y}	Color Coordinates	Green y	0.592	0.622	0.652		
B_x	(CIE 1931)	Blue x	0.124	0.154	0.184	-	By SR-3
B_{y}		Blue y	0.049	0.079	0.109		
W_{x}		White x	0.283	0.313	0.343		
W_{y}		White y	0.299	0.329	0.359		
СТ	Crosstalk	-	-	1.5	[%]	Note 2-7 By SR-3	
F_{dB}	Flicker (Center of s	-	-	-20	[dB]	Note 2-8 By SR-3	



Note 2-2: Equipment setup:

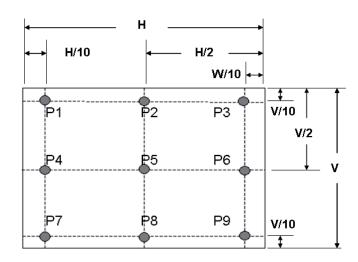


Note 2-3: Luminance Uniformity Measurement

Definition:

 $Luminance\ Uniformity = \frac{Minimum\ Luminance\ of\ 9\ Points\ (P1 \sim P9)}{Maximum\ Luminance\ of\ 9\ Points\ (P1 \sim P9)}$

a. Test pattern: White Pattern



Note 2-4: Contrast Ratio Measurement

Definition:

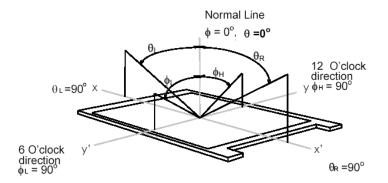
 $Contrast Ratio = \frac{Luminance of White pattern}{Luminance of Black pattern}$

a. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^{\circ}$)

Note 2-5: Viewing angle measurement

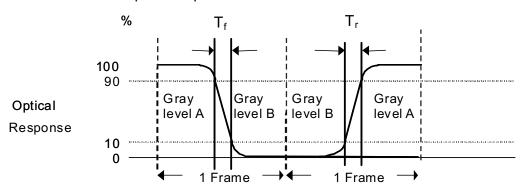
Definition: The angle at which the contrast ratio is greater than 10 & 5.

a. Horizontal view angle: Divide to left & right $(\theta_L \& \theta_R)$ Vertical view angle: Divide to up & down $(\Phi_H \& \Phi_L)$



Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Gray level A" to "Gray level B" (falling time, TF), and from "Gray level B" to "Gray level A" (rising time, TR), respectively. The response time is interval between the 10% and 90% of optical response.



The gray to gray response time is defined as the following table.

Croy Level to Croy Level		Target gray level					
Gray Level to G	Gray Level to Gray Level		L63	L127	L191	L255	
	L0						
	L63						
Start gray level	L127						
	L191						
	L255						

■ T_{GTG_typ} is the total average time at rising time and falling time of gray to gray.

Note 2-7: Crosstalk measurement

Definition:

 $CT = Max. (CT_H, CT_V);$

Where

a.Maximum Horizontal Crosstalk:

$$CT_H = Max. (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

$$CT_V = Max. (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

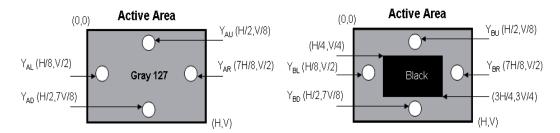
b. Y_{AU} , Y_{AD} , Y_{AL} , Y_{AR} = Luminance of measured location without Black pattern

 Y_{BU} , Y_{BD} , Y_{BL} , Y_{BR} = Luminance of measured location with Black pattern



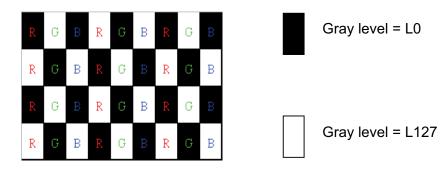
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Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.



R: Red, G: Green, B:Blue

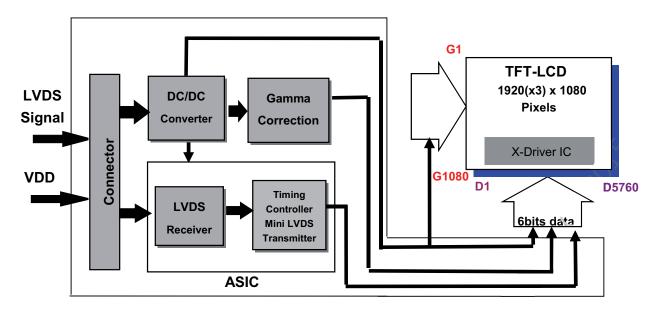
b. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^{\circ}$)



3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 23.8 inch Color TFT-LCD Module.



Control Board

3.2 Interface Connection

3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM
11 1-LCD Connector	Part Number	187034-3009	MSBKT2407P30HB
Matina Connector	Manufacturer	JAE or or Compatible	
Mating Connector	Part Number	FI-X30HL (Locked Type)	

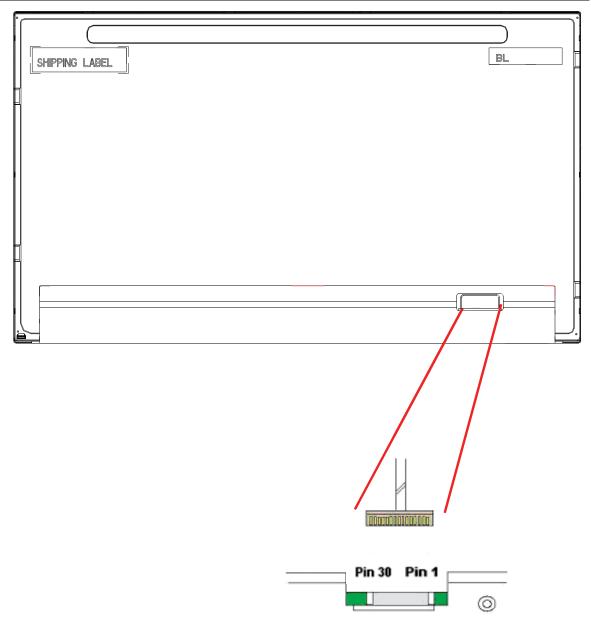
3.2.2 Connector Pin Assignment

PIN#	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	GND	Must Connect to GND	
25	NC	No connection . Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	



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27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	





3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

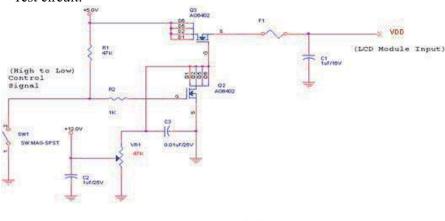
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt	Ta=25°C

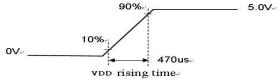
3.3.2 Recommended Operating Condition

3.3.2 Itee01	mmenaca Operating	Contain	011			
Symbol	Description	Min	Тур	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply	-	1.0	1.2	[A]	VDD= 5.0V, All white Pattern , Fv=60Hz
ממו	Input Current (RMS)	-	1.1	1.4	[A]	VDD= 5.0V, All white Pattern, Fy=75Hz
PDD	VDD Power	-	5.0	6.0	[Watt]	VDD= 5.0V, All white Pattern , Fv=60Hz
עעז	Consumption	-	5.5	6.6	[Watt]	VDD= 5.0V, All white Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, All white Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:







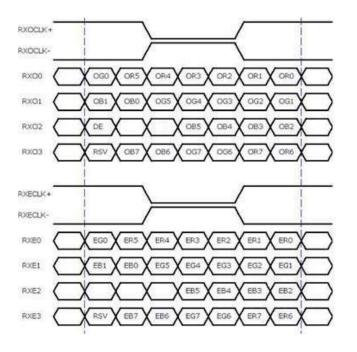
The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format

	:	1		2												1	91	9	19	920	O
1st Line	R (ЗΒ	R	G	В											R	G	В	R	G	В
		•		•							•						•			•	
		•		•							•						•			•	
		•		•													:			:	
		•		•							•						•			•	
		•		:													:			÷	
				:							•						:			:	
		•		•							•						•				
		•		•							•						•			•	
1080 Line	R	G B	R	G	В	•	•	•	•	•	•	•	•	 •	•	R	G	В	R	G	В

3.4.2 LVDS Data Format



8 Bit Color Bit Order												
MSB	R7	G7	В7									
8 8	R6	G6	B6									
R5 G5 B5												
	R4	G4	B4									
	R3	G3	В3									
	R2	G2	B2									
	R1	G1	B1									
LSB	R0	G0	B0									

Note 3-2:

- a. O = "Odd Pixel Data" E = "Even Pixel Data"
- b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).



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3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

	Grav											Col	or Inp	out D	ata											
Color	Gray Level					data , LSE				GREEN data (MSB:G7, LSB:G0)					BLUE data (MSB:B7, LSB:B0)				Remark							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	В4	ВЗ	В2	B1	В0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
	Ю	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Red	:					:			:	:	:	:	:		:	:	:	:	:	:	:	:	:	:		
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	ம	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	ம	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	



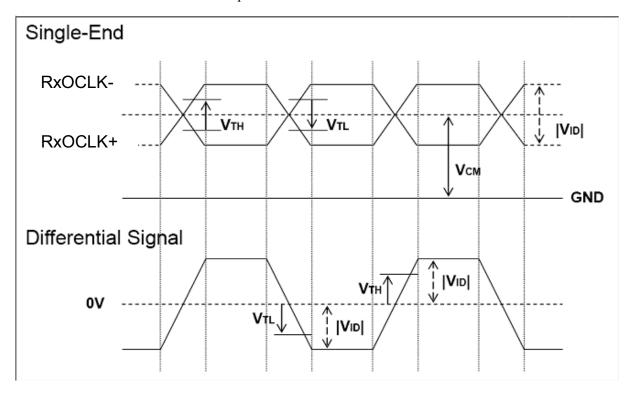
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Тур	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	1	-	+100	[mV]	$V_{CM} = 1.2V$
V_{TL}	LVDS Differential Input Low Threshold	-100	-	1	[mV]	$V_{CM} = 1.2V$
	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH} - $V_{TL} = 200 \text{mV}$

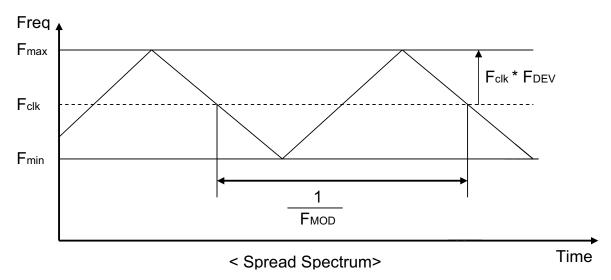
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
$F_{ m DEV}$	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



Fclk: LVDS Clock Frequency

3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Descript	ion	Min.	Тур.	Max.	Unit	Remark
Tv		Period	1094	1130	1836	Th	
Tdisp (v)	Vertical Section	Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	756	Th	
Fv		Frequency	49	60	76	Hz	
Th		Period	1000	1050	1678	Tclk	
Tdisp (h)	Horizontal Section	Active	960	960	960	Tclk	
Tblk (h)	110112011011	Blanking	40	90	718	Tclk	
Fh		Frequency	53.7	67.8	90	KHz	<i>Note 3-3</i>
Telk	LVDS Clock	Period	11.2	14.0	18.6	ns	1/Fclk
Fclk	=:= 2 010 0 11	Frequency	53.7	71.2	90.0	MHz	Note 3-4

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

Fh (Min.) = Fclk (Min.) / Th (Min.);

Fh (Typ.) = Fclk (Typ.) / Th (Typ.);

Fh (Max.)= Fclk (Max.) / Th (Min.);

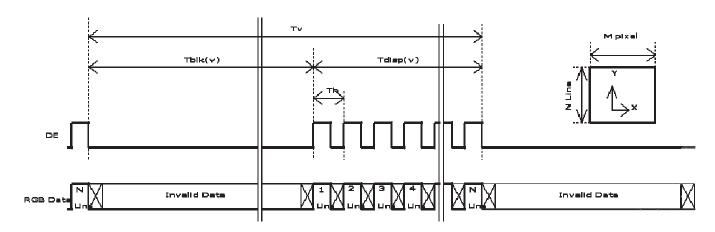
Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

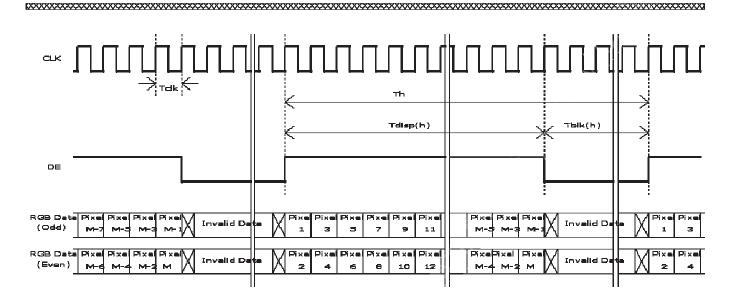
Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.);

Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);

Fclk (Max.) = Fv (Max.) x Th (Typ.) x Tv (Typ.);

3.4.6 Input Timing Diagram

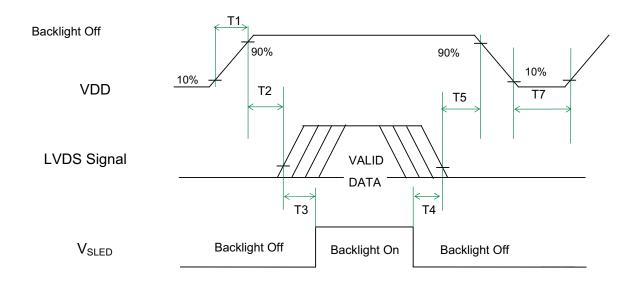






3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol		Value	¥1	Remark	
Symbol	Min.	Тур.	Max.	Unit	
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
Т3	500	-	-	[ms]	
T4	100	-	-	[ms]	
Т5	0		50	[ms]	Note 3-5 Note 3-6
T7	1000	-	-	[ms]	

Note 3-5: Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

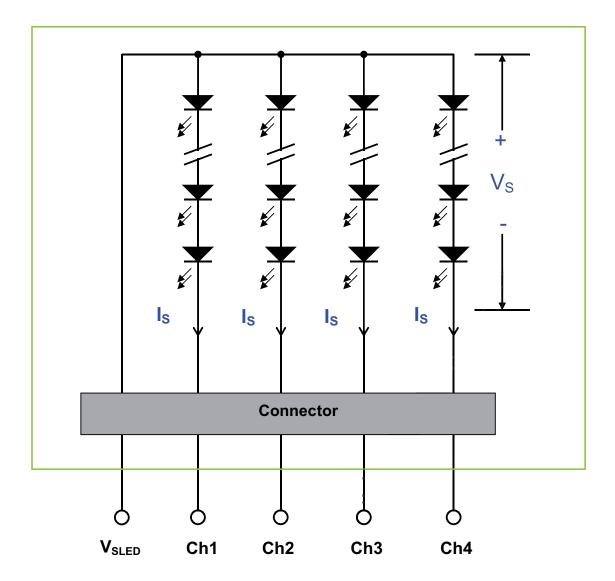
Note 3-6: During T5 period, please keep the level of input LVDS signals with Hi-Z state.



4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 23.8 inch Backlight Unit. And it includes 68 pcs LED in the LED light bar. (4 strings and 17pcs LED of one string).



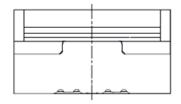
4.2 Interface Connection

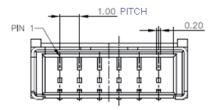
4.2.1 Connector Type

	Manufacturer	ENTERY				
Backlight Connector	Part Number	3707K-S06N-21R (BLACK) (Locking Type, Wire Harness)				
N. i. G.	Manufacturer	ENTERY or Equivalent				
Mating Connector	Part Number	H112K-P06N-00B (Non-Locking type) H112K-P06N-03B (Locking type)				

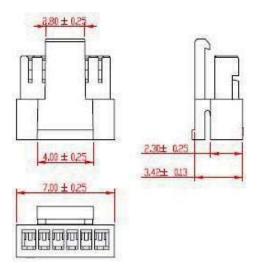
Backlight Connector dimension:

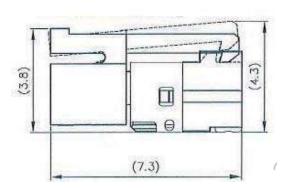
 $H \times V \times D = 13.9 \times 3.00 \times 4.25$, Pitch = 1.0(unit = mm)





Mating Connector dimension:

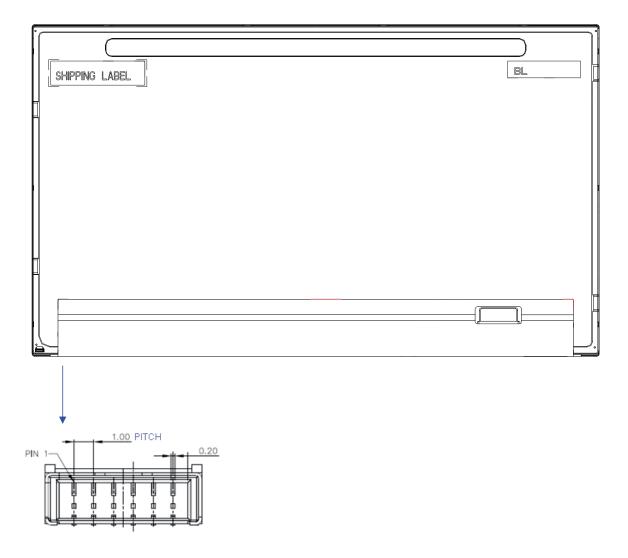






4.2.2 Connector Pin Assignment

Pin#	Symbol	Remark	
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	$V_{ m SLED}$	LED Power Supply Voltage Input Terminal	
4	$V_{ m SLED}$	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	



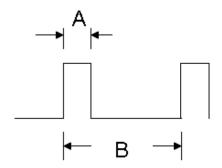
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
			90	[mA]	100% duty ratio
Is	LED String Current	0	150	[mA]	Duty ratio $\leq 10\%$ Pulse time=10 ms



Duty ratio= (A / B) X 100%; (A: Pulse time, B: Period)

4.3.2 Recommended Operating Condition

(Ta=25°C)

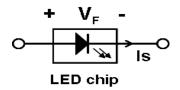
Symbol	Description	Min.	Тур.	Max.	Unit	Remark
Is	LED String Current	-	70	77	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	47.6	51	54.4	[Volt]	Is=70mA @ 100% duty ratio; <i>Note 4-1, Note 4-5</i>
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	3.4	[Volt]	Is=70mA @ 100% duty ratio; <i>Note 4-2</i>
P_{BLU}	LED Light Bar Power Consumption	-	14.3	15.2	[Watt]	Note 4-3
LT_{LED}	LED Life Time	30,000	-	-	[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	110% Vs (max)	-	-	[Volt]	Note 4-5



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- *Note 4-1:* Vs (Typ.) = V_F (Typ.) X LED No. (one string);
 - a. V_F: LED chip forward voltage, V_F (Min.)=2.8V, V_F(Typ.)=3.0V, V_F(Max.)=3.2V
 - b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective V_F (Min.) & V_F(Max.);



- **Note 4-2:** ΔVs (Max.) = $\Delta V_F X$ LED No. (one string);
 - a. ΔV_F: LED chip forward voltage deviation; (0.2 V, each Bin of LED V_F)
- Note 4-3: P_{BLU} (Typ.) = Vs (Typ.) X Is (Typ.) X 4; (4 is total String No. of LED Light bar) P_{BLU} (Max.) = Vs (Max.) X Is (Typ.) X 4;
- *Note 4-4:* Definition of life time:
 - a. Brightness of LED becomes to 50% of its original value
 - b. Test condition: Is = 70mA and 25°C (Room Temperature)
- *Note 4-5:* Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

Note 4-6: AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.



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5 Reliability Test

AUO reliability test items are listed as following table. (Bare Panel only)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50°C, 50%RH, 300hours	
Low Temperature Operation (LTO)	$Ta=0^{\circ}C$, 300hours	
High Temperature Storage (HTS)	$Ta=60^{\circ}C$, 300hours	
Low Temperature Storage (LTS)	Ta= -20°C, 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	Note 5-1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Statio Discharge)	Contact Discharge: \pm 15KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point.	- Note 5-2
ESD (Electro Static Discharge)	Air Discharge: \pm 15KV, 150pF(330 Ω) 1sec 8 points, 25 times/ point.	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft	

- **Note 5-1**: a. A cycle of rapid temperature change consists of varying the temperature from -20° C to 60° C, and back again. Power is not applied during the test.
 - b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed

No data lost Self-recoverable No hardware failures.

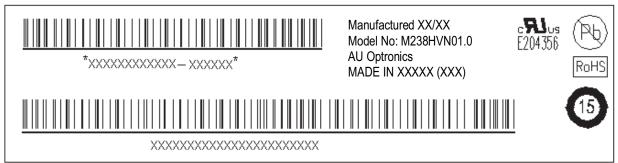


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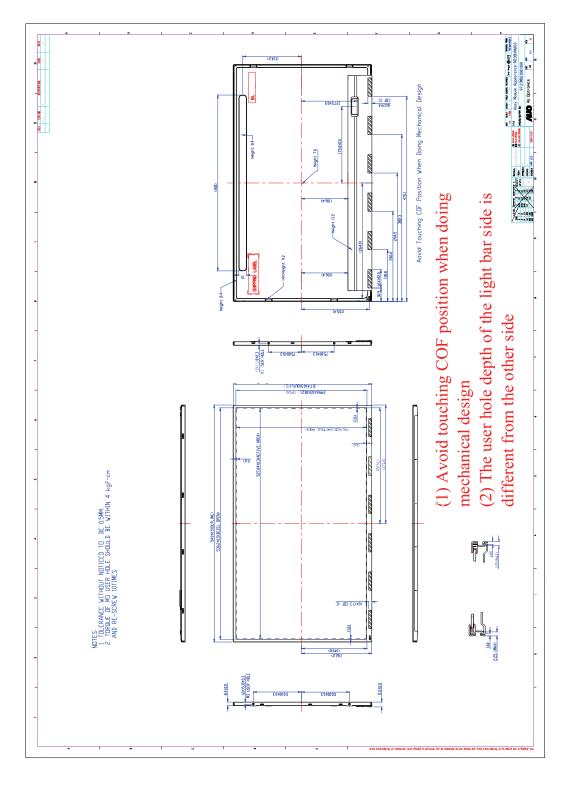
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6 Shipping Label

The label is on the panel as shown below:

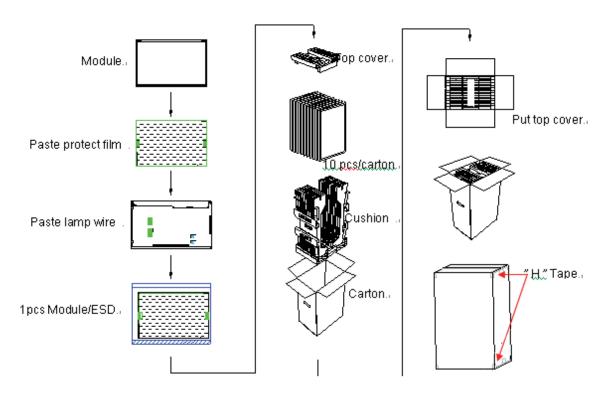


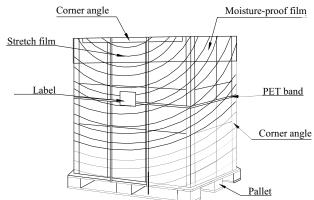
- Note 6-1: For Pb Free products, AUO will add (9) for identification.
- Note 6-2: For RoHS compatible products, AUO will add RoHS for identification.
- Note 6-3: For China RoHS compatible products, AUO will add for identification.
- **Note 6-4:** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.



8 Packing Specification

8.1 Packing Flow





8.2 Pallet and shipment information

012 I WII 01 WII 0 II					
Item		Remark			
nem	Q'ty	Dimension	Weight(kg)	Remark	
Panel	1	543.0(H) x 317.4(V) x 11.2(D) (Typ.)	2.280		
Cushion	1	-	1.7		
Box	1	406(L)mm x 281(W)mm x 651(H)mm	1.2	without Panel & cushion	
Packing Box	10 pcs/Box	406(L)mm x 281(W)mm x 651(H)mm	25.7	with panel & cushion	
Pallet	1	1150(L)mm x 840(W)mm x 138(H)mm	12		
Pallet after Packing	16 boxes/pallet	1150(L)mm x 840(W)mm x 1440(H)mm	423.2		