

Version: 1.2

Total pages: 20

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AU OPTRONICS CORPORATION

Product Functional Specifications

10.4" SVGA Color TFT-LCD Module

Model Name: G104SN02

V.0

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Product Functional Specification

10.4 inch SVGA Color TFT LCD Module
Model Name: B104SN02
V.0

This is a RoHS compliant product

- (...) Preliminary Specification
- (u) Final Specification

Note: This Specification is subject to change without notice.



Record of Revision

Version	Revise Date	Page	Old Description	New Description	Remark
0	28/May./2003	20	First draft	First draft	
0.1	04/July/2003	14		Add CR min. 250	
		14		Add Brightness min. 280nit	
		17		Packing: 20pcs	
		18		Add 5.6±0.3 mm dimensions	
		19		Add rear view dimensions	
0.2	01/August/2003	6	Pin 18: Aging (High)	Cancel the note for Aging	
		10	Input signal timing	Describe the same as B104SN01	
		12	Lamp current (I _L)	Min: 5, Max: 7	
		12	Backlight driving conditions	Note 1& 4: I _L = 6Ma	
		12	Note 5: Pin no. 1~2	Pin no. 1~3, CN2: JST BHR-03VS-1 Mating: SM02(8.0)B-BHS-1-TB	
		17	Packing form	Add weight, carton outline dimension and drawing	
	12/Augst/2003	12	Note 5: Mating connector	SM03 (4.0) B-BHS-1-TB	
0.3	03/Dec./2003	5	Dimension 11max. (D) Weight: 510(g)	Dimension 10.6typ. (D) Weight: 485±10(g)	
		13	Contrast ration: 250(min), 350(typ)	Contrast ratio: 400(min), 500(typ)	
		13	Brightness: 280(min), 350(typ)	Brightness: 320(min), 400(typ)	
		13	White uniformity: 1.43	White uniformity: 1.3	
		18-19		Drawing updated	
1.0	14/Jan./2005			Final spec.	
1.1	01/Feb./2005		Final spec.	Updated	
1.2	05/May./2006		N/A	Add product identification label	



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Product identification label

The main difference between B104SN02 and G104SN02 is that G104SN02 is a RoHS compliant product. Therefore AUO provide the differentiation of shipping label, being able to clarify easily from label what the difference is.

B104SN02 V0 (Non-RoHS compliance)

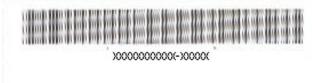


XXXXXX-XXXXXXXXX

Manufactured 06/11 Model No. B 1 04 SNO2 All Optronics MADE IN CHINA (\$1)

V.O HAXXX c**%**us E204356

G104SN02 V0 (RoHS compliance)



Manufactured 06/11 Model No: G1045N02 AU Optronics MADE IN CHINA (S1)

CAUS E204356 RoHS

Green Design (RoHS Compliance)



A. Physical specifications

NO.	Item	Specification	Remark
1	Display resolution (pixel)	800(H)×600(V)	
2	Active area (mm)	211.2(H)×158.4(V)	
3	Screen size (inch)	10.4(Diagonal)	
4	Pixel pitch (mm)	0.264(H)×0.264(V)	
5	Color configuration	R. G. B. Vertical stripe	
6	Overall dimension (mm)	243.0(W)×184.0(H)×10.6(D)	Note 1
7	Weight (g)	485 ±10	

Note 1: Refer to Fig. 1. & 2.



B. Electrical specifications

1. Pin assignment

(1) Input signal interface

	Symbol	Function	Etc.
1	V _{CC}	+3.3 V power supply	
2	V _{CC}	+3.3 V power supply	
3	GND	Ground	
4	GND	Ground	
5	RxIN0-	LVDS receiver signal channel 0	
6	RxIN0+		
7	GND	Ground	
8	RxIN1-	LVDS receiver signal channel 1	
9	RxIN1+		
10	GND	Ground	
11	RxIN2-	LVDS receiver signal channel 2	
12	RxIN2+		
13	GND	Ground	
14	CKIN-	LVDS receiver signal clock	
15	CKIN+		
16	GND	Ground	
17	NC	No Connection	
18	NC	No Connection	
19	GND	Ground	
20	GND	Ground	

CN1 (20P) connector: HRS DF 19K-20P-1H or compatible



(2) LVDS transmitter/receiver signal mapping

	Symbol	Function	
TxIN0	R0	Red data (LSB)	
TxIN1	R1	Red data	
TxIN2	R2	Red data	6 bit rad display data
TxIN3	R3	Red data	6 bit red display data
TxIN4	R4	Red data	
TxIN5	R5	Red data (MSB)	
TxIN6	G0	Green data (LSB)	
TxIN7	G1	Green data	
TxIN8	G2	Green data	C hit areas display data
TxIN9	G3	Green data	6 bit green display data
TxIN10	G4	Green data	
TxIN11	G5	Green data (MSB)	
TxIN12	В0	Blue data (LSB)	
TxIN13	B1	Blue data	
TxIN14	B2	Blue data	C hita hiya dianlay data
TxIN15	В3	Blue data	6 bits blue display data
TxIN16	B4	Blue data	
TxIN17	B5	Blue data (MSB)	
TxIN18	Hs	Horizontal sync.	
TxIN19	Vs	Vertical sync.	
TxIN20	DE	Data enable	
TxCLKIN	CLK	Clock	Dot clock

2. Absolute maximum ratings

(GND = 0 V)

Parameter	Symbol	Val	ues	Unit	Remark
i arameter	Symbol	Min.	Max.		Kemark
Power voltage	V _{cc}	-0.3	4	V_{DC}	At 25℃
Input signal voltage	V_{LH}	-0.3	V _{CC} +0.3	V_{DC}	At 25°℃
Operating temperature	Тор	0	+50	$^{\circ}\!\mathbb{C}$	Note 1
Storage temperature	T _{ST}	-20	+60	$^{\circ}\!\mathbb{C}$	Note 1

Note 1:The relative humidity must not exceed 90% non-condensing at temperatures of 40 $^{\circ}$ C or less. At temperatures greater than 40 $^{\circ}$ C, the wet bulb temperature must not exceed 39 $^{\circ}$ C. When operate at low temperatures, the brightness of CCFL will drop and the life time of CCFL will be reduced.

Note 2:The unit should not be exposed to corrosive chemicals.

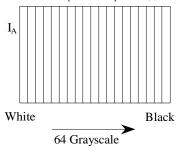


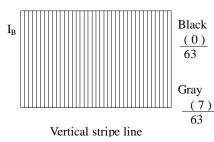
3. Electrical characteristics

a. Typical operating conditions

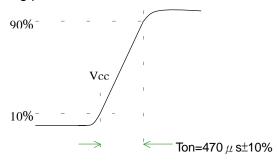
Item		Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Input voltage	V _{CC}	3.0	3.3	3.6	V	
supply	Current	I _A		230		mArms	No.
voltage consumption		I _B		260	310	mArms	Note 1
	Inrush current		-	-	1500	mApeak	Note 2
Internal logic	Low voltage	V _{IL}	0	-	0.3 V _{CC}		
logic	High voltage	V _{IH}	0.7V _{CC}	-	V _{cc}		
Power ripple voltage		V_{RP}	-	-	100	mVp-p	

Note 1:Effective value (mArms) at $V_{CC} = 3.3 \text{ V}/25^{\circ}\text{C}$.

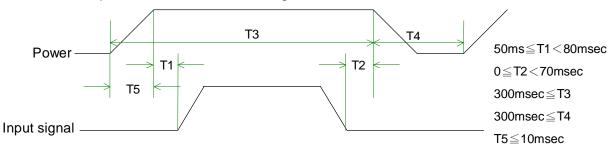




Note 2: Refer to the following power-on condition.



Sequence of Power-on/off and signal-on/off



Apply the lamp voltage within the LCD operating range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal.



Caution

The above on/off sequence should be applied to avoid abnormal function in the display. In case of handling:

Make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

b. Display color v.s. input data signals

Display colors						D	ata s	ignal	(0:	Low	leve	ıl, 1:	High	leve	el)				
Display	colors	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
colors	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
COIOIS	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	1																		
grayscale	↓																		
	bright					_			_										
		1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	Î																		
grayscale	↓																		
	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	Λ	Λ	0	Λ	0
		0	0	0	0	0	0	1	1	1	1	1	1 0	0	0 0	0	0	0	0
	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Green																		
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark	0	0 0	0	0	0	0 0	0	0	0	0 0	0 0	0 0	0	0 0	0	0 0	0 1	1 0
	Daik ↑	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	U	1	U
Blue																			
grayscale	briaht																		
	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Note: Fook I				liania			1 ~~~	J			~ +b.	- C h			•		200.00	_ '	•

Note: Each basic color can be displayed in 64 gray scales using the 6 bit data signals. By combining the 18-bit data signals(R, G, B), the 262, 144 colors can be achieved on the display.



c. Input signal timing

Timing diagrams of input signal are shown in Fig 2.

(1) Timing characteristics of input signals

(a) DE mode

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock frequency	Fck	38	40	48	MHz	
Horizontal blanking	Thb1	50	256	500	Clk	
Vertical blanking	Tvb1	10	28	150	Th	

(b) HV mode

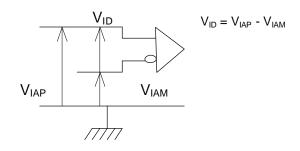
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock frequency	Fck	38	40	48	MHz	
Hsync period	Th	850	1056	1300	Clk	
Hsync pulse width	Thw	10	128	-	Clk	
Hsync front porch	Thf	15	40	-	Clk	
Hsync back porch	Thb	10	88	-	Clk	
Hsync blanking	Thb1	50	256	500	Clk	
Vsync period	Tv	610	628	750	Th	
Vsync pulse width	Tvw	1	4	-	Th	
Vsync front porch	T∨f	0	1	-	Th	
Vsync blanking	Tvb1	10	28	150	Th	
Hsync/Vsync phase shift	Tvpd	2	320	-	Clk	

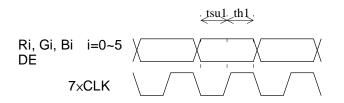
Item	Symbol	Value	Unit	Description
Horizontal display start	The	218	Clk	After falling edge of Hsync, counting 218clk, then getting valid data from 219th clk's data.
Vertical display start	Tve	25	Th	After falling edge of Vsync, counting 25th, then getting 26th Th's data.



(2) The timing condition of LVDS

Item	Symbol	Min.	Тур.	Max.	Unit
The differential level	VID	0.1	-	0.6	V
The common mode input voltage	VIC	<u>VID </u> 2	-	$2.4 - \frac{ VID }{2}$	V
The input setup time	tsu1	500	-	-	ps
The input hold time	th1	500	-	-	ps





d. Display position

D(1,1)	D(2,1)	 D(X,1)	 D(799,1)	D(800,1)
D(1,2)	D(2,2)	 D(X,2)	 D(799,2)	D(800,2)
:		 :	 •	•
D(1,Y)	D(2,Y)	 D(X,Y)	 D(799,Y)	D(800,Y)
:		 :	 •	:
D(1,599)	D(2,599)	 D(X,599)	 D(799,599)	D(800,599)
D(1,600)	D(2,600)	 D(X,600)	 D(799,600)	D(800,600)



e. Backlight driving conditions

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp voltage	V _L	456	480	504	Vrms	Note 1
Lamp current	IL	3	6	7	mArms	Note 1
Power consumption	P _L	-	8.3	-	W	Note 2
	Vs	-	-	905	\/#***	T=0°C
Lamp starting voltage		-	-	725	Vrms	T=25°C
Frequency	F∟	-	50	-	KHz	Note 3
Lamp life time	LL	45,000	50,000	-	Hr	Note 1, 4

Note 1: $T=25^{\circ}C$, $I_L=6mA$

- Note 2: Inverter should be designed with the characteristic of lamp. When you are designing the inverter, the output voltage of the inverter should comply with the following conditions.
 - (1) The area under the positive and negative cycles of the waveform of the lamp current and lamp voltage should be area symmetric (the symmetric ratio should be larger than 90%).
 - (2) There should not be any spikes in the waveform.
 - (3) The waveform should be sine wave as possible.
 - (4) Lamp current should not exceed the maximum value within the operating Temperature (It is prohibited to over the maximum lamp current even if operated in The non-guaranteed temperature). When lamp current over the maximum value for a long time, it may cause fire. Therefore, it is recommend that the inverter should have the current limited circuit.
 - (5) Power consumption 8.3 Watt (without inverter, All black pattern) @LCM circuit 1.3 W (typ), Backlight 7W (typ)
- Note 3: Lamp frequency may produce interference with horizontal synchronous frequency and this may cause line flow on the display. Therefore lamp frequency shall be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.
- Note 4: Brightness (I_L =6mA) to be decrease to the 50% of the initial value.

Note 5: CN2 connector (backlight): JST BHR-03VS-1 Mating connector: JST SM03(4.0)B-BHS-1-TB

Pin no.	Symbol	Function	Remark
1	L	CCFL power supply (GND)	Cable color: White
2	Н	CCFL power supply (H.V.)	Cable color: Pink
3	Н	CCFL power supply (H.V.)	Cable color: Pink



C. Optical specifications (Note 1, Note 2)

lto-m	Symbol Condition	Condition	Specification			I Imit	Damanla
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response time Rising time Falling time	Tr Tf	<i>θ</i> =0°	-	10 25	20 30	ms	Note 4
Contrast ratio	CR	θ =0°	400	500	-		Note 3,5
Viewing angle Top Bottom Left Right		CR≧10	- - -	40 60 60 60	- - -	deg.	Note 3,6
Brightness	Y _L	θ =0°	320	400	-	nit	Note 3,7,8,9
Color chromaticity(CIE)	Wx	0 0 °	0.290	0.320	0.350		Note 3,8,9
Color chromaticity(CIE)	Wy	<i>θ</i> =0°	0.300	0.330	0.360		
	Rx		0.540	0.570	0.600		
	Ry		0.290	0.320	0.350		
	Gx		0.270	0.300	0.330		
	Gy		0.530	0.560	0.590		
	Bx		0.115	0.145	0.175		
	Ву		0.100	0.130	0.160		
White uniformity	δw		-	-	1.3		Note 3,9,10

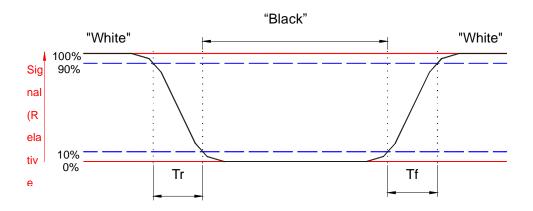
Note 1: Ambient temperature = 25° C.

Note 2: To be measured in dark room after backlight warm up 30 minutes.

Note 3: To be measured with a viewing cone of 1 by Topcon luminance meter BM-5A.

Note 4: Definition of response time:

The output signals of BM-7 are measured when the input signals are changed from "Black" to "White" (falling time) and from "White" to "Black" (rising time), respectively. The response time means the interval between the 10% and 90% of amplitudes. Refer to figure as below.





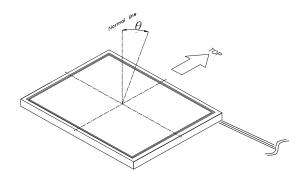
Note 5. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

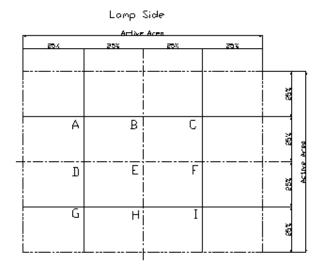
Contrast ratio (CR)= Luminance on the white raster

Luminance on the black raster

Note 6: Definition of viewing angle:



Note 7: Definition of the 9 points (from A to I) on panel, refer to figure as below



Note 8: Definition of brightness: To measure at center point of the screen (E)

Note 9: Driving conditions for CCFL: I_L =6 mA, 50KHz Frequency

Note 10: Definition of white uniformity:

 $\delta w = \frac{\text{Maximum Luminance of nine points (brightness)}}{\text{Minimum Luminance of nine points (brightness)}}$



D. Reliability test items (Note 1)

Test tem	Test Condition	Remark
High temperature storage	60℃, 240Hrs	Note 1, 2, 3
Low temperature storage	-20℃, 240Hrs	Note 1, 2, 3
High temperature & high humidity operation	40°C, 90%RH, 240Hrs (No condensation)	Note 1, 2, 3
High temperature operation	50°C, 240Hrs	Note 1, 2, 3
Low temperature operation	0℃, 240Hrs	Note 1, 2, 3
Electrostatic discharge (non-operation)	150 pF,150 Ω ,10kV,1 second, 9 position on the panel, 10 times each place	Note 3
Vibration (non-operation)	1.5G, $10H_Z \sim 200H_Z \sim 10H_Z$ 30 minutes for each Axis (X, Y, Z)	Note 1, 2, 3
Mechanical shock (non-operation)	50G/20ms, ±X, ±Y, ±Z half-sin, one time	Note 1, 2, 3
Thermal shock (non-operation)	 -20°C±3°C30minutes 60°C±3°C30minutes 100 cycles Temperature transition time within 5 minutes 	Note 1, 2, 3

Note 1: Evaluation should be tested after storage at room temperature for one hour.

Note 2: There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Note 3: Judgement: 1. Function OK

2. No serious image quality degradation



E. Display quality

The display quality of the color TFT-LCD module should be in compliance with the AUO's OQC inspection standard.

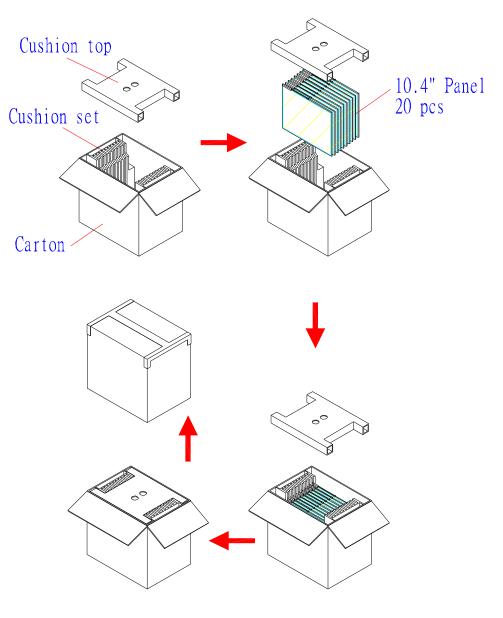
F. Handling precaution

The Handling of the TFT-LCD should be in compliance with the AUO's handling principle standard.

G. Packing form:

NOTE:

- 1. Max. Capacity: 20 pcs LCD Modules / Per Carton
- 2. Max. Weight: 12Kg / Per Carton
- 3. The outside dimension of carton is 570mm(L)x 270mm(W)x 345mm(H)





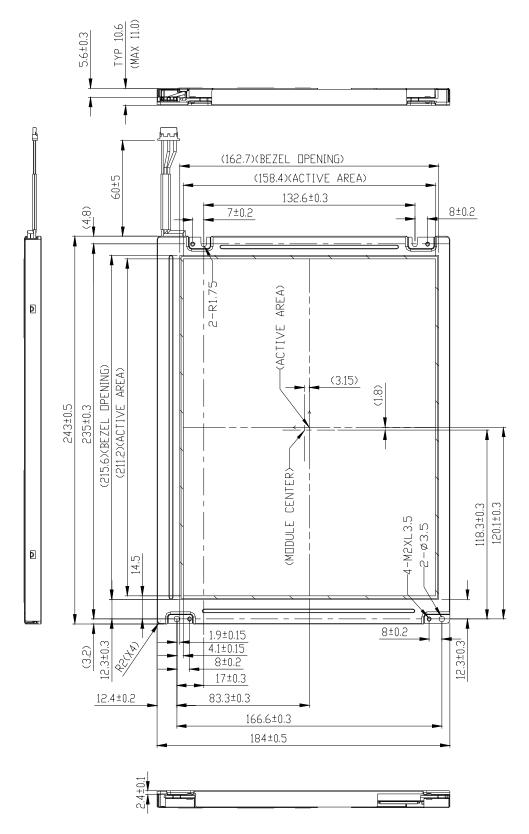


Fig.1 LCM outline dimensions - Front View



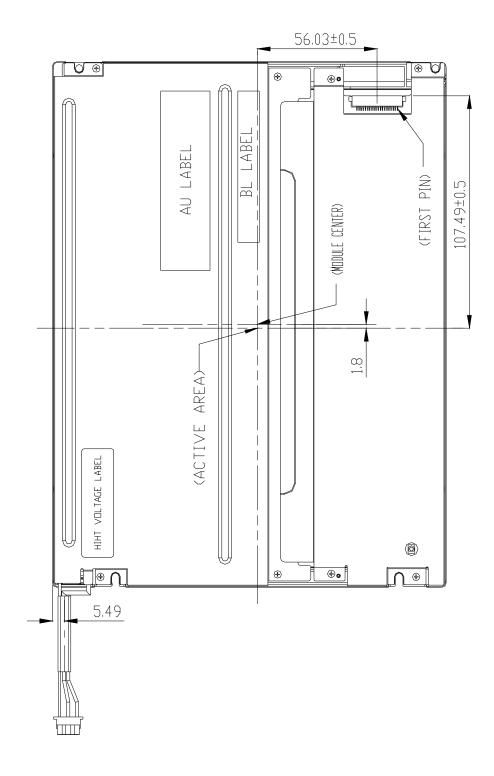


Fig.2 LCM outline dimensions - Back View



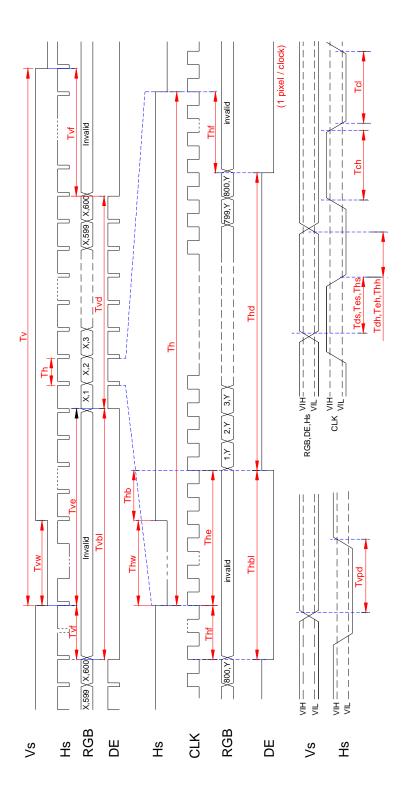


Fig.3 Timing chart