

SPECIFICATION FOR APPROVAL

(♦) Preliminar	y Specification
---	----------	--------------	-----------------

() Final Specification

Title	15.6" HD TFT LCD

Customer	HP
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP156WH2		
Suffix	TLD1		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE				
/					
/					
Please return 1 copy for your confirmation with					

APPROVED BY	SIGNATURE				
K. S. Kwon / S.Manager					
REVIEWED BY	-				
J. H. Park / Manager					
PREPARED BY					
B. T. Jang / Engineer					
B. D. Jun / Engineer					
Products Engineering Dept. LG Display Co., Ltd					

Ver. 0.5 Dec. 12, 2008 1 / 31



Contents

No	ITEM		
	COVER	1	
	CONTENTS	2	
	RECORD OF REVISIONS	3	
1	GENERAL DESCRIPTION	4	
2	ABSOLUTE MAXIMUM RATINGS	5	
3	ELECTRICAL SPECIFICATIONS		
3-1	ELECTRICAL CHARACTREISTICS	6	
3-2	INTERFACE CONNECTIONS	7	
3-3	LVDS SIGNAL TIMING SPECIFICATION	8-9	
3-3	SIGNAL TIMING SPECIFICATIONS	10	
3-4	SIGNAL TIMING WAVEFORMS	10	
3-5	COLOR INPUT DATA REFERNECE		
3-6	POWER SEQUENCE		
4	OPTICAL SFECIFICATIONS	13-15	
5	MECHANICAL CHARACTERISTICS	16-20	
Α	APPENDIX. LPL PROPOSAL FOR SYSTEM COVER DESIGN	21-23	
6	RELIABLITY	24	
7	INTERNATIONAL STANDARDS		
7-1	SAFETY	25	
7-2	EMC	25	
8	PACKING		
8-1	DESIGNATION OF LOT MARK	26	
8-2	PACKING FORM	26	
9	PRECAUTIONS	27-28	
Α	APPENDIX. Enhanced Extended Display Identification Data	29-31	



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Sep. 12. 2008	-	First Draft (Preliminary Specification)	-
0.1	Sep. 18. 2008	18	Update Rear View Drawing	-
0.2	Oct. 28. 2008	7	Change Pin Assignment (#22, 25, 28 : NC → GND)	0.0
		28-30	Update EDID data (Checksum : 2B)	.
0.3	Oct. 31. 2008	19	Add Rear view – Circuit component placement>	
0.4	Dec.05. 2008	6	Updated Electrical Characteristics (added LED electrical characteristics)	
		12	Updated Power sequence(added LED sequence)	
0.5	Dec. 12. 2008	29-31	Update EDID data (Checksum : 4A)	0.1
				.
		[
				<u> </u>
				

Ver. 0.5 Dec. 12, 2008 3 / 31

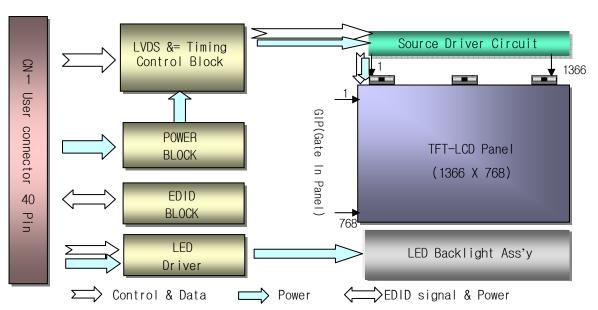


1. General Description

The LP156WH2 is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution(768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP156WH2 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP156WH2 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP156WH2 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.3(H, typ) × 209.5(V, typ) × 5.5(D,max) [mm]
Pixel Pitch	0.252mm × 0.252 mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.5 point)
Power Consumption	Total 4.8Watt(Typ.) @ LCM circuit 1.3 Watt(Typ.), B/L input 3.5Watt(Typ.)
Weight	450g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard Coating(3H), Anti-Glare treatment of the front polarizer
RoHS Comply	Yes

Ver. 0.5 Dec. 12, 2008 4 / 31



2. Absolute Maximum Ratings

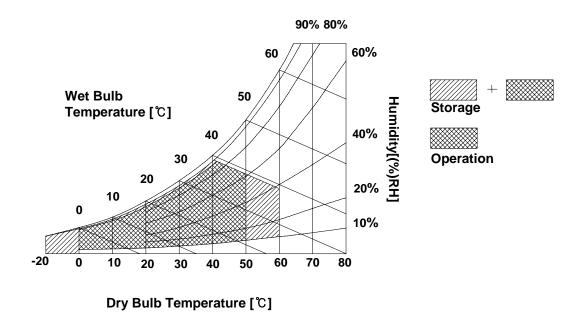
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Symbol	Min	Max	Office		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 0.5 Dec. 12, 2008 5 / 31

Hrs

6



Product Specification

3-1. Electrical Characteristics

The LP156WH2 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL.with LED Driver.

Values Parameter Symbol Unit Notes Min Typ Max LOGIC: Power Supply Input Voltage 3.0 3.3 3.6 Vcc Power Supply Input Current 385 445 mΑ 1 Icc **Power Consumption** 1.3 W 1 Pcc 1.5 Power Supply Inrush Current Icc_p 1500 mΑ LVDS Impedance 90 100 2 ZLVDS 110 Ω BACKLIGHT: (with LED Driver) LED Power Input Voltage VLED 7.0 12.0 20.0 V **LED Power Input Current** ILED 290 mΑ 3 3.5 W 3 **LED Power Consumption** PLED LED Power Inrush Current ILED_P mΑ PWM Dimming (Duty) Ratio -12.5 _ 100 % 4 **PWM** Impedance 40 60 **Z**PWM 20 $\mathbf{k}\Omega$ **PWM Frequency F**PWM 200 10000 Hz 5 PWM High Level Voltage 3.0 5.3 ٧ $V_{PWM\ H}$ PWM Low Level Voltage 0 0.5 V_{PWM_L} LED_EN High Voltage 3.0 5.3 ٧ $V_{\mathsf{LED_EN_H}}$ LED EN Low Voltage 0 0.5 V $V_{LED_EN_L}$

Table 2. ELECTRICAL CHARACTERISTICS

Note)

Life Time

1. The specified Icc current and power consumption are under the Vcc = 3.3V, 25°C, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.

12,000

- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The specified LED current and power consumption are under the VIed = 12.0V, 25 ℃, Dimming of Max luminance whereas White pattern is displayed and fv is the frame frequency.
- 4. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 5. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 6. The life time is determined as the time at which brightness of LCD is 50% compare to that of initial value at the typical LED current. These LED backlight has 6 strings on it and the typical current of LED's string is base on 19mA.

6/31 Ver. 0.5 Dec. 12, 2008



3-2. Interface Connections

This LCD employs one interface connections, a 40 pin connector is used for the module electronics interface and LED Driver.

The electronics interface connector is a model 20455-040E-0x manufactured by I-PEX.

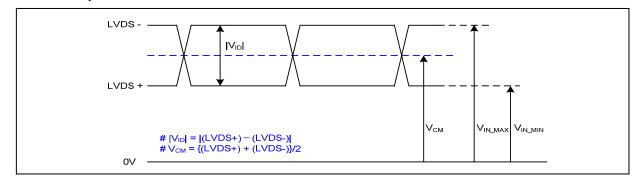
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No connection	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	No Connection	1.1 LCD: SW, SW0624 (LCD Controller)
6	Clk EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	1.2 System : THC63LVDF823A
8	Odd_R _{IN} 0-	Negative LVDS differential data input	or equivalent * Pin to Pin compatible with LVDS
9	Odd_R _{IN} 0+	Positive LVDS differential data input	Time Time of the desired with EVBO
10	GND	Ground	2. Connector
11	Odd_R _{IN} 1-	Negative LVDS differential data input	2.1 LCD :20455-040E-0x, I-PEX or its compatibles
12	Odd_R _{IN} 1+	Positive LVDS differential data input	2.2 Mating : 20453-040T-0x, I-PEX
13	GND	Ground	or equivalent.
14	Odd_R _{IN} 2-	Negative LVDS differential data input	2.3 Connector pin arrangement
15	Odd_R _{IN} 2+	Positive LVDS differential data input	40 1
16	GND	Ground	ا آرا آ ا
17	Odd_CLKIN-	Negative LVDS differential clock input	[""
18	Odd_CLKIN+	Positive LVDS differential clock input	
19	GND	Ground	[LCD Module Rear View]
20	NC NC	No Connection	
21	NC NC	No Connection	
22	GND	Ground	
23	NC NC	No Connection	
24	NC NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
1			
34	NC	No Connection	
35	BLIM	PWM for Luminance control	
36	BL_On	Backlight On/Off Control	
37	NC	No Connection	
38	VLED	LED Power Supply (7V-20V)	
39	VLED	LED Power Supply (7V-20V)	
40	VLED	LED Power Supply (7V-20V)	
٠,٥	•===		



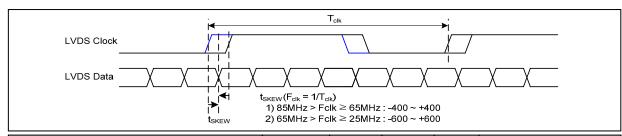
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

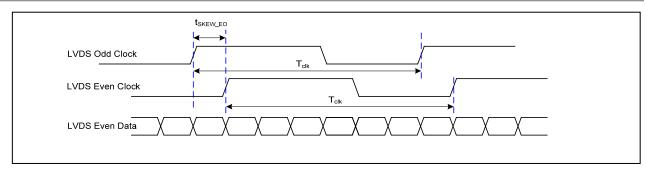
3-3-2. AC Specification



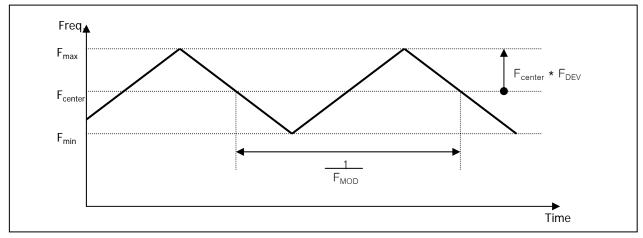
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

Ver. 0.5 Dec. 12, 2008 8 / 31





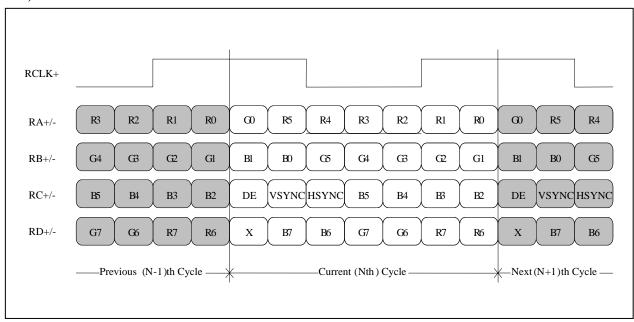
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Condition: VCC =3.3V

10/31



Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	72.3	-	MHz	
	Period	t _{HP}	1470	1526	1586		
Hsync	Width	t _{WH}	23	32	40	tCLK	
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	779	790	801		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	72	80	124	+CLV	
Data	Horizontal front porch	t _{HFP}	8	48	48	tCLK	
Enable	Vertical back porch	t _{VBP}	8	14	20	+I ID	
	Vertical front porch	t _{VFP}	1	3	5	tHP	

3-5. Signal Timing Waveforms

Ver. 0.5

High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc **DCLK** t_{HP} Hsync **t**WHA t_{HFP} t_{HBP} Data Enable t_{VP} Vsync t_{VFP} twva t_{VBP} Data Enable

Dec. 12, 2008



3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	Đ					GRE	EN					BL	UE		
		MSE						MSE						MSE					LSB
	<u></u>	R 5	R 4	R 3	R 2		R 0	-		G 3	G 2	G 1		B 5	B 4	В3	B 2	B 1	В 0
	Black	0	0				0	0		0	0	0		0		0		0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0		0	0	1 	1				1	0				0	0
Basic	Blue	0	0	0			0	0	0	0	0	0		1	. 1 		1		1
Color	Cyan	0	0	0		0	0	1	1	. 1 			1	1	. 1 	1		1	1
	Magenta	1	1	1	. 1	1		0	0	0	0	0	0	1	1	1	. 1	1	
	Yellow	1	1	1	. 1	. 1		1	1	1			1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED]																	
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN		ļ			 												 		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		ļ			 												 		• • • • • •
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	1	1	
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



3-7. Power Sequence

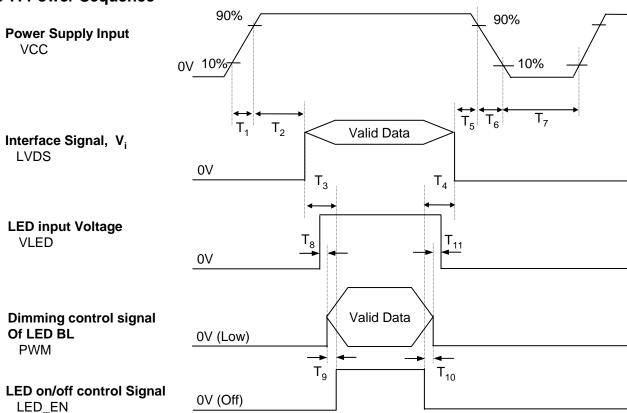


Table 6. POWER SEQUENCE TABLE

Devementes		Value		Unita
Parameter	Min.	Тур.	Max.	Units
T ₁	0.5	-	10	ms
T ₂	0	-	50	ms
T ₃	200	-	-	ms
T ₄	200	-	-	ms
T ₅	0	-	50	ms
T ₆	3	-	10	ms
T ₇	400	-	-	ms
T ₈	50	-	100	ms
T ₉	0	-	100	ms
T ₁₀	0	-	100	ms
T ₁₁	50	-	100	ms

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



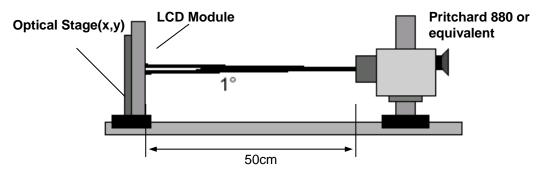


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 72.3MHz, I_{LED} = ???mA

Б			Values		, OEK	N
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	300	-			1
Surface Luminance, white	L _{WH}	170	200		cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	Tr_{R} + Tr_{D}	-	16	-	ms	4
Color Coordinates]	
RED	RX	TBD	TBD	TBD	1	
	RY	TBD	TBD	TBD		
GREEN	GX	TBD	TBD	TBD	[
	GY	TBD	TBD	TBD		
BLUE	BX	TBD	TBD	TBD		
	BY	TBD	TBD	TBD		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359	ļ	
Viewing Angle					<u> </u>	5
x axis, right(Φ=0°)	Θr	40	-	-	degree	
x axis, left (Φ=180°)	Θl	40	-	-	degree	
y axis, up (Φ=90°)	Θu	10	-	-	degree	
y axis, down (Φ=270°)	Θd	30	-	-	degree	
Gray Scale						6

Ver. 0.5 Dec. 12, 2008 13 / 31



Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{ WHITE}} = \frac{\text{Maximum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \, \dots \, \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \, \dots \, \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_{V} = 60$$
Hz

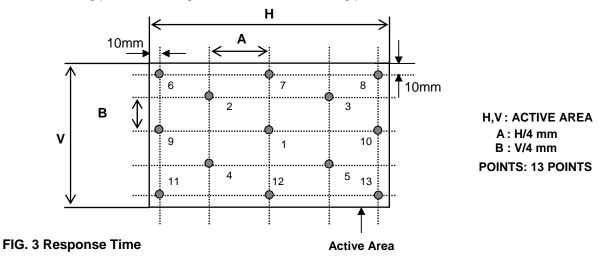
Gray Level	Luminance [%] (Typ)
LO	0
L7	TBD
L15	TBD
L23	TBD
L31	TBD
L39	TBD
L47	TBD
L55	TBD
L63	100

Ver. 0.5 Dec. 12, 2008 14 / 31

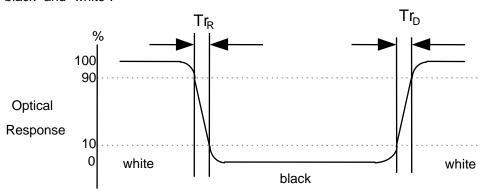


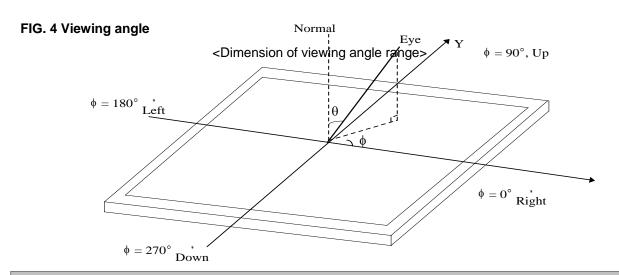
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





Ver. 0.5 Dec. 12, 2008 15 / 31



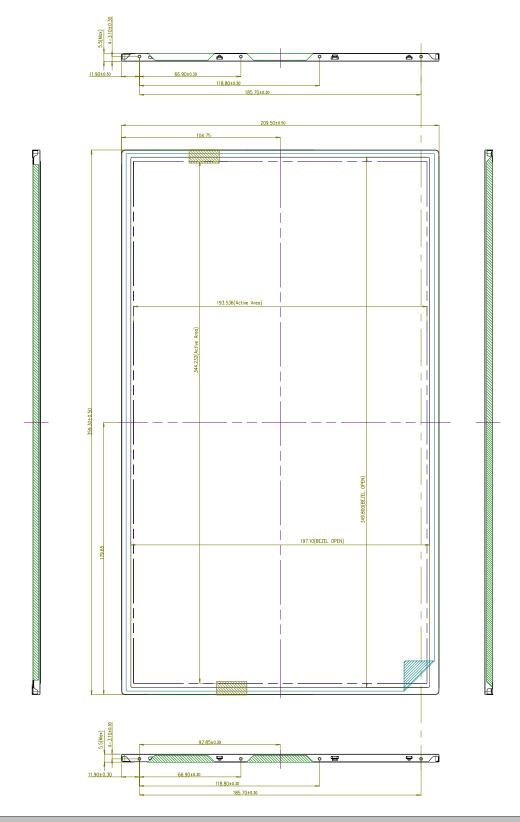
5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP156WH2. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.3 ± 0.5mm		
Outline Dimension	Vertical	209.5 ± 0.5mm		
	Thickness	5.5mm (max)		
Bezel Area	Horizontal	349.8 ± 0.5mm		
bezei Alea	Vertical	197.1 ± 0.5mm		
Active Display Area	Horizontal	344.232 mm		
Active Display Area	Vertical	193.536 mm		
Weight	450g (Max.)			
Surface Treatment	Hard Coating(3H), Anti-Glare treatm	nent of the front polarizer		

Ver. 0.5 Dec. 12, 2008 16 / 31

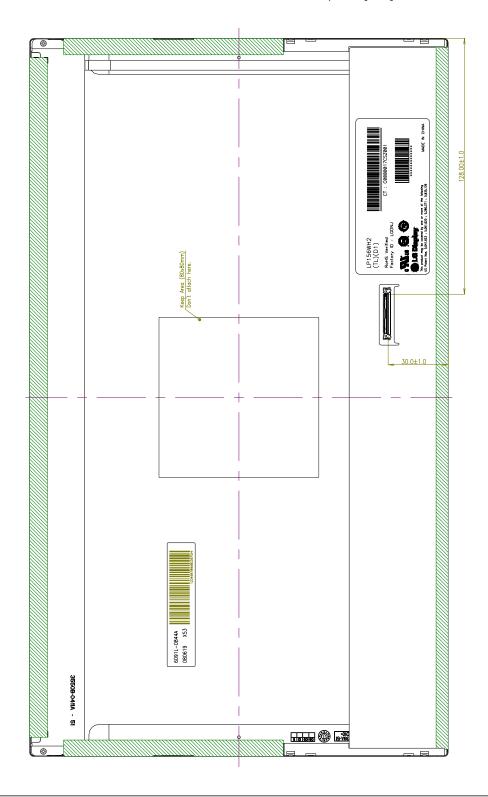






<REAR VIEW>

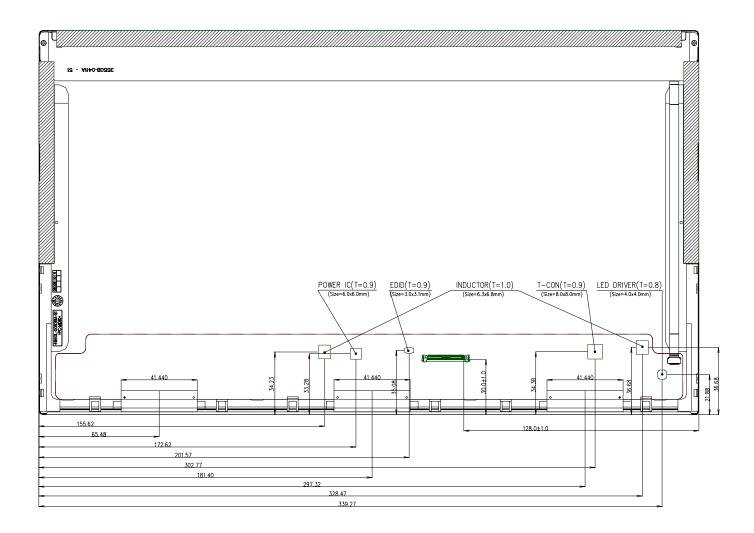
Note) Unit:[mm], General tolerance: \pm 0.5mm





<REAR VIEW - Circuit component placement>

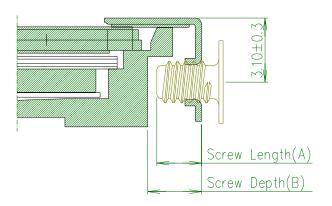
Note) Unit:[mm], General tolerance: \pm 0.5mm



Ver. 0.5 Dec. 12, 2008 19 / 31



[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



```
*Mounting Screw Length (A)
```

= 2.0(Min) / 2.5(Max)

*Mounting Screw Hole Depth (B)

= 2.5(Min)

*Mounting Hole Location : 3.10(typ.)

*Torque : 2.0 kgf.cm(Max)

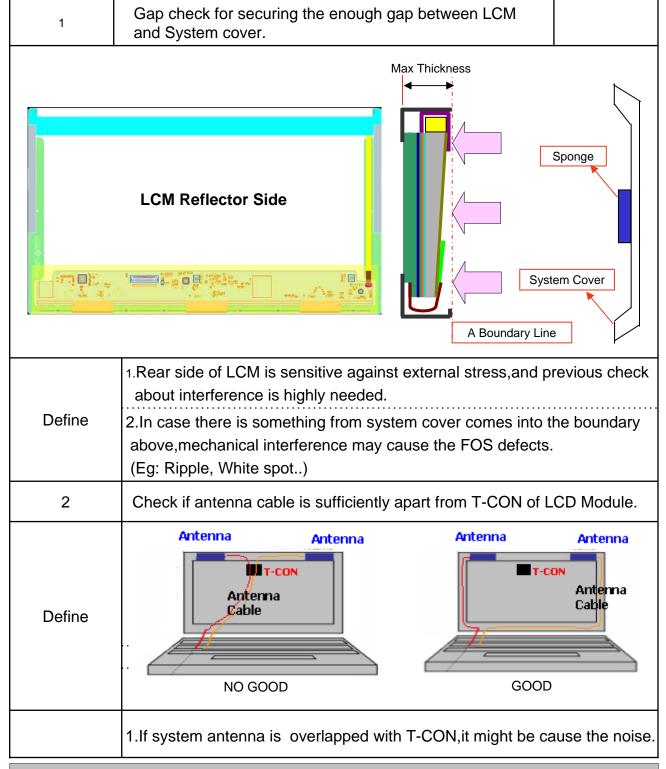
(Measurement gauge : torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

Ver. 0.5 Dec. 12, 2008 20 / 31

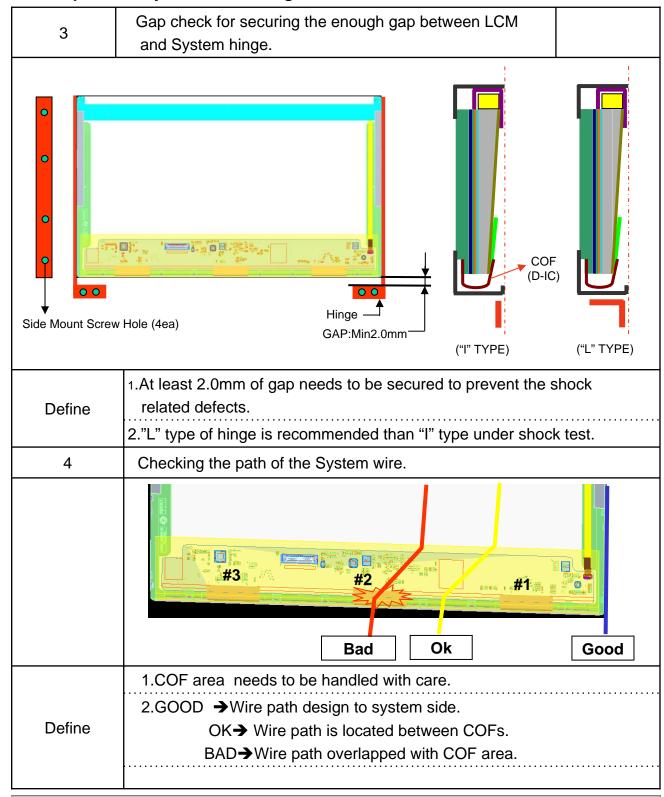


LPL Proposal for system cover design.(Appendix)



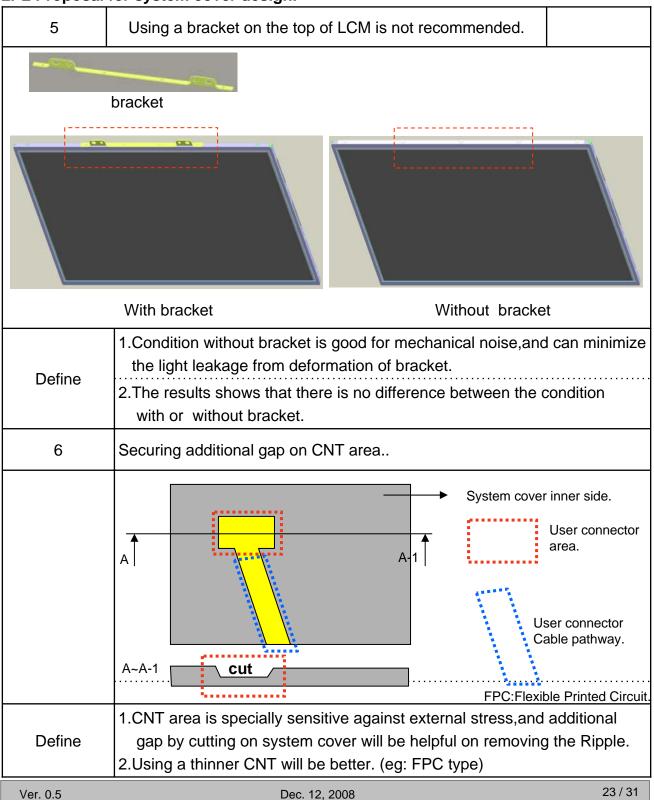


LPL Proposal for system cover design.





LPL Proposal for system cover design.





6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Ver. 0.5 Dec. 12, 2008 24 / 31



7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

Ver. 0.5 Dec. 12, 2008 25 / 31



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L	А	В	С	D	Е	F	G	Н	I	J	К	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20 pcs

b) Box Size: 482 x 371 x 275



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 0.5 Dec. 12, 2008 27 / 31



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

Dyto#	Byto#		\/a	llue	Value	
Byte#	Byte#	Field Name and Comments			(binary)	
(decimal)						
0		Header			0000 0000	
1	01	Header	F			
2	02	Header	F		1111 1111	I I a a alam
3	03 04	Header	F	F	1111 1111	Header
5	05	Header Header	F	F		
6		Header	F		1111 1111	
7		Header	0		0000 0000	
8		EISA manufacturer code(3 Character ID) = LGD		_	0011 0000	
9	09	Compressed ASCII	E	_	1110 0100	
10	0A	Product code = (01E0)	_	_	1110 0000	
11	0B	(Hex, LSB first)	-	_	0000 0001	
12	0C				0000 0000	\/andar/
		LCD module Serial No - Preferred but Optional ("0" if not used)	0	_		Vender/
13	0D	LCD module Serial No - Preferred but Optional ("0" if not used)	1-	+	0000 0000	Product ID
14	0E	LCD module Serial No - Preferred but Optional ("0" if not used)	0	_	0000 0000	
15	0F	LCD module Serial No - Preferred but Optional ("0" if not used)	0	_	0000 0000	
16	10	Week of Manufacture	0	_	0000 0000	
17	11	Year of Manufacture = 2008	1		0001 0010	
18	12	EDID Structure version # = 1	_	_	0000 0001	EDID Version/
19		EDID Revision # = 3			0000 0011	Revision
20	14	Video Input Definition = Digital I/P,non TMDS CRGB			1000 0000	
21	15	Max H image size(cm)=34.4232cm(34)	2			Display
22		Max V image size(cm)=19.3536cm(19)	1		0001 0011	Parameter
23	17	Display gamma =2.2	7		0111 1000	
24	18	Feature support(DPMS) = Active off, RGB Color	0		0000 1010	
25		Red/Green low Bits	0	_	0000 0000	
26		Blue/White Low Bits Red X = (TBD)	0	_	0000 0000	
27 28	1B 1C	Red Y = (TBD)	0		0000 0000	
29	1D	Green X = (TBD)	0		0000 0000	Color
30	1E	Green Y = (TBD)	0		0000 0000	Characteristic
31	1F	Blue X = (TBD)	_		0000 0000	Ondraotonotio
32	20	Blue Y = (TBD)	0		0000 0000	
33	21	White $X = 0.313$	5	0	0101 0000	
34	22	White Y = 0.329	5	4	0101 0100	
35	23	Established Timing I = 00h(If not used)	0	0	0000 0000	Established
36	24	Established Timing II = 00h(If not used)	0	0	0000 0000	Timings
37	25	Manufacturer's Timings = 00h(If not used)	0	0	0000 0000	
38	26	Standard Timing Identification 1 was not used	0	1	0000 0001	
39	27	Standard Timing Identification 1 was not used	0	1	0000 0001	
40	28	Standard Timing Identification 2 was not used	0	1	0000 0001	
41	29	Standard Timing Identification 2 was not used	0	1	0000 0001	
42	2A	Standard Timing Identification 3 was not used	0	_		
43	2B	Standard Timing Identification 3 was not used	0	_	0000 0001	
44	2C	Standard Timing Identification 4 was not used	0	_	0000 0001	Standard
45	2D	Standard Timing Identification 4 was not used Standard Timing Identification 4 was not used	0	_	0000 0001	Timing ID
		-	0	+	0000 0001	עו מוווווון
46	2E	Standard Timing Identification 5 was not used	1	1		
47	2F	Standard Timing Identification 5 was not used	0	1	0000 0001	
48	30	Standard Timing Identification 6 was not used	0	-	0000 0001	
49	31	Standard Timing Identification 6 was not used	0	_	0000 0001	
50	32	Standard Timing Identification 7 was not used	0	_	0000 0001	
51	33	Standard Timing Identification 7 was not used	0	_	0000 0001	
52	34	Standard Timing Identification 8 was not used	0	1	0000 0001	
53	35	Standard Timing Identification 8 was not used	0	1	0000 0001	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

Byte#	Byte#	Field News and Community	Val	lue	Value	
(decimal)	(HEX)	Field Name and Comments	(HE			
54	36	1366X768 @60Hz mode pixel clock (LSB) => 72.3MHz	3	Ε	0011 1110	
55	37	(Stored LSB first)	1		0001 1100	
56	38	Horizontal Active = 1366 pixels (lower 8bits)	5	6	0101 0110	
57	39	Horizontal Blanking = 160 pixels (lower 8bits)	Α	0	1010 0000	
58	3A	Horizontal Active: Horizontal Blanking (upper 4:4bits)	5	0	0101 0000	
59	3B	Vertical Avtive = 768 lines (lower 8bits)	0		0000 0000	
60	3C	Vertical Blanking = 22 lines (lower 8bits)	1		0001 0110	
61	3D	Vertical Active: Vertical Blanking (upper 4:4bits)	3		0011 0000	Timing
62	3E	Horizontal Sync. Offset = 48 pixels	3		0011 0000	Descriptor
63	3F	Horizontal Sync Pulse Width = 32 pixels	2		0010 0000	#1
64	40	Vertical Sync Offset = 3 lines : Sync Width = 5 lines	3		0011 0101	
65	41	Horizontal Vertical Sync Offset/Width upper 2bits = 0	0		0000 0000	
66	42	Horizontal Image Size = 344.232mm(344)	5		0101 1000	
67	43	Vertical Image Size = 193.536mm(194)	С		1100 0010	
68	44	Horizontal & Vertical Image Size	1		0001 0000	
69		Horizontal Border = 0	0		0000 0000	
70	46	Vertical Border = 0	0		0000 0000	
71	47	Non-interlaced,Normal display,no stereo,Digital separate sync,H/V pol negatives	-		0001 1001	
72	48 49	Flag	0		0000 0000	
73 74	49 4A	Flag Flag	0		0000 0000	
75	4A 4B	Data Type Tag (Descriptor Defined by manufacturer)	0		0000 0000	
76	4C	Flag	0		0000 0000	
77	4D	Descriptor Defined by manufacturer	0		0000 0000	
78	4E	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	
79	4E 4F	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	Timing
80		Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	Description
81	51	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	#2
82	52	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	#2
83	53	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	
84	54	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	
85		Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	
86	56	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	0		0000 0000	
87	57	Descriptor Defined by manufacturer	0		0000 0000	
88	58	Descriptor Defined by manufacturer	0		0000 0000	
89	59	Descriptor Defined by manufacturer	0		0000 0000	
90	5A	Flag	0		0000 0000	
91	5B	Flag	0		0000 0000	
92	5C	Flag	0	0	0000 0000	
93	5D	Data Type Tag (ASCII String)	F		1111 1110	
94	5E	Flag	0		0000 0000	
95	5F	L	4		0100 1100	
96	60	G	4		0100 0111	
97	61		2		0010 0000	Timing
98	62	D	4		0100 0100	Description
99	63		6		0110 1001	#3
100	64	\$	7		0111 0011	
101	65	p	7		0111 0000	
102	66		6		0110 1100	
103	67	a			0110 0001	
104 105	68 69	Manufacturar P/N/If<12 char NAb, then terminate with ASCII, and a NAb act remaining of			0111 1001 0000 1010	
105		Manufacturer P/N(If<13 char> 0Ah, then terminate with ASCII code 0Ah,set remaining ch Manufacturer P/N(If<13 char> 0Ah, then terminate with ASCII code 0Ah,set remaining ch			0000 1010	
107	6B	Manufacturer P/N(II<13 char> 0Ah, then terminate with ASCII code 0Ah,set remaining ch			0010 0000	
107	UD	manuracturer i /nytii×ro char/ dan, then terminate with Aoch code dan,set remaining ch	۷	U	0010 0000	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte#	Byte#	Field Name and Comments	Va	lue	Value	
(decimal)	(HEX)	Tield Name and Comments	(H	EX)	(binary)	
108	6C	Flag	0	0	0000 0000	
109		Flag	0	0	0000 0000	
110	6E	Flag	0	0	0000 0000	
111	6F	Data Type Tag (Monitor Name, stored as ASCII)	F	С	1111 1100	
112	70	Flag	0	0	0000 0000	
113	71	L	4	С	0100 1100	
114	72	P	5	0	0101 0000	
115	73	1	3	1	0011 0001	Timing
116	74	5	3	5	0011 0101	Description
117	75	6	3	6	0011 0110	#4
118	76	W	5	7	0101 0111	
119	77	Н	4	8	0100 1000	
120	78	2	3	2	0011 0010	
121	79	-	2	D	0010 1101	
122	7A	T	5	4	0101 0100	
123	7B	L	4	С	0100 1100	
124	7C	D	4	4	0100 0100	
125	7D	1	3		0011 0001	
126	7E	Extension flag = 00	0	0	0000 0000	Extension Flag
127		Checksum			0100 1010	

Ver. 0.5 Dec. 12, 2008 31 / 31