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NV156FHM-T00

Preliminary Product Specification

Rev. P1

CHONGQING BOE OPTOELECTRONICS TECHNOLOGY CO.,LTD

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REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
P0	-	Initial Release	2016.05.06	Xuzhijun zuocheng
P1	-	EDID update, Add Touch Timing	2016.8.12	Xuzhijun wangxiaojun

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1.0 General Description

1.1 Application

- Notebook PC With Touch function

1.2 General Specification

1.2.1.General LCM Specification(Table 1.)

<Table 1. General LCM Specifications>

Parameter	Specification	Unit	Remarks
Active area	344.16 (H) × 193.59 (V)	mm	
Number of pixels	1920 (H) × 1080 (V)	pixels	
Pixel pitch	0.17925 (H) X 0.17925 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	262K	colors	
Display mode	Normally Black		
Outline Dimension	350.76(H) × 205.825(V)	mm	
Weight	385 (max)	g	
Back-light	Lower Down side, 1-LED Lighting Bar type		Note 1
Power consumption	P _D : 0.9	W	@mosaic
	P _{BL} : 3.6	W	
	P _{total} :4.5	W	@mosaic

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1.2.2.General Touch Specification(Table 2.)

<Table 2. General Touch Specifications>

Parameter	Specification	Unit	Remarks
Type of Touch Sensor	Self Capacitance		
Touch Structure	On Cell		
Panel Size	15.6”		
Outline Dimension	N/A(Cover Lens Free)	mm	
TP View Area	N/A(Cover Lens Free)	mm	
TP Active Area	X 345.96 × Y 193.74	mm	
Total Thickness	N/A(Cover Lens Free)	mm	
Interface	USB		
Report Rate	Follow win8 – 100Hz		
Multi-Touch Point	10 points		
Input method	Finger		
Touch panel sensor IC	G7500		
Channel	1500		
Surface treatment	Normal AG		
Surface Hardness	3	H	
Support OS	Win8.x and Win10 compliant		
TP Power Consumption	200 max.	mW	@ 5 finger

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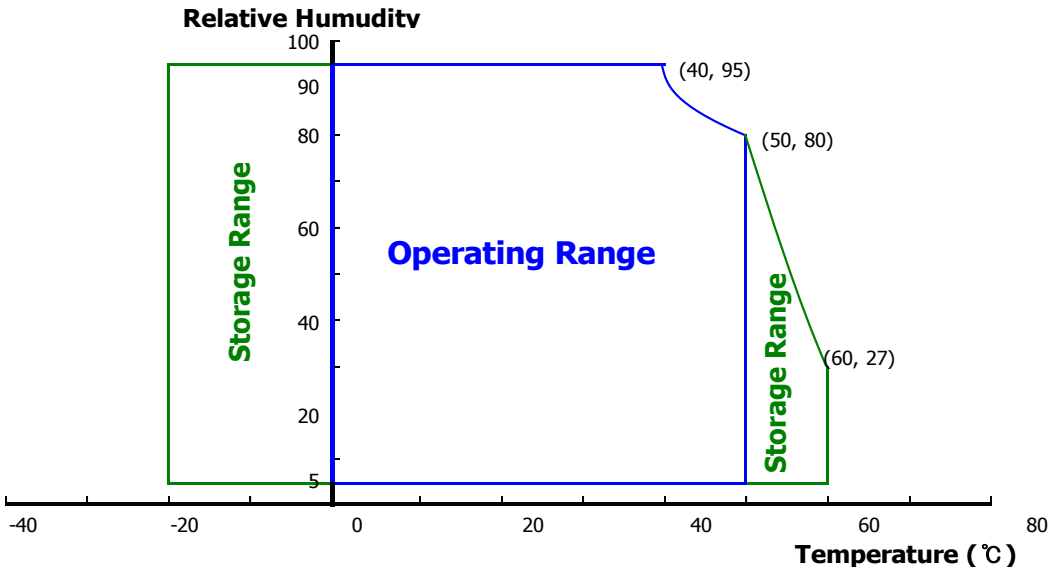
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 3.

< Table 3. Absolute Maximum Ratings> Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.3	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{ss} -0.3	V _{DD} +0.3	V	
Operating Temperature	T _{OP}	0	+50	°C	Note 2
Storage Temperature	T _{ST}	-20	+60	°C	

- Notes : 1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
2. Temperature and relative humidity range are shown in the figure below.
 95 % RH Max. (40 °C ≥ Ta)
 Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 4. Electrical specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V _{RF}	-	-	100	mV	At V _{DD} = 3.3V
Power Supply Current	I _{DD}	-	-	273	mA	Note 1
Differential Input Voltage	V _{ID}	100	-	600	mV	
Power Consumption	P _D	-	-	0.9	W	Note 1
	P _{BL}	-	-	3.6	W	Note 2
	P _{total}	-	-	4.5	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for 3.3V at 25°C.

a) Typ : Mosaic Pattern

b) Max : Skip sub pixel255

2. Calculated value for reference (VLED× ILED)

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3.2 Backlight Unit

< Table 5. LED Driving guideline specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks	
LED Forward Voltage		V _F	-	-	3..3	V	-
LED Forward Current		I _F	-	23	-	mA	-
LED Power Consumption		P _{LED}	-	-	3.6	W	Note 1
LED Life-Time		N/A	15,000	-	-	Hour	I _F = 23mA
Power supply voltage for LED Driver		V _{LED}	5	12	21	V	
EN Control Level	Backlight on		2.5		5.0	V	
	Backlight off		0		1.0	V	
PWM Control Level	PWM High Level		2.5		5.0	V	
	PWM Low Level		0		0.1	V	
PWM Control Frequency		F _{PWM}	100	-	10,000	Hz	
Duty Ratio		-	1	-	100	%	Note3

Notes : 1. Power supply voltage 12V for LED Driver

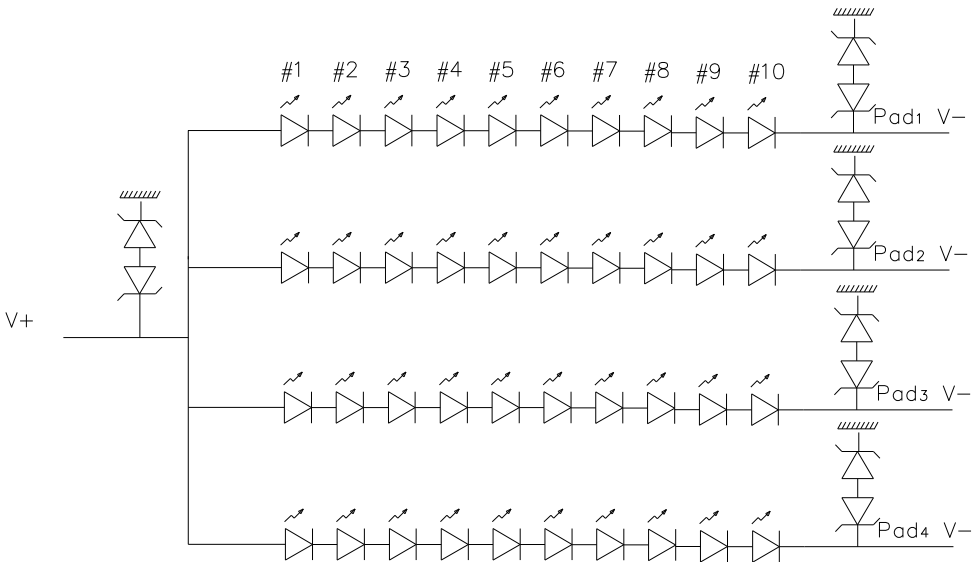
Calculator Value for reference $I_F \times V_F \times 36 / \text{efficiency} = P_{LED}$

2. The LED Life-time define as the estimated time to 50% degradation of initial luminous.

3. 1% duty cycle is achievable with a dimming frequency less than 1KHz.

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3.3 LED structure



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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to θ0=0 (=θ3) as the 3 o'clock direction (the “right”), θ0=90 (= θ12) as the 12 o'clock direction (“upward”), θ0=180 (= θ9) as the 9 o'clock direction (“left”) and θ0=270(= θ6) as the 6 o'clock direction (“bottom”). While scanning θand/or Φ, the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be 3.3+/- 0.3V at 25°C. Optimum viewing angle direction is 6 ’clock.

4.2 Optical Specifications

<Table 6. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ ₃	CR > 10	-	85	-	Deg.	Note 1
		Θ ₉		-	85	-	Deg.	
	Vertical	Θ ₁₂		-	85	-	Deg.	
		Θ ₆		-	85	-	Deg.	
Luminance Contrast ratio		CR	Θ = 0°	-	800			Note 2
Luminance of White	5 Points	Y _w	Θ = 0° I _{LED} = 23.0mA	210	250	-	cd/m²	Note 3
White Luminance uniformity	5 Points	ΔY5		80%	-	-		Note 4
	13 Points	ΔY13		60%	-	-		
White Chromaticity		x _w	Θ = 0°	0.283	0.313	0.343		Note 5
		y _w		0.299	0.329	0.359		
Reproduction of color	Red	x _R	Θ = 0°	-0.03	0.585	+0.03		
		y _R			0.364			
	Green	x _G			0.350			
		y _G			0.580			
	Blue	x _B			0.163			
		y _B			0.143			
Gamut					45		%	
Response Time (Rising + Falling)		T _{RT}	Ta= 25° C Θ = 0°	-	30	35	ms	Note 6
Cross Talk		CT	Θ = 0°	-	-	2.0	%	Note 7

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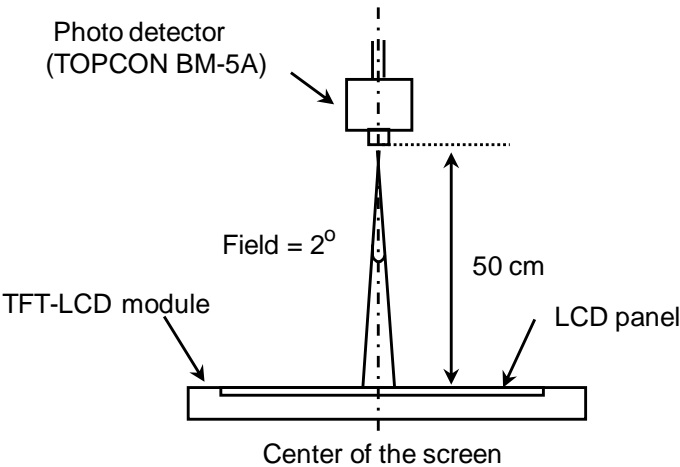
- Notes :
- Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
 - Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state .
(see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$
 - Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
 - The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = \text{Minimum Luminance of 5(or 13) points} / \text{Maximum Luminance of 5(or 13) points}$.
(see FIGURE 2 and FIGURE 3).
 - The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
 - The electro-optical response time measurements shall be made as FIGURE 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_d .
 - Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark.
(See FIGURE 5).

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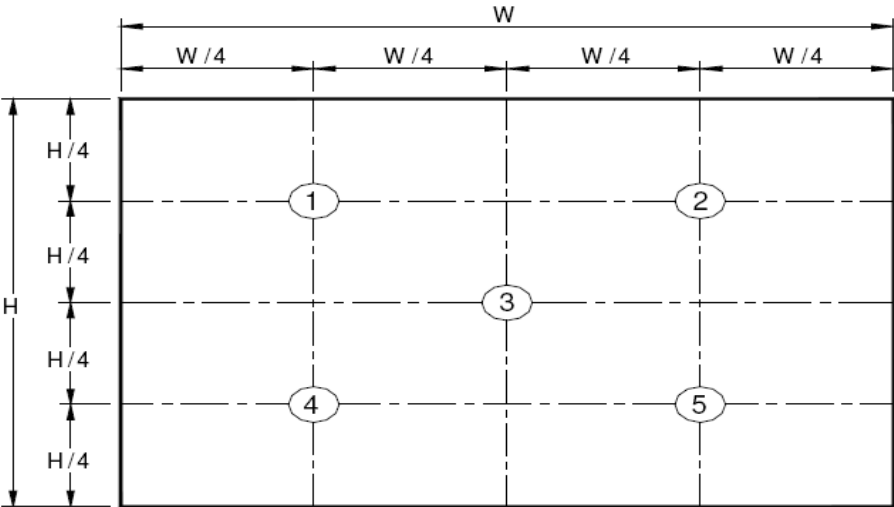
4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

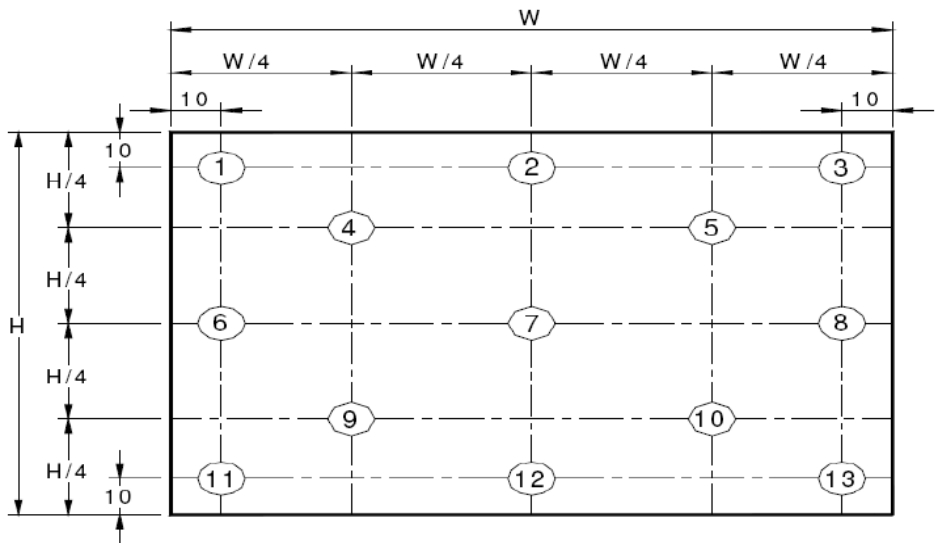
Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

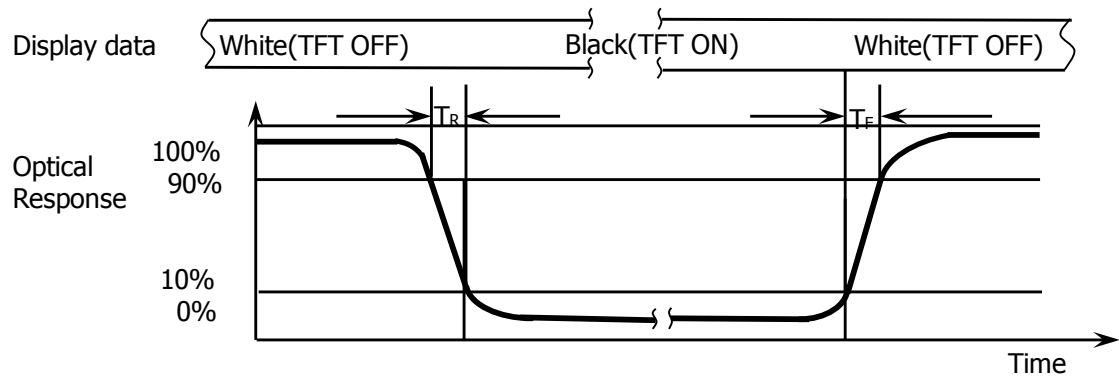
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Figure 3. Uniformity Measurement Locations (13 points)



The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5$ = Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2) , $\Delta Y13$ = Minimum Luminance of 13 points /Maximum Luminance of 13 points (see FIGURE 3).

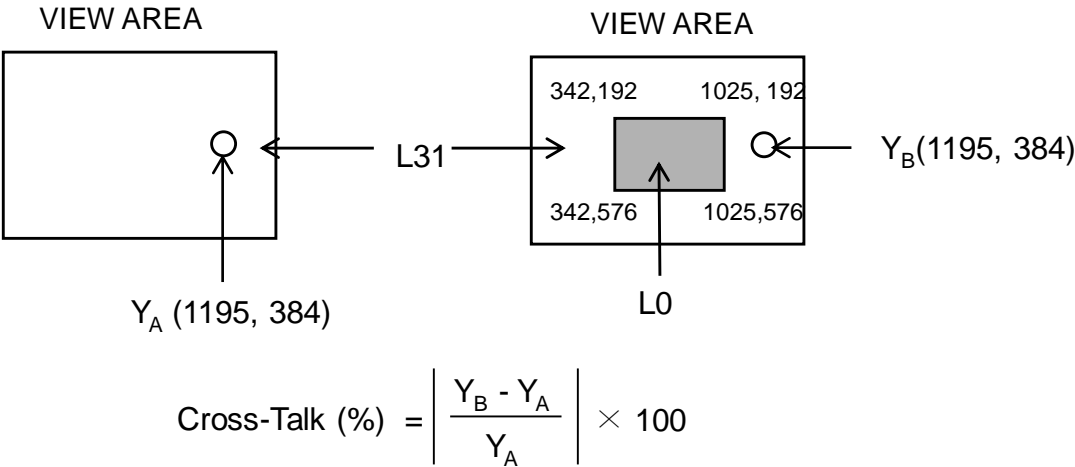
Figure 4. Response Time Testing



The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

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Figure 5. Cross Modulation Test Description



Where:

Y_A = Initial luminance of measured area (cd/m²)

Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).

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5.0 INTERFACE CONNECTION.

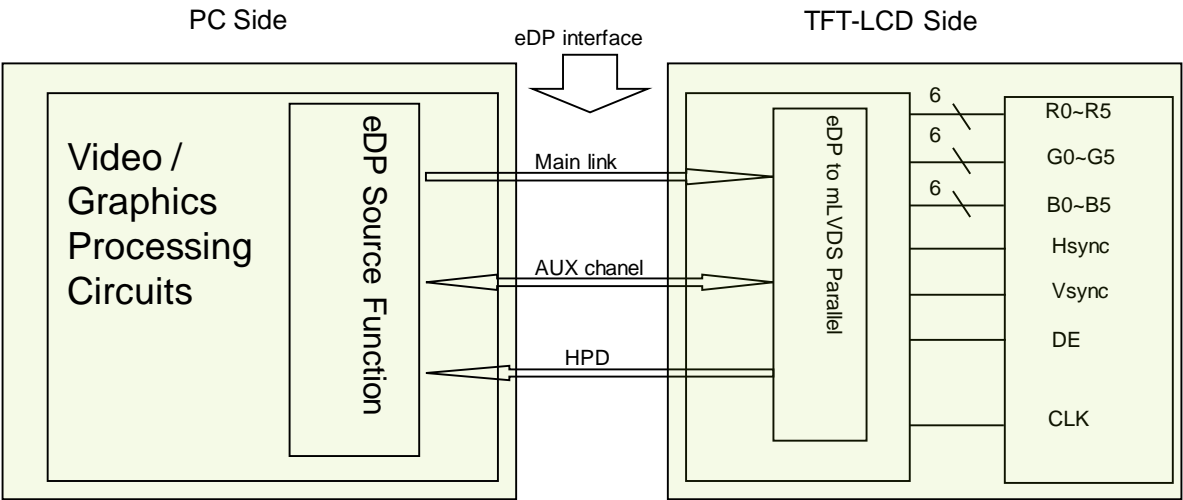
5.1 Electrical Interface Connection

The electronics interface connector is 20525-040E-02.
The connector interface pin assignments are listed in Table 7.

<Table 7. Pin Assignments for the Interface Connector>

PIN NO	Symbol Function	PIN NO	Symbol Function
1	CABC_ENIN	21	LED Ground
2	Ground	22	LED enable pin(+3.3V Input)
3	eDP RX channel 1 negative	23	System PWM Signal Input
4	eDP RX channel 1 positive	24	Line synchronization
5	Ground	25	No Connection
6	eDP RX channel 0 negative	26	LED Power Supply 5V-21V
7	eDP RX channel 0 positive	27	LED Power Supply 5V-21V
8	Ground	28	LED Power Supply 5V-21V
9	eDP AUX CH positive	29	LED Power Supply 5V-21V
10	eDP AUX CH negative	30	No Connection
11	Ground	31	No Connection
12	Power Supply, 3.3V (typ.)	32	No Connection
13	Power Supply, 3.3V (typ.)	33	No Connection
14	Panel self test enable	34	No Connection
15	Ground	35	Ground
16	Ground	36	D+
17	Hot plug detect output	37	D-
18	LED Ground	38	Ground
19	LED Ground	39	Touch enable
20	LED Ground	40	Touch power supply 3.3V

5-2. eDP Interface



Note. Transmitter : Novatek NT71832 or equivalent.
Transmitter is not contained in Module.

5.3.eDP Input signal

Lane 0	
R0-5:0	G0-5:4
G0-3:0	B0-5:2
B0-1:0	R1-5:0
G1-5:0	B1-5:4
B1-3:0	R2-5:2
R2-1:0	G2-5:0
B2-5:0	R3-5:4
R3-3:0	G3-5:2
G3-1:0	B3-5:0

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5.4 Back-light & LCM Interface Connection

<Table 8. Pin Assignments for the BLU & LCM Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	Vout	LED anode connection	6	NC	NC Connection
2	Vout	LED anode connection	7	LED1	LED cathode connection
3	Vout	LED anode connection	8	LED2	LED cathode connection
4	NC	NC	9	LED3	LED cathode connection
5	NC	NC	10	LED4	LED cathode connection

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6.0 SIGNAL TIMING SPECIFICATION

6.1 The NV156FHM-T00 is operated by the DE only.

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	100	152.6	160	MHz
Frame Period		Tv	1112	1125	1238	lines
			-	60	-	Hz
			25	16.67	15.15	ms
Vertical Display Period		Tvd	-	1080	-	lines
One line Scanning Period		Th	2080	2230	2400	clocks
Horizontal Display Period		Thd	-	1920	-	clocks

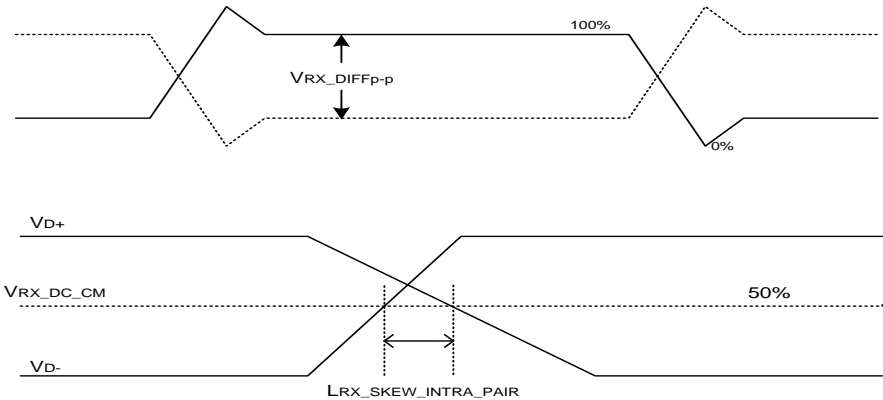
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6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 9.

<Table 9.1 eDP Rx Interface Timing Specification>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock	ssc		0.5		%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	500	0	1000	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	-	V	
Differential termination resistance	RRX-DIFF	80	-	100	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	20	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_INTRA_PAIR	-	-	150	ps	

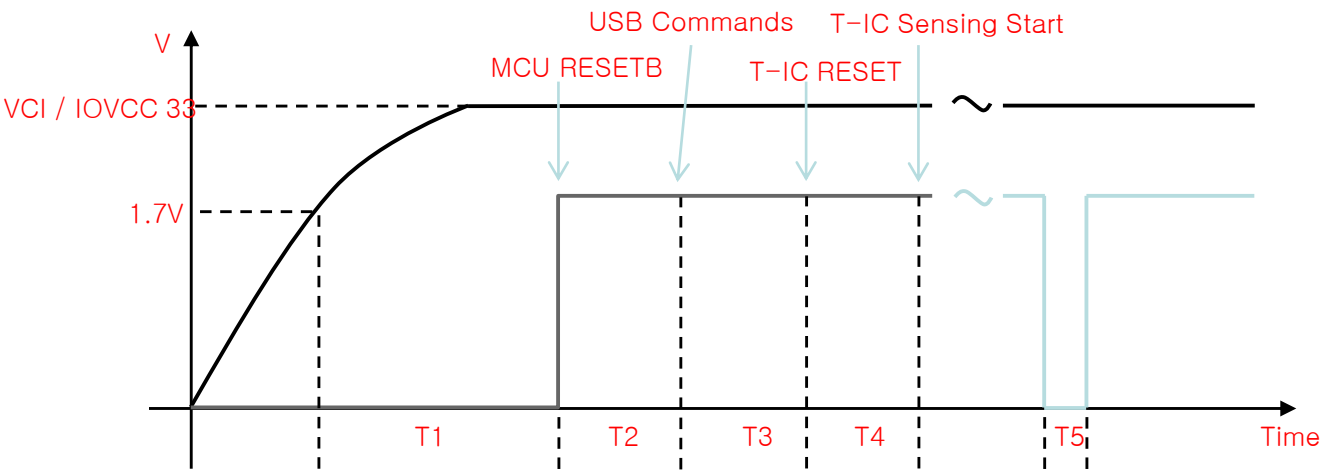


6.3 Touch Interface Timing Parameter

The specification of the Touch interface timing parameter is shown in Table 9.2

<Table 9.2 Touch Interface Timing Specification>

ITEM	Symbol	Time	Unit
MCU Power ON ~ MCU Reset(MCU Internal Reset)	T1	≥400us	us
MCU Reset ~ USB Commands	T2	≥50ms	ms
USB Commands ~ Touch IC Reset	T3	≥600ms	ms
Touch IC Reset ~ Touch IC Sensing Start	T4	≥500ms	ms
Touch IC Reset low-level width	T5	≥10	us



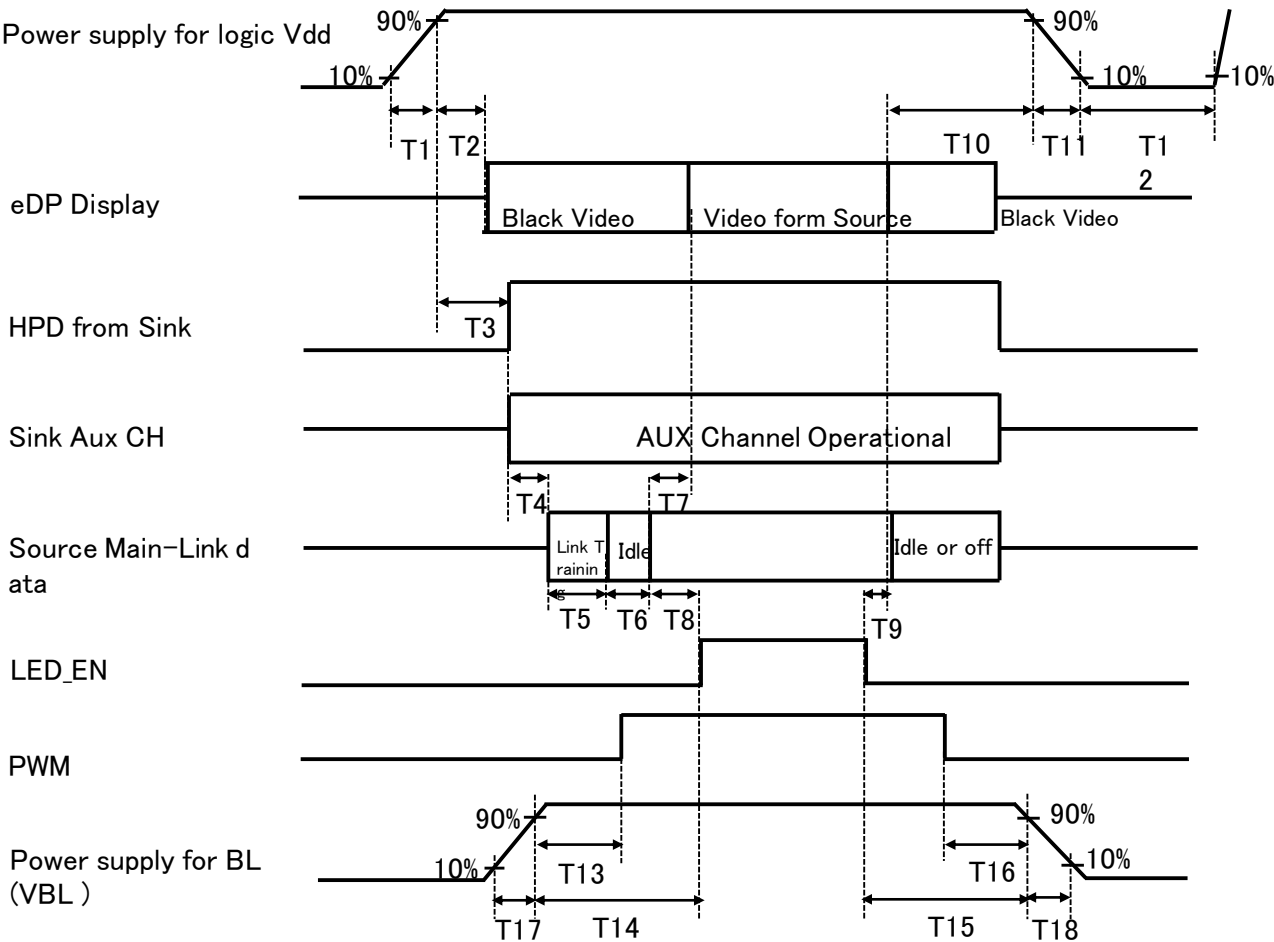
7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

	Colors & Gray scale	Data signal																	
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Light Blue	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Purple	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△ ▽	↑ ↓						↑ ↓						↑ ↓					
	Brighter	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	▽	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Gray scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
△	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	
Darker	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	
△ ▽	↑ ↓						↑ ↓						↑ ↓						
Brighter	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	
▽	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	
Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Gray scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	△ ▽	↑ ↓						↓ ↑						↑ ↓					
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Gray scale of White & Black	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
△	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	0	
Darker	0	1	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0	
△ ▽	↑ ↓						↑ ↓						↑ ↓						
Brighter	1	0	1	1	1	1	1	0	1	1	1	1	1	0	1	1	1	1	
▽	0	1	1	1	1	1	0	1	1	1	1	1	0	1	1	1	1	1	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- 0.5ms ≤ T1 ≤ 10 ms
 - 0ms ≤ T2 ≤ 200 ms
 - 0ms ≤ T3 ≤ 200 ms
 - 0ms ≤ T13
 - 0ms ≤ T14
 - 0ms ≤ T17
- 0ms ≤ T7 ≤ 50ms
 - 0ms ≤ T10 ≤ 500 ms
 - 0 ms ≤ T11 ≤ 10 ms
 - 150ms ≤ T12
 - 0ms ≤ T15
 - 0ms ≤ T16
 - 0ms ≤ T18

Notes:

- When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

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9.0 MECHANICAL CHARACTERISTICS

9.1 Dimensional Requirements

FIGURE 6 shows mechanical outlines for the model NV156FHM-T00.

Other parameters are shown in Table 10.

<Table 10. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	344.16 (H) × 193.59(V)	
Number of pixels	1920 (H) X 1080 (V) (1 pixel = R + G + B dots)	
Pixel pitch	0.17925 (H) X 0.17925 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display colors	262K	
Display mode	Normally Black	
Dimensional outline	350.76(H)*216.195(V) (W/PCB)*3.2(Max)	mm
Weight	385(Max)	gram
Back Light	Connector :IS050-L30B-C10	
	LED, Horizontal-LED Array type	

9.2 Mounting

See FIGURE 6.

9.3 Glare and Polarizer Hardness.

The surface of the LCD has an glare coating to maximize readability and hard coating to reduce scratching.

9.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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10.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 11. Reliability test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60 °C, 240 hrs
2	Low temperature storage test	Ta = -20 °C, 240 hrs
3	High temperature & high humidity operation test	Ta = 40 °C, 80%RH, 240 hrs
4	High temperature operation test	Ta = 50 °C, 240 hrs
5	Low temperature operation test	Ta = -20 °C, 240 hrs
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 hr), 100 cycle
7	Vibration test (non-operating)	10 ~ 1000 Hz, 10 m/s ² Half Sine X,Y,Z / Sweep rate : 1 hour
8	Shock test (non-operating)	220G, Half Sine Wave 2msec ± X, ± Y, ± Z Once for each direction
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV

11.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation

- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

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- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble the LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - When returning the module for repair, please ensure the module not to be broken. We recommend to use the original shipping packages.
- TBD

12.0 LABEL

(1) TLCM label

NT156WHM-T00



XXXXXXXXXXXXXXXXXXXX



CN-OHXNNJ-79088-
XXX-XXXX-X00

MADE IN CHINA

DP/N OHXNNJ

BOE

ECO
RoHS Compliant

C[®]ULUS


MADE IN CHINA

TLCM ID 编码规则:

序号号	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
代码																	
描述	GBN		等级	line	年	月	FG-Code后4位					Serial Number					

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(2) High voltage caution label



HIGH VOLTAGE
CAUTION

RISK OF ELECTRIC SHOCK.
DISCONNECT THE ELECTRIC
POWER BEFORE SERVICING

COLD CATHODE FLUORESCENT LAMP IN LCD
PANEL CONTAINS A SMALL AMOUNT
OF MERCURY. PLEASE FOLLOW LOCAL OR-
DINANCES OR REGULATIONS FOR DISPOSAL.

(3) Box label

BOE

CHONGQING BOE OPTOELECTRONICS TECHNOLOGY CO., LTD.

MODEL: XXXXXXX-XXX ①

Q'TY: XX ②

SERIAL NO: XXXXXXXXXXXX ③

DATE: XXXXXXXX ④

BOX ID 条形码



XXXXXXXXX ⑤

XXXX ⑥

XXXX ⑦

蓝色字体为后打印标识, 说明如下:

- 1. FG-CODE
- 2. Box 产品数量
- 3. Box ID, 编码规则如下
- 4. Box Packing 日期
- 5. 产品物料号(客户端)
- 6. FG-CODE 后四位

Box ID 编码规则

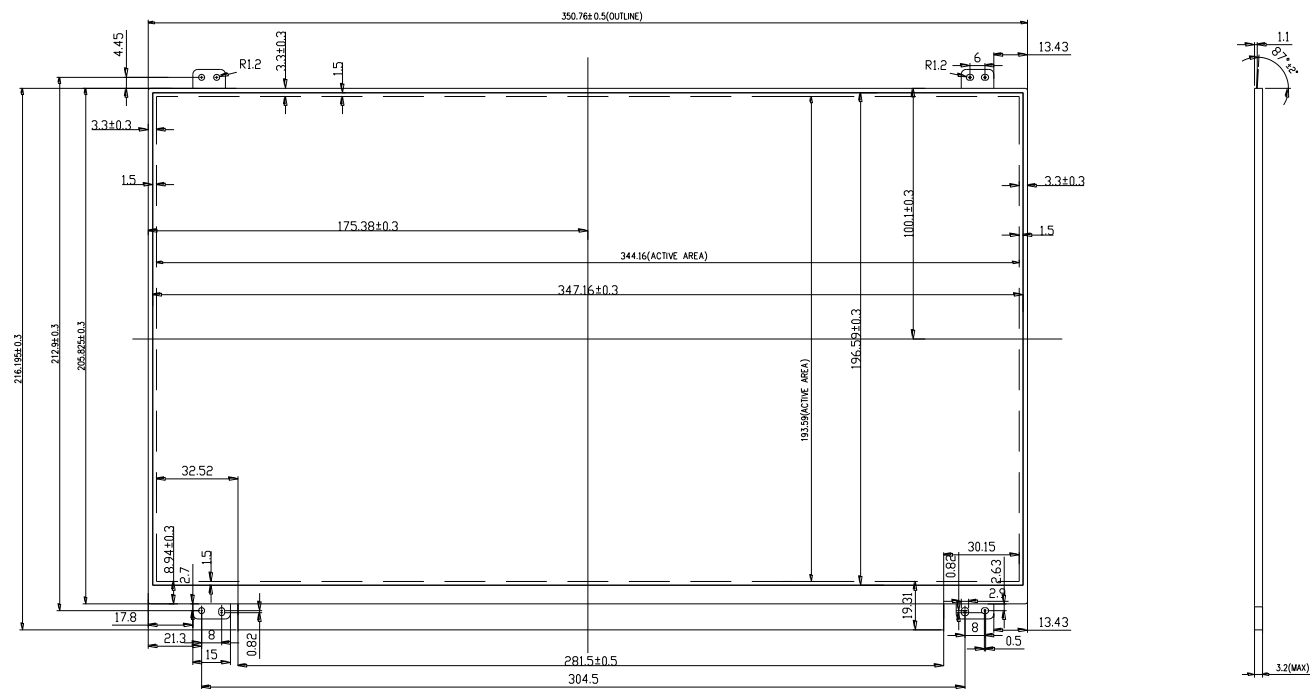
序号号	1	2	3	4	5	6	7	8	9	10	11	12	13
代码	S	L	S	T	1	4	3	D	0	0	1	H	D
描述	GBN代码		等级	TM1	年份		月	Rev	Serial Number				

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14. MECHANICAL OUTLINE DIMENSION

14.1 Outline Dimension

Figure 6. Outline Dimensions (Front view)



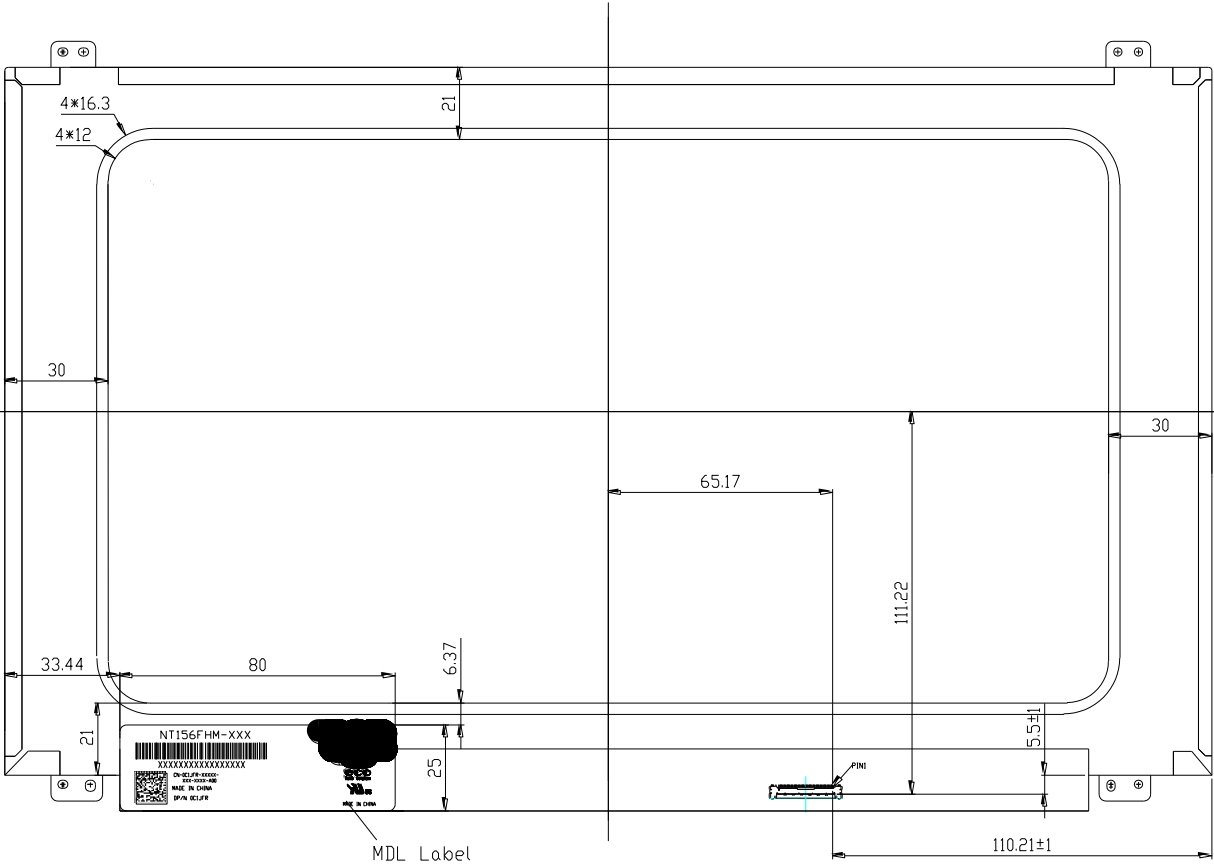
- Note
1. PCB side is lower than Top Polarizer, and any other PCB component is lower than Top Polarizer.
 2. Warps and Deformation are 0≤d≤0.5mm MAX.
 3. No light leakage from all 4 corners of LCM.
 4. Screw Bracket Angle is 87°±2°.



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14.2 Total Solution Outline Dimension

Figure 7. Outline Dimensions (Rear view)



15.0 EDID Table						
Address s (HEX)	Function	Hex	Dec	crc	Input values.	Notes
00	Header	00	0		0	EDID Header
01		FF	255		255	
02		FF	255		255	
03		FF	255		255	
04		FF	255		255	
05		FF	255		255	
06		FF	255		255	
07		00	0		0	
08	ID Manufacturer Name	09	9		BOE	ID = BOE
09		E5	229			
0A	ID Product Code	DD	221		1757	ID = 1757
0B		06	6			
0C	32-bit serial No.	00	0			
0D		00	0			
0E		00	0			
0F		00	0			
10	Week of manufacture	01	1		1	
11	Year of Manufacture	1A	26		2016	Manufactured in 2016
12	EDID Structure Ver.	01	1		1	EDID Ver 1.0
13	EDID revision #	04	4		4	EDID Rev. 0.4
14	Video input definition	95	149		-	digital signal/DP input
15	Max H image size	22	34		34	34 cm (Approx)
16	Max V image size	13	19		19	19 cm (Approx)
17	Display Gamma	78	120		2.2	Gamma curve = 2.2
18	Feature support	02	2			RGB display, Preferred Timming mode/RGB 4:4:4
19	Red/Green low bits	C9	201		-	Red / Green Low Bits
1A	Blue/White low bits	A0	160		-	Blue / White Low Bits
1B	Red x high bits	95	149	599	0.585	Red (x) = 10010101 (0.585)
1C	Red y high bits	5D	93	372	0.364	Red (y) = 01011101 (0.364)
1D	Green x high bits	59	89	358	0.350	Green (x) = 01011001 (0.35)
1E	Green y high bits	94	148	593	0.580	Green (y) = 10010100 (0.58)
1F	Blue x high bits	29	41	166	0.163	Blue (x) = 00101001 (0.163)
20	BLue y high bits	24	36	146	0.143	Blue (y) = 00100100 (0.143)
21	White x high bits	50	80	320	0.313	White (x) = 01010000 (0.313)
22	White y high bits	54	84	336	0.329	White (y) = 01010100 (0.329)
23	Established timing 1	00	0		-	
24	Established timing 2	00	0		-	

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25	Established timing 3	00	0		-	
26	Standard timing #1	01	1			Not Used
27		01	1			
28	Standard timing #2	01	1			Not Used
29		01	1			
2A	Standard timing #3	01	1			Not Used
2B		01	1			
2C	Standard timing #4	01	1			Not Used
2D		01	1			
2E	Standard timing #5	01	1			Not Used
2F		01	1			
30	Standard timing #6	01	1			Not Used
31		01	1			
32	Standard timing #7	01	1			Not Used
33		01	1			
34	Standard timing #8	01	1			Not Used
35		01	1			
36	Detailed timing/monitor descriptor #1	9C	156		152.6	152.6MHz Main clock
37		3B	59			
38		80	128		1920	Hor Active = 1920
39		36	54		310	Hor Blanking = 310
3A		71	113		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		38	56		1080	Ver Active = 1080
3C		3C	60		60	Ver Blanking = 60
3D		40	64		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E		30	48		48	Hor Sync Offset = 48
3F		20	32		32	H Sync Pulse Width = 32
40		36	54		3	V sync Offset = 3 line
41		00	0		6	V Sync Pulse width : 6 line
42		58	88		344	Horizontal Image Size = 344 mm (Low 8 bits)
43		C2	194		194	Vertical Image Size = 194 mm (Low 8 bits)
44		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0		0	Hor Border (pixels)
46		00	0		0	Vertical Border (Lines)
47		1A	26			Refer to right table

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48	Detailed timing/monitor descriptor #2	FA	250		117.7	117.7MHz Main clock
49		2D	45			
4A		80	128		1920	Hor Active = 1920
4B		0E	14		270	Hor Blanking = 270
4C		71	113		-	4 bits of Hor. Active + 4 bits of Hor. Blanking
4D		38	56		1080	Ver Active = 1080
4E		28	40		40	Ver Blanking = 40
4F		40	64		-	4 bits of Ver. Active + 4 bits of Ver. Blanking
50		30	48		48	Hor Sync Offset = 48
51		20	32		32	H Sync Pulse Width = 32
52		36	54		3	V sync Offset = 3 line
53		00	0		6	V Sync Pulse width : 6 line
54		58	88		344	Horizontal Image Size = 344 mm (Low 8 bits)
55		C2	194		194	Vertical Image Size = 194 mm (Low 8 bits)
56		10	16		-	4 bits of Hor Image Size + 4 bits of Ver Image Size
57		00	0		0	Hor Border (pixels)
58		00	0		0	Vertical Border (Lines)
59		1A	26		0	
5A	Detailed timing/monitor descriptor #3	00	0		ASCII Data Sting Tag	
5B		00	0			
5C		00	0			
5D		FE	254			
5E		00	0			
5F		42	66		B	Manufacture name : BOECQ
60		4F	79		O	
61		45	69		E	
62		20	32			
63		43	67		C	
64		51	81		Q	
65		0A	10			
66		20	32			
67		20	32			
68		20	32			
69		20	32			
6A		20	32			
6B		20	32			

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6C	Detailed timing/monitor descriptor #4	00	0			Product Name Tag (ASCII)
6D		00	0			
6E		00	0			
6F		FE	254			
70		00	0			
71		4E	78		N	Model name : NV156FHM-T00
72		56	86		V	
73		31	49		1	
74		35	53		5	
75		36	54		6	
76		46	70		F	
77		48	72		H	
78		4D	77		M	
79		2D	45		-	
7A		54	84		T	
7B		30	48		0	
7C		30	48		0	
7D		0A	10			
7E	Extension flag	00	0			
7F	Checksum	26	38	38	-	