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# **CUSTOMER APPROVAL SHEET**

MODEL	
CUSTOMER	Title:
<b>APPROVED</b>	Name :
APPROVAL FOR SPECIFICATION CUSTOMER REMARK:	ATIONS AND CS SAMPLE (Spec. Ver)

1 Li-Hsin Rd. 2. Science-Based Industrial Park Hsinchu 300, Taiwan, R.O.C. Tel: +886-3-500-8800

Fax: +886-3-564-5785



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Page:

e: 2/23

Doc. Version	0.1
Total Page	23
Date	2010/03/25

# **Product Specification**

# 4.3" COLOR TFT-LCD MODULE

**MODEL NAME: A043FW02 V8** 

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**Planned Lifetime:** From 2010/Apr. To 2011/Sep.

Phase-out Control: From 2011/Apr. To 2011/Sep.

**EOL Schedule:** 2011/Sep.

< > >Preliminary Specification

< >Final Specification

Note: The content of this specification is subject to change.

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Page: 3/23

0.1

# **Record of Revision**

Version	Revise Date	Page	Content
0.0	2010/3/11		First draft.
0.1	2010/3/25	12	Update application circuit
		XV.	
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0.1

Page:

4/23

## **Contents:**

<u>A.</u>	General Description	<u>5</u>
<u>B.</u>	<u>Features</u>	5
<u>C.</u>	Physical Specifications	<u>6</u>
<u>D.</u>	Outline Dimension (Tentative)	7
	Electrical Specifications	
<u>E.</u>		
	1. Pin Assignment	8
	2. Absolute Maximum Ratings	
	3. Electrical Characteristics	
	a. TFT- LCD Panel	11
	b. Backlight Driving Conditions	11
	4. Suggested Application Circuit	12
	5. AC Timing	13
	a. Power on/off sequence	13
	b. Timing Condition	14
	c. Timing Diagram	15
<u>F.</u>	Optical specifications (Note 1, 2)	
<u>G.</u>	Touch Screen Panel Specifications	19
_ 1.	Electrical Characteristics	
	2. Mechanical Characteristics	
	3. Life test Condition	
	4. Attention	
<u>H.</u>		
Ŀ	Packing Form	23
4	Packing Form	



Page: 5/23

0.1

### A. General Description

A043FW02 V8 is an amorphous transmissive type Thin Film Transistor Liquid crystal Display (TFT-LCD). This model is composed of a TFT-LCD, a driver, an FPC (flexible printed circuit), a backlight unit and a resistive type touch panel.

#### **B. Features**

- 4.3-inch display with touch panel
- WQVGA resolution in RGB stripe dot arrangement
- DC/DC integrated
- High brightness
- Interfaces: parallel RGB 24-bit
- Wide viewing angle
- Integrated touch screen panel (resistive type)
- 3-in-1 FPC for LCD signals, backlight LED power and touch panel

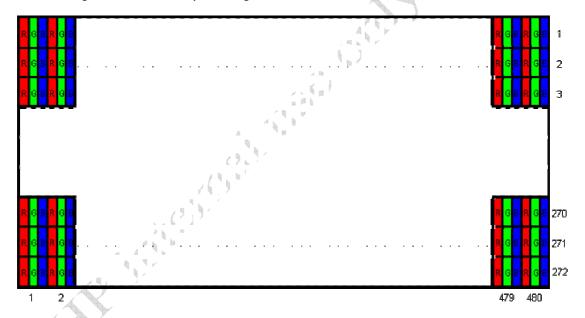


Page: 6/23

# C. Physical Specifications

NO.	Item	Unit	Specification	Remark
1	Display Resolution	dot	480 RGB (H)×272(V)	
2	Active Area	mm	95.04(H)×53.856(V)	
3	Screen Size	inch	4.3(Diagonal)	
4	Dot Pitch	mm	0.066(H)×0.198(V)	
5	Color Configuration		R. G. B. Stripe	Note 1
6	Color Depth		16.7M Colors	- James
7	Overall Dimension	mm	105.5(H) × 67.2(V) × 3.9(T)	Note 2
8	Weight	g	56.8	
9	Touch panel surface treatment		Hard coating ( <u>Haze 8%</u> ) 3H	
10	Display Mode		Normally White	
11	Gray Level Inversion Direction		6 o'clock	

Note 1: Below figure shows dot stripe arrangement.



Note 2: Not including FPC. Refer to the drawing next page for further information.

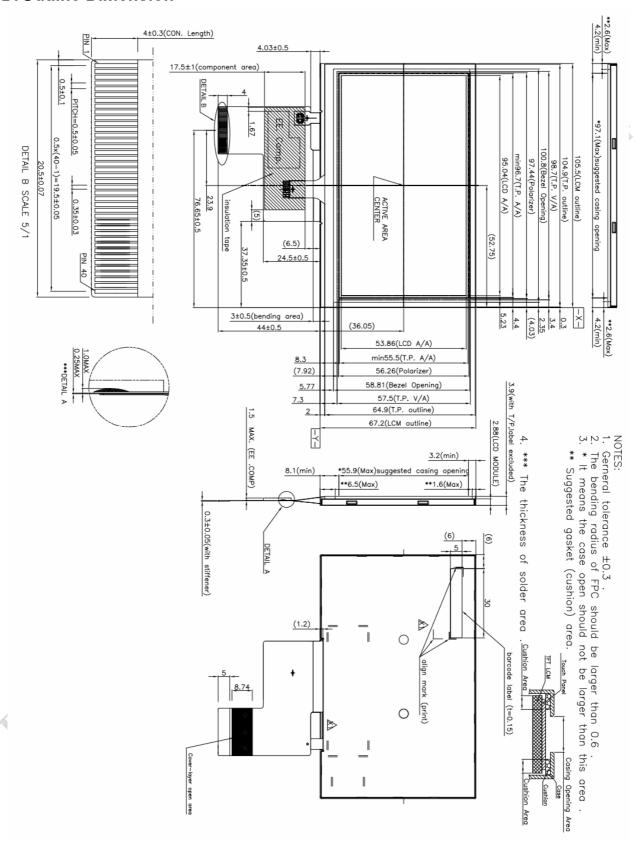




Page: 7/23

0.1

### **D. Outline Dimension**





Page: 8/23

0.1

# E. Electrical Specifications

# 1. Pin Assignment

No.	Pin Name	I/O	Description	Remarks
1	VLED-	PI	LED backlight cathode	
2	VLED+	PI	LED backlight anode	
3	GND	G	Ground	
4	VDDIO	PI	power supply for digital interface	
5	R0	I	Red Data Signal (LSB)	
6	R1	I	Red Data Signal	
7	R2	I	Red Data Signal	
8	R3	I	Red Data Signal	
9	R4	I	Red Data Signal	
10	R5	I	Red Data Signal	
11	R6	I	Red Data Signal	
12	R7	I	Red Data Signal (MSB)	
13	G0	I	Green Data Signal (LSB)	
14	G1	I	Green Data Signal	
15	G2	I	Green Data Signal	
16	G3	I	Green Data Signal	
17	G4	I	Green Data Signal	
18	G5	I	Green Data Signal	
19	G6	I	Green Data Signal	
20	G7	I	Green Data Signal (MSB)	
21	В0	I	Blue Data Signal (LSB)	
22	B1	I	Blue Data Signal	
23	B2	I	Blue Data Signal	
24	В3	I	Blue Data Signal	
25	B4	I	Blue Data Signal	
26	B5	I	Blue Data Signal	
27	B6	I	Blue Data Signal	
28	B7	I	Blue Data Signal (MSB)	
29	GND	G	ground	
30	DCLK	I	pixel clock	
31	DISP	I	display on/off signal	
32	HSYNC	I	horizontal synchronizing signal	
33	VSYNC	I	vertical synchronizing signal	



Page: 9/23

2.4	DE	<sub>T</sub>		
	DE		data enable	
	VDD	PI	power supply for analog circuitry	
	NC		no connect	
	TP_R	I/O	X right	
	TP_B	I/O	Y down	
	TP_L	I/O	X left	
<u> </u>	TP_U	I/O	Y up	
	tai sigirai iriput, O.		nal output, G: GND, PI: Power input, C: Capacitor	



Page: 10/23

0.1

### 2. Absolute Maximum Ratings

Items	Symbol	Va	lues	Unit	Condition	
items	Syllibol	Min. Max.		Oiiit	Condition	
Power Supply Voltage	VDD	-0.3	4.5	V		
Interface Supply Voltage	VDDIO	-0.3	4.5	V		
LED Reverse Voltage	V <sub>r</sub>	2.9	3.5	V	One LED (note 2.)	
LED Forward Current	I <sub>f</sub>		25	mA	One LED	
Operation Temperature	T <sub>op</sub>	-20	70	°C		
Storage Temperature	T <sub>st</sub>	-30	80	°C		

Note 1.If the operating condition exceeds the absolute maximum ratings, the TFT-LCD module may be damaged permanently. Also, if the module operated with the absolute maximum ratings for a long time, its reliability may drop.

Note 2. Using 29 Volt (2.9x10=29) as the backlight LED power source will cause low LED power supply current. Please make sure the power supply current needs to be 20mA.

Signal LED driving Voltage						
Min.	2.9	Тур.	3.2	Max.	3.5	



Page: 11/23

#### 3. Electrical Characteristics

The following items are measured under stable condition and suggested application circuit.

#### a. TFT-LCD Panel

Parameter	Symbol	Min	Тур	Max	Unit	Notes
Power Supply Voltage	VDD	3	3.3	3.6	V	
Interface Supply Voltage	VDDIO	1.7	3.3	VDD	V	
Input Cianal Valtage	$V_{ih}$	0.7* VDDIO		VDDIO	V	
Input Signal Voltage	V <sub>il</sub>	GND		0.3* VDDIO	V	
Power Supply Current	$I_{VDD}$		11	13	mA	
Frame Frequency	<b>f</b> <sub>Frame</sub>		60	70	Hz	
Dot Data Clock	DCLK	5	9.2	12	MHz	

Note 1. Panel surface temperature should be kept less than content of section E.2. "Absolute maximum ratings"

Note 2. I<sub>VDD</sub> Typ is in color bar pattern. I<sub>VDD</sub> Max is in black pattern.

### b. Backlight Driving Conditions

Parameter	Symb	Min.	Тур.	Max.	Unit	Remark
LED Supply Current	ΙL		20	22	mA	single serial
Power Consumption	PBL	1	640	770	mW	
LED Life Time	L	10,000			Hr	Note 2

Note 1: LED backlight is 10 LEDs serial type. Suggestion is driven by current 20mA for each LED string.



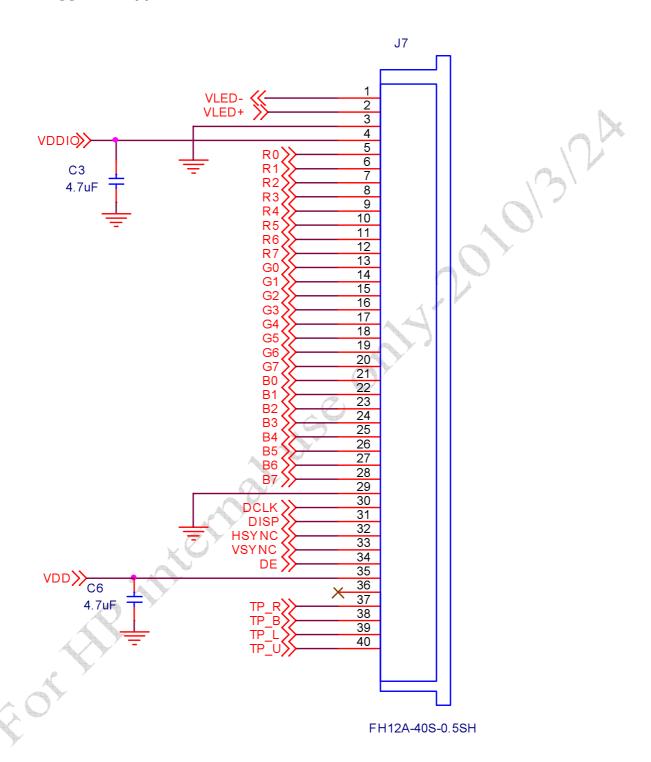
- Note 2: Define "LED Lifetime": brightness is decreased to 50% of the initial value. LED Lifetime is restricted under normal condition, ambient temperature = 25°C and LED lightbar current = 20 mA.
- Note 3: If it uses larger LED lightbar voltage/ current more than 20mA, it maybe decreases the LED lifetime



Page: 12/23

0.1

# 4. Suggested Application Circuit



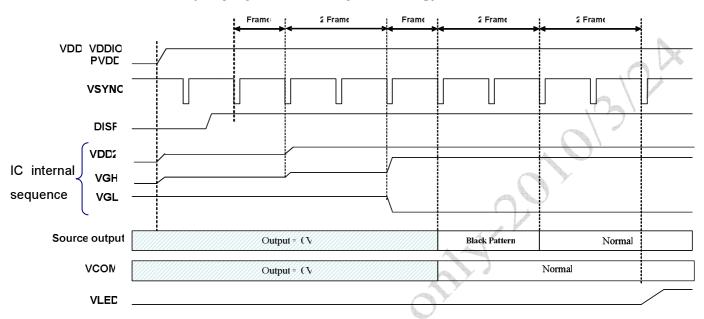


Page: 13/23

### 5. AC Timing

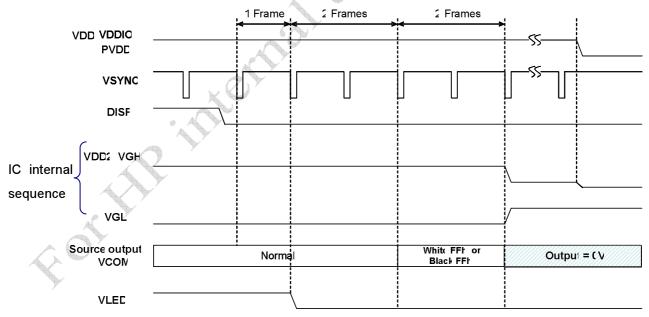
### a. Power on/off sequence

# Power On (Display ON; Standby Disabling)



Notes: The driver IC default is on standby mode. It can be changed to normal operation by using DISP hard pin.

# Power-Off (Display Off; Standby Enabling)





Page: 14/23

0.1

### b. Timing Condition

Pa	rameter	Symbol	Min.	Тур.	Max.	Unit.	Remark
	Frequency	1/Tdclk	5	9.2	12	MHz	
DCLK	CLK pulse duty	Tcwh	40			ns	
	CLK pulse duty	Tcwl	40			ns	
Data	Setup Time	Tdsu	12			ns	
Data	Hold Time	Tdhd	12			ns	
DE	Setup Time	Tdesu	12			ns	
DE	Hold Time	Tdehd	12			ns	
Frame Frequency	Cycle	tv		16.7		ms	
	Cycle	tv	275	288	335	Н	
1 Frame	Display Period	tvdisp		272		Н	
Scanning Time	Front porch	Tvfp	1	4		Н	
Scarning Time	Pulse width	Tvw		10		Н	
	Back porch	Tvbp	2	12		Н	
	Cycle	Th 🗷	490	531	605	DCLK	
1 Line Coopping	Display Period	Thdisp		480		DCLK	
1 Line Scanning Time	Front porch	Thfp	2	8		DCLK	
Time	Pulse width	Thw	1	1		DCLK	
	Back porch	thbp	8	43		DCLK	

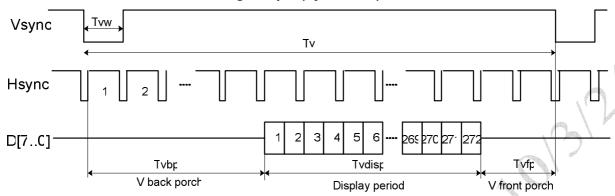
Note 1: Sync mode just can be used on the typical timing setting.



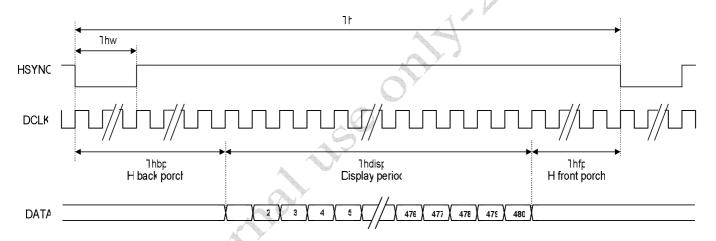
Page: 15/23

### c. Timing Diagram

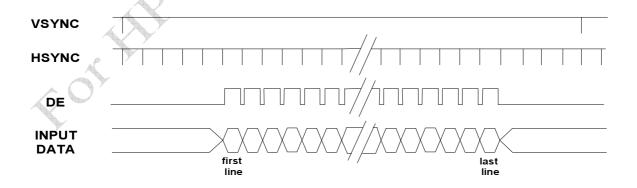
### **Vertical Timing of Input (Sync mode)**



#### **Horizontal Timing of Input (Sync mode)**



### **Vertical Timing of Input (Sync-DE mode)**

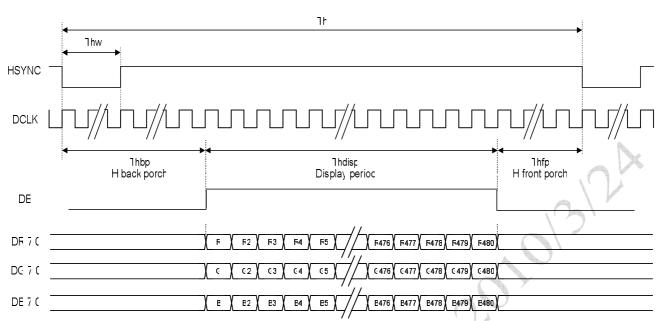


#### **Horizontal Timing of Input (Sync-DE mode)**

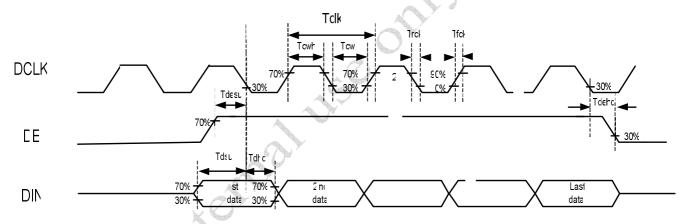
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Page: 16/23



# Clock and data input timing diagram





Page: 17/23

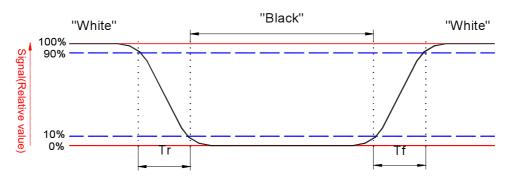
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## F. Optical specifications (Note 1, 2)

ltem	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response Time							
Rise	Tr	<i>θ</i> =0°	-	15		ms	Note 3
Fall	Tf		-	20		ms	
Contrast ratio	CR	At optimized	200	300	300 -		Note 5, 6
Contrast ratio	CR	viewing angle					Note 5, 6
Viewing Angle							1
Тор				50	-	103	) \
Bottom		CR≧10		60	-	deg.	Note 7, 8
Left				70	-		
Right				70	- (	Y	
Brightness	Y <sub>L</sub>	<i>θ</i> =0°	320	400		cd/m <sup>2</sup>	Note 9
White Chromaticity	Х	<i>θ</i> =0°	0.27	0.32	0.37		
	у	<i>θ</i> =0°	0.29	0.34	0.39		

- Note 1: Measurement should be performed in the dark room, optical ambient temperature =25°C, and backlight current I<sub>1</sub>=20 mA
- Note 2: To be measured on the center area of panel with a field angle of 1°by Topcon luminance meter BM-7, after 10 minutes operation.
- Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively.



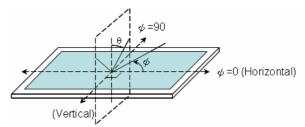
- Note 4. From liquid crystal characteristics, response time will become slower and the color of panel will become darker when ambient temperature is below 25℃.
- Note 5. Contrast ratio is calculated with the following formula.

 $Contrastratio = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$ 

Note 6. Definition of viewing angle: refer to figure as below.



Page: 18/23



Note 7. The viewing angles are measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 8. Brightness is measured at the center of the display perpendicular to the panel surface.

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Page: 19/23

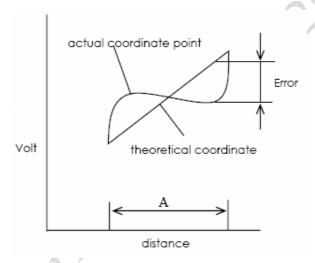
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### **G. Touch Screen Panel Specifications**

### 1. Electrical Characteristics

ltem	Min.	Max.	Unit	Remark		
Rate DC Voltage			5	V		
Resistance	X (Film)	500	1400	Ω	At connector	
	Y (Glass)	100	700	2.2	At connector	
Linearity		-1.5%	1.5%		Note 1, test by 250 gf	
Chattering				ms	At connector pin	
Insulation Resistance		20		ΜΩ	DC 25V	

Note 1: Measurement condition of Linearity: difference between actual voltage & theoretical voltage is an error at any points. Linearity is the value max. error voltage divided by voltage difference on active area.

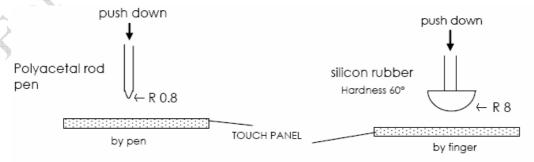


### 2. Mechanical Characteristics

ltem	Min.	Max.	Unit	Remark
Hardness of Surface	3		Н	JIS K-5600
Operation Force (Pen or Finger)		38	g	Note 1

Note 1: Within "guaranteed active area", but not on the edge and dot-spacer.

Note 2: Operation force measurement is under test condition as figure below.



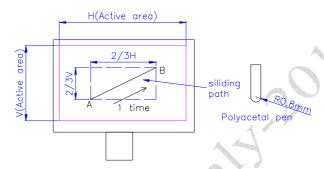


Page: 20/23

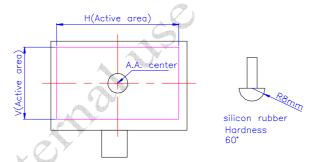
#### 3. Life test Condition

Item	Min.	Max.	Unit	Remark
Notes Life	10 <sup>5</sup>	=	times	Note 1, 2
Input Life	10 <sup>6</sup>	=	times	Note 1, 3

Note 1: Notes Life test condition (by pen): slide on central 2/3 of active area and use R 0.8mm polyacecal pen, input force: 250gf, frequency: 60mm/sec. Sliding from A to B complete 1 time. shown as figure 2.



Note 2: Input Life test condition (by finger): test position on active area center and use R8.0mm silicon rubber (hardness 60°), test force: 250gf, frequency: 2times/sec. shown as figure.



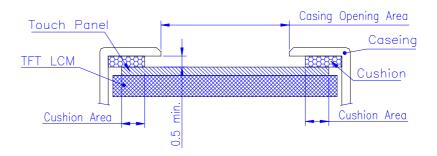
#### 4. Attention

Please pay attention for below matters at mounting design of touch panel of LCD module.

- 1. Do not design enclosure pressing the view area to prevent from miss input.
- 2. Enclosure support must not touch with view area.
- 3. Use elastic or non-conductive material to enclosure touch panel.
- 4. Do not bond film of touch panel with enclosure.
- 5. The touch panel edge is conductive. Do not touch it with any conductive part after mounting.



Page: 21/23



- 6. If user wants to clean touch panel by air gun, pressure 2kg/cm2 below is suggested. Not to blow glass from FPC site to prevent FPC peeled off.
- 7. Do not put a heavy shock or stress on touch panel and film surface. Ex. Don't lift the panel by film face with vacuum.
- 8. Do not lift LCD module by FPC.
- 9. Please use dry cloth or soft cloth with neutral detergent (after wring dry) or one with ethanol at cleaning.

  Do not use any organic solvent, acid or alkali liquor.
- 10. Do not pile touch panel. Do not put heavy goods on touch panel.
- 11. In order to get the optimal mapping between TFT-LCD and touch panel, each touch panel needs to be executed calibration (5 points at least) before operating touch functions. For detail calibration algorisms, please refer to touch panel driving IC user manuals.



Page: 22/23

0.1

# H. Reliability Test Items

No.	Test items	Conditions	Remark	
1	High Temperature Storage	Ta= 80°C 240Hrs		
2	Low Temperature Storage	Ta= -30°C 240h		
3	High Temperature Operation	Ta= 70°C 240h		
4	Low Temperature Operation	Ta= -20°C	240Hrs	
5	High Temperature & High Humidity	Ta= 60°C. 90% RH	240Hrs	Operation
6	Heat Shock	-25°C ~70°C, 50 cycle, 2	Hrs/cycle	Non-operation
		Random vibration:		1001
7	Vibration (With Carton)	0.015G <sup>2</sup> /Hz from 5~200Hz		IEC 68-34
		–6dB/Octave from 200∼500H	z ,	) '
8	Drop (With Carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	207	

Note 1: Ta: Ambient temperature.

Note 2: In the standard condition, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.



Page: 23/23

0.1

# I. Packing Form

