SPECIFICATION FOR APPROVAL

()	Preliminary Specification
(0)	Final Specification

Title	27.0" UHD TFT	27.0" UHD TFT LCD			
BUYER	SUPPLIER	LG Display Co., Ltd.			
MODEL	*MODEL	LM270WR2			
	SUFFIX	SPA1			

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
/	
/	
Please return 1 copy for you your signature and	

APPROVED BY	SIGNATURE DATE
W.G. Kweon / G.Manager	1/2/2°
REVIEWED BY	
J.H. Song / Manager [C]	12514.
Y.S. Chung / Manager [M]	7/100
S.H. Han / Manager [O]	78014.13
J.C. Lim / Manager [P]	14.
PREPARED BY K.W. Lee / Engineer	augh Xium.



Contents

No	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTREISTICS	6
3-2	INTERFACE CONNECTIONS	9
3-3	LVDS CHARACTERISTICS	12
3-4	SIGNAL TIMING SPECIFICATIONS	15
3-5	SIGNAL TIMING WAVEFORMS	16
3-6	COLOR INPUT DATA REFERENCE	17
3-7	POWER SEQUENCE & DIP CONDITION FOR LCD MODULE	18
4	OPTICAL SPECIFICATIONS	20
5	MECHANICAL CHARACTERISTICS	25
6	RELIABLITY	28
7	INTERNATIONAL STANDARDS	29
7-1	SAFETY	29
7-2	EMC	29
7-3	ENVIRONMENT	29
8	PACKING	30
8-1	DESIGNATION OF LOT MARK	30
8-2	PACKING FORM	30
9	PRECAUTIONS	31



RECORD OF REVISIONS

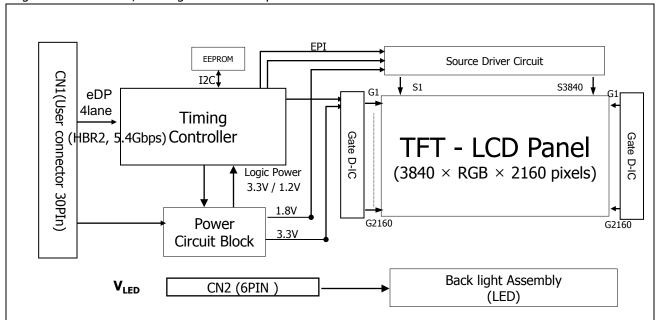
Revision No	Revision Date	Page	Description
0.0	Aug., 04, 2014	-	First Draft, Preliminary Specifications
0.1	Dec., 03, 2014	9, 18	Update the BIST pin configuration Add LCD CNT Name & Revise Pin1-5 symbol (Vin→VLCD)
		15	Update Time table (add min., max. spec.)
		20	Update color coordinate data & Modify response time spec.
		24	Revise table 10. (Gray Level)
1.0	Dec., 16, 2014	-	First Draft, Final Specifications



1. General Description

LM270WR2 is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode (White LED) backlight system without LED driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 27 inch diagonally measured active display area with UHD resolution (3840 horizontal by 2160 vertical pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus, presenting a palette of more than 1.07Billion colors with A-FRC (Advanced Frame Rate Control). It has been designed to apply eDP(HBR2, 5.4Gbps) interface.

It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



General Features

[FIG.1] Block diagram

Active Screen Size	27 inches(68.47cm) diagonal
Outline Dimension	630.0(H) x 368.2(V) x 18.0(D) mm (Typ.)
Pixel Pitch	0.1554 mm x 0.1554 mm
Pixel Format	3840 horiz. By 2160 vert. Pixels RGB stripes arrangement
Color Depth	1.07 Billion colors, 10Bit (8Bit + A-FRC)
Luminance, White	350 cd/m² (Center 1 Point, Typ.)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 30.6 Watt (Typ.) (5.7 Watt @VLCD, 24.9 Watt @ Is=120mA)
Weight	3,215g (Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Advanced Anti-glare treatment of the front polarizer (3H)



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

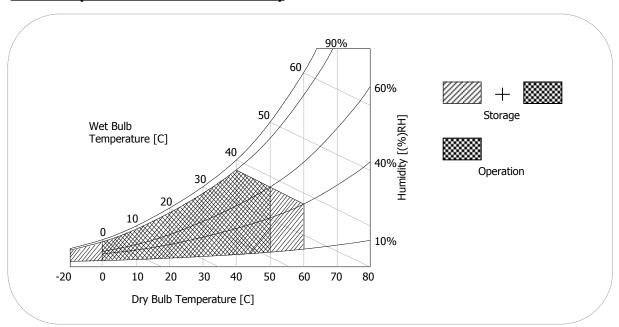
Parameter	Symbol Values			Units	Notes	
Parameter	Syllibol	Min	Max	Offics	Notes	
Power Input Voltage	VLCD	-0.3	12	Vdc	at 25 ± 2°C	
Operating Temperature	Тор	0	50	°C		
Storage Temperature	Tst	-20	60	°C	1, 2, 3	
Operating Ambient Humidity	Нор	10	90	%RH		
Storage Humidity	Hst	10	90	%RH		
LCM Surface Temperature (Operation)	T _{Surface}	0	65	°C	1,4	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.

- 2. Maximum Storage Humidity is up to 40°C, 70% RH only for 4 corner light leakage Mura.
- 3. Storage condition is guaranteed under packing condition
- 4. LCM Surface Temperature should be Min. 0° C and Max. 65° C under the VLCD=10.0V, fV=60Hz, 25° C ambient Temp. no humidity control and LED string current is typical value.

FIG.2 Temperature and relative humidity





3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
Parameter	Symbol	Min	Тур	Max	Onit	Notes
MODULE:						
Power Supply Input Voltage	VLCD	9.5	10.0	10.5	Vdc	5
Permissive Power Input Ripple	VdRF			200	mV _{p-p}	1
Downer Cumply Input Cumput	ILCD	-	570	713	mA	2
Power Supply Input Current		-	820	1025	mA	3
Dower Congumenties	Pc TYP	-	5.7	7.13	Watt	2
Power Consumption	Pc MAX	-	8.2	10.25	Watt	3
Rush current	Irush	-		3	А	4

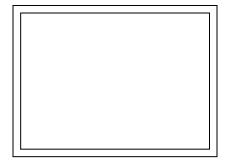
Note:

- 1. Permissive power ripple should be measured under V_{LCD} =10.0V, 25 ± 2°C, f_V =60Hz condition and at that time, we recommend the bandwidth configuration of oscilloscope is to be under 20Mhz.
- 2. The specified current and power consumption are under the V_{LCD} =10.0V, 25 ± 2°C, f_V =60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 3. The current is specified at the maximum current pattern.
- 4. The duration of rush current is about 2ms and rising time of power Input is 1ms(min.).
- 5. VLCD level must be measured at two points on LCM PCB between VLCD(test point) and LCM Ground. The measured results need to meet the Power supply input voltage spec. (Test condition: maximum power pattern, $25\pm2^{\circ}$ C, fV=60Hz)

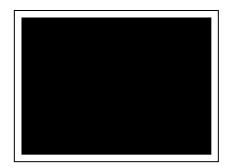
Ver. 1.0 Dec., 16, 2014 6 / 32



 \bullet **Permissive Power input ripple (**V_{LCD} =10.0V, 25°C, fv (frame frequency)=MAX condition**)**

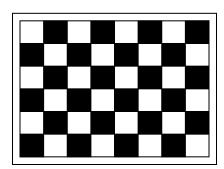


White pattern

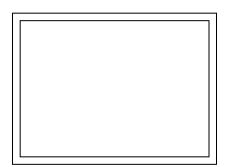


Black pattern

 \bullet **Power consumption (**V_{LCD} =10V, 25°C, fV (frame frequency=60Hz condition)



Typical power Pattern



Maximum power Pattern

FIG.3 Mosaic pattern & White Pattern for power consumption measurement



Table 2-2. LED Bar ELECTRICAL CHARACTERISTICS

Dawawahau	Creekal	Values			II.u.it	Notes
Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
LED String Current	Is	-	120	125	mA	1, 2, 5
LED String Voltage	Vs	48.5	51.9	55.3	V	1, 5
Power Consumption	PBar	-	24.9	26.5	Watt	2, 4
LED Life Time	LED_LT	30,000	-	-	Hrs	3

Notes) The LED Bar consists of 68ea LED packages, 4 strings (parallel) x 17 packages (serial)

LED driver design guide

1) The design of the LED driver must have specifications for the LED in LCD Assembly.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by

the characteristics of the LED driver. So all the parameters of an LED driver should be carefully designed and output current should be

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs. When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

- 2) LGD strongly recommend Analog Dimming method for Backlight Brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.
- 1. The specified values are for a single LED bar .
- 2. The specified current is defined as the input current for a single LED string with 100% duty cycle.
- 3. The LED life time is defined as the time when brightness of LED packages become 50% or less than the initial value under the conditions at $Ta = 25 \pm 2^{\circ}C$ and LED string current is typical value.
- 4. The power consumption shown above does not include loss of external driver. The typical power consumption is calculated as $P_{Bar} = V_s(Typ.) \times I_s(Typ.) \times No.$ of strings. The maximum power consumption is calculated as $P_{Bar} = V_s(Max.) \times I_s(Typ.) \times No.$ of strings.
- 5. LED operating conditions are must not exceed Max. ratings.



3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(CN1): GT05Q-30S-H10-MN (LSMtron), HD2S030HA1 (JAE), KN38B-30S-0.5H(HIROSE)

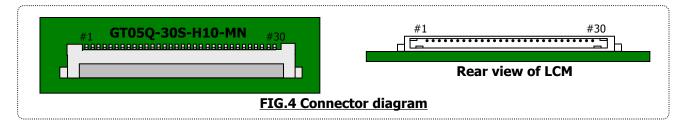
-or Equivalent

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	VLCD	Power Supply +10.0V	16	Lane0P	True Signal for Main Link 0
2	VLCD	Power Supply +10.0V	17	Lane0N	Component Signal for Main Link 0
3	VLCD	Power Supply +10.0V	18	GND	Ground
4	VLCD	Power Supply +10.0V	19	Lane1P	True Signal for Main Link 1
5	VLCD	Power Supply +10.0V	20	Lane1N	Component Signal for Main Link 1
6	NC	No connection	21	GND	Ground
7	GND	Ground	22	Lane2P	True Signal for Main Link 2
8	NC	No Connection	23	Lane2N	Component Signal for Main Link 2
9	NC	No Connection	24	GND	Ground
10	GND	Ground	25	Lane3P	True Signal for Main Link 3
11	HPD	Hot Plug Detect Signal	26	Lane3N	Component Signal for Main Link 3
12	GND	Ground	27	GND	Ground
13	AUX_CHN	Component Signal for Auxiliary Channel	28	BIST	L(GND): Black, H(3.3V): Rotational Pattern
14	AUX_CHP	True Signal for Auxiliary Channel	29	NC	No Connection
15	GND	Ground	30	GND	Ground

Note: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. BIST (Build In Self Test): If Pin No.28 is "High(3.3V)", T-Con generates Rotational Pattern. Time to stay at every pattern is about 2sec.



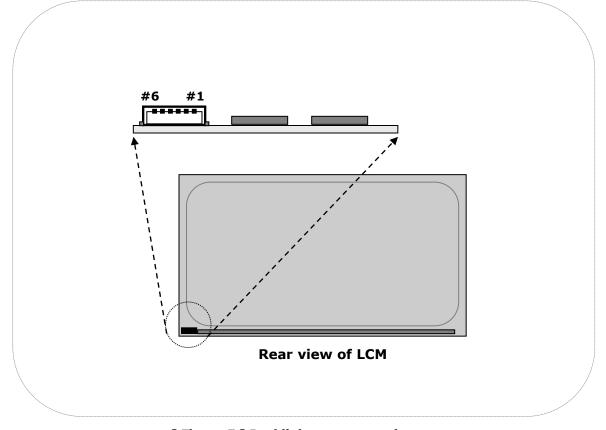


3-2-2. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2)

The LED interface connector is a model SM06B-SHJH(HF)_Manufactured by JST or equivalent. The mating connector is a SHJP-06V-S(HF), SHJP-06V-A-K(HF) or equivalent. The pin configuration for the connector is shown in the table below.

Table 3-1. LED CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description
1	FB1	Channel1 Current Feedback
2	FB2	Channel2 Current Feedback
3	VLED	LED Power Supply
4	VLED	LED Power Supply
5	FB3	Channel3 Current Feedback
6	FB4	Channel4 Current Feedback

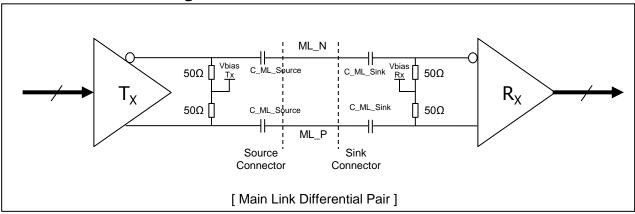


[Figure 5] Backlight connector view



3-3. eDP Signal Specifications

3-3-1. eDP Main Link Signal



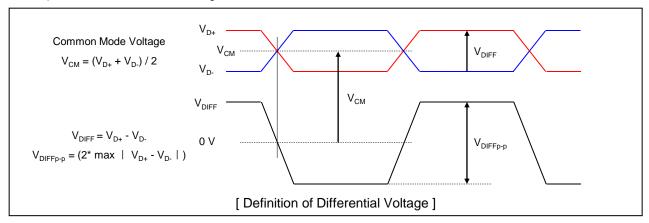
Parameter	Symbol	Min	Тур	Max	Unit	Notes
Unit Interval for high bit rate (5.4Gbps / lane)	UI_HBR2	-	185	-	ps	
Link Clock Down Spreading	Amplitude	0	-	0.5	%	
Link Glock Down Spreading	Frequency	30		33	kHz	
Differential peak-to-peak voltage at Sink side connector	V _{RX-DIFFp-p}	150	-	1320	mV	Note 7,8)
EYE width at Sink side connector	T _{RX-EYE-CONN}	0.51	-	-	UI	Note 7,8)
Rx DC common mode voltage	V _{RX CM}	ı	0	2.0	V	
Lane-to-Lane skew	L _{Rx-SKEW-}	-	-	5200	ps	
Lane intra-pair skew	L _{Rx-SKEW-}	-	-	50	ps	
AC Coupling Capacitor	C _{SOURCE—ML}	75		200	nF	Source side

Note)

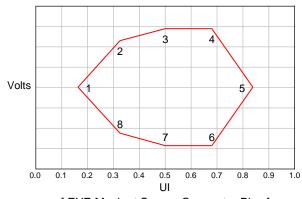
- 1. Termination resistor is typically integrated into the transmitter and receiver implementations.
- 2. In cabled embedded system, it is recommended the system designer ensure that EYE width and voltage are met at the sink side connector pins.
- 3. Mismatched common mode voltage will occur abnormal display.
- 4. All eDP electrical spec is measured at sink connector side.
- 5. eDP cable Impedance should be 100ohm \pm 5%.
- 6. At sink side main link cap. are for protection ESD/EOS damage.



Note7) Definition of Differential Voltage



Note8) Main Link EYE Diagram



[EYE Mask at Source Connector Pins]

Deint	High Bit Rate 2					
Point	Time(UI)	Voltage(V)				
1	0.210	0.000				
2	0.355	0.140				
3	0.500	0.175				
4	0.645	0.175				
5	0.790	0.000				
6	0.645	-0.175				
7	0.500	-0.175				
8	0.355	-0.140				

[EYE Mask Vertices at Source Connector Pins]

Volts 2
0.0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1.0 UI

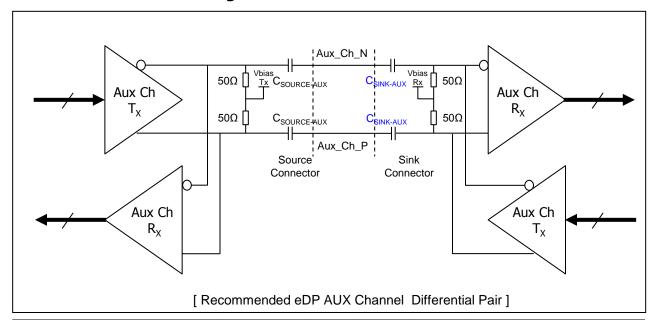
[EYE Mask at Sink Connector Pins]

Doint	High Bit Rate					
Point	Time(UI)	Voltage(V)				
1	0.246	0.000				
2	0.500	0.075				
3	0.755	0.000				
4	0.500	-0.075				

[EYE Mask Vertices at embedded DP Sink Connector Pins]



3-3-2. eDP AUX Channel Signal



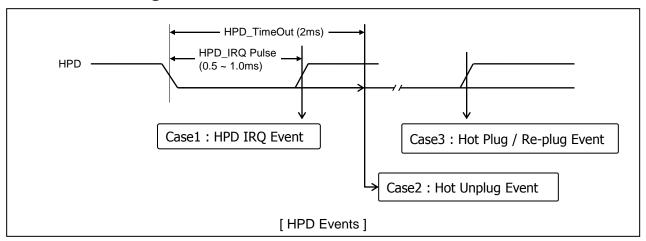
Parameter	Symbol	Min	Тур	Max	Unit	Notes
AUX Unit Interval	UI	0.4	-	0.6	us	
AUX Jitter at Tx IC Package Pins	T	-	-	0.04	UI	Equal to 24ns
AUX Jitter at Rx IC Package Pins	T _{jitter}	-	-	0.05	UI	Equal to 30ns
AUX Peak-to-peak voltage at Connector Pins of Receiving		0.32	-	1.36	V	
AUX Peak-to-peak voltage at Connector Pins of Transmitting	V _{AUX-DIFFp-p}	0.39	-	1.38	V	
AUX EYE width at Connector Pins of Tx and Rx		0.98	-	-	UI	
ALIV DC common mode voltage	V _{AUX-CM _Rx}	-	0	2.0	٧	
AUX DC common mode voltage	V _{AUX-CM _Tx}	-	0.15	2.0	٧	
AUX AC Coupling Capacitor	C _{SOURCE-AUX}	75		200	nF	Source side

Note)

- 1. Termination resistor is typically integrated into the transmitter and receiver implementations.
- 2. $V_{AUX-DIFFp-p} = 2^* \mid V_{AUXP} V_{AUXN} \mid$
- 3. Termination resistor should be ± 50 ohm at source side to AUX level.
- 4. At sink side AUX cap. are for protection ESD/EOS damage.
- 5. Mismatched common mode voltage will occur abnormal display.



3-3-5. eDP HPD Signal



Parameter	Symbol	Min	Тур	Max	Unit	Notes
HPD Voltage		2.25	-	3.6	V	Sink side Driving
Hot Plug Detection Threshold	HPD	2.0	-	-	V	Course side Detection
Hot Unplug Detection Threshold		-	-	0.8	٧	Source side Detecting
HPD_IRQ Pulse Width	HPD_IRQ	0.5	-	1.0	ms	
HPD_TimeOut		2.0	-	-	ms	HPD Unplug Event

Note)

- 1. HPD IRQ: Sink device wants to notify the Source device that Sink's status has changed so it toggles HPD line, forcing the Source device to read its Link / Sink Receiver DPCD field via the AUX-CH
- 2. HPD Unplug: The Sink device is no longer attached to the Source device and the Source device may then disable its Main Link as a power saving mode
- 3. Plug / Re-plug: The Sink device is now attached to the Source device, forcing the Source device to read its Receiver capabilities and Link / Sink status Receiver DPCD fields via the AUX-CH



3-4. Signal Timing Specifications

This is signal timing required at the input of the TMDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

Table 6. TIMING TABLE

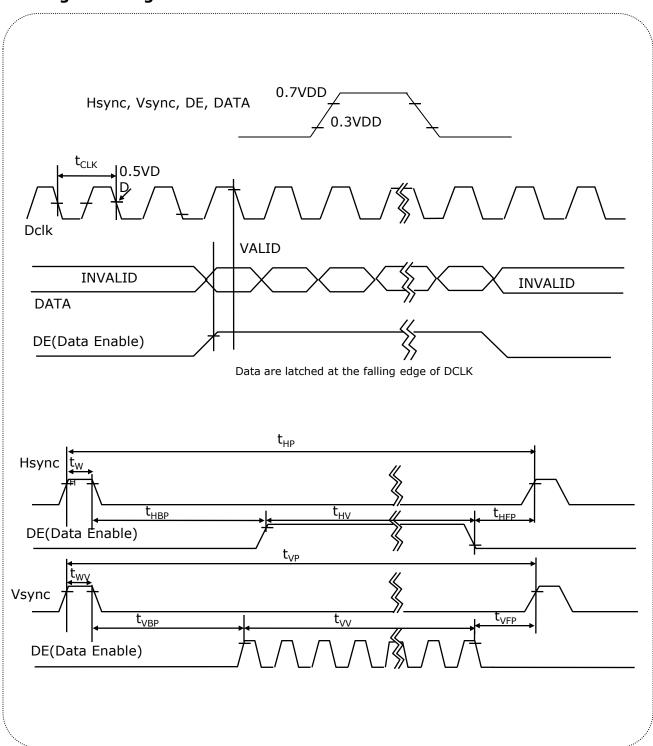
	ITEM	SYMBOL	Min	Тур	Max	Unit	Note
	Period	tCLK	1.9	1.9	1.9	ns	
DCLK	Frequency	fCLK	533.25	533.25	533.25	MHz	-
	Period	tHP	4000	4000	4000		
Hsync	Width-Active	twH	32	32	32	tCLK	
	Period	tVP	2222	2222	2222	tHP	
Vsync	Frequency	fV	59.997	59.997	59.997	Hz	
	Width-Active	twv	5	5	5	tHP	
	Horizontal Valid	tHV	3840	3840	3840		
	Horizontal Back Porch	tHBP	80	80	80	tCLK	
	Horizontal Front Porch	tHFP	48	48	48		
Data	Horizontal Blank	-	160	160	160		twn+ thbp+ thfp
Enable	Vertical Valid	tvv	2160	2160	2160		
	Vertical Back Porch	tVBP	54	54	54	_	
	Vertical Front Porch	tVFP	3	3	3	tHP	
	Vertical Blank	-	62	62	62		twv+ tvbp+ tvfp

Note:

- 1. DE Only mode operation. The input of Hsync & Vsync signal does not have an effect on LCD normal operation.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. Horizontal period should be even.



3-5. Signal Timing Waveforms





3-6. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

		Input Color Data								
	Color		RED		GREEN		BLUE			
		MSB	LSB	MSB		MSB	LSB			
		R9 R8 R7	R6 R5 R4 R3 R2 R1 R0	G9 G8 G7 G	66 G5 G4 G3 G2 G1 G0	B9 B8 B7 B	5 B5 B4 B3 B2 B1 B0			
	Black	0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	Red (1023)	1 1 1	1 1 1 1 1 1 1	0 0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	Green (1023)	0 0 0	0 0 0 0 0 0 0	1 1 1 1	1 1 1 1 1 1 1	0 0 0 0	0 0 0 0 0 0			
Basic	Blue (1023)	0 0 0	0 0 0 0 0 0 0	0000	0 0 0 0 0 0	1111	1 1 1 1 1 1			
Color	Cyan	0 0 0	0 0 0 0 0 0 0	1 1 1 1	1 1 1 1 1 1 1	1 1 1 1	1 1 1 1 1 1			
	Magenta	1 1 1	1 1 1 1 1 1 1	0000	000000	1 1 1 1	1 1 1 1 1 1			
	Yellow	1 1 1	1 1 1 1 1 1 1	111:	1 1 1 1 1 1	0 0 0 0	000000			
	White	1 1 1	1 1 1 1 1 1 1	1 1 1 :	1 1 1 1 1 1 1	1 1 1 1	1 1 1 1 1 1			
	RED (000)	0 0 0	0 0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	RED (001)	0 0 0	0 0 0 0 0 0 1	0000	000000	0000	000000			
RED			···							
	RED (1022)	1 1 1	1111110	0000	0 0 0 0 0 0	0000	0 0 0 0 0 0			
	RED (1023)	1 1 1	111111	0 0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	GREEN (000)	0 0 0	0 0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	GREEN (001)	0 0 0	0 0 0 0 0 0 0	0000	000001	0000	000000			
GREEN										
	GREEN (1022)	0 0 0	0 0 0 0 0 0 0	1 1 1 1	111110	0 0 0 0	0 0 0 0 0 0			
	GREEN (1023)	0 0 0	0 0 0 0 0 0 0	1 1 1 1	111111	0 0 0 0	0 0 0 0 0 0			
	BLUE (000)	0 0 0	0 0 0 0 0 0 0	0000	0 0 0 0 0 0	0 0 0 0	0 0 0 0 0 0			
	BLUE (001)	0 0 0	0 0 0 0 0 0 0	0000	000000	0 0 0 0	000001			
BLUE							•••			
	BLUE (1022)	0 0 0	0 0 0 0 0 0 0	0000	000000	1 1 1 1	1 1 1 1 1 0			
	BLUE (1023)	0 0 0	0 0 0 0 0 0	0 0 0 0	000000	1 1 1 1	111111			



3-7. Power Sequence

3-7-1. Power Sequence

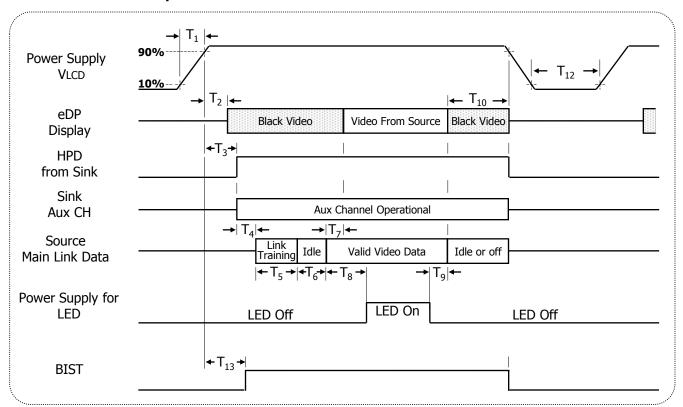


Table 7. POWER SEQUENCE TABLE

Timing	Required	Lin	nits	Units	Notes
Timing	Ву	Min	Max	Units	notes
T ₁	Source	0.5	10	ms	ı
T ₂	Sink	10	200	ms	ı
T ₃	Sink	15	200	ms	•
T ₄	Source	ı	1	ms	ı
T ₅	Source	-	-	ms	-
T ₆	Source	-	100	ms	
T ₇	Sink	0	50	ms	-

Timing	Required	Limits		Uni	Notes	
riiiiig	Ву	Min	Max	ts	Notes	
T ₈	Source	1	1	ms	LGD recommend Min 200ms	
T ₉	Source	•	ı	ms	•	
T ₁₀	Source	0	500	ms	•	
T ₁₂	Source	1000	ı	ms		
T ₁₃	Source	250	-	ms		

Note) 1. Do not insert the mating cable when system turn on.

- 2. Valid Data have to meet "3-4. Signal Timing Specifications"
- 3. When the Video signal is invalid, be sure to pull down the power supply for LCD VLCD to 0V.
- 4. The above power sequence should be satisfied the basic power on/off and resolution, timing transition.
- 5. LED power must be turn on after power supply for LCD and valid status of Video Signal.



3-7-2. VLCD Power Dip Condition

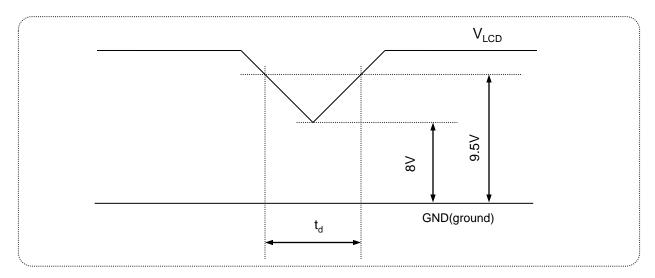


FIG.5 Power dip condition

1) Dip condition

$$8V \le V_{LCD} < 9.5V$$
, $t_d \le 20ms$

2)
$$V_{LCD}$$
< 8V

 $V_{\text{LCD}}\text{-dip}$ conditions should also follow the Power On/Off conditions for supply voltage.



4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree.

FIG. 6 presents additional information concerning the measurement equipment and method.

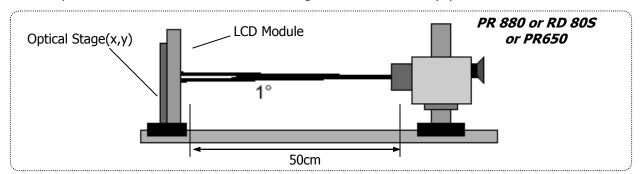


FIG.6 Optical Characteristic Measurement Equipment and Method

Table 9. OPTICAL CHARACTERISTICS (Ta=25 °C, V_{LCD} =10V, f_V =60Hz Dclk=533.25 MHz, Is=120mA)

Parameter		Cymphol		Values		Units	Notes		
Parame	eter	Symbol	Min	Тур	Max	Units	140162		
Contrast	Ratio	CR	600	1000	-		1		
Surface Lumina	nce, white	L _{WH}	280	350	-	cd/m ²	2		
Luminance V	/ariation	δ WHITE	75	-	-	%	3		
Response Time	Gray To Gray	T _{GTG_AVR}	-	14	25	ms	4		
	RED	Rx		0.652					
		Ry		0.334					
	GREEN	Gx		0.307]]	Ì		
Color Coordinates		Gy	Тур	0.637	Тур				
[CIE1931] (By PR650)	BLUE	Bx	-0.03	0.150	+0.03				
		Ву		0.060					
	WHITE	Wx		0.313					
		Wy		0.329					
Colou Chift	Horizontal	θ_{CST_H}	-	178	-	Danua	Е		
Color Shift	Vertical	θ_{CST_V}	-	178	-	Degree	5		
Viewing Angle	(CR>10)								
Conount	Horizontal	θ_{H}	170	178	-	Dogwood	6		
General	Vertical	$\theta_{\sf V}$	170	178	-	Degree	6		
Gray So	ale	-		2.2			7		



Notes 1. Contrast Ratio(CR) is defined mathematically as: (By PR880)

Contrast Ratio = $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$

It is measured at center point(Location P1)

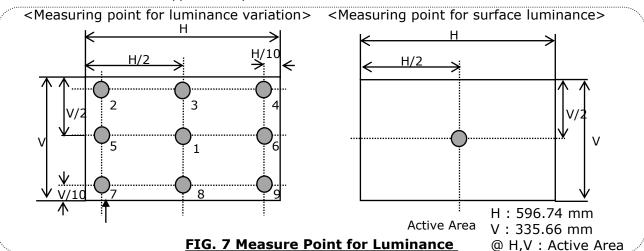
- 2. Surface luminance(Lwh)is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.7 (By PR880)
- 3. The variation in surface luminance , δ WHITE is defined as : (By PR880)

$$\delta_{\textit{WHITE}} = \frac{M \, \text{inimum}(L_{P1}, L_{P2}, \dots, L_{P9})}{M \, \text{aximum} \, (L_{P1}, L_{P2}, \dots, L_{P9})} \times 100$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations. For more information see FIG.7

- 4. Gray to gray response time is the time required for the display to transition from gray to gray. For additional information see Table 10. (By PR880)
- 5. Color shift is the angle at which the average color difference for all Macbeth is lower than 0.02. For more information see FIG.9 (By EZ Contrast)
 - Color difference (Δu'v')

- Pattern size : 25% Box size4
- Viewing angle direction of color shift: Horizontal, Vertical
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 (By PR880)
- 7. Gamma Value is approximately 2.2. For more information see Table 11.





The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

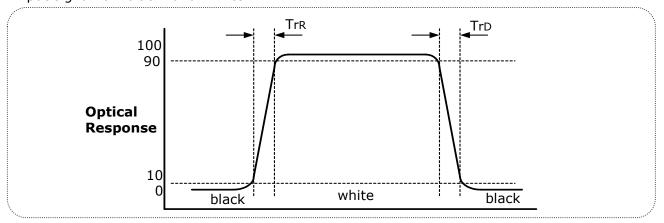


FIG.8 Response time

Color shift is defined as the following test pattern and color.

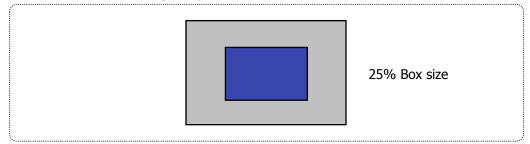


FIG.9 Color Shift Test Pattern

Average RGB values in Bruce RGB for Macbeth Chart

	Dark skin	Light skin	Blue sky	Foliage	Blue flower	Bluish green
R	395	827	343	311	519	459
G	227	571	451	411	475	799
В	183	495	647	187	743	715
	Orange	Purplish blue	Moderate red	Purple	Yellow green	Orange yellow
R	879	227	847	307	643	923
G	419	279	271	159	775	651
В	99	699	351	347	235	119
	Blue	Green	Red	Yellow	Magenta	cyan
R	107	291	791	967	831	143
G	131	595	111	851	251	507
В	583	263	151	147	607	691
	White	Neutral 8	Neutral 6.5	Neutral 5	Neutral 3.5	black
R	963	827	623	443	255	91
G	963	827	623	443	255	91
В	963	827	623	443	255	91



Dimension of viewing angle range.

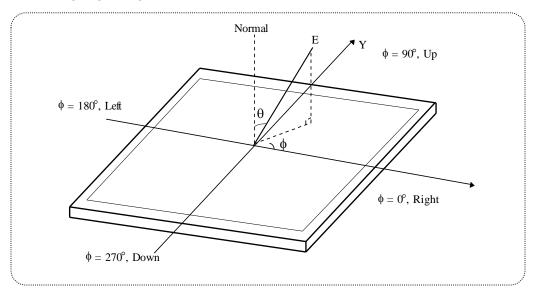


FIG.10 Viewing angle

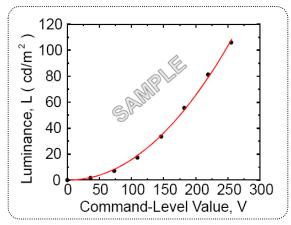


FIG.11 Sample Luminance vs. gray scale (using a 256 bit gray scale)

$$L = aV^r + L_b$$

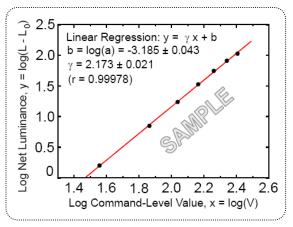


FIG.12 Sample Log-log plot of luminance vs. gray scale

$$\log(L - L_b) = r \log(V) + \log(a)$$

Here the Parameter α and γ relate the signal level V to the luminance L. The GAMMA we calculate from the log-log representation (FIG.11)



Table 10. Gray to gray response time table

Crov to Cr	Rising Time							
Gray to Gr	G1023	G767	G511	G255	G0			
	G1023							
	G767							
Falling Time	G511							
	G255							
	G0							

Table 11. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.10
63	0.30
127	1.08
191	2.50
255	4.71
319	7.70
383	11.52
447	16.18
511	21.72
575	28.15
639	35.51
703	43.81
767	53.07
831	63.30
895	74.52
959	86.75
1023	100



5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

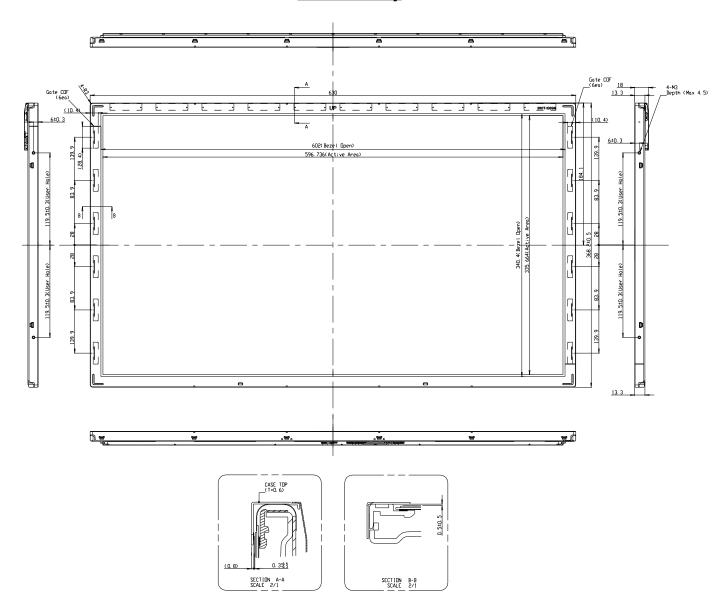
	Horizontal	630.0mm				
Outline Dimension	Vertical	368.2mm				
	Depth	18.0mm				
Bezel Area	Horizontal	602.0mm				
Dezei Alea	Vertical	340.4mm				
Activo Dicplay Area	Horizontal	596.74mm				
Active Display Area	Vertical	335.66mm				
Weight	3,215g (Typ.) / 3,375g (Max.)					
Surface Treatment	Advanced Anti-glare treatment of the front polarizer (3H)					

Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.

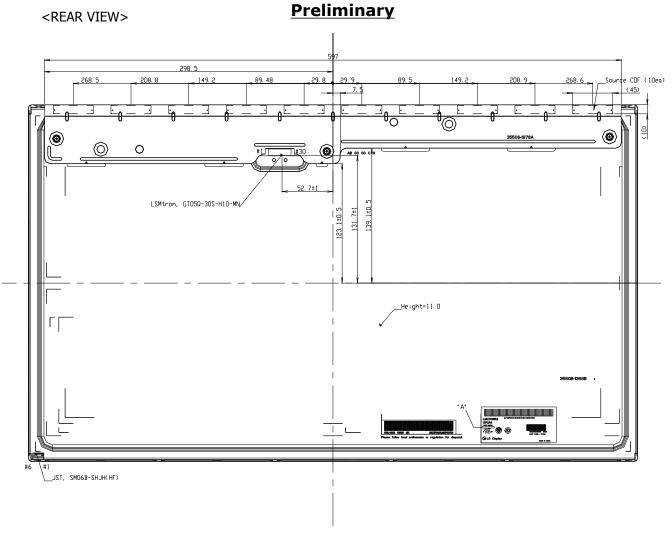


<FRONT VIEW>

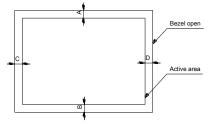
Preliminary







- Notes 1. Unspecified tolerances are to be $\pm 0.5 \, \mathrm{mm}$ 2. Tilt and partial disposition tolerance of display area are as following. .
 - (1) Y-direction : I A-B I \subseteq 1.4mm (2) X-direction : I C-D I \subseteq 1.4mm



- 3. Torque SPEC of Mounting: 3.0 ~4.0kgf.cn 4. I/F Connector Specification: LSMtron, GT050-30S-H10-MN or Equivalent. 5. LED Connector Specification: 317, SM068-SHJMKHF) or or Equivalent. 6. Ther CDF area is weak & sensive, so please don't press the CDF area. 7. Ther 'A' pharase as following.

Production Line	Description						
eMPS line	None						
LGDGZ Line	Factory ID: LGDGZ						



6. Reliability

Environment test condition

No	Test Item	Condition	Notes
1	High temperature storage test	Ta= 60°C 240h	1
2	Low temperature storage test	Ta= -20°C 240h	1
3	High temperature operation test	Ta= 50°C 50%RH 240h	1
4	Low temperature operation test	Ta= 0°C 240h	1
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.00G RMS Bandwidth: 10-300Hz Duration: X, Y, Z, 10 min One time each direction	
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : \pm X, \pm Y, \pm Z One time each direction	
7	Humidity condition Operation	Ta= 40 °C ,90%RH	
8	Altitude operating storage / shipment	0 - 16,400 feet(5,000m) 0 - 40,000 feet(12,192m)	
9	Maximum Storage Humidity for 4 corner light leakage Mura.	Max 70%RH , Ta=40°C	

Note 1. Result Evaluation Criteria:

TFT-LCD panels test should take place after cooling enough at room temperature. In the standard condition, there should be no particular problems that may affect the display function.

%. T_a= Ambient Temperature



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
 Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC). Information Technology Equipment - Safety - Part 1 : General Requirements

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	Е	F	G	Н	I	J	K	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	E	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

1) Package quantity in one box: 8ea

b) Box Size: 355 x 700 x 430



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200$ mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In Higher temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) When LCMs are used for public display defects such as Yogore, image sticking can not be guarantee.
- (11) LCMs cannot support "Interlaced Scan Method"
- (12) Please conduct image sticking test after 2-hour aging with Rolling PTN and normal temperature(25~40℃)



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.