Toshiba Matsushita Display Technology Co., Ltd

39cm COLOUR TFT-LCD MODULE
(15.4 WIDE TYPE)
LTD154EX0S (Z)
(p-Si TFT)

PRODUCT INFORMATION

FEATURES

(1) 15.4WIDE-XGA(1280x800 pixels) display size for notebook PC
(2) LVDS interface system (H-Sync, V-Sync)

TENTATIVE

MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (typ.)	344.5max(W) x 222.5max (H) x 6.5max(D) mm
Number of Pixels	1280(W) x 800(H) pixels
Active Area	332.2 (W) x 207.6(H) mm
Pixel Pitch	0.2595(W) x 0.2595(H)
Weight (approximately)	585 g(Max)
Backlight	Single CCFL, Sidelight type

ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Checked Terminal
Supply Voltage	V_{DD}	-0.3	+4.0	V	V_{DD} – GND
Input Voltage of Signals	V_{IN}	-0.3	V_{DD}+0.3	V	LVDS interface
Operating Ambient Temperature	T_{OP}	0	50	°C	
Operating Ambient Humidity	H_{OP}	10	90	%(RH)	
Storage Temperature	T_{STG}	-20	+60	°C	
Storage Humidity	H_{STG}	10	90	%(RH)	
Operating Temperature for Panel	-	0	+60	°C	

ELECTRICAL SPECIFICATION(T.B.D)

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Supply Voltage [1]	V_{DD}	3.0	3.3	3.6	V	
	V_{FL}	---	(920)	---	V(rms)	I_{FL}=6.0mA(rms)
FL Start Voltage (Ta=0°C)	V_{SFL}	TBD	---	TBD	V(rms)	
Differential Input Voltage[2]	V_{ID}	100	-	600	mV	
Common Mode Input Voltage [2]	V_{CM}	1.0	-	2.4 –(V_{ID})/2	V	
Current Consumption	I_{DD}	---	(335)	---	mA	
	I_{FL}	---	6.0	6.0	mA(rms)	
Power Consumption		---	(6.6)	---	W	I_{FL}=6.0 mA(rms)

*1) The module should be always operated within these ranges. The "Typ." shows the recommendable value.

*2) Recommended LVDS transmitter: DS90CF365
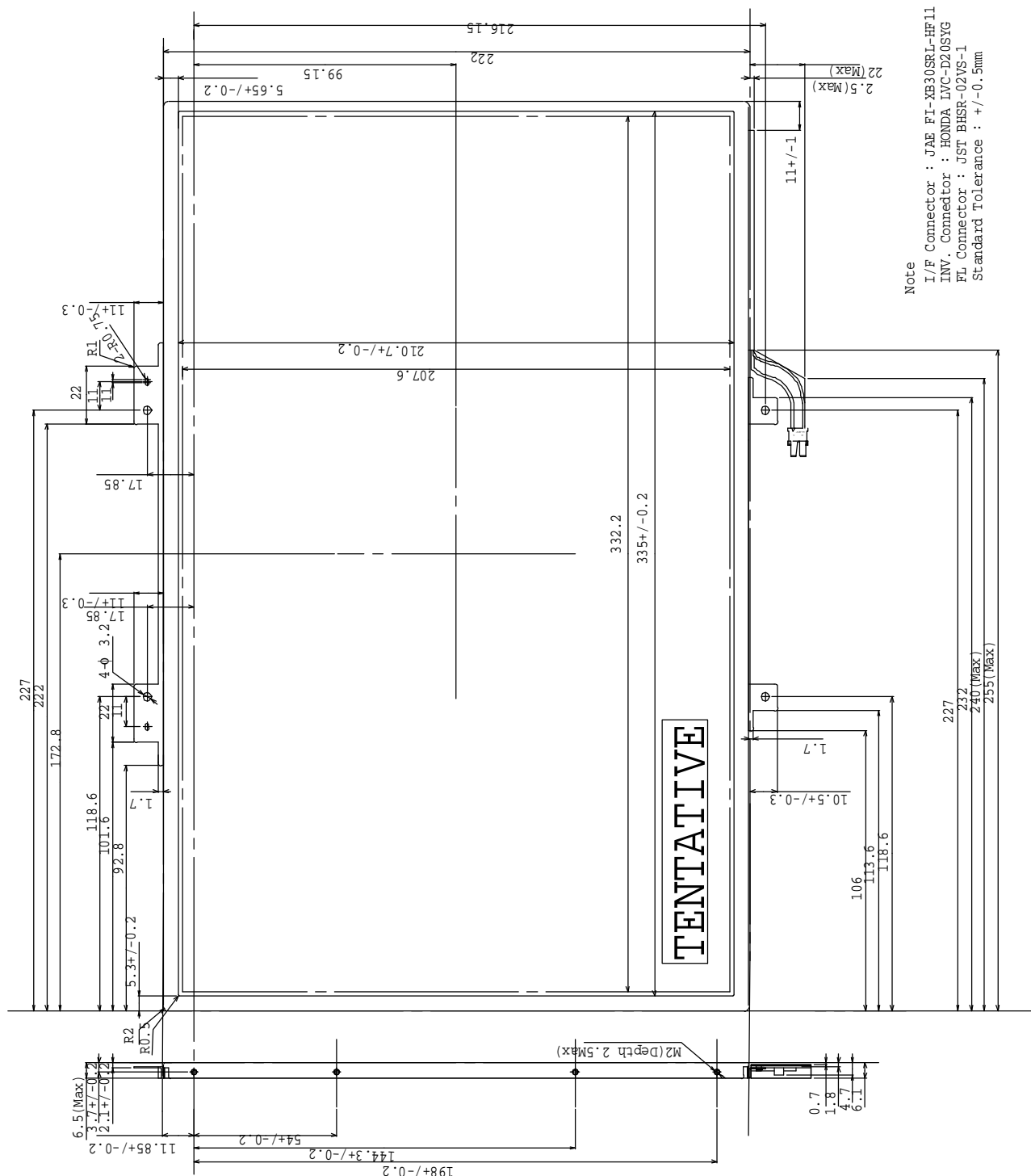
OPTICAL SPECIFICATION (Ta=25°C)(T.B.D)

Item		Min.	Typ.	Max.	Unit	Remarks
Contrast Ratio (CR)		(150)	(400)	---	---	
Response Time	(t_{ON}+ t_{OFF})	---	---	40	ms	@25deg Brack <=> White
Luminance (L)		(150)	(190)	---	cd/m^2	I_{FL}=6.0mA(rms)

DIMENSIONAL OUTLINE

(Front side)

TENTATIVE

Unit : mm

Standard tolerance : ±0.5

Note
I/F Connector : JAE FI-XB30SRL-HF11
INV. Connector : HONDA LVC-D20SYG
FL Connector : JST BHSR-02VS-1
Standard Tolerance : +/-0.5mm

216.15

222

99.15

5.65+/-0.2

22(Max)

2.5(Max)

11+/-1

11+/-0.3

R1.0 R0.75

22

11 11

17.85

210.7+/-0.2

207.6

332.2

335+/-0.2

11+/-0.3

17.85

3.2

4-φ

22 11

172.8

227 222

118.6

101.6

92.8

1.7

5.3+/-0.2

R2

R0.5

227

232

240(Max)

255(Max)

1.7

10.5+/-0.3

106

113.6

118.6

TENTATIVE

M2(Depth 2.5max)

6.5(Max)

3.7+/-0.2

2.1+/-0.2

11.85+/-0.2

54+/-0.2

144.3+/-0.2

198+/-0.2

0.7

1.8

4.7

6.1

DIMENSIONAL OUTLINE

(Back side)

TENTATIVE

Unit : mm

Standard tolerance : ±0.5

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BLOCK DIAGRAM

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                                    ┌─────────────────────────────────┐
                                    │         Source driver           │
                                    └─────────────────────────────────┘
          ┌──────────┐              ┌─────────────────────────────────┐
          │  DC/DC   │              │ G                             G │
          │ converter│──────────────│ a   Liquid Crystal Panel     a │
          └──────────┘              │ t                             t │
               │                    │ e    1280 x 800 pixels        e │
          ┌──────────┐              │                                 │
          │Gray scale│              │ d                             d │
          │Manipula- │              │ r                             r │
     CN1  │tion      │              │ i                             i │
          │Voltage   │              │ v                             v │
          │Generation│              │ e                             e │
          │Circuit   │              │ r                             r │
          └──────────┘              └─────────────────────────────────┘
          ┌──────────┐              ┌─────────────────────────────┐
          │ Panel    │              │        Backlight            │─── CN1
          │Controller│              └─────────────────────────────┘
          │ LVDS     │
          └──────────┘
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1280 pixels

1, 1	2, 1		$X2_{n-1}$, 1	$X2_n$, 1		1280, 1
1, 2						
1, Y			X_{2n-1}, Y	X_{2n}, Y		
1, 800						1280,800

800 pixels

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TIMING CHART(T.B.D)
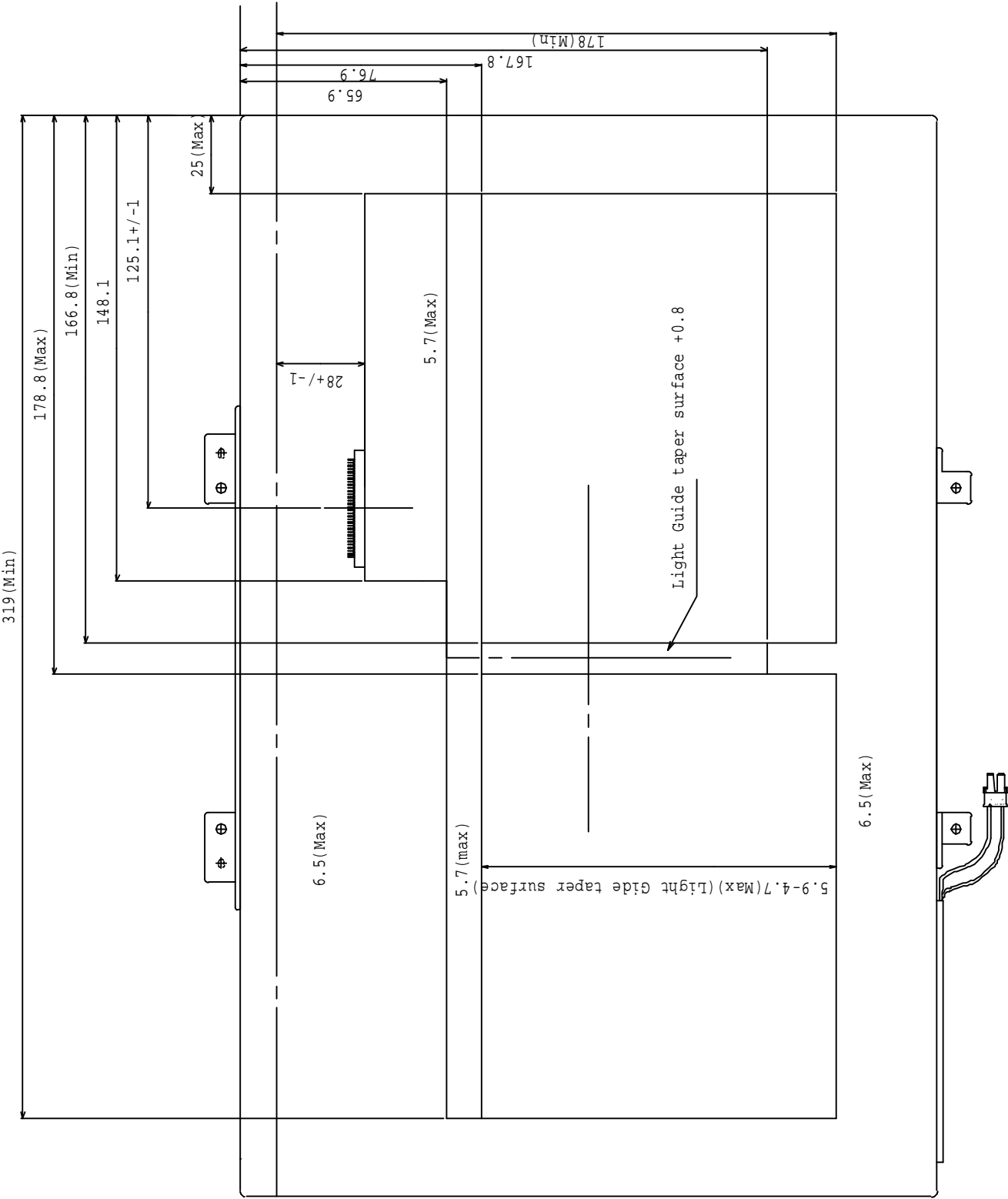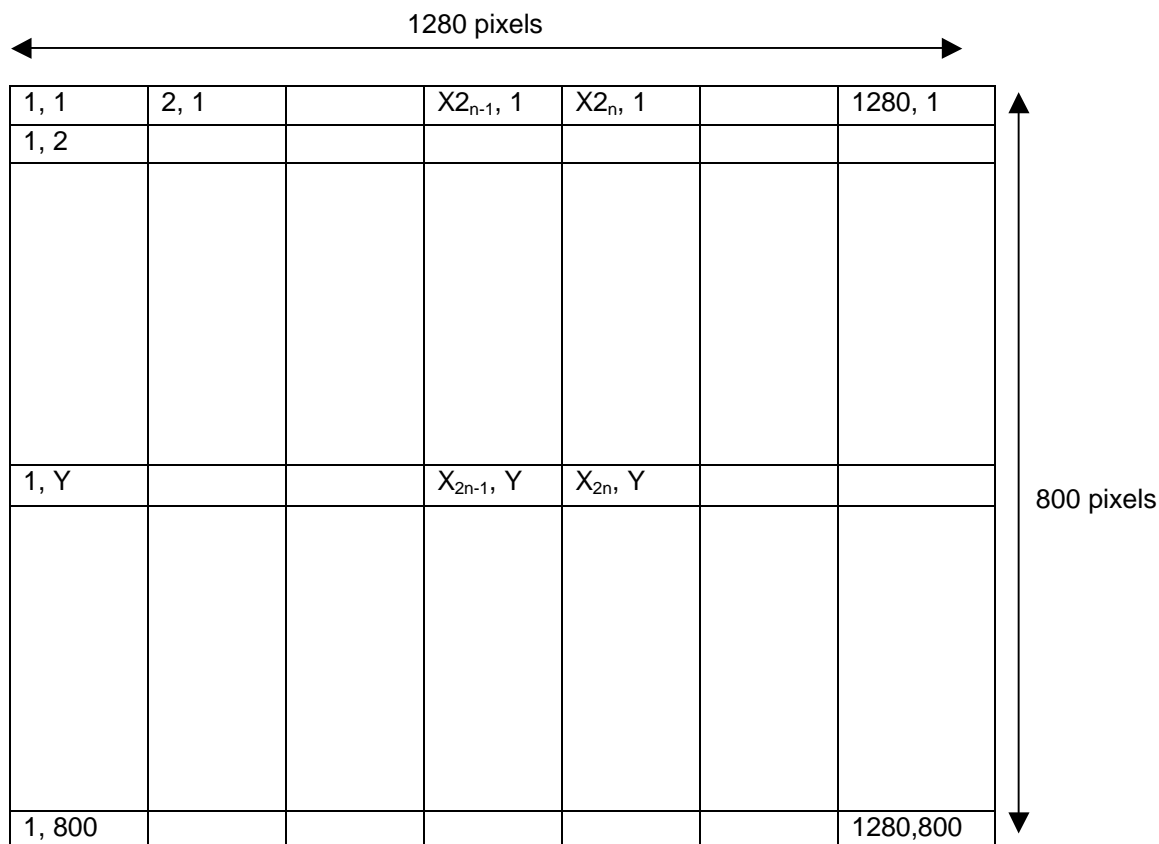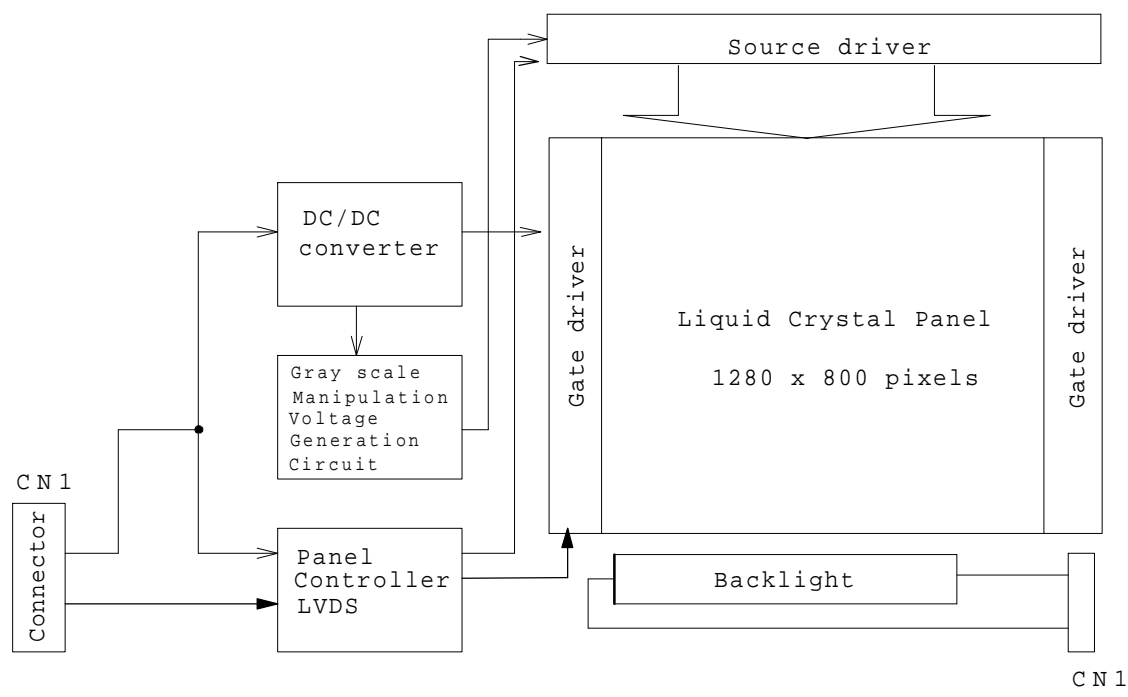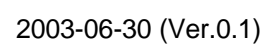
TIMING SPECIFICATION [1) 2) 3) 4) 5) 6)]

(T.B.D)

Item	Symbol	min.	typ.	max.	unit
Horizontal Active pixel	-	-	1280	-	Pixels
H Sync Front Porch	HSO	-	4	-	Pixels
H Sync Pulse Width	HSPW	-	4	-	Pixels
H Sync Back Porch	HBO	-	112	-	Pixels
Horizontal Total Pixels	-	-	1400	-	Pixels
Horizontal Blank Pixel	-	-	120	-	Pixels
Horizontal Total Time	-	-	20	-	Us
Horizontal Blank Time	-	-	1.7	-	Us
Vertical Active Line	-	-	800	-	Lines
V Sync Front Porch	VSO	-	1	-	Lines
V Sync Pulse Width	VSPW	-	2	-	Lines
V Sync Back Porch	VBO	-	30	-	Lines
Vertical Total Line	-	-	833	-	Lines
Vertical Blank Line	-	-	33	-	Lines
Vertical Total Time	-	-	16.7	-	Ms
Vertical Blank Time	-	-	0.7	-	Ms
Pixel Clock	-	-	70	-	MHz
Frame Rate	-	-	60	-	Hz

Note 1) Refer to "Timing Chart" and LVDS specifications by chip vendor.

Note 2) If NCLK is fixed to "H" or "L" level for certain period while V_{DD} is supplied, the panel may be damaged.

Note 3) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.
There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications.

Note 4) Do not make tv, tvhd and tvds fluctuate.
If tv, tvhd, and tvds are fluctuate, the panel displays black.

Note 5) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note 6) NCLK count of each Horizontal Scanning Time should be always the same.
V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer)
Frame period should be always the same.

CONNECTOR PIN ASSIGNMENT FOR INTERFACE

CN1 INPUT SIGNAL

Connector : FI-XB30SR-HF11(Locking Type) / JAPAN AVIATION ELECTRONICS INDUSTRY,LTD.

Mating Connector :

Wire Type:FI-X30H (Housing), FI-XC3-A-15000 (Contact)

FPC Type:FI-X30M or FI-X30M R, Coax Type:FI-X30C or FI-X30C2(Housing), FI-X30CH-7000(Shell)

Terminal No.	Symbol	Function
1	GND	
2	VDD	Power Supply : +3.3V
3	VDD	Power Supply : +3.3V
4	NC	Non-Connection
5	NC	Non-Connection
6	NC	Non-Connection
7	NC	Non-Connection
8	RxOIN0-	Odd Negative LVDS differential data input (R0-R5,G0)
9	RxOIN0+	Odd Positive LVDS differential data input (R0-R5,G0)
10	GND	
11	RxOIN1-	Odd Negative LVDS differential data input (G1-G5, B0-B1)
12	RxOIN1+	Odd Positive LVDS differential data input (G1-G5, B0-B1)
13	GND	
14	RxOIN2-	Odd Negative LVDS differential data input (B2-B5, HS, VS, DE)
15	RxOIN2+	Odd Positive LVDS differential data input (B2-B5, HS, VS, DE)
16	GND	
17	OCLK-	Odd Clock Signal(-)
18	OCLK+	Odd Clock Signal(+)
19	GND	
20	NC	Non-Connection
21	NC	Non-Connection
22	NC	Non-Connection
23	NC	Non-Connection
24	NC	Non-Connection
25	NC	Non-Connection
26	NC	Non-Connection
27	NC	Non-Connection
28	NC	Non-Connection
29	NC	Non-Connection
30	NC	Non-Connection

Note 1) Please connect GND pin to ground. Don't use it as no-connect nor connection with high impedance.

CN2 CCFL POWER SOURCE

Connector : BHSR-02VS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Mating Connector : SM02B-BHS-1 / JAPAN SOLDERLESS TERMINAL MFG CO., LTD.

Terminal No.	Symbol	Function
1	VFLH	CCFL Power Supply (high voltage)
2	VFLL	CCFL Power Supply (low voltage)

RECOMMENDED TRANSMITTER (DS90CF365)
TO LTD154LX0S INTERFACE ASSIGNMENT

Case1: 6bit Transmitter

(DS90CF365)				Output Signal Symbol	LTM154LX0S (Z) Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)				
Symbol	Terminal	Symbol	Function		Terminal	Symbol
TA0	44	R0	Red Pixels Display Data (LSB)	TA- TA+	No.5 No.6	RxIN0- RxIN0+
TA1	45	R1	Red Pixels Display Data			
TA2	47	R2	Red Pixels Display Data			
TA3	48	R3	Red Pixels Display Data			
TA4	1	R4	Red Pixels Display Data			
TA5	3	R5	Red Pixels Display Data (MSB)			
TA6	4	G0	Green Pixels Display Data (LSB)	TB- TB+	No.8 No.9	RxIN1- RxIN1+
TB0	6	G1	Green Pixels Display Data			
TB1	7	G2	Green Pixels Display Data			
TB2	9	G3	Green Pixels Display Data			
TB3	10	G4	Green Pixels Display Data			
TB4	12	G5	Green Pixels Display Data (MSB)			
TB5	13	B0	Blue Pixels Display Data (LSB)			
TB6	15	B1	Blue Pixels Display Data			
TC0	16	B2	Blue Pixels Display Data	TC- TC+	No.11 No.12	RxIN2- RxIN2+
TC1	18	B3	Blue Pixels Display Data			
TC2	19	B4	Blue Pixels Display Data			
TC3	20	B5	Blue Pixels Display Data (MSB)			
TC4	22	HSYNC	Horizontal Synchronization Signal			
TC5	23	VSYNC	Vertical Synchronization Signal			
TC6	25	DE	Compound Synchronization Signal			
CLK IN	26	CLK	Data Sampling Clock	TCLK- TCLK+	No.14 No.15	CLK- CLK+

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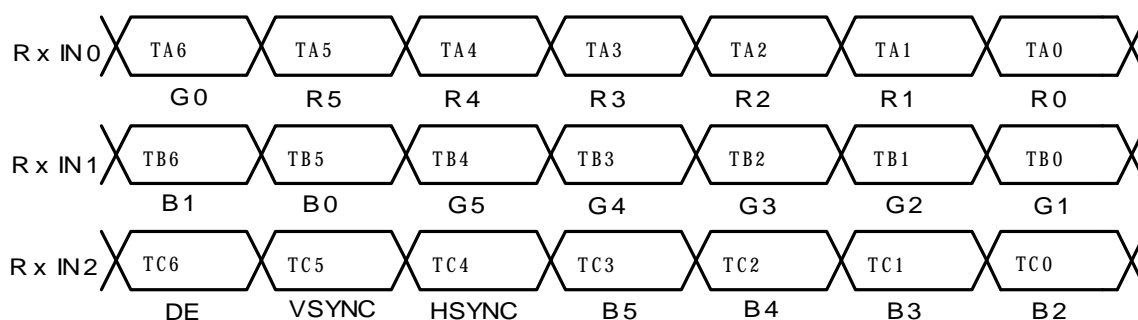
256k (k=1024) COLORS COMBINATION TABLE

	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0	B5 B4 B3 B2 B1 B0	Gray Scale Level
Basic Color	Black	L L L L L L	L L L L L L	L L L L L L	-
	Blue	L L L L L L	L L L L L L	H H H H H H	-
	Green	L L L L L L	H H H H H H	L L L L L L	-
	Light Blue	L L L L L L	H H H H H H	H H H H H H	-
	Red	H H H H H H	L L L L L L	L L L L L L	-
	Purple	H H H H H H	L L L L L L	H H H H H H	-
	Yellow	H H H H H H	H H H H H H	L L L L L L	-
	White	H H H H H H	H H H H H H	H H H H H H	-
Gray Scale of Red	Black	L L L L L L	L L L L L L	L L L L L L	L 0
	Dark	L L L L L H	L L L L L L	L L L L L L	L 1
		L L L L H L	L L L L L L	L L L L L L	L 2
	↑ ↓	: :	: :	: :	L3… L60
	Light	H H H H L H	L L L L L L	L L L L L L	L61
		H H H H H L	L L L L L L	L L L L L L	L62
	Red	H H H H H H	L L L L L L	L L L L L L	Red L63
Gray Scale of Green	Black	L L L L L L	L L L L L L	L L L L L L	L 0
	Dark	L L L L L L	L L L L L H	L L L L L L	L 1
		L L L L L L	L L L L H L	L L L L L L	L 2
	↑ ↓	: :	: :	: :	L3… L60
	Light	L L L L L L	H H H H L H	L L L L L L	L61
		L L L L L L	H H H H H L	L L L L L L	L62
	Green	L L L L L L	H H H H H H	L L L L L L	Green L63
Gray Scale of Blue	Black	L L L L L L	L L L L L L	L L L L L L	L 0
	Dark	L L L L L L	L L L L L L	L L L L L H	L 1
		L L L L L L	L L L L L L	L L L L H L	L 2
	↑ ↓	: :	: :	: :	L3… L60
	Light	L L L L L L	L L L L L L	H H H H L H	L61
		L L L L L L	L L L L L L	H H H H H L	L62
	Blue	L L L L L L	L L L L L L	H H H H H H	Blue L63
Gray Scale of White & Black	Black	L L L L L L	L L L L L L	L L L L L L	L 0
	Dark	L L L L L H	L L L L L H	L L L L L H	L 1
		L L L L H L	L L L L H L	L L L L H L	L 2
	↑ ↓	: :	: :	: :	L3… L60
	Light	H H H H L H	H H H H L H	H H H H L H	L61
		H H H H H L	H H H H H L	H H H H H L	L62
	White	H H H H H H	H H H H H H	H H H H H H	White L63

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DISPLAY QUALITY SPECIFICATION

Item	Description / Specifications	Class
Function	No display, Malfunction	Major
Display Quality [1)2)3)]	Missing line	Major
	Missing Sub-Pixels **1) Bright defects : 15pcs. maximum** **2) Dark defects : 15pcs. maximum** **3) Total sub-pixel defects : 20pcs. maximum**	Minor
	Various uniformity (mura) : neglect	-
	Inconspicuous flicker, crosstalk, Newton's ring and other defects : neglect	-
Black and White Spots/line	Inconspicuous defects : neglect	-
Backlight	Missing (Non-operating)	Major

Note 1) Defects of both color filter and black matrix are counted as bright or dark defects.
  Inspection area should be within the active area.
Note 2) Bright defect means a bright spot(sub-pixel) on the display pattern of gray scale L0.
  Dark defect means a dark spot(sub-pixel) on the display pattern of gray scale L63.
Note 3) Bright spot which can not be found by using 5%ND-Filter shall not be counted as a defect.

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⚠️ FOR SAFETY

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.
In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA LCD MODULES".
Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

1) SPECIAL PURPOSES

A) Toshiba Matsushita Display Technology's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.

B) Since Toshiba Matsushita Display Technology's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display Technology's published specification limits.

C) In addition, since Toshiba Matsushita Display Technology's Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.
Toshiba Matsushita Display Technology doses not warrant the module, if customer disassembled or modified it.

3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.
If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.
If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.
DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.
Employ protection circuit for power supply, whenever the specification or TD specifies it.
Suitable protection circuit should be applied for each system design.

7) DISPOSAL
When dispose LCD module, obey to the applicable environmental regulations.