

## **CUSTOMER APPROVAL SHEET**

Company Name	
MODEL	C070VW04 V6
CUSTOMER	Title:
APPROVED	Name:
	TIONS ONLY (Spec. Ver. 0.7) TIONS AND ES SAMPLE (Spec. Ver.0.7) TIONS AND CS SAMPLE (Spec. Ver.0.7)

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Date	2011/09/13

# Product Specification 7" COLOR TFT-LCD MODULE

Model Name: C070VW04 V6

**Planned Lifetime:** From 2010/Jun To 2016/Jun

Phase-out Control: TBD EOL Schedule: TBD

< ◆ >Preliminary Specification

< >Final Specification

Note: The content of this specification is subject to change.

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#### **Record of Revision**

Version	Revise Date	Page	Content
0.0	2010/07/02		First draft.
0.1	2010/11/05	6	Modify Mechanical Drawing
0.2	2011/ 3/23	6	Modify Mechanical Drawing
0.3	2011/6/15	6	Add Weight
		7	Modify Mechanical Drawing
		8	Add Electrical Characteristics
		16	Modify Reliability Test Items
0.4	2011/6/24	7	Modify Mechanical Drawing
0.5	2011/7/5	6	Add Surface Treatment
0.6	2011/7/28	18	Modify Module/Panel Label Information
0.7	2011/9/13	7	Modify Mechanical Drawing
			<b>Y</b>



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	Connector = HRS FH28D-50S-0.5SH(05)		
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#### A. General Description

C070VW04 V6 is an a-Si &Transmissive type Thin Film Transistor Liquid crystal Display (TFT-LCD) with AMVA-Mobile (Advanced MVA) technology. This model is composed of a TFT-LCD, a driver, an FPC (flexible printed circuit), and a backlight unit. TCON (timing controller) is also embedded in source driver.

#### **B.** Features

- 7-inch (15:9) display
- 800RGB x 480 resolution in RGB stripe dot arrangement
- High brightness
- Interfaces: parallel RGB 18-bit
- Advanced MVA -- wide view technology
- RoHs compliance

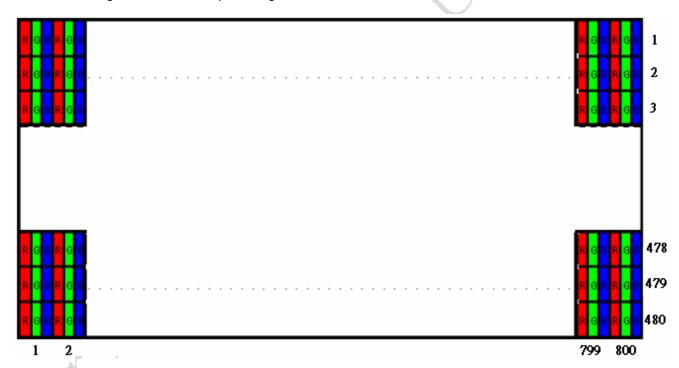


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### C. Physical Specifications

NO.	Item	Unit	Specification	Remark
1	Display Resolution	dot	800 RGB (H)×480(V)	
2	Active Area	mm	152.4(H)×91.44(V)	
3	Screen Size	inch	7(Diagonal)	
4	Dot Pitch	mm	0.0635(H)×RGBx0.1905(V)	
5	Color Configuration		R. G. B. Stripe	Note 1
6	Color Depth		262K Colors	
7	Overall Dimension	mm	164.55(H) × 105.01(V) × 6.68(T)	Note 2
8	Weight	g	159.2+/-10%	1
9	Display Mode		Normally Black	
10	Surface Treatment		AGAR	

Note 1: Below figure shows dot stripe arrangement.



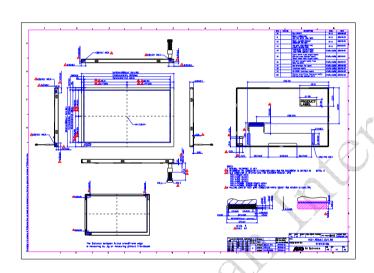
Note 2: Not including BOSS &FPC. Please refer to the drawing in page 6 for further information.



D. Outline Dimension

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### E. Electrical Specifications

1. Pin Assignment
Connector= HRS FH28D-50S-0.5SH(05)

	Din Nama	1/0	Description	- m ulc-		
No.	Pin Name	I/O		emarks		
	VCOM	Р	Common electrode driving voltage			
	VGH	Р	Positive power supply voltage for TFT			
	VGL	Р	Negative power supply voltage for TFT			
	VDD	Р	Digital power supply voltage.			
5	GND	G	Ground			
6	V1	I	Gamma correction voltage			
7	V2	I	Gamma correction voltage			
8	V3	I	Gamma correction voltage			
9	V4	I	Gamma correction voltage			
10	V5	I	Gamma correction voltage			
11	V6	I	Gamma correction voltage			
	V7	I	Gamma correction voltage			
	V8	I	Gamma correction voltage			
	V9	i	Gamma correction voltage			
	V10	i	Gamma correction voltage			
	V11	i	Gamma correction voltage			
	V12	i	Gamma correction voltage			
	V13	i	Gamma correction voltage			
	V14	i	Gamma correction voltage			
	AVDD	P	Analog power supply voltage			
	AVDD	P	Analog power supply voltage  Analog power supply voltage			
	GND	G	Ground			
	DE	ı G	Data enable Input (High active)			
	DCLK	1				
		- 1	Data clock Input			
	GND	G	Ground			
	DB5	!	Blue data input(MSB)			
	DB4	!	Blue data input			
	DB3	<u> </u>	Blue data input			
	DB2	!	Blue data input			
	DB1	<u> </u>	Blue data input			
	DB0	<u> </u>	Blue data input(LSB)			
	DG5	!	Green data input (MSB)			
	DG4	l	Green data Input			
	DG3	l	Green data Input			
	DG2	l	Green data Input			
	DG1	I	Green data Input			
	DG0	l	Green data Input (LSB)			
	DR5	I	Red data input (MSB)			
	DR4	I	Red data input			
	DR3	- 1	Red data Input			
	DR2	<u> </u>	Red data Input			
	DR1	I	Red data Input			
43	DR0	I	Red data Input (LSB)			
44	GND	G	Ground			
45	SHLR	ı	Horizontal scan direction control. "H"→Left to Right;			
16		1	"L"→Right to Left  Vertical scan direction control. "H"→Down to Up;			
46	UPDN	I	"L"→Up to Down			
47	STBYB	I	Standby mode. "H" for normal operation. "L" for standby mode.			
48	RSTB	1	Global reset pin. (low active)			





49	GND	G	Ground	
50	V-COM	Р	Common electrode driving voltage	

I: Digital signal input, O: Digital signal output, G: GND, P: Power input

#### 2. Absolute Maximum Ratings

Items	Symbol	Va	lues	Unit	Condition	
items	Symbol	Min.	Max.	Oilit	Condition	
	VDD	-0.3	4.5	V		
	AVDD	-0.5	13.5	V		
Power Voltage	VGH	-0.3	20	V		
	VGL	-15	0.3	V		
	VGH-VGL	0	35	V		
	Vi	-0.3	Vdd+ 0.3	V		
Input Signal Voltage	Vref(V1~V7)	0.4AVDD	AVDD-0.3	V	,	
input Signal Voltage	Vref(V8~V14)	0.1	0.6AVDD	V		
	VCOM	-0.3	10	V		
Operation Temperature	Topa	-30	+85	. ℃	Ambient	
Storage Temperature	Tstg	-40	+95	$^{\circ}\mathbb{C}$	Ambient	
LED	Vf	-0.5	24	V		
LED	If	0	85	mA		

Note 1: Functional operation should be restricted under normal ambient temperature.

#### 3. Electrical Characteristics

The following items are measured under stable condition and suggested application circuit.

#### a. TFT- LCD Panel

Parameter	Symbol	Min	Тур	Max	Unit	Notes
A	VDD	3.1	3.3	3.5	V	
	IVDD		6.7	8.7	mA	
	AVDD	V1+0.1	12.5	12.8	<b>V</b>	
	IAVDD	-	26	34	mΑ	
Power Supply	VGH	14.5	15	15.5	<b>V</b>	
1 ower Supply	IVGH	-	0.15	0.2	mΑ	
	VGL	-9.5	-9	-8.5	V	
	IVGL	-	0.2	0.3	mΑ	
	VCOM	5.22	5.42	5.62	V	
	IVCOM	-	0.3	0.65	uA	
	Vi	-0.3	ı	Vdd+ 0.3	>	
Input Signal Voltage	Vref(V1~V7)	0.4AVdd	ı	AVdd-0.3	<b>V</b>	
	Vref(V8~V14)	0.1	1	0.6AVdd	V	
Input high voltage	Vh	0.7Vdd	•	Vdd	<b>V</b>	
Input low voltage	VI	0	-	0.3Vdd		
Vertical cycle	f <sub>V</sub>	50	60	80	Hz	
Horizontal cycle	f <sub>H</sub>	28.9	31.5	42	kHz	
Dot Frequency	$f_{DCLK}$		33.3	45	MHz	

#### b. Recommend Gamma Voltage

to committee com							
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
Gamma Voltage	V1	-	12.23	-	V		
	V2	-	11.70	-	V		
	V3	_	10 46	_	V		



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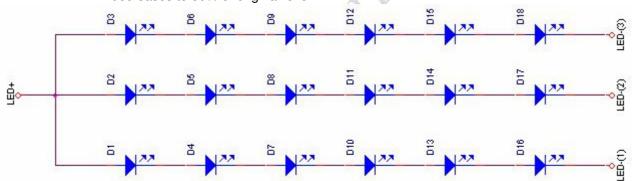
V4	-	9.84	-	V	
V5	-	9.41	-	V	
V6	-	8.65	-	V	
V7	-	6.80	-	V	
V8	-	6.40	-	V	
V9	-	4.45	-	V	
V10	-	3.68	-	V	
V11	-	3.16	-	V	
V12	-	2.49	-	V	. 1
V13	-	1.07	-	VA	
V14	-	0.27	-	V	

c. Backlight Driving Conditions (Note 1)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
LED Supply Current	Ι <u>ι</u>	-	80	85	mA	single serial (Note 3)
LED Supply Voltage	$V_{L}$	1	21	24	>	Note 3,25° C
LED Life Time	L <sub>L</sub>	10000			Hr	Note 2

Note 1: LED backlight is 18 LEDs (3 strings, 6pcs for each string).

Note 2: The LED lifetime 10000hrs means , after normal use at 80mA, under +25 °C, the brightness decreases to 50% of original level.



Note 3: The LED supply power is for 3 string of LED

Note 4: The voltage capacity of LED driver IC must be over max. of LED Voltage.





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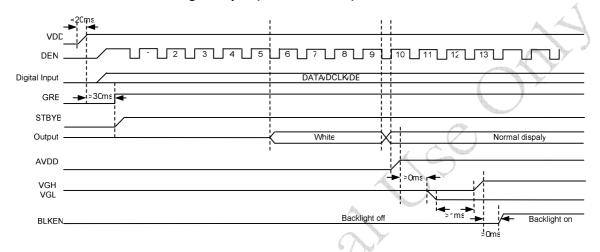
#### 4. AC Timing

#### a. Power on/off sequence

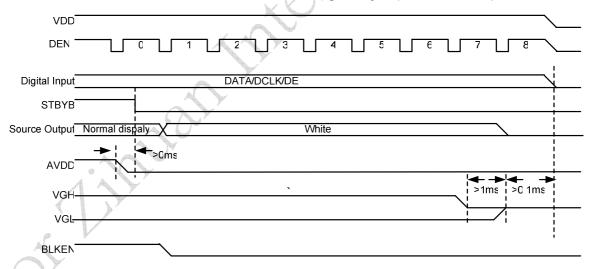
The LCD adopts high voltage driver IC, so it could be permanently damaged under a wrong power on/off sequence. The suggested LCD power sequence is below:

#### Power on sequence:

VDD -> GRB -> STBYB -> Digital input (Data/DCLK/DE) -> AVDD -> VGL -> VGH -> BLKEN



Power-Off STBYB -> BLKEN -> AVDD -> VGH -> VGL -> Digital input (Data/DCLK/DE)& VDD



**DEN**: Defined a frame period and created internally by DE. It is similar vertical sync.

**GRB**: Global reset ,normally pulled high.

Suggest to connecting with an RC reset circuit for stability .Normally pull high.

STBYB: Standby mode, normally pulled high.

STBYB ="1", normal operation

STBYB="0", timing controller, source driver will turn off, all output are High-Z



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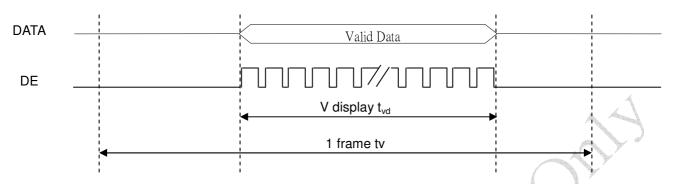
#### b. Timing Condition

Item	Symbol	Min	Тур	Max	Unit	Remark
Clock frequency	dclk	-	33.3	45	MHZ	
DCLK cycle time	Tdclk	22	30	-	ns	
DCLK pulse duty	Tcwh	40	50	60	%	
Vertical display area	Tvd	480	480	480	Н	
Vertical period area	Tv	522	525	762	Н	1
Vertical blanking area	Tvb	42	45	282	Н	
Horizontal display area	Thd	800	800	800	dclk	
Horizontal period area	Th	910	1056	1138	dclk	
Horizontal blanking area	Thb	110	256	338	dclk	
Data/DE setup time	Tds	8	-	-	ns	
Data/DE hold time	Tdh	8	-	-	ns	

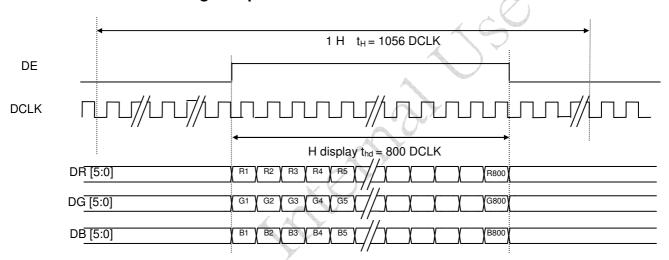


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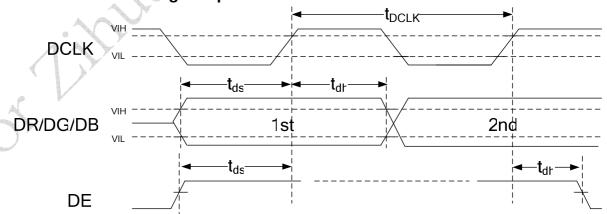
## c. Timing Diagram Vertical Timing of Input



#### **Horizontal Timing of Input**



## **Clock and Data Timing of Input**



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F. Optical specifications

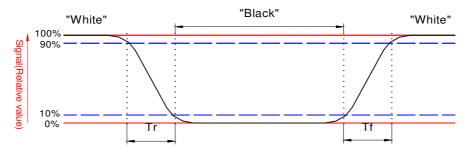
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Response Time Rise Fall	Tr Tf	θ =0°		15 20	50 60	ms ms	Note 3
Contrast ratio	CR	θ =0°	400	600	-		Note 4, 5, 6
Viewing Angle Top Bottom Left Right		CR≧10	70 70 70 70	80 80 80 80	- - - -	deg.	Note 7, 8
Brightness	Y <sub>L</sub>	θ =0°	400	450	40	cd/m <sup>2</sup>	Note 1,2,9
White Chromaticity	Х	θ =0°	0.288	0.318	0.348		
Willie Officinations	Υ	θ =0°	0.288	0.318	0.348		
Red Chromaticity	Х	θ =0°	-	0.600	-		
Red Chromaticity	Y	θ =0°	- p4	0.335	-		Note 8
Green Chromaticity	Х	θ =0°	0	0.312	-		11010
Green Chromaticity	Υ	θ =0°		0.577	-		
Blue Chromaticity	Х	θ =0°	<b>–</b>	0.149	-		
	Υ	θ =0°	-	0.107	-		
Uniformity		9-point, θ=0°	80	_	-	%	Note 10

Note 1: Measurement should be performed in the dark room, optical ambient temperature =25 $^{\circ}$ C, and backlight current  $I_{L}$ =80 mA

Note 2: To be measured on the center area of panel with a field angle of 1 °by Topcon luminance meter BM-7, after 10 minutes operation.

#### Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively.



Note 4. From liquid crystal characteristics, response time will become slower and the color of panel will become darker when ambient temperature is below 25°C.

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 $Contrastratio = \frac{Photo\ detector\ output\ when\ LCD\ is\ at\ "\ White"\ state}{Photo\ detector\ output\ when\ LCD\ is\ at\ "\ Black"\ state}$ 

Note 5. Contrast ratio is calculated with the following formula.

Note 6. White  $Vi=Vi50 \pm 1.5V$ 

Black Vi=Vi50 ± 2.0V

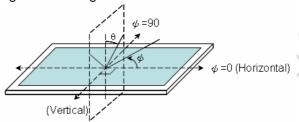
"±" means that the analog input signal swings in phase with COM signal.

"7" means that the analog input signal swings out of phase with COM signal.

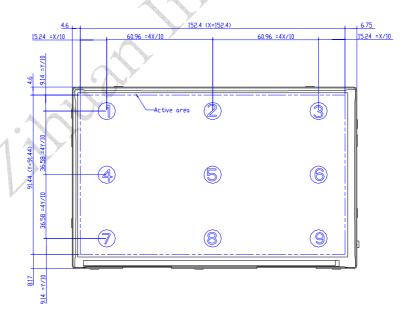
Vi50 :The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

Note 7. Definition of viewing angle: refer to figure as below.



- Note 8. The viewing angles are measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.
- Note 9. Brightness is measured at the center of the display with white pattern in 80mA
- Note 10. Luminance Uniformity is defined as following within the 9 measurements (L1~L9), Luminance Uniformity(%) =Minimum luminance(brightness)/Maximum luminance(brightness)





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#### G. Reliability Test Items

No.	Test items	Conditio	Remark		
1	High temperature storage	Ta= 95℃	240Hrs		
2	Low temperature storage	Ta= -40°C	240Hrs	Note1	
3	High temperature operation	Ta= 85℃ 240Hrs			
4	Low temperature operation	Ta= -30°C	240Hrs	Note1, 3	
5	High temperature and high humidity	Ta= 60°C, 90% RH	240Hrs	Operation	
6	Heat shock	-30°C ~85°C /100 cycl∈	Non-operation		
7	Electrostatic discharge	Contact = $\pm$ 8 kV, class B (R Air = $\pm$ 15 kV, class B (R=,10 times for each	Operation (Note 4)		
		Frequency range	8~33.3Hz		
8 Vibration		Stoke	1.3mm	JIS D1601,A10 Condition A	
	Vibration	Sweep	2.9G, 33.3~400Hz		
		Cycle	OGNIGITA		
		2 hours for each dire 4 hours for Y di			
9	Mechanical shock	100G, 6ms, ±X 3 times for each			
10	Vibration (with carton)	Random vibra 0.015G <sup>2</sup> /Hz from –6dB/Octave from 2	IEC 68-34		
11	Drop (with carton)	Height: 60d 1 corner, 3 edges,			

Note 1: Ta: Ambient temperature.

Note 2: In the standard condition, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.

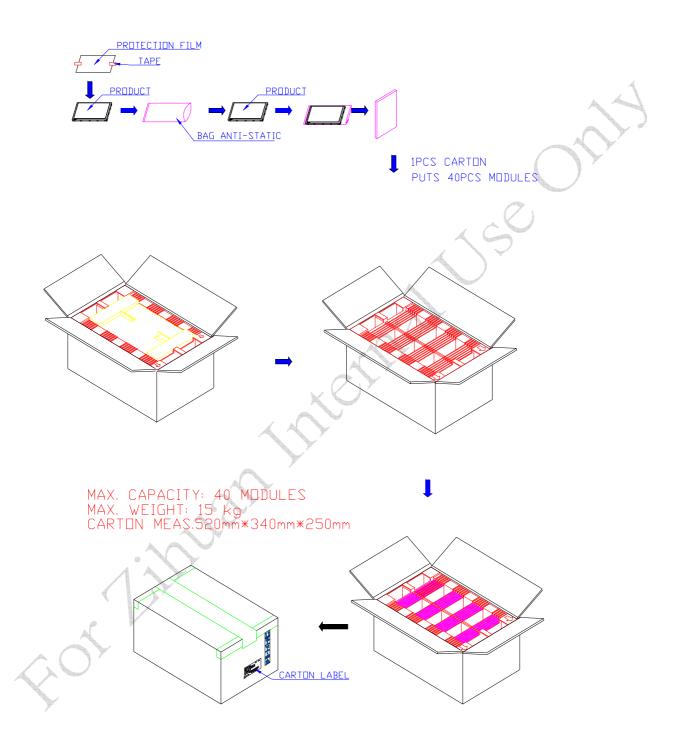
Note 3: Short time operation between -40°C ~-30°C doesn't provide full performance but a correct image on the LCD. The LCD is guaranteed to suffer no permanent damage.

Note 4: Test techniques follow IEC61000-4-2 standard



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# H. Packing Form and Marking1. Packing Form





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#### 2. Module/Panel Label Information

The module/panel (collectively called as the "Product") will be attached with a label of Shipping Number which represents the identification of the Product at a specific location. Refer to the Product outline drawing for detailed location and size of the label. The label is composed of a 22-digit serial number with the following definition:

#### **ABCDEFGHIJKLMNOPQRSTUV**

For internal system usage and production serial numbers.

LAUO Module or Panel factory code, represents the final production factory to complete the Product Product version code, ranging from 0~9 or A~Z (for Version after 9)

Week Code, the production week when the product is finished at its production process

Example:

501M06ZL06123456781Z05:

Product Manufacturing Week Code: WK50

Product Version: Version 1

Product Manufacturing Factory: L3A (The manufacturing site at Taiwan)

Note:

M06: The manufacturing site in Taiwan
 S16: The manufacturing site in Xiamen

PS. In order to avoid mix material, the modle name printed on module label will change from C070VW04 V6 to C070VW04 V61.

#### 3. Carton Label Information

The packing carton will be attached with a carton label where packing Q'ty, AUO Model Name, AUO Part Number, Customer Part Number (Optional) and a series of Carton Number in 13 or 14 digits are printed. The Carton Number is appearing in the following format:

#### ABC-DEFG-HIJK-LMN

DEFG appear after first "-" represents the packing date of the carton

-Date from 01 to 31

lacksquare Month, ranging from 1~9, A~C. A for Oct, B for Nov and C for Dec.

► A.D. year, ranging from 1~9 and 0. The single digit code reprents the last number of the year

Refer to the drawing of packing format for the location and size of the carton label.