

TFT LCD Approval Specification

MODEL NO.: N121IB - L05

Customer:	HP
Approved by: _	
Note:	

核准時間	部門	審核	角色	投票
2009-04-30 17:24:06	NB 產品管理處	徐 2009.04.30 凡 琇	Director	Accept



- CONTENTS -

REVISION HISTORY	 3
1. GENERAL DESCRIPTION 1.1 OVERVIEW 1.2 FEATURES 1.3 APPLICATION 1.4 GENERAL SPECIFICATIONS 1.5 MECHANICAL SPECIFICATIONS	4
2. ABSOLUTE MAXIMUM RATINGS 2.1 ABSOLUTE RATINGS OF ENVIRONMENT 2.2 ELECTRICAL ABSOLUTE RATINGS 2.2.1 TFT LCD MODULE 2.2.2 BACKLIGHT UNIT	 5
3. ELECTRICAL CHARACTERISTICS 3.1 TFT LCD MODULE 3.2 BACKLIGHT UNIT	 7
4. BLOCK DIAGRAM 4.1 TFT LCD MODULE 4.2 BACKLIGHT UNIT	 9
5. INPUT TERMINAL PIN ASSIGNMENT 5.1 TFT LCD MODULE 5.2 BACKLIGHT UNIT 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL 5.4 COLOR DATA INPUT ASSIGNMENT	 11
6. CONVERTER SPECIFICATION 6.1 ABSOLUTE MAXIMUM RATINGS 6.2 RECOMMENDED OPERATING RATINGS	 17
7. INTERFACE TIMING 7.1 INPUT SIGNAL TIMING SPECIFICATIONS 7.2 POWER ON/OFF SEQUENCE	 18
8. OPTICAL CHARACTERISTICS 8.1 TEST CONDITIONS 8.2 OPTICAL SPECIFICATIONS	 20
9. PRECAUTIONS 9.1 ASSEMBLY AND HANDLING PRECAUTIONS 9.2 SAFETY PRECAUTIONS	 23
10. PACKING 10.1 CARTON 10.2 PALLET	 24
11. DEFINITION OF LABELS 11.1 CMO MODULE LABEL 11.2 CMO CARTON LABEL	 26



REVISION HISTORY

Version	Date	Page (New)	Section	Description
3.0	Apr. 22, 2009		All	Approval specification was first issued.



1. GENERAL DESCRIPTION

1.1 OVERVIEW

N121IB - L05 is a 12.1" TFT Liquid Crystal Display module with LED Backlight unit and 40 pins LVDS interface. This module supports 1280 x 800 WXGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The converter module for Backlight is built in.

1.2 FEATURES

- WXGA (1280 x 800 pixels) resolution
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock
- Meet RoHS requirement
- LED Backlight

1.3 APPLICATION

- TFT LCD Notebook

1.4 GENERAL SPECIFICATIONS

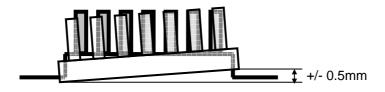
Item	Unit	Note	
Active Area	261.12 (H) x 163.2 (V) (12.1" diagonal)	mm	(4)
Bezel Opening Area	263.67 (H) x 165.75 (V)	mm	(1)
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1280 x R.G.B. x 800	pixel	-
Pixel Pitch	0.204 (H) x 0.204 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	Hard coating (3H), Anti-glare type	-	-

1.5 MECHANICAL SPECIFICATIONS

Į.	ltem		Тур.	Max.	Unit	Note
	Horizontal(H)	275.3	275.8	276.3	mm	
Module Size	Vertical(V)	177.4	178	178.6	mm	(1)
	Depth(D)		5.0	5.3	mm	
W	Weight 270 285 g					
I/F connector mounting position The mounting inclination of the connector makes the screen					(2)	
center within ±0.5mm as the horizontal.						

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

Note (2) Connector mounting position





2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

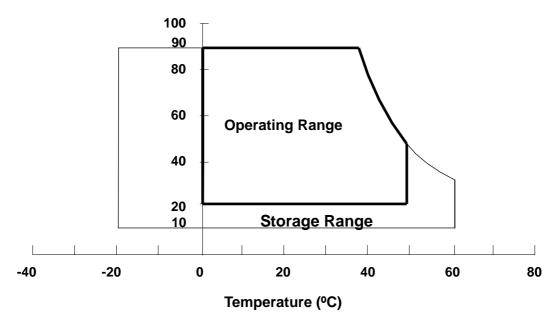
Item	Symbol	Va	lue	Unit	Note	
item	Symbol	Min.	Max.	Offic		
Storage Temperature	T _{ST}	-20	+60	оС	(1)	
Operating Ambient Temperature	T _{OP}	0	+50	оС	(1), (2)	
Shock (Non-Operating)	S _{NOP}	-	220/2	G/ms	(3), (5)	
Vibration (Non-Operating)	V_{NOP}	-	1.5	G	(4), (5)	

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. (Ta 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.

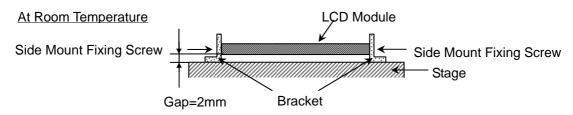
Note (2) The temperature of panel display surface area should be 0 °C Min. and 60 °C Max.

Relative Humidity (%RH)



- Note (3) 1 time for \pm X, \pm Y, \pm Z. for Condition (220G / 2ms) is half Sine Wave,
- Note (4) 10 ~ 500 Hz, 0.5 Hr / Cycle, 1 cycles for each X, Y, Z.
- Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

The fixing condition is shown as below:







Issued Date: Apr, 22, 2009 Model No.: N121IB - L05

Approval

2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Va	lue	Unit	Note
item	Symbol	Min.	Max.	Offic	Note
Power Supply Voltage	V _{cc}	-0.3	+4.0	V	(1)
Logic Input Voltage	V _{IN}	-0.3	V _{CC} +0.3	V	(1)

2.2.2 BACKLIGHT UNIT

Itom	Symbol	Value		Unit	Note	
Item	Symbol	Min.	Max.	Offic	Note	
LED Light Bar Power Supply Voltage	V_L	-35	23.8	V	(1) (2)	
LED Light Bar Power Supply Current	ΙL	0	150	mA	(1), (2)	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for LED (Refer to 3.2 for further information).



3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

 $Ta = 25 \pm 2 \, ^{\circ}C$

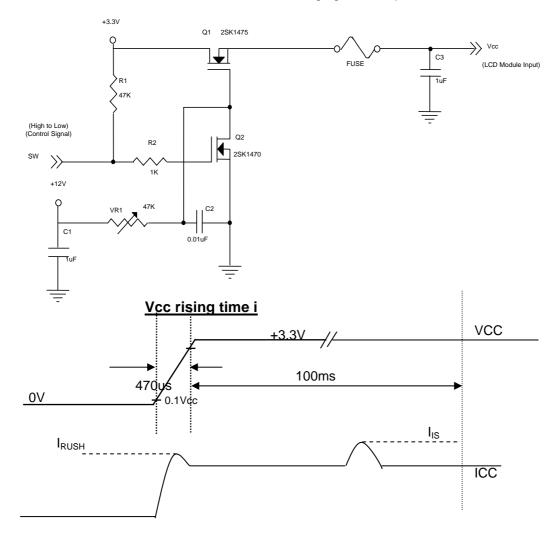
Parameter		Symbol		Value	Unit	Note	
		Symbol	Min.	Тур.	Max.	Offic	Note
Power Supply Voltage		Vcc	3.0	3.3	3.6	V	-
Permissive Ripple Voltage	ge	V_{RP}			100	mV	-
Rush Current		I _{RUSH}			1.5	Α	(2)
Initial Stage Current		I _{IS}			1.0	Α	(2)
Power Supply Current	White	lcc -		270	300	mA	(3)a
Fower Supply Current	Black			330	360	mA	(3)b
LVDS Differential Input F	High Threshold	$V_{TH(LVDS)}$	+100			mV	(5), V _{CM} =1.2V
LVDS Differential Input Low Threshold		V _{TL(LVDS)}			-100	mV	$V_{CM} = 1.2V$ (5) $V_{CM} = 1.2V$
LVDS Common Mode Voltage		V_{CM}	1.125		1.375	V	(5)
LVDS Differential Input Voltage		$ V_{ID} $	100		600	mV	(5)
Terminating Resistor		R_T		100		Ohm	
Power per EBL WG		P _{EBL}		1.69		W	(4)

Note (1) The ambient temperature is $Ta = 25 \pm 2$ °C.

Note (2) I_{RUSH}: the maximum current when VCC is rising

 I_{IS} : the maximum current of the first 100ms after power-on

Measurement Conditions: Shown as the following figure. Test pattern: black.







Note (3) The specified power supply current is under the conditions at Vcc = 3.3 V, Ta = 25 \pm 2 °C, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern

Active Area





Active Area

Note (4) The specified power are the sum of LCD panel electronics input power and the converter input power. Test conditions are as follows.

- (a) Vcc = 3.3 V, $Ta = 25 \pm 2 \, ^{\circ}\text{C}$, $f_v = 60 \text{ Hz}$,
- (b) The pattern used is a black and white 32 x 36 checkerboard, slide #100 from the VESA file "Flat Panel Display Monitor Setup Patterns", FPDMSU.ppt.
- (c) Luminance: 60 nits.

Note (5) The parameters of LVDS signals are defined as the following figures.

Single Ended

OV

V_{TH(LVDS)}

OV

V_{TL(LVDS)}



Issued Date: Apr, 22, 2009 Model No.: N121IB - L05

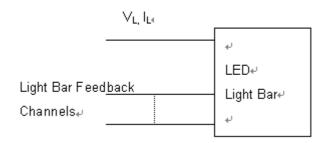
Approval

3.2 BACKLIGHT UNIT

		\sim \sim		\sim	\sim
Iа	=	25	+	2	°C

Parameter	Symbol		Value		Unit	Note	
Farameter	Symbol	Min.	Тур.	Max.	Offic	Note	
LED light bar Power Supply Voltage	V _L	20.3	22.4	23.8	V_{dc}	(1), (2)	
LED light bar Power Supply Current	IL	99.7	105	110.2	mA	(Duty 100%)	
LED Life Time	L _{BL}	15,000	-		Hrs	(4)	
Power Consumption	P_L	2.02	2.35	2.62	W	(3), $I_L = 105 \text{mA}$	

Note (1) LED light bar configuration is shown as below:



Note (2) For better LED light bar driving quality, it is recommended to utilize the adaptive boost converter with current balancing function to drive LED light-bar.

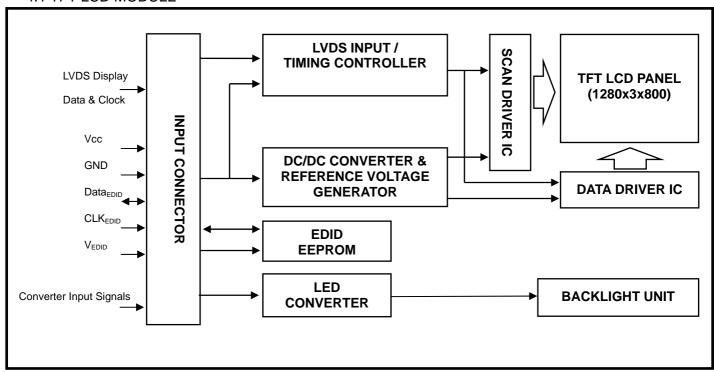
Note (3) $P_L = I_L \times V_L$

Note (4) The lifetime of LED is defined as the time when it continues to operate under the conditions at Ta = 25 ± 2 °C and I_L = 17.5 mA(Per EA) until the brightness becomes 50% of its original value.



4. BLOCK DIAGRAM

4.1 TFT LCD MODULE





5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

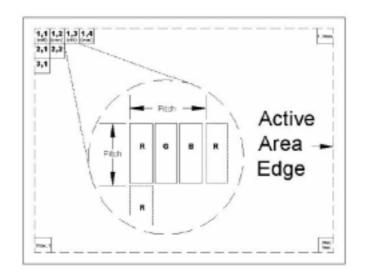
Din	Cumbal	Description	Dolority	Domork
Pin	Symbol	Description	Polarity	Remark
1	NC	No Connection (Reserved for supplier)		-
2	VCCS	Power Supply, 3.3V (typical)		-
3	VCCS	Power Supply, 3.3V (typical)		-
4	EE_VDD	DDC 3.3V power		
5	BIST	Panel Self Test		
6	EE_SC	DDC Clock		
7	EE_SD	DDC Data	.	20 27 00
8	Rin0-	- LVDS differential data input (R0-R5, G0)	Negative	R0~R5,G0-
9	Rn0+	+ LVDS differential data input (R0-R5, G0)	Positive	
10	GND	Ground		
11	Rin1-	- LVDS differential data input (G1-G5, B0-B1)	Negative	
12	Rn1+	+ LVDS differential data input (G1-G5, B0-B1)	Positive	G1~G5,B0,B1
13	GND	Ground		
14	Rin2-	- LVDS differential data input (B2-B5,HS,VS, DE)	Negative	-
15	Rn2+	+ LVDS differential data input (B2-B5,HS,VS, DE)	Positive	B2~B5,Hsync,Vsync,DE
16	GND	Ground		
17	CLK-	- LVDS differential clock input	Negative	LVDS Level
18	CLK+	+ LVDS differential clock input	Positive	
19	GND	Ground	-	-
20	NC	No Connection	-	-
21	NC	No Connection		
22	GND	Ground		
23	NC	No Connection		
24	NC	No Connection		
25	GND	Ground		
26	NC	No Connection		
27	NC	No Connection		
28	GND	Ground		
29	NC	No Connection		
30	NC	No Connection		
31	GND	LED Ground		
32	GND	LED Ground		
33	GND	LED Ground		
34	NC	Non-Connection		
35	LED_VCCS	LED Power		
36	LED_VCCS	LED Power		
37	LED_VCCS	LED Power		
38		PWM Control Signal of LED Converter		
39	LED_EN	Enable Control Signal of LED Converter		
40		Non-Connection		

Note (1) Connector Part No.: FI-NXB40SL-HF10 (JAE) or equivalent

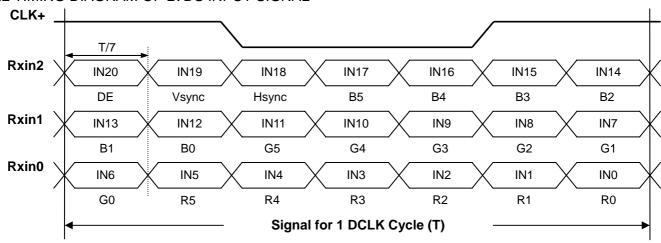
Note (2) User's connector Part No: FI-NX40CL or equivalent

Note (3) The first pixel is odd as shown in the following figure.





5.2 TIMING DIAGRAM OF LVDS INPUT SIGNAL





5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

									[Data	Sign	al							
	Color		Red				Green				Blue								
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
_	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Scale	:					:	:			:		:		:	:			:	
Of	:	:	:	:	:	-	:	:	:		:		:	:	:	:	:	:	:
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)		0	0		0		1	1	1	1	1	1			0		0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Cross	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		:	
Blue	Pluo(61)				•										1				;
Diue	Blue(61) Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Diue(03)	U	U	U	U	U	U	U	U	U	U	U	U	I	l	I	I	ı	

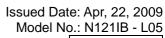
Note (1) 0: Low Level Voltage, 1: High Level Voltage



5.5 EDID DATA STRUCTURE

The EDID (Extended Display Identification Data) data formats are to support displays as defined in the VESA Plug & Display and FPDI standards.

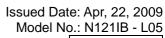
Byte # (decimal)	Byte # (hex)	Field Name and Comments	Value (hex)	Value (binary)
0	0	Header	00	00000000
1	1	Header	FF	11111111
2	2	Header	FF	11111111
3	3	Header	FF	11111111
4	4	Header	FF	11111111
5	5	Header	FF	11111111
6	6	Header	FF	11111111
7	7	Header	00	00000000
8	8	EISA ID manufacturer name ("CMO")	0D	00001101
9	9	EISA ID manufacturer name (Compressed ASCII)	AF	10101111
10	0A	ID product code (N121IB-L05)	37	00110111
11	0B	ID product code (hex LSB first; N121IB-L05)	12	00010010
12	0C	ID S/N (fixed "0")	00	00000000
13	0D	ID S/N (fixed "0")	00	00000000
14	0E	ID S/N (fixed "0")	00	00000000
15	0F	ID S/N (fixed "0")	00	00000000
16	10	Week of manufacture (fixed week code)	14	00010100
17	11	Year of manufacture (fixed year code)	13	00010011
18	12	EDID structure version # ("1")	01	0000001
19	13	EDID revision # ("3")	03	00000011
20	14	Video I/P definition ("digital")	80	10000000
21	15	Max H image size ("26.112cm")	1A	00011010
22	16	Max V image size ("16.575cm")	11	00010001
23	17	Display Gamma (Gamma = "2.2")	78	01111000
24	18	Feature support ("Active off, RGB Color")	0A	00001010
25	19	Rx1 Rx0 Ry1 Ry0 Gx1 Gx0 Gy1 Gy0	98	10011000
26	1A	Bx1 Bx0 By1 By0 Wx1 Wx0 Wy1 Wy0	D5	11010101
27	1B	Rx=0.572	92	10010010
28	1C	Ry=0.360	5C	01011100
29	1D	Gx=0.346	58	01011000
30	1E	Gy=0.578	94	10010100
31	1F	Bx=0.155	27	00100111
32	20	By=0.110	1C	00011100
33	21	Wx=0.313	50	01010000
34	22	Wy=0.329	54	01010100
35	23	Established timings 1	00	00000000
36	24	Established timings 2	00	00000000
37	25	Manufacturer's reserved timings	00	00000000
38	26	Standard timing ID # 1	01	00000001
39	27	Standard timing ID # 1	01	00000001
40	28	Standard timing ID # 2	01	00000001







44		0. 1.1	04	00000004
41 42	29	Standard timing ID # 2	01	00000001
	2A	Standard timing ID # 3	01	00000001
43 44	2B	Standard timing ID # 3	01	00000001
	2C	Standard timing ID # 4	01	00000001
45	2D	Standard timing ID # 4	01	00000001
46	2E	Standard timing ID # 5	01	00000001
47	2F	Standard timing ID # 5	01	00000001
48	30	Standard timing ID # 6	01	00000001
49	31	Standard timing ID # 6	01	00000001
50	32	Standard timing ID # 7	01	00000001
51	33	Standard timing ID # 7	01	00000001
52	34	Standard timing ID # 8	01	00000001
53	35	Standard timing ID # 8	01	00000001
54	36	Detailed timing description # 1 Pixel clock ("69.3MHz", According to VESA CVT Rev1.1)	12	00010010
55	37	# 1 Pixel clock (hex LSB first)	1B	00011011
56	38	# 1 H active ("1280")	00	00000000
57	39	# 1 H blank ("132")	84	10000100
58	3A	# 1 H active : H blank ("1280 : 132")	50	01010000
59	3B	# 1 V active ("800")	20	00100000
60	3C	# 1 V blank ("18")	12	00010010
61	3D	# 1 V active : V blank ("800 :18")	30	00110000
62	3E	# 1 H sync offset ("40")	28	00101000
63	3F	# 1 H sync pulse width ("26")	1A	00011010
64	40	# 1 V sync offset : V sync pulse width ("3 : 4")	34	00110100
65	41	# 1 H sync offset : H sync pulse width : V sync offset : V sync width ("40: 26 : 3 : 4")	00	00000000
66	42	# 1 H image size ("260 mm")	04	00000100
67	43	# 1 V image size ("170 mm")	AA	10101010
68	44	# 1 H image size : V image size ("260 : 170")	10	00010000
69	45	# 1 H boarder ("0")	00	00000000
70	46	# 1 V boarder ("0")	00	00000000
71	47	# 1 Non-interlaced, Normal, no stereo, Separate sync, H/V pol Negatives	18	00011000
72	48	Detailed timing description # 2	00	00000000
73	49	# 2 Flag	00	00000000
74	4A	# 2 Reserved	00	00000000
75	4B	# 2 FE (hex) defines ASCII string (Model Name "N121IB-L05", ASCII)	FE	11111110
76	4C	# 2 Flag	00	00000000
77	4D	# 2 1st character of name ("N")	4E	01001110
78	4E	# 2 2nd character of name ("1")	31	00110001
79	4F	# 2 3rd character of name ("2")	32	00110010
80	50	# 2 4th character of name ("1")	31	00110001
81	51	# 2 5th character of name ("I")	49	01001001
82	52	# 2 6th character of name ("B")	42	01000010
83	53	# 2 7th character of name ("-")	2D	00101101
84	54	# 2 8th character of name ("L")	4C	01001100
85	55	# 2 9th character of name ("0")	30	00110000
86	56	# 2 9th character of name ("5")	35	00110101







87	57	# 2 New line character indicates end of ASCII string	0A	00001010
88	58	# 2 Padding with "Blank" character	20	00100000
89	59	# 2 Padding with "Blank" character	20	00100000
90	5A	Detailed timing description # 3	00	00000000
91	5B	# 3 Flag	00	00000000
92	5C	# 3 Reserved	00	00000000
93	5D	# 3 FE (hex) defines ASCII string (Vendor "CMO", ASCII)	FE	11111110
94	5E	# 3 Flag	00	00000000
95	5F	# 3 1st character of string ("C")	43	01000011
96	60	# 3 2nd character of string ("M")	4D	01001101
97	61	# 3 3rd character of string ("O")	4F	01001111
98	62	# 3 New line character indicates end of ASCII string	0A	00001010
99	63	# 3 Padding with "Blank" character	20	00100000
100	64	# 3 Padding with "Blank" character	20	00100000
101	65	# 3 Padding with "Blank" character	20	00100000
102	66	# 3 Padding with "Blank" character	20	00100000
103	67	# 3 Padding with "Blank" character	20	00100000
104	68	# 3 Padding with "Blank" character	20	00100000
105	69	# 3 Padding with "Blank" character	20	00100000
106	6A	# 3 Padding with "Blank" character	20	00100000
107	6B	# 3 Padding with "Blank" character	20	00100000
108	6C	Detailed timing description # 4	00	00000000
109	6D	# 4 Flag	00	00000000
110	6E	# 4 Reserved	00	00000000
111	6F	# 4 FE (hex) defines ASCII string (Model Name"N121IB-L05", ASCII)	FE	11111110
112	70	# 4 Flag	00	00000000
113	71	# 4 1st character of name ("N")	4E	01001110
114	72	# 4 2nd character of name ("1")	31	00110001
115	73	# 4 3rd character of name ("2")	32	00110010
116	74	# 4 4th character of name ("1")	31	00110001
117	75	# 4 5th character of name ("I")	49	01001001
118	76	# 4 6th character of name ("B")	42	01000010
119	77	# 4 7th character of name ("-")	2D	00101101
120	78	# 4 8th character of name ("L")	4C	01001100
121	79	# 4 9th character of name ("0")	30	00110000
122	7A	# 4 9th character of name ("5")	35	00110101
123	7B	# 4 New line character indicates end of ASCII string	0A	00001010
124	7C	# 4 Padding with "Blank" character	20	00100000
125	7D	# 4 Padding with "Blank" character	20	00100000
126	7E	Extension flag	00	00000000
127	7F	Checksum	8F	10001111



6. CONVERTER SPECIFICATION

6.1 ABSOLUTE MAXIMUM RATINGS

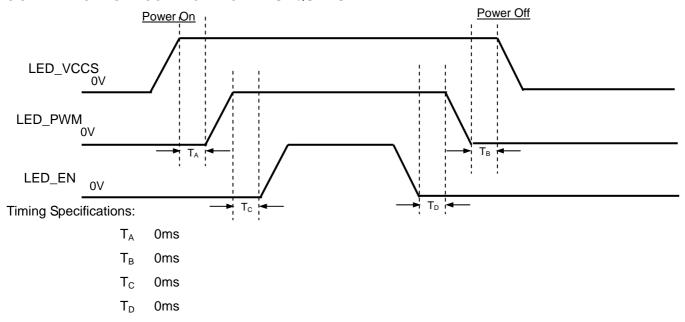
Symbol	Ratings
Vin	28V
Gnd	+/-0.3V
PWM, EN	-0.3V~5.5V

6.2 RECOMMENDED OPERATING RATINGS

Paramete	Parameter			Value			Note
Faramete	Symbol	Min.	Тур.	Max.	Unit	Note	
Converter Input power sup	ply voltage	V_{in}	6.0	12.0	20.0	V	
EN Control Level	Backlight On		2.0		5.5	V	
LIN COILLOI Level	Backlight Off		0		0.8	V	
PWM Control Level PWM High			2.0		5.5	V	
r www.control.cever	PWM Low Level		0		0.8	V	
PWM Control Duty Ratio			10		100	%	
PWM Control Ripple Voltage		V_{PWM_pp}			100	mV	
PWM Control Frequency		f_{PWM}	200		1000	Hz	
Vin=6V			418	510	602	mA	(1)
LED Power Current Vin=12V		I_{BL}	209	255	301	mA	(1)
	Vin=20V		125	153	180	mA	(1)

Note (1) The specified LED power supply current is under the conditions at "LED_VCCS = Min, Typ, Max", Ta = 25 ± 2 °C, $f_{PWM} = 200$ Hz, Duty=100%.

6.3 LED BACKLIGHT CONTROLL POWER SEQUENCE



Note (1) Please follow the LED backlight power sequence as above. If the customer could not follow, it might cause backlight flash issue during display ON/OFF or damage the LED backlight controller



7. INTERFACE TIMING

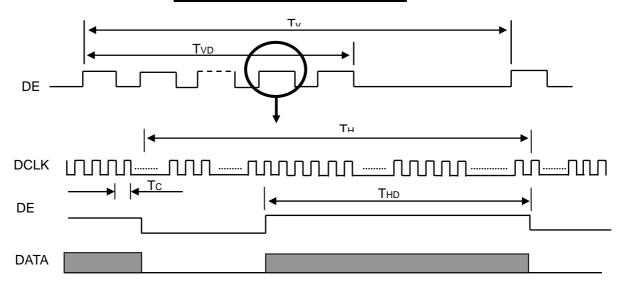
7.1 INPUT SIGNAL TIMING SPECIFICATIONS

he specifications of input signal timing are as the following table and timing diagram.

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
DCLK	Frequency	1/Tc	65.835	69.3	72.765	MHz	-
DE	Vertical Total Time	TV	802	818	1023	H	-
	Vertical Active Display Period	TVD	800	800	800	H	-
	Vertical Active Blanking Period	TVB	TV-TVD	18	TV-TVD	H	
	Horizontal Total Time	TH	1380	1412	1600	Tc	-
	Horizontal Active Display Period	THD	1280	1280	1280	Tc	-
	Horizontal Active Blanking Period	THB	TH-THD	132	TH-THD	Tc	

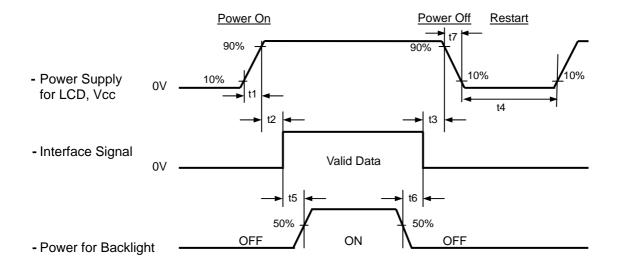
Note (1) Because this module is operated by DE only mode, Hsync and Vsync are ignored.

INPUT SIGNAL TIMING DIAGRAM





7.2 POWER ON/OFF SEQUENCE



Timing Specifications:

0.5	t1	10 ms
0	t2	50 ms
0	t3	50 ms
	t4	500 ms
	t5	200 ms
	t6	200 ms

- Note (1) Please follow the power on/off sequence described above. Otherwise, the LCD module might be damaged.
- Note (2) Please avoid floating state of interface signal at invalid period. When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.
- Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.
- Note (4) Sometimes some slight noise shows when LCD is turned off (even backlight is already off). To avoid this phenomenon, we suggest that the Vcc falling time is better to follow 50us to 10ms.



8. OPTICAL CHARACTERISTICS

8.1 TEST CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V _{cc}	3.3	V
Input Signal	According to typical value	alue in "3. ELECTRICAL	CHARACTERISTICS"
LED Light Bar Input Current	IL	105	mA

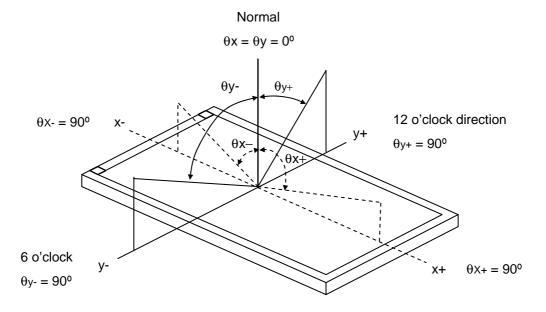
The measurement methods of optical characteristics are shown in Section 8.2. The following items should be measured under the test conditions described in Section 8.1 and stable environment shown in Note (5).

8.2 OPTICAL SPECIFICATIONS

Ite	m	Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast Ratio	Contrast Ratio			300	500	1	ı	(2), (5)
Response Time	Donners Time			-	3	8	ms	(2)
Kesponse fille		T_F		-	7	12	ms	(3)
Luminance of W	/hite (5P)	L _{AVE}		180	220	-	cd/m ²	(4), (5)
White Variation		δW		-	1.40	1.60	-	(5), (6)
	Red	Rx	$\theta_x=0^\circ$, $\theta_Y=0^\circ$		0.572		ı	
	Red	Ry	Viewing Normal Angle		0.360		ı	(1), (5)
	Green	Gx		Тур	0.346		ı	
Color		Gy			0.578	Тур.+	1	
Chromaticity	Chromaticity Blue			0.03	0.155	0.03	- '	(1), (5)
					0.110			
	\\/hito	Wx			0.313		-	
	White				0.329		1	
	Uorizontol	θ_x +		40	45	ı		
Viewine Anale	Horizontal	θ_{x} -	CR≥10	40	45	-	Deg.	(1), (5)
Viewing Angle	Vertical	θ _Y +		15	20	-		
	Vertical	θ _Y -		40	45	-		



Note (1) Definition of Viewing Angle (θx , θy):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

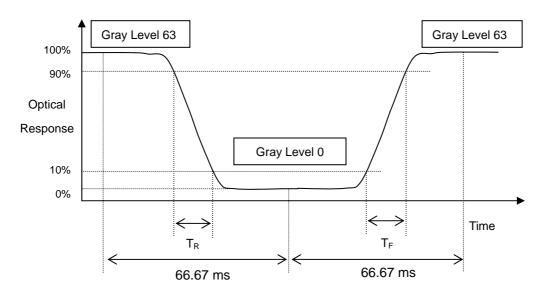
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R, T_F) and measurement method:







Note (4) Definition of Average Luminance of White (L_{AVE}):

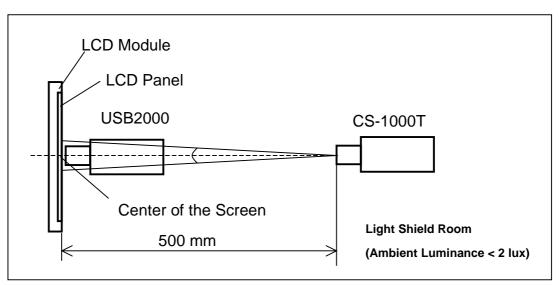
Measure the luminance of gray level 63 at 5 points

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

L(x) is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

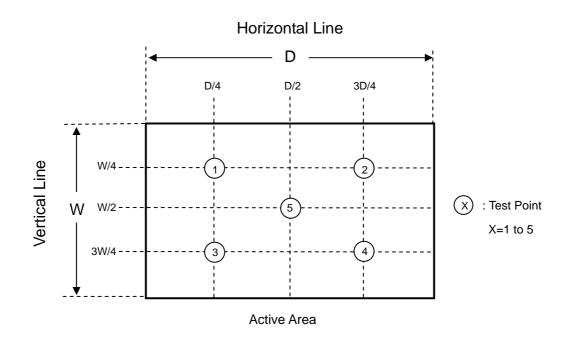
The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 63 at 5 points

 $\delta W = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]$





Issued Date: Apr, 22, 2009 Model No.: N121IB - L05

Approval

9. PRECAUTIONS

9.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) The module should be assembled into the system firmly by using every mounting hole. Be careful not to twist or bend the module.
- (2) While assembling or installing modules, it can only be in the clean area. The dust and oil may cause electrical short or damage the polarizer.
- (3) Use fingerstalls or soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (4) Do not press or scratch the surface harder than a HB pencil lead on the panel because the polarizer is very soft and easily scratched.
- (5) If the surface of the polarizer is dirty, please clean it by some absorbent cotton or soft cloth. Do not use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanently damage the polarizer due to chemical reaction.
- (6) Wipe off water droplets or oil immediately. Staining and discoloration may occur if they left on panel for a long time.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contacting with hands, legs or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static electricity, it may cause damage to the C-MOS Gate Array IC.
- (9) Do not disassemble the module.
- (10) Do not pull or fold the lamp wire.
- (11) Pins of I/F connector should not be touched directly with bare hands.

9.2 SAFETY PRECAUTIONS

- (1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with converter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

9.3 OPERATION PRECAUTIONS

- (1) Do not pull the I/F connector in or out while the module is operating.
- (2) Always follow the correct power on/off sequence when LCD module is connecting and operating. This can prevent the CMOS LSI chips from damage during latch-up.
- (3) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with converter. Do not disassemble the module or insert anything into the Backlight unit.

9.4 OTHER PRECAUTIONS

(1) When fixed patterns are displayed for a long time, remnant image is likely to occur.



10. PACKAGING

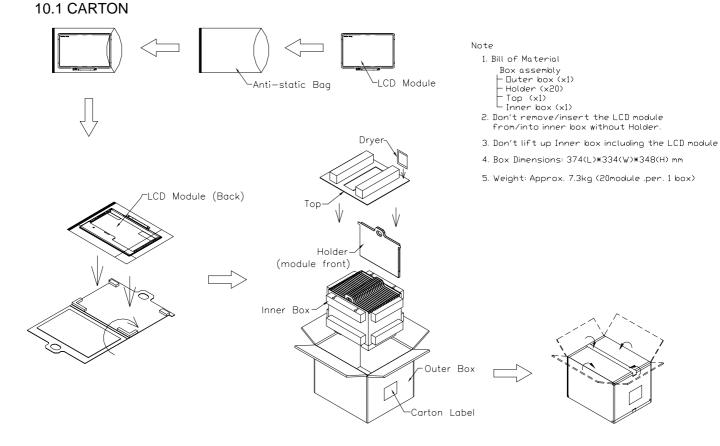


Figure. 10-1 Packing method



10.2 PALLET

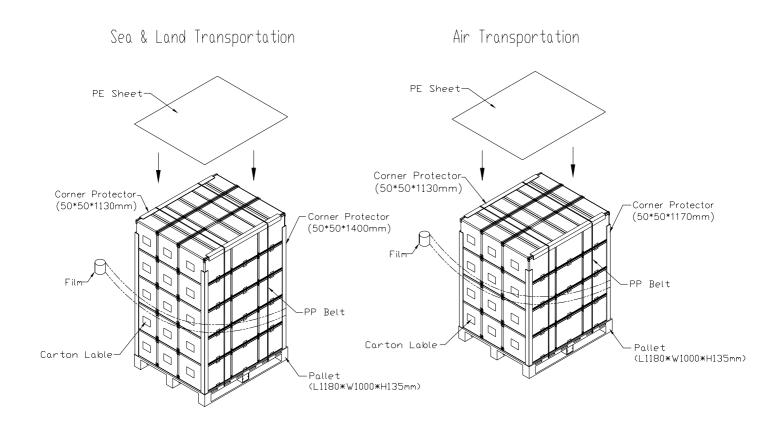


Figure. 10-2 Packing method



11. DEFINITION OF LABELS

11.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: N121IB L05
- (b) Revision: Rev. XX, for example: A1, ..., C1, C2 ...etc.
- (c) Serial ID: X X X X X X X Y M D X N N N N

 Serial No.

 CMO Internal Use

 Year, Month, Date

 CMO Internal Use

 Revision

 CMO Internal Use
- (d) Production Location: MADE IN XXXX. XXXX stands for production location.
- (e)UL/CB logo: LEOO especially stands for panel manufactured by CMO NingBo satisfying UL/CB requirement. The panel without LEOO mark stands for manufactured by CMO Taiwan satisfying UL/CB requirement.

Serial ID includes the information as below:

(a) Manufactured Date: Year: 1~9, for 2001~2009

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I, O and U

- (b) Revision Code: cover all the change
- (c) Serial No.: Manufacturing sequence of product

CT Label

S/N	CT:CAWWPXXVRXXXXX					
CT:	Title					
С	LCD Display Module					
AWWP	Assembly Code					
XX	Revision					
VR	Supplier /Site of MFG					
XX	Week/Year of MFG					
XXX	Serial number. From 000000 to 999999					



11.2 CMO CARTON LABEL



