

## FEATURES

- (1) 12.1"XGA display size for notebook PC
- (2) LVDS interface system

**TENTATIVE**

## MECHANICAL SPECIFICATIONS

Item	Specifications
Dimensional Outline (typ.)	261.0(W) x 199.6(H) x 4.5max(D) mm 6.4max(D) mm
Number of Pixels	1024(W) x 768(H) pixels
Active Area	245.76(W) x 184.32(H) mm
Pixel Pitch	0.24(W) x 0.24(H)
Weight (approximately)	390g
Backlight	Single CCFL, Sidelight type

## ABSOLUTE MAXIMUM RATINGS

Item	Min.	Max.	Unit
Supply Voltage ( $V_{DD}$ )	-0.3	4.0	V
( $V_{FI}$ )	0	2.0	kV(rms)
FL Driving Frequency ( $f_{FL}$ )	-	100	kHz
Input Signal Voltage ( $V_{IN}$ )	-0.3	$V_{DD}+0.3$	V
Operating Temperature	0	50	°C
Storage Temperature	-20	60	°C
Storage Humidity	10	90	% (RH)

## ELECTRICAL SPECIFICATION

Item	Min.	Typ.	Max.	Unit	Remarks
Supply Voltage					
( $V_{DD}$ )	3.0	3.3	3.6	V	
( $V_{FI}$ )	550	600	650	V(rms)	$I_{FI}=6.0$ mA(rms)
FL Start Voltage ( $T_a=0^\circ\text{C}$ )	1200	---	---	V(rms)	
Receiver Input Voltage	0	---	2.4	V	
Differential Input High Threshold( $V_{TH}$ )*1	---	---	$V_{OS}+0.1$	V	$V_{OS}$ :Offset Mode Voltage $V_{OS}=+1.2$ V
Differential Input Low Threshold( $V_{TL}$ )*1	$V_{OS}-0.1$	---	---	V	
Current Consumption					
*2 ( $I_{DD}$ )	---	240	---	mA	
*3 ( $I_{FI}$ )	2	6.0	6.0	mA(rms)	
*2 *3 Power Consumption	---	4.4	---	W	$I_{FI}=6.0$ mA(rms) : $V_{FI}=600$ V(rms)

\*1 : Refer to DF90CF364 Specification by National Semiconductor Corporation.

\*2 : 8 color bars pattern

\*3 : Excepting the efficiency FL inverter

\*4: Not use Hsync nor Vsync. Only ENAB control.

## OPTICAL SPECIFICATION ( $T_a=25^\circ\text{C}$ )

Item	Min.	Typ.	Max.	Unit	Remarks
Contrast Ratio (CR)	100	250	---	---	
Response Time					
( $t_{ON}$ )	---	---	50	ms	
( $t_{OFF}$ )	---	---	50	ms	
Luminance ( $L$ )	---	170	---	cd/m <sup>2</sup>	$I_{FI}=6.0$ mA(rms)

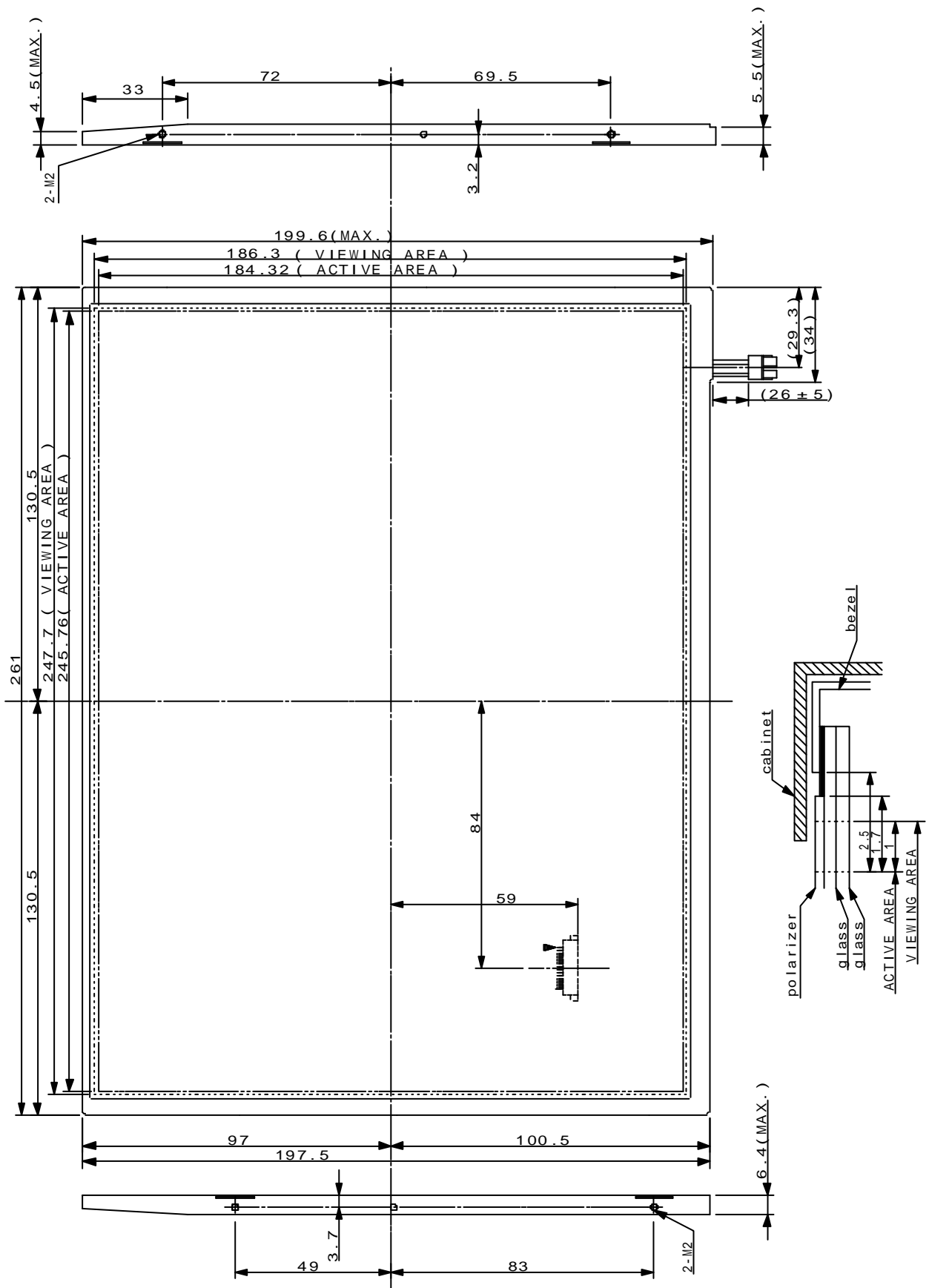
\*The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba or others.

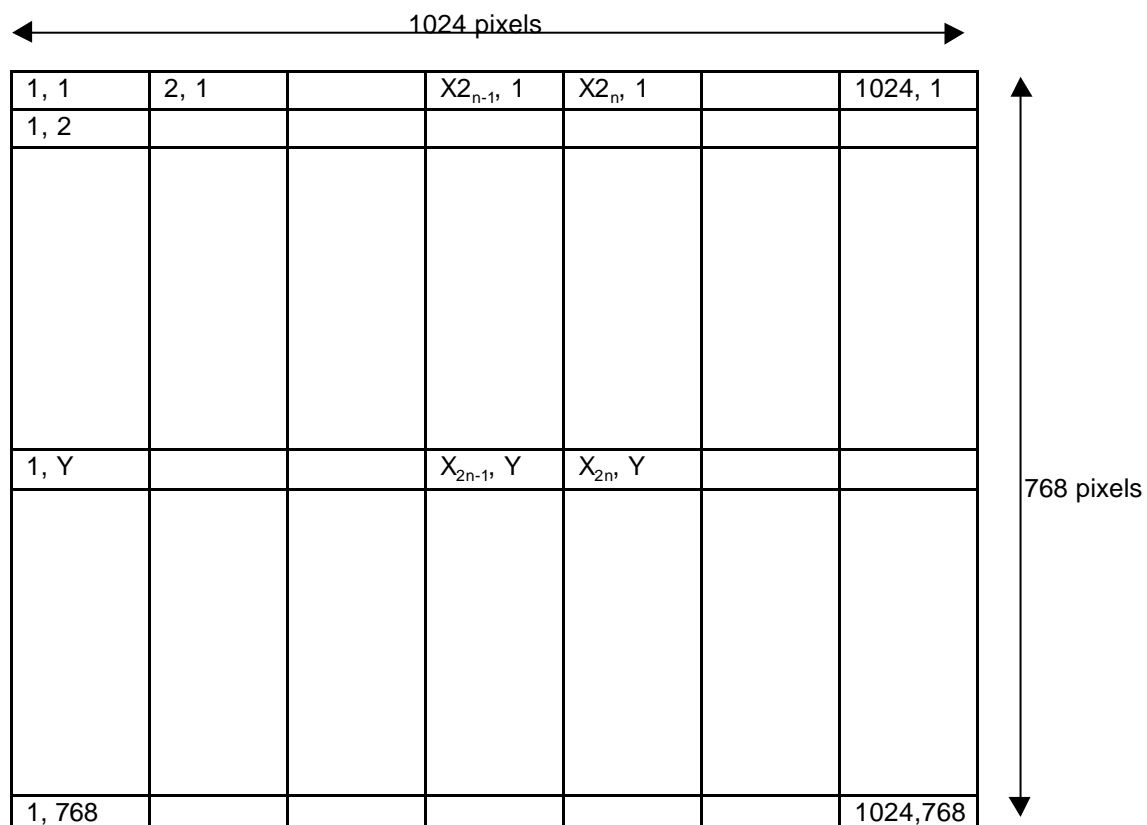
\*The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba before proceeding with the design of equipment incorporating this product.

**DIMENSIONAL OUTLINE**  
(front figure)

**TENTATIVE**

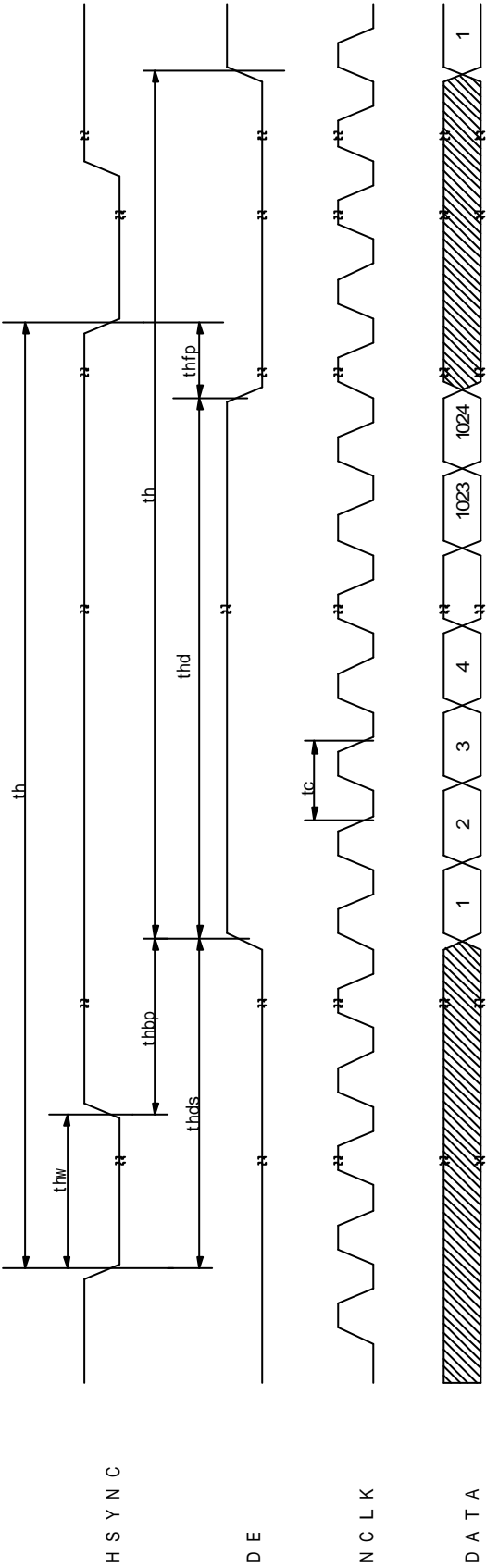
Unit : mm  
Standard tolerance :  $\pm 0.5$



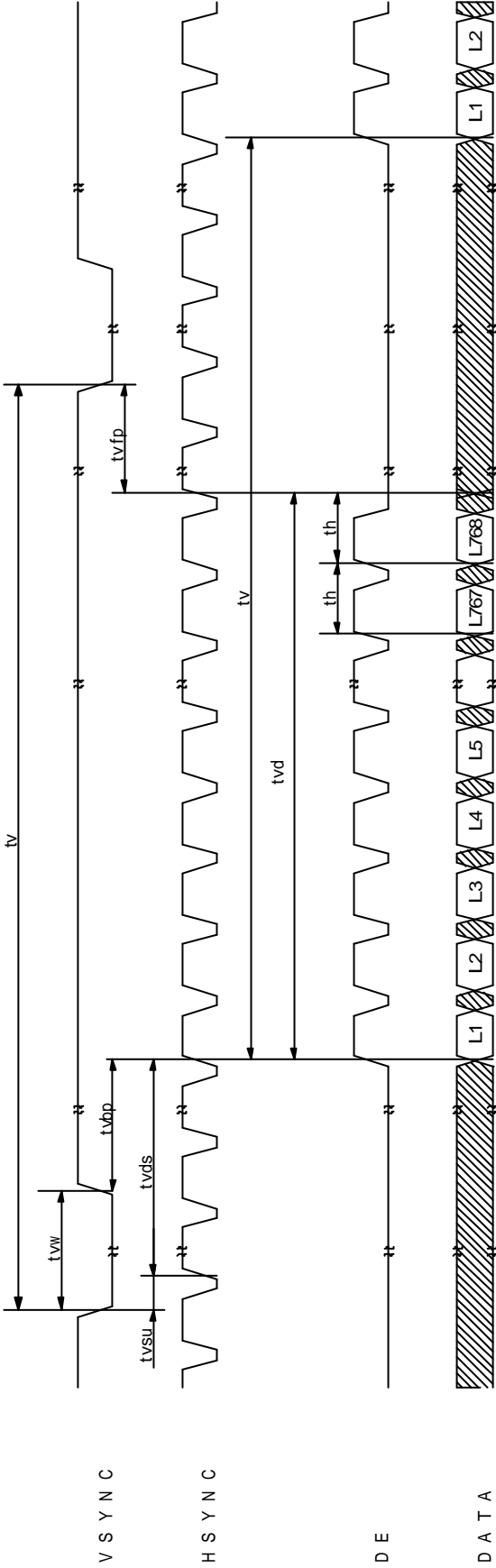


TIMING CHART

Horizontal Data Timing



Vertical Data Timing



**TIMING SPECIFICATION** 1)2) 3) 4)5)6)

Signal	Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
NCLK	Clock Period	$t_c$	15	15.38	-	ns	
	Frequency	$1/t_c$	-	65	66.6	MHz	
HSYNC	Line Period	$t_h$	$1319 \times t_c$ 20.04	$1344 \times t_c$ 20.68	$1462 \times t_c$ 22.16	- $\mu s$	
	Horizontal Display Time	$t_{hd}$	$1024 \times t_s$	$1024 \times t_s$	$1024 \times t_s$	-	
	Horizontal Front Porch	$t_{hfp}$	$8 \times t_c$	-	-	-	
	Pulse Width	$t_{lw}$	$8 \times t_c$	-	-	-	
	Horizontal Back porch	$t_{hbp}$	$8 \times t_c$	-	-	-	
VSYNC	Frame Period	$t_v$	$778 \times t_{lpd}$	$806 \times t_{lpd}$	$860 \times t_{lpd}$	-	
	Frame Frequency	$1/t_v$	58	60	-	Hz	
	Vertical Display Time	$t_{vd}$	$768 \times t_{lpd}$	$768 \times t_{lpd}$	$768 \times t_{lpd}$	-	
	Vertical Front porch	$t_{vfp}$	$1 \times t_h$	-	$250 \times t_h$	-	
	Pulse Width	$t_{lw}$	$3 \times t_h$	$6 \times t_h$	$7 \times t_h$	-	
	Vertical back porch	$t_{vbp}$	$4 \times t_h$	-	-	-	
	VSYNC to DATA	$t_{vds}$	$7 \times t_h$	$35 \times t_h$	$250 \times t_h$	-	
	Setup to HSYNC	$t_{vsu}$	$0 \times t_c$	-	$1024 \times t_c$	-	
DE	Display Start	$t_{drds}$	$32 \times t_s$	$296 \times t_s$	$400 \times t_s$	-	

Note 1) Refer to TIMING CHART and LVDS (THC63LVDF64A) specifications by Thine Electronics.inc.

Note 2) If NCLK is fixed to "H" or "L" level for certain period, the panel may be damaged.

Note 3) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving Condition (especially driving frequency ), even if the condition satisfies the above timing specifications and the recommended operating conditions shown in 3.

Note 4)Do not hold NCLK on "H" level nor "L" level during VDD(+3.3V) is supplied. When it holds on, DC voltage supplies to liquid crystal materials and it may cause damage to liquid crystal materials.

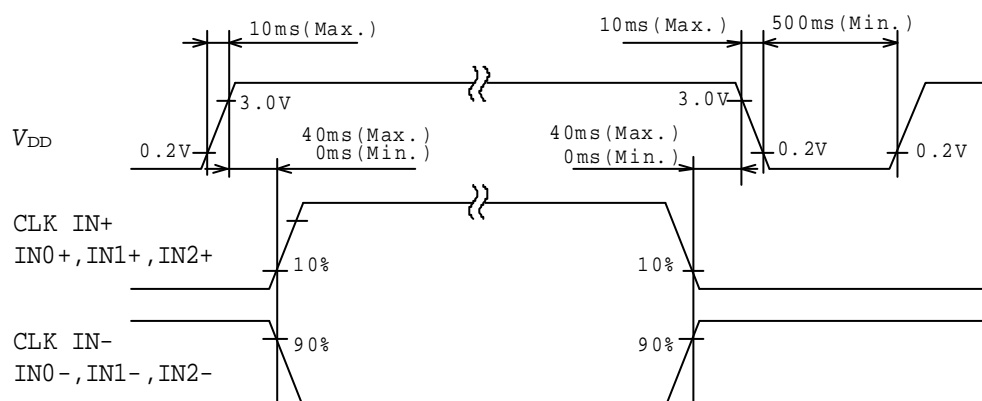
Note 5) Do not make  $t_v$ ,  $t_{vhd}$  and  $t_{vds}$  fluctuate.

If  $t_v$ ,  $t_{vhd}$ , and  $t_{vds}$  are fluctuate, the panel displays black.

Note 6) NCLK count of each Horizontal Scanning Time should be always the same.

V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer)

Frame period should be always the same.

**SEQUENCE OF POWER SUPPLIES AND SIGNALS**

## CONNECTOR PIN ASSIGNMENT FOR INTERFACE

### CN1 INPUT SIGNAL

Connector : DF19L-14P-1H / HIROSE ELECTRIC CO.,LTD.

Mating Connector : DF19G-14S-1C / HIROSE ELECTRIC CO.,LTD.

Terminal No.	Symbol	Function
1	$V_{DD}$	Power Supply : +3.3V
2	$V_{DD}$	Power Supply : +3.3V
3	GND	
4	GND	
5	IN0-	Transmission Data of Pixels 0 (Negative : - )
6	IN0+	Transmission Data of Pixels 0 (Positive : + )
7	IN1-	Transmission Data of Pixels 1 (Negative : - )
8	IN1+	Transmission Data of Pixels 1 (Positive : + )
9	IN2-	Transmission Data of Pixels 2 (Negative : - )
10	IN2+	Transmission Data of Pixels 2 (Positive : + )
11	CLK IN-	Sampling Clock (Negative : - )
12	CLK IN+	Sampling Clock (Positive : + )
13	GND	
14	GND	

### CN2 CCFL POWER SOURCE

Connector : BHTR-02VS-1 / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

Mating Connector : SM02B-BHSS-1 / JAPAN SOLDERLESS TERMINAL MFG CO.,LTD.

Terminal No.	Symbol	Function
1	VFLH	CCFL Power Supply ( high voltage)
2	VFLL	CCFL Power Supply (low voltage)

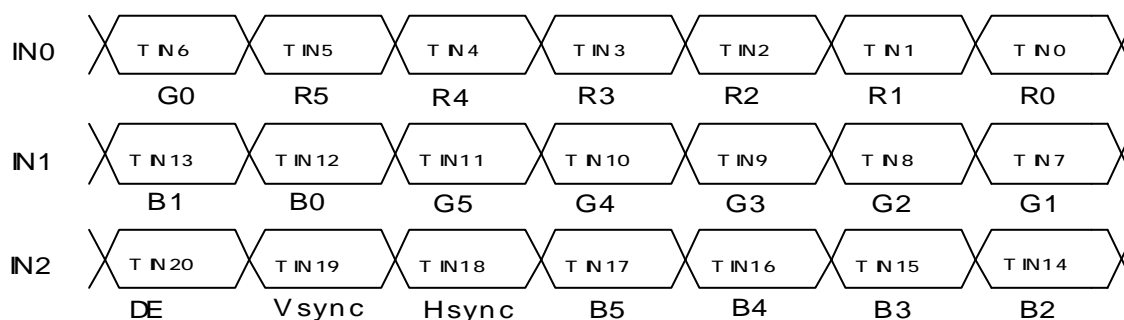
Note 1) Please connect GND pin to ground.

Don't use it as no-connect nor connection with high impedance.

## RECOMMENDED TRANSMITTER (THC63LVDM63A) TO LTM12C318S INTERFACE ASSIGNMENT

## Case1: 6bit Transmitter

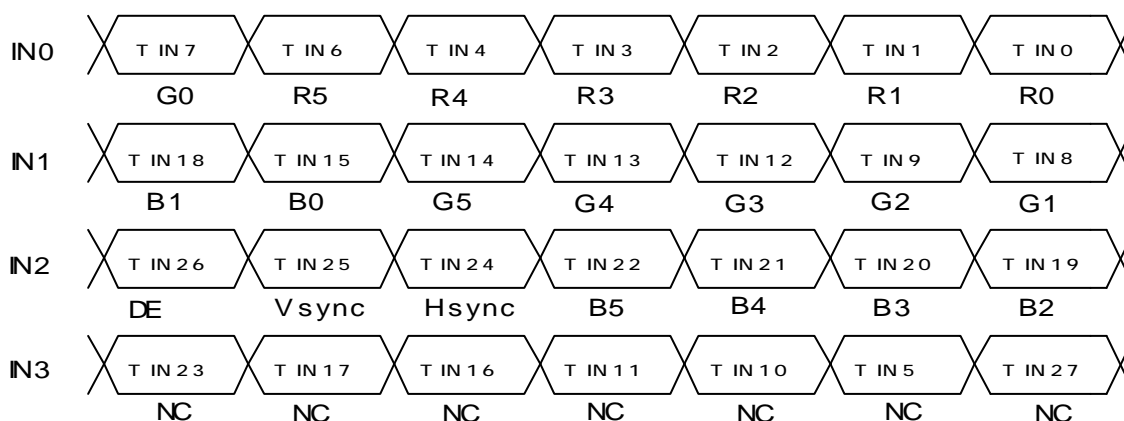
THC63LVDM63A				LTM12C318S Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	
Symbol	THC63LVDM63A	Symbol	Function		Terminal Symbol
TIN0	44	R0	Red Pixels Display Data (LSB)	TOUT0- TOUT0+	No.5 No.6 IN0- IN0+
TIN1	45	R1	Red Pixels Display Data		
TIN2	47	R2	Red Pixels Display Data		
TIN3	48	R3	Red Pixels Display Data		
TIN4	1	R4	Red Pixels Display Data		
TIN5	3	R5	Red Pixels Display Data (MSB)		
TIN6	4	G0	Green Pixels Display Data (LSB)	TOUT1- TOUT1+	No.7 No.8 IN1- IN1+
TIN7	6	G1	Green Pixels Display Data		
TIN8	7	G2	Green Pixels Display Data		
TIN9	9	G3	Green Pixels Display Data		
TIN10	10	G4	Green Pixels Display Data		
TIN11	12	G5	Green Pixels Display Data (MSB)		
TIN12	13	B0	Blue Pixels Display Data (LSB)	TOUT2- TOUT2+	No.9 No.10 IN2- IN2+
TIN13	15	B1	Blue Pixels Display Data		
TIN14	16	B2	Blue Pixels Display Data		
TIN15	18	B3	Blue Pixels Display Data		
TIN16	19	B4	Blue Pixels Display Data		
TIN17	20	B5	Blue Pixels Display Data (MSB)		
TIN18	22	Hsync	Horizontal Synchronization Signal	TCLK OUT- TCLK OUT+	No.11 No.12 CLK IN- CLK IN+
TIN19	23	Vsync	Vertical Synchronization Signal		
TIN20	25	DE	Compound Synchronization Signal		
CLK IN	26	NCLK	Data Sampling Clock		



## RECOMMENDED TRANSMITTER (THC63LVDM83A) TO LTM12C318S INTERFACE ASSIGNMENT

## Case2: 8bit Transmitter

THC63LVDM83A				LTM12C318S Interface (CN1)	
Input Terminal No.		Input Signal (Graphics controller output signal)		Output Signal Symbol	
Symbol	THC63LVDM83A	Symbol	Function		Terminal Symbol
TIN0	51	R0	Red Pixels Display Data (LSB)	TOUT0- TOUT0+	No.5 No.6 IN0- IN0+
TIN1	52	R1	Red Pixels Display Data		
TIN2	54	R2	Red Pixels Display Data		
TIN3	55	R3	Red Pixels Display Data		
TIN4	56	R4	Red Pixels Display Data		
TIN6	3	R5	Red Pixels Display Data (MSB)	TOUT1- TOUT1+	No.7 No.8 IN1- IN1+
TIN7	4	G0	Green Pixels Display Data (LSB)		
TIN8	6	G1	Green Pixels Display Data		
TIN9	7	G2	Green Pixels Display Data		
TIN12	11	G3	Green Pixels Display Data		
TIN13	12	G4	Green Pixels Display Data	TOUT2- TOUT2+	No.9 No.10 IN2- IN2+
TIN14	14	G5	Green Pixels Display Data (MSB)		
TIN15	15	B0	Blue Pixels Display Data (LSB)		
TIN18	19	B1	Blue Pixels Display Data		
TIN19	20	B2	Blue Pixels Display Data		
TIN20	22	B3	Blue Pixels Display Data	TOUT3- TOUT3+	
TIN21	23	B4	Blue Pixels Display Data		
TIN22	24	B5	Blue Pixels Display Data (MSB)		
TIN24	27	Hsync	Horizontal Synchronization Signal		
TIN25	28	Vsync	Vertical Synchronization Signal		
TIN26	30	DE	Compound Synchronization Signal	TCLK OUT- TCLK OUT+	No.11 No.12 CLK IN- CLK IN+
TIN27	50	NC	Non Connection (open)		
TIN5	2	NC	Non Connection (open)		
TIN10	8	NC	Non Connection (open)		
TIN11	10	NC	Non Connection (open)		
TIN16	16	NC	Non Connection (open)		
TIN17	18	NC	Non Connection (open)		
TIN23	25	NC	Non Connection (open)		
CLK IN	31	NCLK	Data Sampling Clock		





256k (k=1024) COLORS COMBINATION TABLE

	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0	B5 B4 B3 B2 B1 B0	Gray Scale Level
Basic Color	Black	L L L L L L	L L L L L L	L L L L L L	-
	Blue	L L L L L L	L L L L L L	H H H H H H	-
	Green	L L L L L L	H H H H H H	L L L L L L	-
	Light Blue	L L L L L L	H H H H H H	H H H H H H	-
	Red	H H H H H H	L L L L L L	L L L L L L	-
	Purple	H H H H H H	L L L L L L	H H H H H H	-
	Yellow	H H H H H H	H H H H H H	L L L L L L	-
	White	H H H H H H	H H H H H H	H H H H H H	-
Gray Scale of Red	Black	L L L L L L	L L L L L L	L L L L L L	L0
	Dark ↑ ↓ Light	L L L L L H	L L L L L L	L L L L L L	L1
		L L L L H L	L L L L L L	L L L L L L	L2
		⋮	⋮	⋮	L3... L60
		H H H H L H	L L L L L L	L L L L L L	L61
		H H H H H L	L L L L L L	L L L L L L	L62
	Red	H H H H H H	L L L L L L	L L L L L L	Red L63
Gray Scale of Green	Black	L L L L L L	L L L L L L	L L L L L L	L0
	Dark ↑ ↓ Light	L L L L L L	L L L L L H	L L L L L L	L1
		L L L L L L	L L L L H L	L L L L L L	L2
		⋮	⋮	⋮	L3... L60
		L L L L L L	H H H H L H	L L L L L L	L61
		L L L L L L	H H H H H L	L L L L L L	L62
	Green	L L L L L L	H H H H H H	L L L L L L	Green L63
Gray Scale of Blue	Black	L L L L L L	L L L L L L	L L L L L L	L0
	Dark ↑ ↓ Light	L L L L L L	L L L L L L	L L L L L H	L1
		L L L L L L	L L L L L L	L L L L H L	L2
		⋮	⋮	⋮	L3... L60
		L L L L L L	L L L L L L	H H H H L H	L61
		L L L L L L	L L L L L L	H H H H H L	L62
	Blue	L L L L L L	L L L L L L	H H H H H H	Blue L63
Gray Scale of White & Black	Black	L L L L L L	L L L L L L	L L L L L L	L0
	Dark ↑ ↓ Light	L L L L L H	L L L L L H	L L L L L H	L1
		L L L L H L	L L L L H L	L L L L H L	L2
		⋮	⋮	⋮	L3... L60
		H H H H L H	H H H H L H	H H H H L H	L61
		H H H H H L	H H H H H L	H H H H H L	L62
	White	H H H H H H	H H H H H H	H H H H H H	White L63

**FOR SAFETY**

LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions. In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA LCD MODULES". Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

**1) SPECIAL PURPOSES**

A) Toshiba's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.

B) Since Toshiba's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba's published specification limits.

C) In addition, since Toshiba Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

**2) DISASSEMBLING OR MODIFICATION**

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba does not warrant the module, if customer disassembled or modified it.

**3) BREAKAGE OF LCD PANEL**

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

**4) GLASS OF LCD PANEL**

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

**5) ELECTRIC SHOCK**

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

**6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT**

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

**7) DISPOSAL**

When dispose LCD module, obey to the applicable environmental regulations.