SAMSUNG

ELECTRONICS

TO

DATE : Apr. 29, 2008

SAMSUNG TFT-LCD

MODEL NO. : LTN184KT01-A01

NOTE : Extension code [-A01]
　　　　　　→ LTN184KT01-**A**01
　　　Surface type [**Glare**]

Any modification is not allowed without SEC's permission

APPROVED BY : *K. H. Shin*

PREPARED BY : *Kevin Kim*

LCD DEVELOPMENT G1. MOBILE DIVISION
SAMSUNG ELECTRONICS CO., LTD.

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김기홍05043549응용기술파트(Mobile)129250145 20080429113319

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Approval

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Apr. 29. 2008	A00	All	LTN184KT01-A01 model Preliminary spec was issued first.

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GENERAL DESCRIPTION

DESCRIPTION

LTN184KT01-A01 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight unit. The resolution of a 18.4" contains 1680X945 pixels and can display up to 262,144 colors. 6 O'clock direction is the optimum viewing angle.

FEATURES

- High contrast ratio, high aperture structure
- 1680 X 945 pixels resolution (16:9)
- High Color Gamut (Typical 60%)
- Low power consumption
- Fast Response
- Single CCFL
- DE(Data enable) only mode
- 3.3V LVDS Interface
- Onboard EEDID chip
- RoHS Compliance
- Windows Vista Premium Logo compliance

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	408.24(H) x 229.635(V) (18.4" diagonal)	mm	
Driver element	a-Si TFT active matrix		
Display colors	262,144		
Number of pixel	1680 X 945	pixel	16 : 9
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.243(H) x 0.243(V) (TYP.)	mm	
Display Mode	Normally white		
Glass Thickness	0.5	mm	
Surface treatment	Haze 0, Hardness 3H		

Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal (H)	422.0	422.5	423.0	mm	
	Vertical (V)	245.5	246.0	246.5	mm	
	Depth (D)	-	6.2	6.5	mm	(1)
Weight		-	725	750	g	

Note (1) Measurement condition of outline dimension
. Equipment : Vernier Calipers
. Push Force : 500g ·f (minimum)

1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Storage temperate	TSTG	-20	60	°C	(1),(5)
Operating temperate (Temperature of glass surface)	TOPR	0	50	°C	(1),(5)
Shock (non-operating)	Snop	-	240	G	(2),(4)
Vibration (non-operating)	Vnop	-	2.41	G	(3),(4)

Note (1) Temperature and relative humidity range are shown in the figure below.
95 % RH Max. (40 °C $\geq$ Ta)
Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation

Relative Humidity (%RH)

Operating Range (40,90) (50,50.4) (60,27.7)

Storage Range

Temperature (°C)
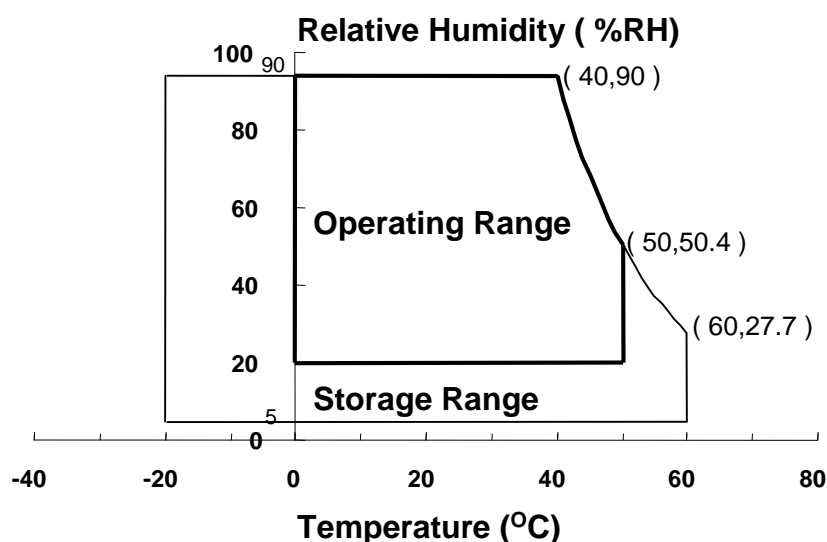
(2) 2ms, half sine wave, one time for ±X, ±Y, ± Z.
(3) 5 - 500 Hz, random vibration, 30min for X, Y, Z.
(4) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be
hard and rigid enough so that the Module would not be twisted or bent by the fixture.
(5) If product is used for extended time excessively or exposed to high temperatures for extended time,
there is a possibility of wide viewing angle film damage which could affect visual characteristics.

1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

$V_{DD} = 3.3V, V_{SS} = GND = 0V$

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	V_{DD} - 0.3	V_{DD} + 0.3	V	(1)
Logic Input Voltage	V_{IN}	V_{DD} - 0.3	V_{DD} + 0.3	V	(1)

Note (1) Within Ta (25 ± 2 °C)

(2) BACK-LIGHT UNIT

Ta = 25 ± 2 °C

Item	Symbol	Min.	Max.	Unit	Note
Lamp Current	I_L	2.0	7.0	mArms	(1)
Lamp frequency	F_L	40	80	kHz	(1)

Note 1) Permanent damage to the device may occur if maximum values are exceeded
Functional operation should be restricted to the conditions described under normal operating conditions.

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2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (5).
Measuring equipment : TOPCON BM-5A and PR-650

* Ta = 25 $\pm$ 2 °C, V_{DD}=3.3V,   fv= 60Hz, f_{DCLK} = 53.82MHz, I_L = 6.5 mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit	Note
Contrast Ratio (5 Points)		CR	Normal Viewing Angle $\phi = 0$ $\theta = 0$	500	800	-	-	(1), (2), (5)
Response Time at Ta (Rising + Falling)		T_{RT}		-	8	16	msec	(1), (3)
Average Luminance of White (5 Points)		$Y_{L,AVE}$		190	220	-	cd/m^2	I_L=6.5mA (1), (4)
Color Chromaticity (CIE)	Red	R_X		0.626	0.656	0.686	-	(1), (5) PR-650
		R_Y		0.297	0.327	0.357		
	Green	G_X		0.190	0.220	0.250		
		G_Y		0.645	0.675	0.705		
	Blue	B_X		0.116	0.146	0.176		
		B_Y		0.045	0.075	0.105		
	White	W_X		0.283	0.313	0.343		
		W_Y		0.299	0.329	0.359		
Viewing Angle	Hor.	θ_L	CR $\geq$ 10	40	45	-	Degrees	(1), (5) BM-5A
		θ_H		40	45	-		
	Ver.	ϕ_H		15	30	-		
		ϕ_L		35	15	-		
	Hor.	θ_L	CR $\geq$ 100	-	-	-		
		θ_H		-	-	-		
	Ver.	ϕ_H		-	-	-		
		ϕ_L		-	-	-		
13 Points/5 Points White Variation		δ_L		-	-	1.7/1.25	-	(6)

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Note 1) Definition of Viewing Angle : Viewing angle range(10 ≤ C/R)

Normal Line
$\phi = 0^o$, $\theta = 0^o$

θ_L

θ_R

ϕ_L ϕ_H

$\theta_L = 90^o$ x

y

12 O'clock
direction
$\phi_H = 90^o$

6 O'clock   y'
direction
$\phi_L = 90^o$

x'

$\theta_R = 90^o$
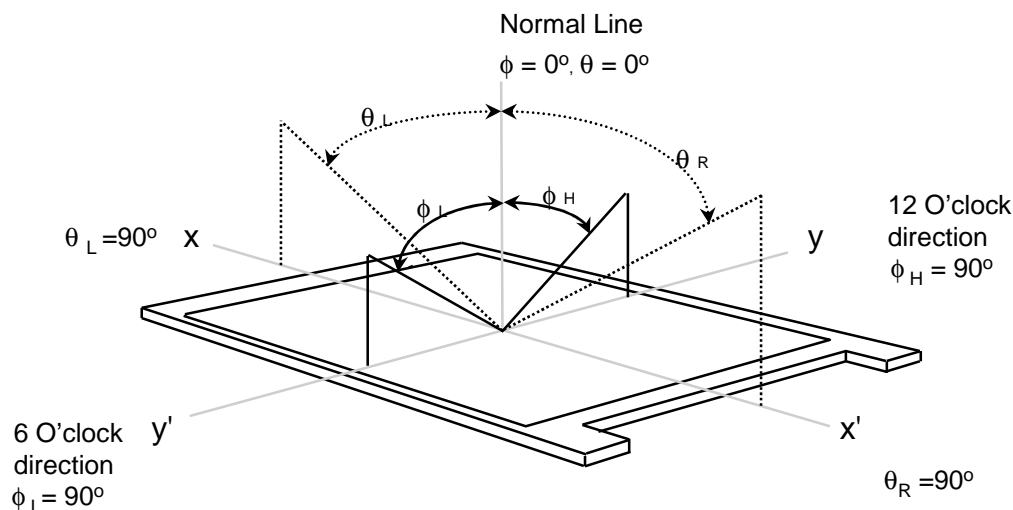
Note 2) Definition of Contrast Ratio (CR) : Ratio of gray max (Gmax) ,gray min (Gmin)
at 5 points(4, 5, 7, 9, 10)

$$CR = \frac{CR(4) + CR(5) + CR(7) + CR(9) + CR(10)}{5}$$

Points   : ④ , ⑤ , ⑦ , ⑨ , ⑩   at the figure of Note (6).

Note 3) Definition of Response time :

Display data   White(TFT OFF) | Black(TFT ON) | White(TFT OFF)

T_R T_F

Optical
Response

100%
90%

10%
0%

Time
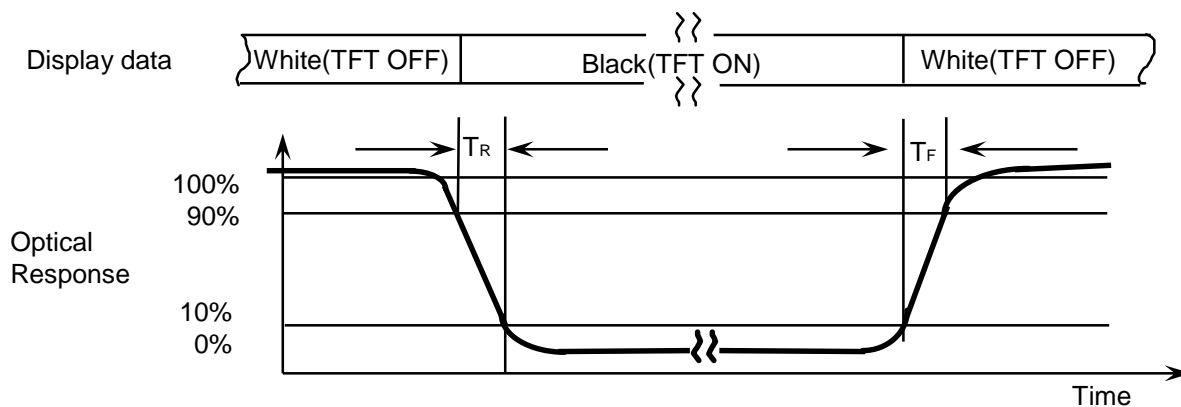
Note 4) Definition of Average Luminance of White : measure the luminance of white at 5 points.

(420) (840) (1260)

VIEW AREA

Average Luminance of White ($Y_{L,AVE}$)

$$Y_{L,AVE} = \frac{Y_{L4} + Y_{L5} + Y_{L7} + Y_{L9} + Y_{L10}}{5}$$

⑩   ⑨   (236)

⑦   (473)

⑤   ④   (709)
(lines)

◯ : test point

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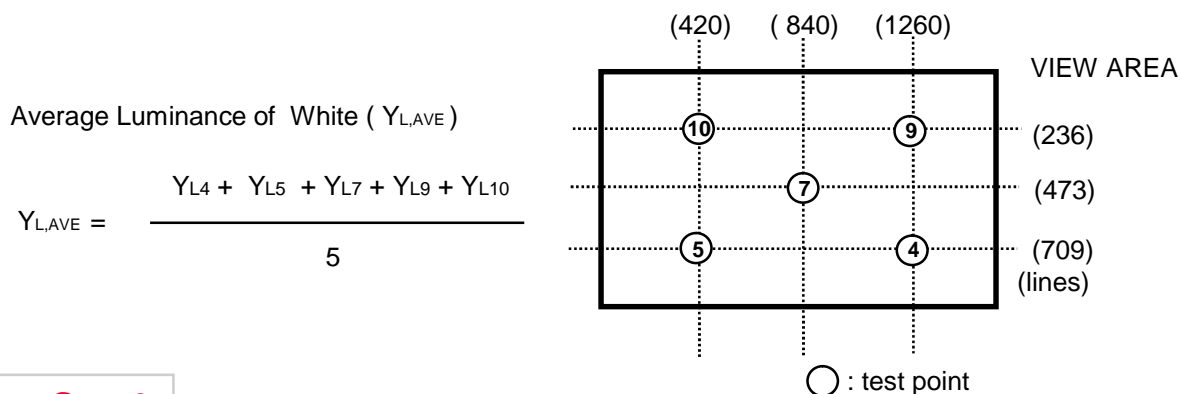
Note 5) After stabilizing and leaving the panel alone at a given temperature for 30 min , the measurement should be executed. Measurement should be executed in a stable, windless,and dark room. 30 min after lighting the backlight. This should be measured in the center of screen.
Lamp current : 6.5mA (Inverter : SIC-130T)
Environment condition : Ta = $25 \pm 2\,°C$

Photo-detector
(TOPCON SR3)

Field = $2°$

50 cm

TFT-LCD module

LCD panel

Center of the screen

[Optical characteristics measurement setup]
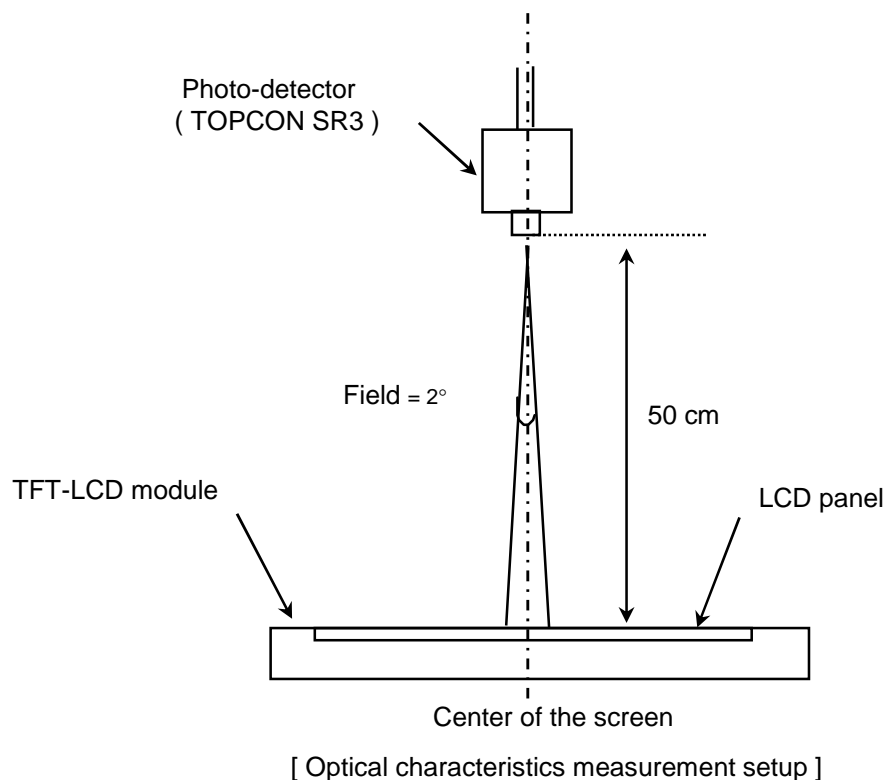
Note 6) Definition of 13 points white variation (δ L), CR variation(CVER) [① ~ ⑬]

$$\delta_L \ = \ \frac{\text{Maximum luminance of 13 points}}{\text{Minimum luminance of 13 points}}$$

10mm   10mm

10mm   420   840   1260

10mm

13   12   11

10   9   236

8   7   6   473   ◯ : test point

5   4   709
(lines)

3   2   1

10mm

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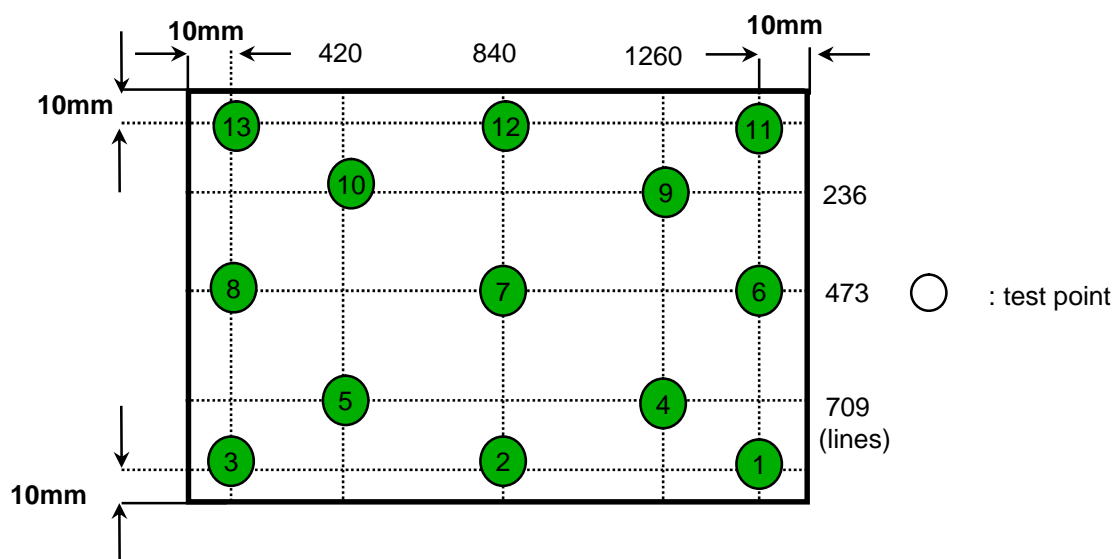
3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta= 25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	3.0	3.3	3.6	V	
Differential Input Voltage for LVDS Receiver Threshold	High	V_{IH}	-	-	+100	mV	V_{CM} = +1.2V
	Low	V_{IL}	-100	-	-	mV	
Vsync Frequency		fv	-	60	-	Hz	
Hsync Frequency		f_H	-	58.5	-	KHz	
Main Frequency		f_{DCLK}	-	60.84	-	MHz	2CH
Rush Current		I_{RUSH}	-	-	1.5	A	(4)
Current of Power Supply	White	I_{DD}	-	500	-	mA	(2),(3)*a
	Mosaic		-	650	-	mA	(2),(3)*b
	V. stripe		-	780	900	mA	(2),(3)*c

Note (1) Display data pins and timing signal pins should be connected.(GND = 0V)

(2) f_V = 60Hz, f_{DCLK} = 53.82 MHZ, V_{DD} = 3.3V , DC Current.

(3) Power dissipation pattern

*a) White Pattern

*b) Mosaic Pattern

VIEW AREA

Display Brightest Gray Level

Display Darkest Gray Level
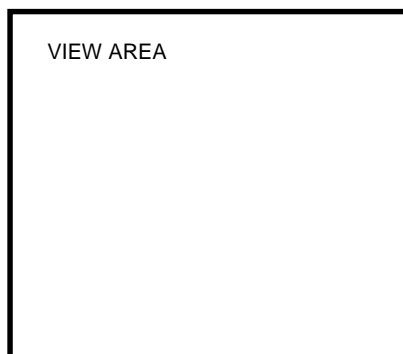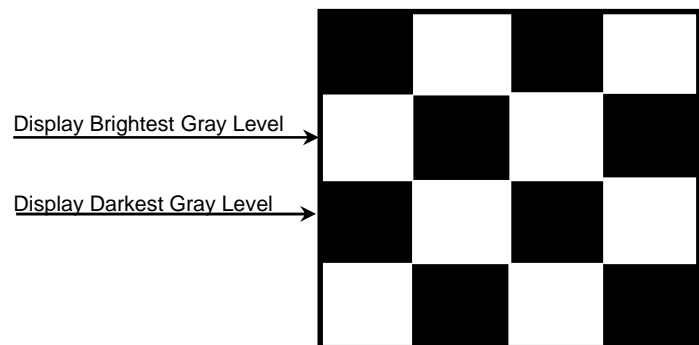
*c) 1dot Vertical stripe pattern

R	G	B	R	G	B	R	G	B	R	G
R	G	B	R	G	B	R	G	B	R	G
R	G	B	R	G	B	R	G	B	R	G
R	G	B	R	G	B	R	G	B	R	G
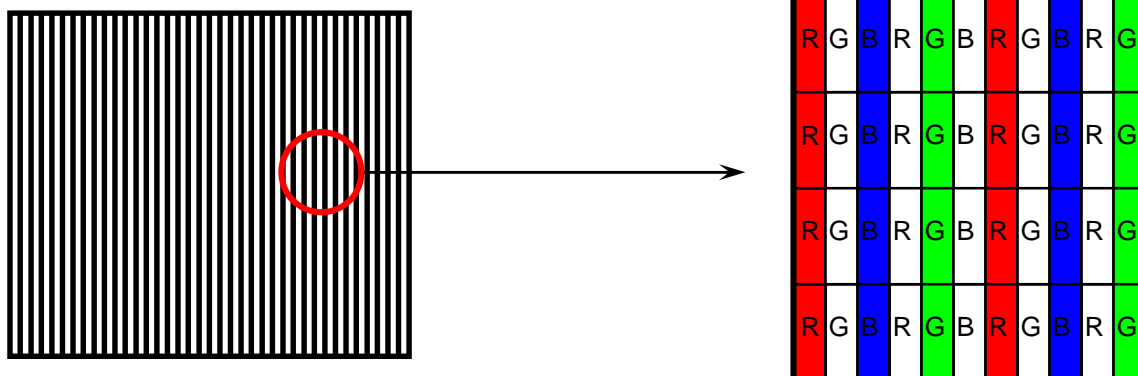
4) Rush current measurement condition

3.3V

M1
2SK1059

R1
47K

FUSE

C1
1uF

V_{DD} (LCD INPUT)

CONTROL SIGNAL
(HIGH to LOW)

R2
1K

M2
2SK1399

12V

R3
47K

C2
10000pF

C3
1uF

V_{DD} rising time is 470us

0.9V_{DD} 3.3V

0.1V_{DD}

GND

470us

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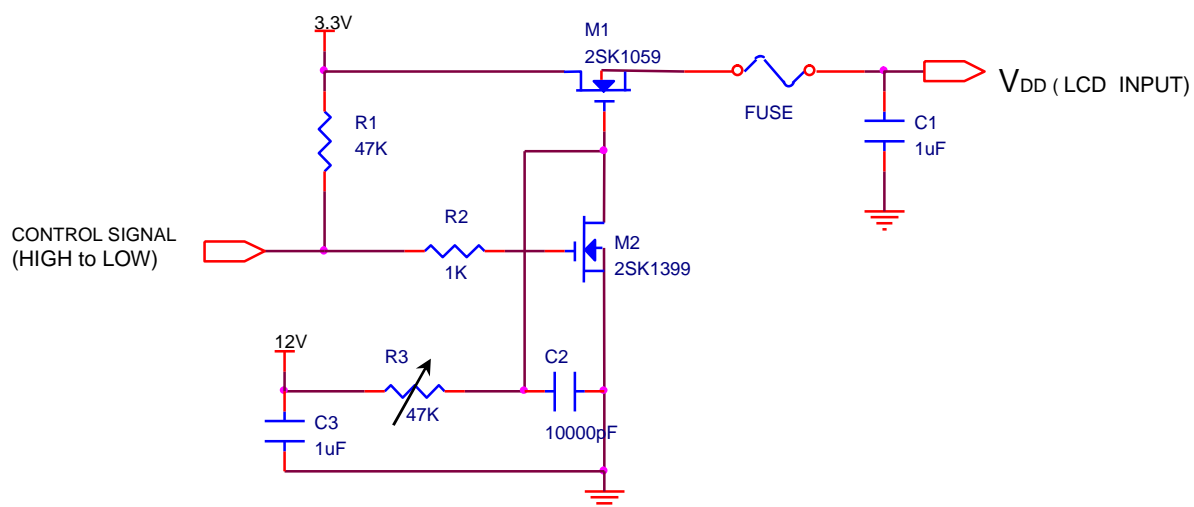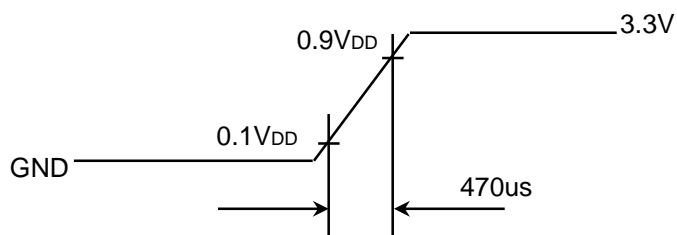
3.2 BACK-LIGHT UNIT

The backlight system is an edge-lighting type with single CCFL (Cold Cathode Fluorescent Lamp).
The characteristics of a single lamp are shown in the following tables.

- INVERTER : (TBD)   Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	4.0	6.5	7.0	mArms	(1)
Lamp Voltage	V_L	-	720	-	Vrms	I_L = 6.0mA
Frequency	f_L	40	60	65	KHz	(2)
Power Consumption	P_L	-	4.32		W	(3) I_L = 6.5mA
Operating Life Time	Hr	12,000	-	-	Hour	(4)
Startup Voltage	V_S	-	-	1350	Vrms	25°C, (5)
				1670	Vrms	0°C, (5)

Note) The waveform of the inverter output voltage must be area symmetric and the design of the inverter must have specifications for the modularized lamp.

The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter(miss lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Note (1) Lamp current is measured with a high frequency current meter as shown below.
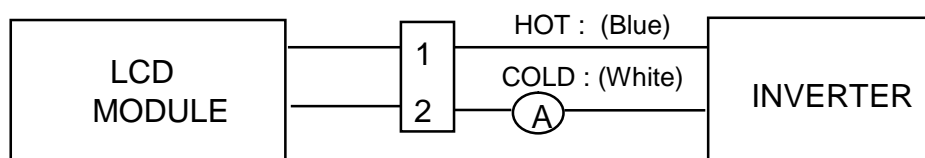
(2) Lamp frequency may produce interference with horizontal synchronous frequency and this may cause line flow on the display. Therefore lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

(3) Refer to $I_L \times V_L$ to calculate.

(4) Life time (Hr) of a lamp can be defined as the time in which it continues to operate under the condition Ta= 25 ± 2 °C and I_L = 6.5 mArms until one of the following event occurs.

1. When the brightness becomes 50% or lower than the original.
2. When the Effective ignition length becomes 80% or lower than the original value.
   (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)

(5) The inverter open voltage - this voltage should be measured after ballast capacitor- have to be larger than the lamp startup voltage, otherwise backlight may has blinking for a moment after turns on or not be turned on.
If an inverter has shutdown function it should keep its open voltage for longer than 1 second even if lamp connector open.

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4. BLOCK DIAGRAM

4.1 TFT LCD Module

EDID Chip

User-Connector
FI-XB30S-HF10
or Compatible

LVDS

LVDS Receiver &
Timing Controller

RSDS

DC-DC
Converter

Gamma
Generator

VCOM
Generator

Eagle
Chip

Source
Driver
IC

18.4" HD+
(1680*3*945)
TFT-LCD Panel

SOURCE PCB

→ Video Signal
→ Control Signal
→ VCOM
→ Gamma
→ DVDD
→ AVDD
→ Von/Voff
→ Gate Signal

4.2 BACK-LIGHT UNIT

Reflector

LAMP

1 HOT (Blue)

2 COLD (White)

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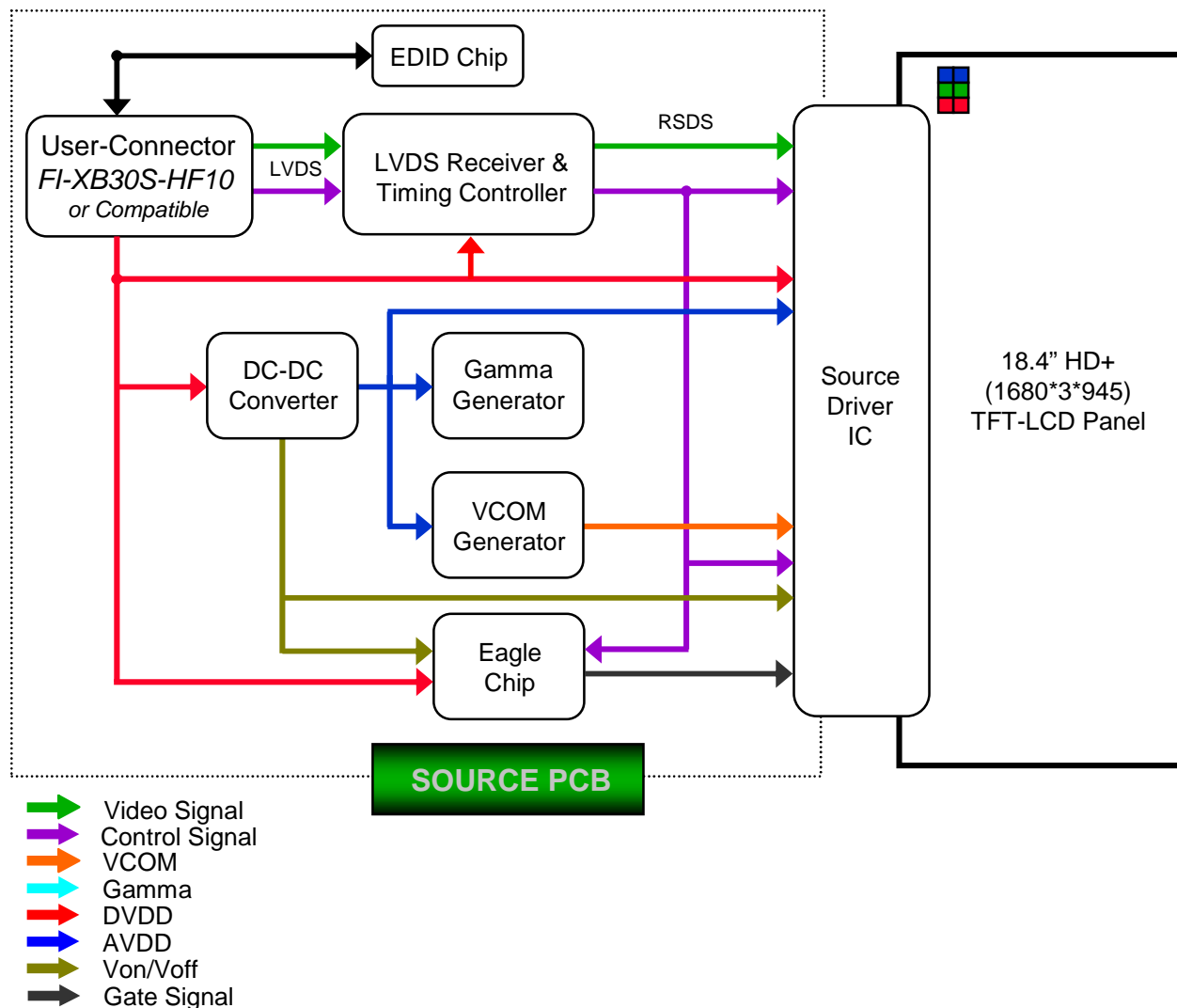
5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power (LVDS, Connector : JAE FI-XB30SRLZ-HF11 or compatible)
Mating Connector : JAE FI-X30M or compatible)

No.	Symbol	Function	Polarity	Remarks
1	VSS	Ground		
2	VDD	POWER SUPPLY +3.3V		
3	VDD	POWER SUPPLY +3.3V		
4	VEEDID	DDC 3.3V Power		
5	NC	No Connection		
6	CLKEDID	DDC Clock		
7	DATAEDID	DDC data		
8	O_RxIN0-	LVDS Differential Data INPUT (Odd R0-R5,G0)	Negative	
9	O_RxIN0+	LVDS Differential Data INPUT (Odd R0-R5,G0)	Positive	
10	GND	Ground		
11	O_RxIN1-	LVDS Differential Data INPUT (Odd G1-G5,B0-B1)	Negative	
12	O_RxIN1+	LVDS Differential Data INPUT (Odd G1-G5,B0-B1)	Positive	
13	GND	Ground		
14	O_RxIN2-	LVDS Differential Data INPUT (Odd B2-B5,Sync,DE)	Negative	
15	O_RxIN2+	LVDS Differential Data INPUT (Odd B2-B5,Sync,DE)	Positive	
16	GND	Ground		
17	O_RxCLK-	LVDS Differential Data INPUT (Odd Clock)	Negative	
18	O_RxCLK+	LVDS Differential Data INPUT (Odd Clock)	Positive	
19	GND	Ground		
20	E_RxIN0-	LVDS Differential Data INPUT (Even R0-R5,G0	Negative	
21	E_RxIN0+	LVDS Differential Data INPUT (Even R0-R5,G0)	Positive	
22	GND	Ground		
23	E_RxIN1-	LVDS Differential Data INPUT (Even G1-G5,B0-B1)	Negative	
24	E_RxIN1+	LVDS Differential Data INPUT (Even G1-G5,B0-B1)	Positive	
25	GND	Ground		
26	E_RxIN2-	LVDS Differential Data INPUT (Even B2-B5,Sync,DE)	Negative	
27	E_RxIN2+	LVDS Differential Data INPUT (Even B2-B5,Sync,DE)	Positive	
28	GND	Ground		
29	E_RxCLK-	LVDS Differential Data INPUT (Even Clock)	Negative	
30	E_RxCLK+	LVDS Differential Data INPUT (Even Clock)	Positive	

5.2 LVDS Interface : Transmitter DS90CF363 or Compatible

LVDS for Odd pixel

Pin No.	Name	RGB Signal	Pin No.	Name	RGB Signal
44	TxIN0	RO0	12	TxIN11	GO5
45	TxIN1	RO1	13	TxIN12	BO0
47	TxIN2	RO2	15	TxIN13	BO1
48	TxIN3	RO3	16	TxIN14	BO2
1	TxIN4	RO4	18	TxIN15	BO3
3	TxIN5	RO5	19	TxIN16	BO4
4	TxIN6	GO0	20	TxIN17	BO5
6	TxIN7	GO1	22	TxIN18	Hsync
7	TxIN8	GO2	23	TxIN19	Vsync
9	TxIN9	GO3	25	TxIN20	DE
10	TxIN10	GO4	26	TxCLK IN	Clock

LVDS for Even pixel

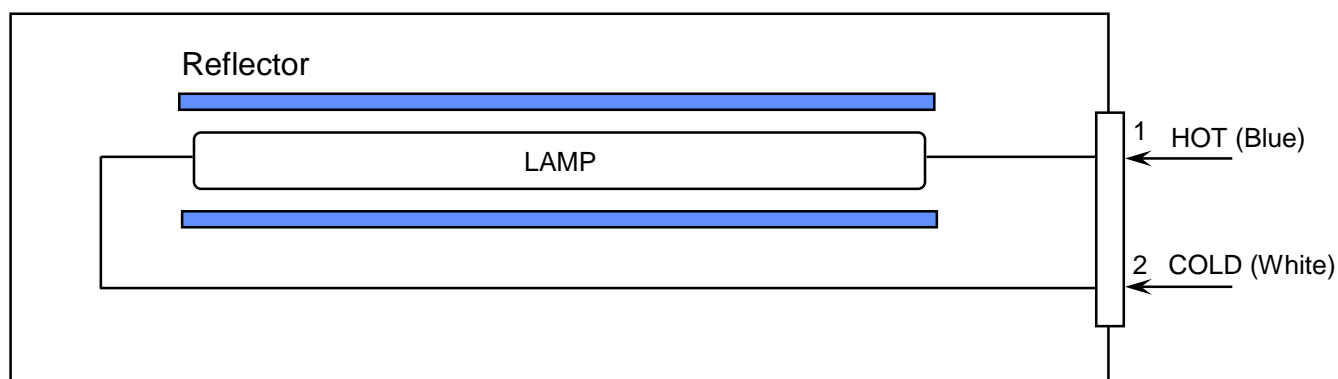| Pin No.	Name	RGB Signal	Pin No.	Name	RGB Signal
44	TxIN0	RE0	12	TxIN11	GE5
45	TxIN1	RE1	13	TxIN12	BE0
47	TxIN2	RE2	15	TxIN13	BE1
48	TxIN3	RE3	16	TxIN14	BE2
1	TxIN4	RE4	18	TxIN15	BE3
3	TxIN5	RE5	19	TxIN16	BE4
4	TxIN6	GE0	20	TxIN17	BE5
6	TxIN7	GE1	22	TxIN18	N/C
7	TxIN8	GE2	23	TxIN19	N/C
9	TxIN9	GE3	25	TxIN20	N/C
10	TxIN10	GE4	26	TxCLK IN	Clock

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Graphics controller 36-bit

ODD RED0	44
ODD RED1	45
ODD RED2	47
ODD RED3	48
ODD RED4	1
ODD RED5	3
ODD GREEN0	4
ODD GREEN1	6
ODD GREEN2	7
ODD GREEN3	9
ODD GREEN4	10
ODD GREEN5	12
ODD BLUE0	13
ODD BLUE1	15
ODD BLUE2	16
ODD BLUE3	18
ODD BLUE4	19
ODD BLUE5	20
Hsync	22
Vsync	23
Enable	25
CLOCK	26

DS90CF363 or compatible

TxOUT0- 41 → 8 O_RxIN0-   100 Ω
TxOUT0+ 40 → 9 O_RxIN0+
TxOUT1- 39 → 11 O_RxIN1-   100 Ω
TxOUT1+ 38 → 12 O_RxIN1+
TxOUT2- 35 → 14 O_RxIN2-   100 Ω
TxOUT2+ 34 → 15 O_RxIN2+
TxCLKOUT- 33 → 17 O_RxCLK-   100 Ω
TxCLKOUT+ 32 → 18 O_RxCLK+

FI-XB30SRLZ-HF11

EVEN RED0	44
EVEN RED1	45
EVEN RED2	47
EVEN RED3	48
EVEN RED4	1
EVEN RED5	3
EVEN GREEN0	4
EVEN GREEN1	6
EVEN GREEN2	7
EVEN GREEN3	9
EVEN GREEN4	10
EVEN GREEN5	12
EVEN BLUE0	13
EVEN BLUE1	15
EVEN BLUE2	16
EVEN BLUE3	18
EVEN BLUE4	19
EVEN BLUE5	20
N/C	22
N/C	23
N/C	25
CLOCK	26

DS90CF363 or compatible

TxOUT0- 41 → 20 E_RxIN0-   100 Ω
TxOUT0+ 40 → 21 E_RxIN0+
TxOUT1- 39 → 23 E_RxIN1-   100 Ω
TxOUT1+ 38 → 24 E_RxIN1+
TxOUT2- 35 → 26 E_RxIN2-   100 Ω
TxOUT2+ 34 → 27 E_RxIN2+
TxCLKOUT- 33 → 29 E_RxCLK-   100 Ω
TxCLKOUT+ 32 → 30 E_RxCLK+

Note : The LCD Module uses a 100ohm resistor between positive and negative lines of each receiver input.

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5.3 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1
Mating Connector : SM02B-BHSS-1(JST)

Pin NO.	Symbol	Color	Function
1	HOT	Blue	High Voltage
2	COLD	White	Low Voltage

5.4 Timing Diagrams of LVDS For Transmission

LVDS Receiver : Integrated T-CON

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김기홍05043549응용기술파트(Mobile)129250145 20080429113319

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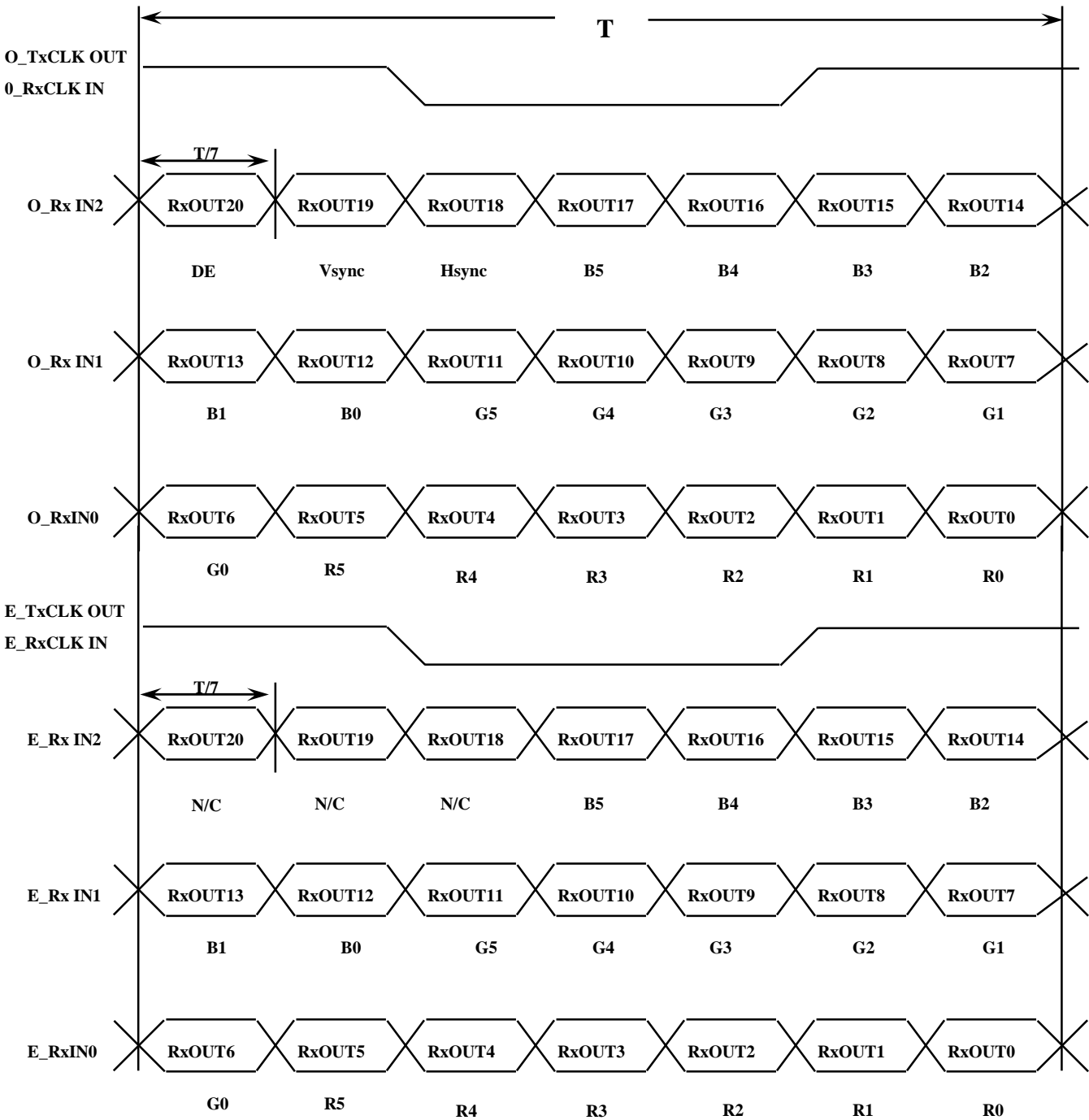
5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

| Color	Display	Red						Green						Blue						Gray Scale Level
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	45	B5	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	-
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	-
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	-
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	-
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	-
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	-
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
Gray Scale Of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	Dark	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	↑	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R61
	Light	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R62
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R63
Gray Scale Of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	Dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	G1
	↑	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	G2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	G61
	Light	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	G62
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	G63
Gray Scale Of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B1
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	B2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	B61
	Light	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	B62
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B63

Note 1) Definition of gray :
Rn: Red gray, Gn: Green gray, Bn: Blue gray (n=gray level)
Note 2)Input signal: 0 =Low level voltage, 1=High level voltage

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5.6 Pixel Format in the display
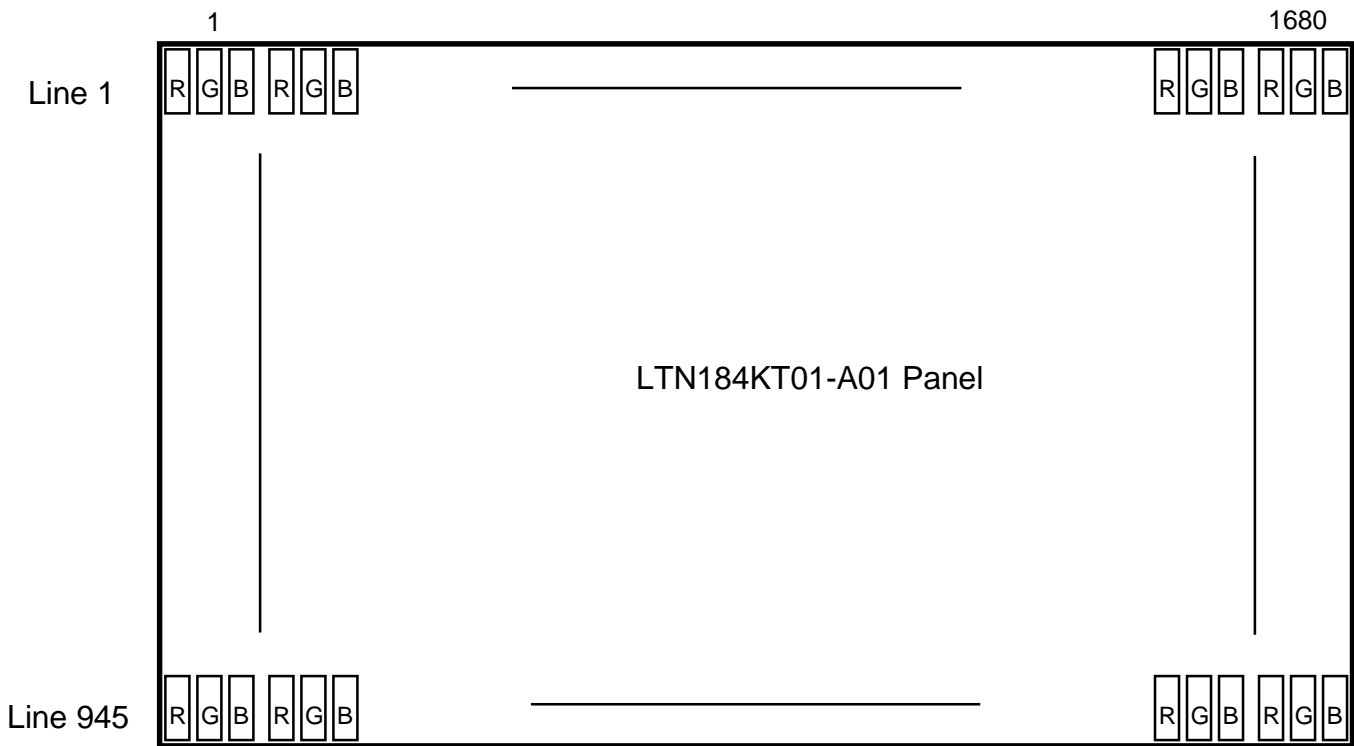
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6. INTERFACE TIMING

6.1 Timing Parameters

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	TV	-	975	-	Lines	
Vertical Active Display Term	Display Period	TVD	-	945	-	Lines	
One Line Scanning Time	Cycle	TH	-	2080	-	Clocks	2ch
Horizontal Active Display Term	Display Period	THD	-	1680	-	Clocks	2ch

6.2 Timing diagrams of interface signal

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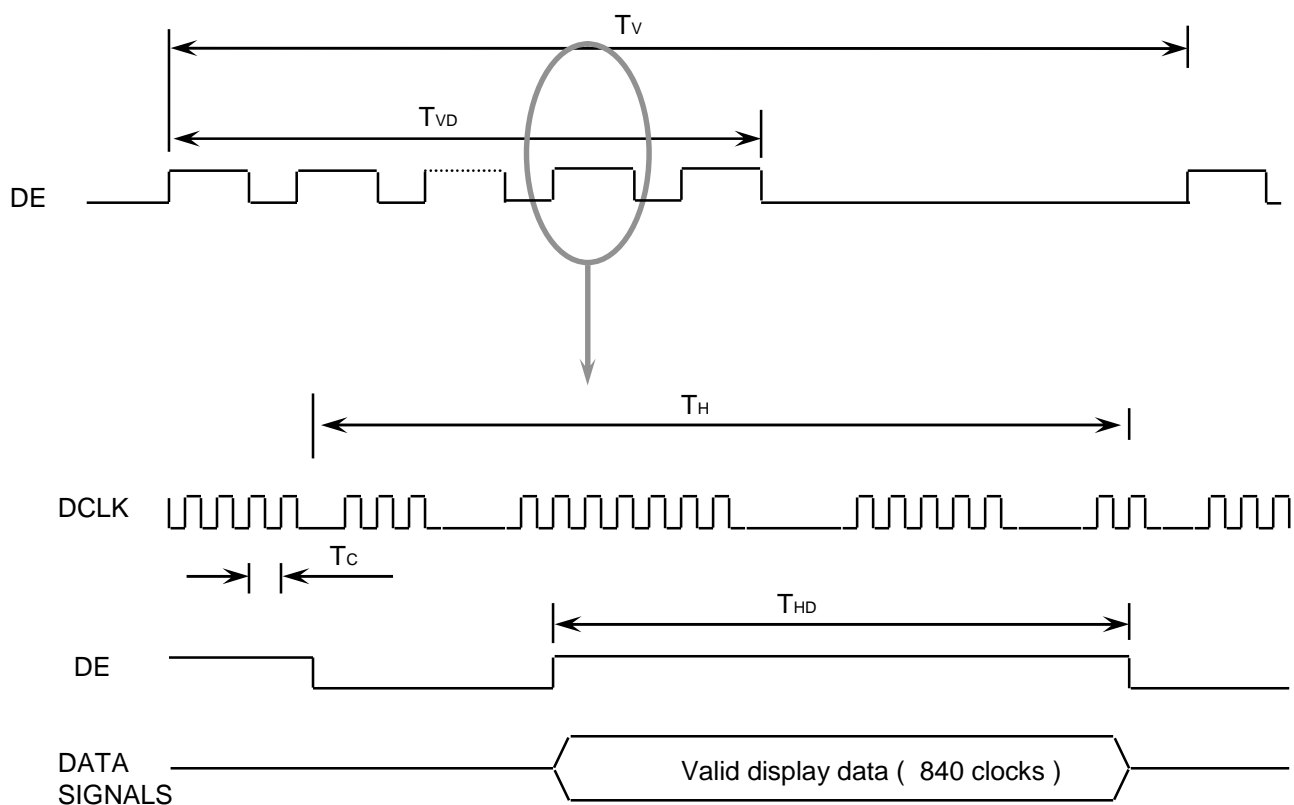
6.3 Power ON/OFF Sequence

: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

Power Supply VDD

0.9 VDD
0.1 VDD
0V

0.5 < T₁ ≤ 10 msec
0 < T₂ ≤ 50 msec
0 < T₃ ≤ 50 msec
500 msec ≤ T₄

Signals
0 V
VALID

Power On Power Off

Back-light
200 msec ≤ T₅
200 msec ≤ T₆

50% 50%

T₅ T₆

Power ON/OFF Sequence

T1 : Vdd rising time from 10% to 90%
T2 : The time from Vdd to valid data at power ON.
T3 : The time from valid data off to Vdd off at power Off.
T4 : Vdd off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

NOTE.
(1) The supply voltage of the external system for the module input should be the same as the definition of VDD.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of VDD = off level, please keep the level of input signals on the low or keep a high impedance.

(4) T4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

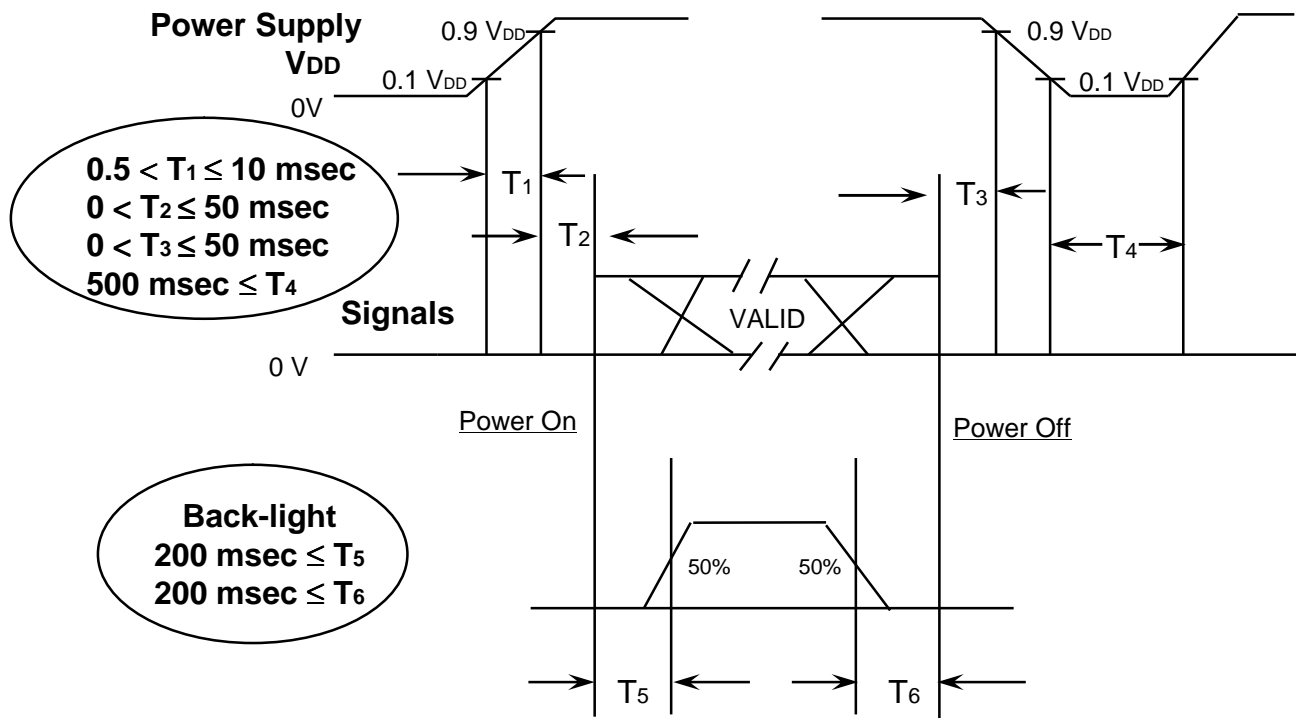**7. Mechanical Outline Dimension**

[Refer to the next page]

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422.5±0.5 (OUTLINE)

(212.15)

6.3
7.7325

412.1 (BEZEL OPENING)
408.24 (ACTIVE AREA)

6.1
8.03

(122.55)

232.5 (BEZEL OPENING)
229.635 (ACTIVE AREA)

246±0.5 (OUTLINE)

212.5±0.3

ACTIVE AREA

31 ±1

112.5±5 (WIRE LENGTH)

6.5 MAX.

3.75 ±0.3

16.3±0.3

58.25±0.3

155.25±0.3

(35.39)　(58.32)

6.05
(2.9)
(10.8)
(14)
(26.5)

150 ±1

2.5 MAX (SCREW PENETRATION)

|A-B| < 1.0 (mm)
|C-D| < 1.0 (mm)
|BM| >= 1.0 (mm)

C

BEZEL OPENING

A   B

ACTIVE AREA

Black Matrix

D

BLACK MATRIX Dimension

* NOTE

1. SIGNAL INTERFACE CONNECTOR TO BE SPECIFIED AS BELOW.
   - MAKER : JAE
   - PART NO : FI-XB30SRLZ-HF11 COMPATIBLE

2. CCFT CONNECTOR FOR BACKLIGHT TO BE SPECIFIED AS BELOW.
   - MAKER : JST
   - PART NO : BHSR-02VS-1 COMPATIBLE
   - LAMP DIAMETER : D2.0

3. CALIFERS MEASURING FORCE :750 ± 250 gf

4. ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 2.5mm MAX

5. WEIGHT SPEC : 750 g MAX

6. USER HOLE TORQUE SPEC : 3.0kgf-cm MAX

184KT01-A01 Exploded View

| Item	Specification
Chassis-Top	SUS,0.4T
Assy-Panel	Glass,0.5T
Sheet-DBEF	DBEF2,T0.2
Sheet-prism(U)	CM30-155,T0.155
Sheet-prism(L)	CM30-210,T0.210
Sheet-diffuser	CH142NB,T0.145
LGP	T2.8/0.7,Normal
Sheet-Reflector	UX225,T0.225
Frame-Mold	PC
Assy-Lamp	L416.8,D2.0
Cover-Lamp	ZR,T0.14
Cover-Back	SUS409HD,T0.3
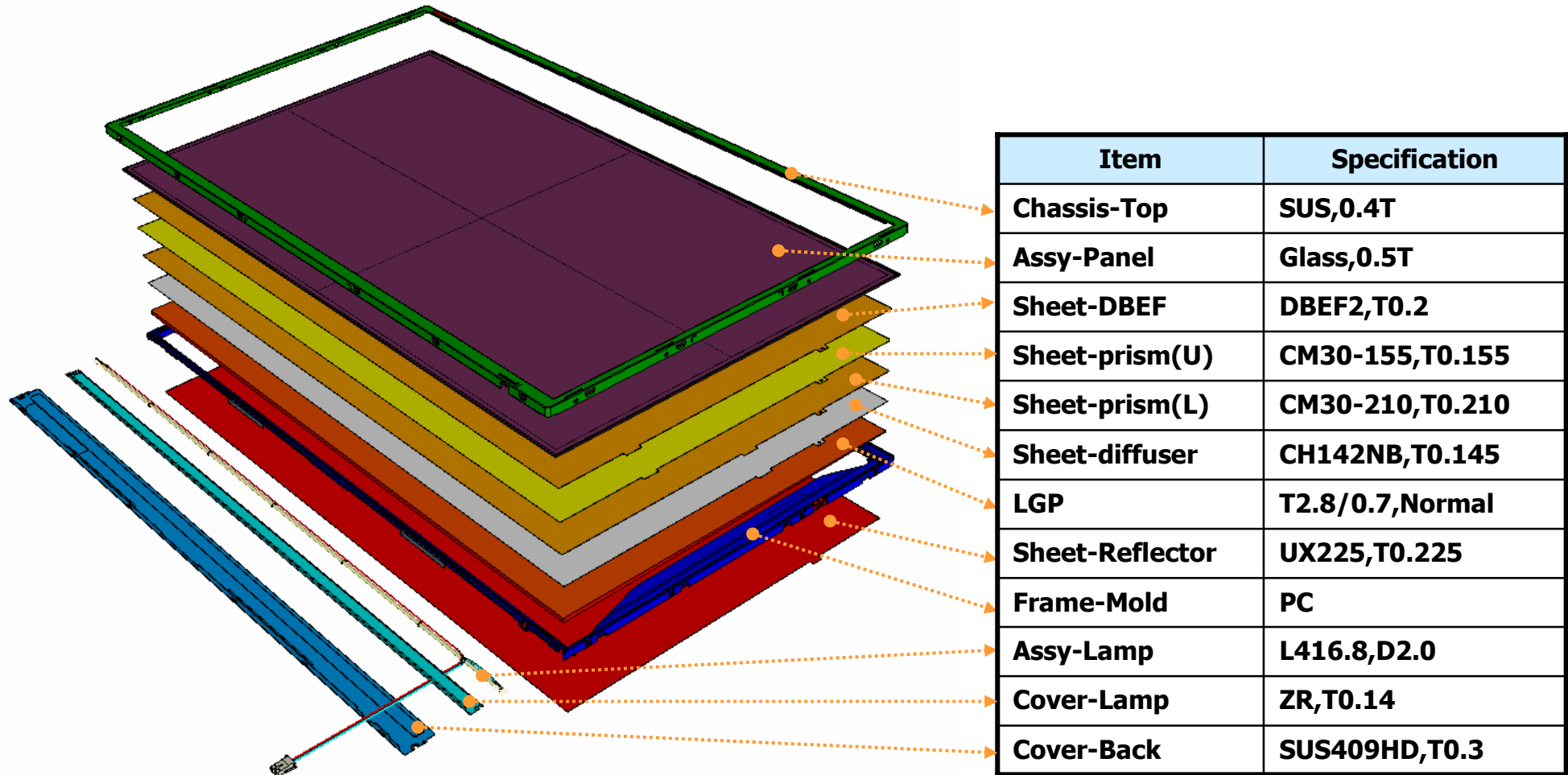
8. PACKING

1. CARTON(Internal Package)

(1) Packing Form
Corrugated Cardboard box and Corrupad form as shock absorber

(2) Packing Method

CUSHION CAP

PANEL

CUSHION PAD
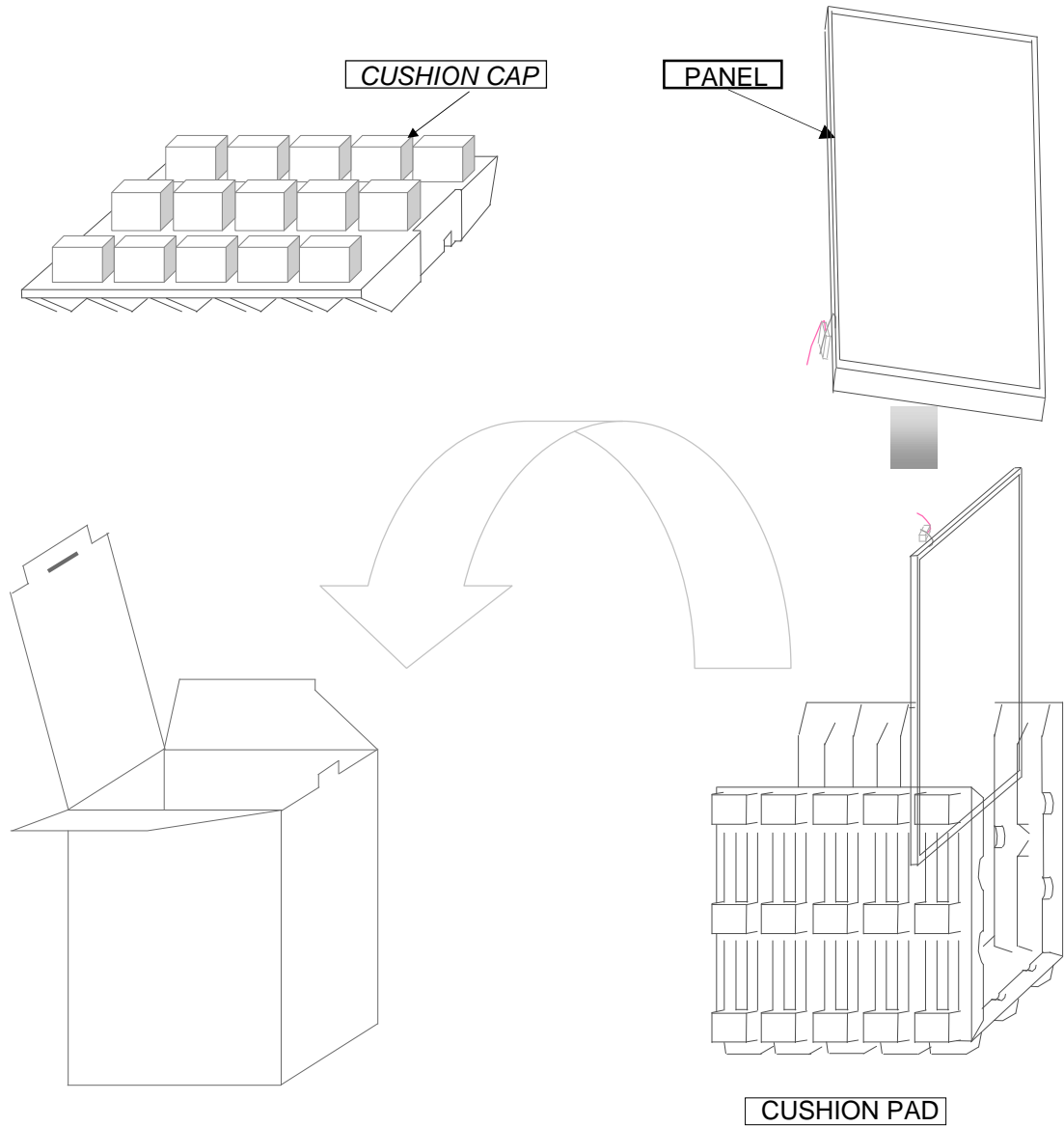
Note 1)Total Weight : Approximately 8.95 kg

2) Acceptance number of piling : 10 sets

3) Carton size : 380×350×492(mm)

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(3)Packing Material

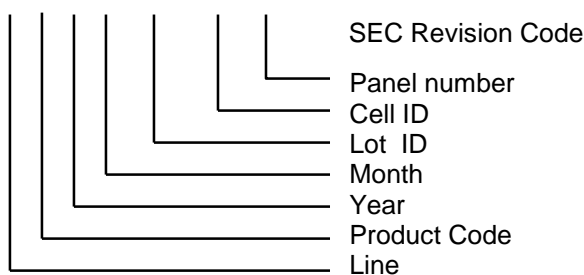| No	Part name	Quantity
1	Static electric protective sack	10 pcs
2	Packing case (Inner box) included shock absorber	1 set
3	Pictorial marking	2 pcs
4	Carton	1 set

9. MARKINGS & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1)Parts number : LTN184KT01
(2)Revision code : 3 letters
(3)Lot number   : X X X X   XX XX XX   A01

SEC Revision Code

Panel number
Cell ID
Lot ID
Month
Year
Product Code
Line

(4) Nameplate Indication

LTN184KT01

0710   XXXXXXXXX   A01

40 mm

80 mm

MADE IN CHINA
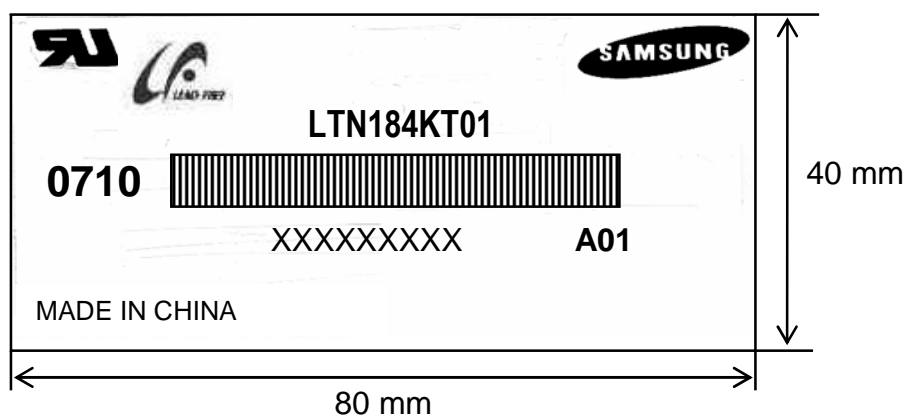
Parts name   : LTN184KT01
Lot number   : XXXXXXXXXX
Inspected work week   : 0710(2007 year 10th week)
Product Revision Code : A01

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High voltage caution label

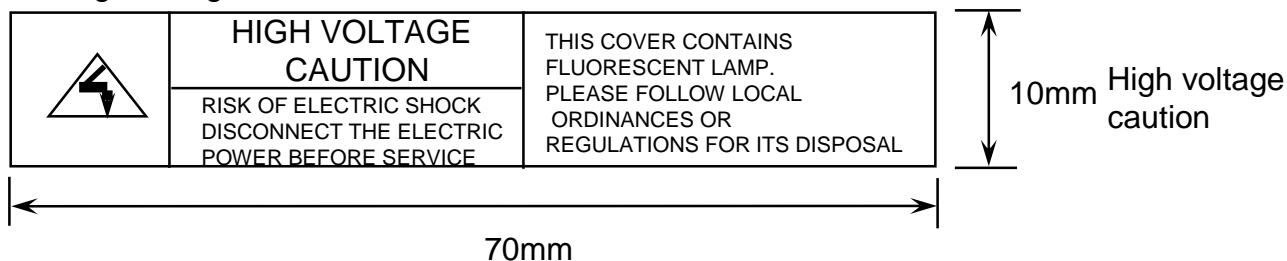| ⚡	**HIGH VOLTAGE CAUTION** RISK OF ELECTRIC SHOCK DISCONNECT THE ELECTRIC POWER BEFORE SERVICE	THIS COVER CONTAINS FLUORESCENT LAMP. PLEASE FOLLOW LOCAL ORDINANCES OR REGULATIONS FOR ITS DISPOSAL

10mm High voltage caution

70mm

(6) Packing small box attach

SAMSUNG
ELECTRONICS

|||||||||||||||||||||||||||||||

DEVICE   : LTN184KT01
TYPE   : A01
QUANTITY   : 10 PCS

||||||||||||||||||||||||||||

CO6040001
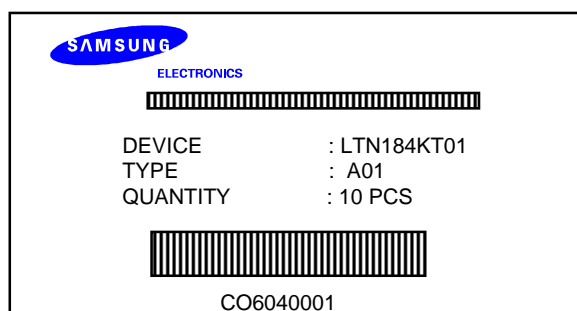
(7) Packing box Marking : Samsung TFT-LCD Brand Name

wise view™

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10. GENERAL PRECAUTIONS

1. Handling

   (a) When the module is assembled, It should be attached to the system firmly
      using every mounting holes. Be careful not to twist and bend the modules.

   (b) Refrain from strong mechanical shock and / or any force to the module. In addition to
      damage, this may cause improper operation or damage to the module and CCFT backlight.

   (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch
      the surface harder than a HB pencil lead.

   (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time,
      Staining and discoloration may occur.

   (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

   (f) The desirable cleaners are water, IPA(Isoprophyl Alcohol) or Hexane.
      Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl
      chloride. It might permanent damage to the polarizer due to chemical reaction.

   (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or
      mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly
      with soap.

   (h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.

   (i) Use fingerstalls with soft gloves in order to keep display clean during the incoming
      inspection and assembly process.

   (j) Do not disassemble the module.

   (k) Do not pull or fold the lamp wire.

   (l) Do not adjust the variable resistor which is located on the back side.

   (m) Protection film for polarizer on the module shall be slowly peeled off just before use so
      that the electrostatic charge can be minimized.

   (n) Pins of I/F connector shall not be touched directly with bare hands.

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2. STORAGE

(a) Do not leave the module in high temperature, and high humidity for a long time.
It is highly recommended to store the module with temperature from 0 to 35 °C and
relative humidity of less than 70%.

(b) Do not store the TFT-LCD module in direct sunlight.

(c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent
light during the store.

3. OPERATION

(a) Do not connect,disconnect the module in the " Power On" condition.

(b) Power supply should always be turned on/off by following item 6.3
" Power on/off sequence ".

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic
interference shall be done by system manufacturers. Grounding and shielding methods
may be important to minimize the interference.

(d) The cable between the backlight connector and its inverter power supply shall be a
minimized length and be connected directly . The longer cable between the backlight
and the inverter may cause lower luminance of lamp(CCFT) and may require higher
startup voltage(Vs).

(e) The standard limited warranty is only applicable when the module is used for general
notebook applications. If used for purposes other than as specified, SEC is not to be
held reliable for the defective operations. It is strongly recommended to contact SEC
to find out fitness for a particular purpose.

4. OTHERS

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection
of electrode.

(c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input
voltage variation, variation in part contents and environmental temperature, so on)
Otherwise the module may be damaged.

(d) If the module displays the same pattern continuously for a long period of time,it can be
the situation when the image "sticks" to the screen.

(e) This module has its circuitry PCB's on the rear side and should be handled carefully in
order not to be stressed.

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11. EDID

Address (HEX)	FUNCTION	Value HEX	BIN	DEC	ASCII or Data	Notes
00	Header	00	00000000	0		EDID Header
01		FF	11111111	255		
02		FF	11111111	255		
03		FF	11111111	255		
04		FF	11111111	255		
05		FF	11111111	255		
06		FF	11111111	255		
07		00	00000000	0		
08	ID Manufacturer Name	4C	01001100	76	S E C	3 character ID
09		A3	10100011	163		"SEC"
0A	ID Product Code	4F	01001111	79	[O]	
0B		30	00110000	48	[0]	
0C	32-bit serial no.	00	00000000	0		
0D		00	00000000	0		
0E		00	00000000	0		
0F		00	00000000	0		
10	Week of manufacture	00	00000000	0		
11	Year of manufacture	11	00010001	17	2007	2007
12	EDID Structure Ver.	01	00000001	1	1	EDID Ver. 1.0
13	EDID revision #	03	00000011	3	3	EDID Rev. 3
14	Video input definition	80	10000000	128		
15	Max H image size	29	00101001	41	41	41 cm(approx)
16	Max V image size	17	00010111	23	23	23 cm(approx)
17	Display Gamma	78	01111000	120	2.2	Gamma 2.2
18	Feature support	0A	00001010	10		
19	Red/green low bits	8A	10001010	138		10001010
1A	Blue/white low bits	D5	11010101	213		11010101
1B	Red x/ high bits	96	10010110	150	0.588	Red x 0.588= 1001011010
1C	Red y	57	01010111	87	0.340	Red y 0.340= 0101011100
1D	Green x	54	01010100	84	0.330	Green x 0.330= 0101010010
1E	Green y	89	10001001	137	0.537	Green y 0.537= 1000100010
1F	Blue x	26	00100110	38	0.151	Blue x 0.151= 0010011011
20	Blue y	1F	00011111	31	0.122	Blue y 0.122= 0001111101
21	White x	50	01010000	80	0.313	White x 0.313= 0101000001
22	White y	54	01010100	84	0.329	White y 0.329= 0101010001
23	Established timing 1	00	00000000	0		
24	Established timing 2	00	00000000	0		
25	Established timing 3	00	00000000	0		
26	Standard timing #1	01	00000001	1		not used
27		01	00000001	1		
28	Standard timing #2	01	00000001	1		not used
29		01	00000001	1		

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Addr	Description	Hex	Binary	Dec	Value	Comment
2A	Standard timing #3	01	00000001	1		not used
2B		01	00000001	1		
2C	Standard timing #4	01	00000001	1		not used
2D		01	00000001	1		
2E	Standard timing #5	01	00000001	1		not used
2F		01	00000001	1		
30	Standard timing #6	01	00000001	1		not used
31		01	00000001	1		
32	Standard timing #7	01	00000001	1		not used
33		01	00000001	1		
34	Standard timing #8	01	00000001	1		not used
35		01	00000001	1		
36	Detailed timing/monitor descriptor #1	88	10001000	136	121.68	Main clock= 121.68(60.84 *2 MHz)
37		2F	00101111	47		
38		90	10010000	144	1680	Hor active=840*2 pixels
39		90	10010000	144	400	Hor blanking=400 pixels
3A		61	01100001	97		4bit : 4bit
3B		B1	10110001	177	945	Vertcal active=945 lines
3C		1E	00011110	30	30	Vertical blanking=30 lines
3D		30	00110000	48		4bit : 4bit
3E		30	00110000	48	48	Hor sync. Offset=48 pixels
3F		20	00100000	32	32	H sync. Width=32 pixels
40		26	00100110	38	2	V sync. Offset=2 lines
					6	V sync. Width=6 lines
41		00	00000000	0		2bit : 2bit :2bit :2bit
42		99	10011001	153	409	H image size= 409 mm(approx)
43		E6	11100110	230	230	V image size = 230 mm(approx)
44		10	00010000	16		
45		00	00000000	0		No Horizontal Border
46		00	00000000	0		No Vertical Border
47		19	00011001	25		
48	Detailed timing/monitor descriptor #2	00	00000000	0		Manufacturer Specified (Timing)
49		00	00000000	0		
4A		00	00000000	0		
4B		0F	00001111	15		
4C		00	00000000	0		
4D		00	00000000	0		**Value**=HSPWmin / 2
4E		00	00000000	0		**Value**=HSPWmax / 2
4F		00	00000000	0		**Value**=Thbpmin /2
50		00	00000000	0		**Value**=Thbpmax /2
51		00	00000000	0		**Value**=VSPWmin /2
52		00	00000000	0		**Value**=VSPWmax /2
53		00	00000000	0		**Value**=Tvbpmin / 2
54		00	00000000	0		**Value**=Tvbpmax / 2
55		32	00110010	50		Thpmin=**value***2 + HA pixelclks
56		A0	10100000	160		Thpmax=**value***2 + HA pixelclks
57		05	00000101	5		Tvpmin=**value***2 + VA lines
58		1B	00011011	27		Tvpmax=**value***2 + VA lines
59		00	00000000	0		Module revision

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5A		00	00000000	0		ASCII Data String Tag
5B		00	00000000	0		
5C		00	00000000	0		
5D		FE	11111110	254		
5E		00	00000000	0		
5F		53	01010011	83	[S]	
60		41	01000001	65	[A]	
61	Detailed timing/monitor	4D	01001101	77	[M]	
62	descriptor #3	53	01010011	83	[S]	
63		55	01010101	85	[U]	
64		4E	01001110	78	[N]	
65		47	01000111	71	[G]	
66		0A	00001010	10	[^]	
67		20	00100000	32	[ ]	
68		20	00100000	32	[ ]	
69		20	00100000	32	[ ]	
6A		20	00100000	32	[ ]	
6B		20	00100000	32	[ ]	
6C		00	00000000	0		Monitor Name Tag (ASCII)
6D		00	00000000	0		
6E		00	00000000	0		
6F		FE	11111110	254		
70		00	00000000	0		
71		4C	01001100	76	[L]	
72		54	01010100	84	[T]	
73	Detailed timing/monitor	4E	01001110	78	[N]	
74	descriptor #4	31	00110001	49	[1]	
75		38	00111000	56	[8]	
76		34	00110100	52	[4]	
77		4B	01001011	75	[K]	
78		54	01010100	84	[T]	
79		30	00110000	48	[0]	
7A		31	00110001	49	[1]	
7B		41	01000001	65	[A]	
7C		30	00110000	48	[0]	
7D		31	00110001	49	[1]	
7E	Extension Flag	00	00000000	0		
7F	Checksum	D8	11011000	216		

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