SPECIFICATION For APPROVAL

| () Preliminary S _I () Final Specificat | | | |
|---|-------------|---|---------------------|
| Title | 15.1" XG | A TFT LCD | |
| | | | |
| BUYER NAME | | SUPPLIER | LG Electronics Inc. |
| MODEL NAME | | MODEL NAME | LM151X2 |
| SIGNATURE / | DATE | APPROVED H. Yoon /G.Manage REVIEWED I. H. Ahn | BY |
| / | | /S.Enginee PREPARED G. T. Kim /S.Enginee | ву |

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Please

confirmation

Product Engineering Dept.

LCD Division LG Electronics Inc.



CONTENTS

| NO. | ITEM | Page |
|-----|--|-------|
| - | COVER | 1 |
| - | CONTENTS | 2 |
| - | RECORD of REVISION | 3 |
| 1. | GENERAL DESCRIPTION | 4 |
| 2. | MAXIMUM RATINGS | 5 |
| 3. | ELECTRICAL SPECIFICATIONS | 5 |
| 4. | OPTICAL SPECIFICATIONS | 6 |
| 5. | INTERFACE CONNECTIONS | 10 |
| 6. | SIGNAL TIMING SPECIFICATIONS | 11 |
| 7. | SIGNAL TIMING WAVEFORMS | 12 |
| 8. | COLOR INPUT DATA REFERENCE | 13 |
| 9. | POWER SEQUENCE | 14 |
| 10. | MECHANICAL CHARACTERISTICS | 15 |
| 11. | ENVIRONMENTAL SPECIFICATION | 16 |
| 12. | INCOMING INSPECTION STANDARD FOR TFT-LCD | 17~19 |
| 13. | DESIGNATION OF LOT MARK | 20 |
| 14. | PACKING FORM | 20 |
| 15. | PRECAUTIONS | 21 |
| 16. | APPENDIX 1. OUTLINE DRAWING | 22~23 |
| 17. | APPENDIX 2. PACKING ASSEMBLY DRAWING | 24~25 |
| 18. | APPENDIX 3. CLASSIFICATION OF DEFECTS | 26 |

Record of Revision

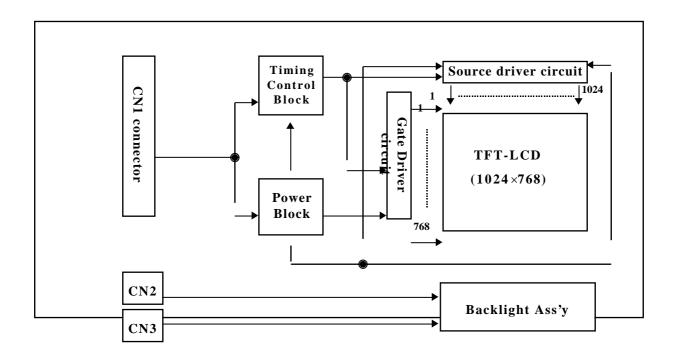
| Revision Version | Date | DESCRIPTION |
|------------------|----------------|---|
| 0.0 | April 13, 1998 | Preliminary |
| 0.1 | May 6, 1998 | Preliminary, Update (Changed Interface |
| | | Connections - Increased V_{DD} Lines, |
| | | Fixed Interface Connector Model Name, |
| | | Updated Outline Drawing) |
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1. General Description

The LG Electronics model LM151X2 LCD is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Tube(CCFT) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has a 15.1 inch diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

LM151X2 has been designed to apply the CMOS logic level interface.

The LM151X2 LCD is intended to support applications where high brightness, wide viewing angle, high color saturation, and high color depth are very important. In combination with the vertical arrangement of the sub-pixels, the LM151X2 characteristics provide an excellent flat panel display for office automation products such as monitors.



General Display Characteristics

The following are general features of the model LM151X2 LCD;

Active display area 15.1 inches(38cm) diagonal

Outsize dimensions 352.6w * 264.6h * 16.0t(typ)mm(Without Inverter and User

Connector)

Pixel pitch 0.30 mm °ø0.30 mm

Pixel format 1024 horiz. By 768 vert. pixels

RGB vertical stripe arrangement

Color depth 6-bit, 262,144 colors

Display operating mode transmissive mode, normally white

Surface treatments hard coating(3H),



anti-glare treatment of the front polarizer



2. Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1 ABSOLUTE MAXIMUM RATINGS

| Parameter | symbol | Va | llues | Units | Notes |
|---|--|------------------|--------------------|-----------------|-------------|
| Parameter | symbol | Min. | Max. | UIIILS | Notes |
| Power Input Voltage Operating Temperature Storage Temperature | $egin{array}{c} oldsymbol{V_{DD}} \ oldsymbol{T_{OP}} \ oldsymbol{T_{ST}} \end{array}$ | -0.3 0 -20 | +3.6 +50 +60 | V _{DC} | at 25° 1 |

Note: 1. The Relative Humidity must not exceed 95% non-condensing at temperatures of 40° ... or less. At temperatures greater than 40° , the wet bulb temperature must not exceed 39° ...

3. Electrical Specifications

The LM151X2 requires two power inputs. One input is employed to power the LCD electronics and to drive the voltages to drive the TFT array and liquid crystal. And the second input for the backlight CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2 ELECTRICAL CHARACTERISTICS:

| Parameter | Symbol | | Values | Units | Notes | |
|--|--|-----------------------------|----------------------|--|--|---|
| Faranteter | Зуппоот | Min. | Min. Typ. | | UIIILS | Notes |
| MODULE : Power Supply Input Voltage Power Supply Input Current | V_{DD} I_{DD} | 3.15 - | 3.3 1.0 | 3.45 TBD | V _{DC} | 1 1, 2 |
| Data Input High Voltage Data Input Low Voltage | $\begin{matrix} V_{IH} \\ V_{IL} \end{matrix}$ | 0.7°₩ _{DD} -0.5 | - - | V _{DD} + 0.5 0.3°₩ _{DD} | V V | 1 1 |
| BACK LIGHT: Back light Input voltage Backlight Input Current Lamp Kick-Off Voltage | V _{BL} I _{BL} | 650 3.0 840 925 | 550 8.0 - - | 525 9.0 - - | V _{RMS} mA V _{RMS} V _{RMS} V _{RMS} | 3 At 25°æ° At 0°æ° 4 At 25°æ° |
| Operating Frequency Life time | F_{BL} | 1360 30 - | 50 40,000 | - 80 - | V _{RMS} KHz hours | At 0°æ° 5 |

Notes: 1. All values shall be measured at the user connection.

- 2. The input current shall be measured at V_{DD} of 3.3V at 25°, refresh rate of 60Hz, and clock frequency of 65MHz under 9 gray pattern.
- 3. The backlight input current shall be measured at the ground cable and does not include loss of external inverter.
- 4. Voltages at both ends of the lamp.
- 5. Voltages at secondary side of transformer using the balancing capacitor, 22pF in inverter. These voltages can be changed with customer's own design of inverter.
- 6. The life time is defined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.



4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°...The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of and equal to 0° and aperture 1 degree. The test equipment is PhotoResearch Prichard SpectroRadiometer Model 1980B-SC or equivalent. The input signal voltage and timing specification are V_{DD} of 3.3V and VESA XGA @60Hz respectively. The input current of backlight is $8mA(F_{BL}=50KHz)$ at the ground terminals.

Table 2 OPTICAL CHARACTERISTICS

| Parameter | | Symbol | | Values | | Units | Notes |
|---|------------------------------------|--|--|--|--|-----------|-------|
| Taramete | 2.1.2.1.1.2.2.1 | | Min. | Тур. | Max. | Offics | Notes |
| Contrast Ratio | | CR | 150 | 200 | - | | 1 |
| Average Brightness, | white | SB_WH | 170 | 200 | - | cd/m² | 2 |
| Brightness Variation | า | SB_V | - | - | 30 | % | 3 |
| | ise Time ecay Time | Tr Tr _R Tr _D | - - | 45 10 35 | 60 15 45 | msec | 4 |
| G B | es ed reen lue /hite | X _R Y _R X _G Y _B X _W Yw | TBD TBD TBD TBD TBD TBD TBD TBD | TBD TBD TBD TBD TBD TBD TBD TBD | TBD TBD TBD TBD TBD TBD TBD TBD | | |
| Viewing Angle by CR x axis, righ x axis, left y axis, up(y axis, dow | it (•'=0°) (•'=180°) •'=90°) | • • • • • • • • • • • • • • • • • • • | 55 55 40 40 | 60 60 45 45 | - - - - | degree, ° | 5 |
| Half Luminance Angl x axis, righ x axis, left y axis, up(y axis, dow | it (•'=0°) (•'=180°) •'=90°) | ::: | 50 50 35 35 | - - - | - - - | degree, ° | 6 |
| Cross talk | | | - | - | 4 | % | 7 |
| Flicker | | | _ | _ | -30 | dB | 8 |
| Gamma value | | | - | - | - | - | 9 |



Notes 1. Contrast Ratio (CR) is defined mathematically as:

(Surface Brightness with all white pixels)

(Surface Brightness with all black pixels)

Contrast ratio shall be measured at the center of the display (Location 1).

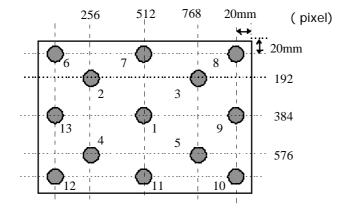
2. Average Brightness is the average of brightness value at location 1 to 5 with all pixels displaying white.

$$B(AVE) = \frac{B1 + B2 + B3 + B4 + B5}{5}$$

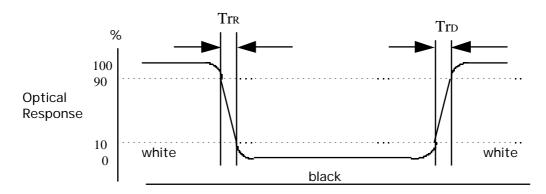
3. The variation in surface brightness, SB_{ν} is defined as :

$$\frac{\text{Maximum } (B_1, B_2, \dots B_{13}) \text{ - Minimum } (B_1, B_2, \dots, R_{00})}{\text{Average } (B_1, B_2, \dots, B_5)}$$

Where B1 to B13 are the brightness with all pixels displaying white at 13 locations.

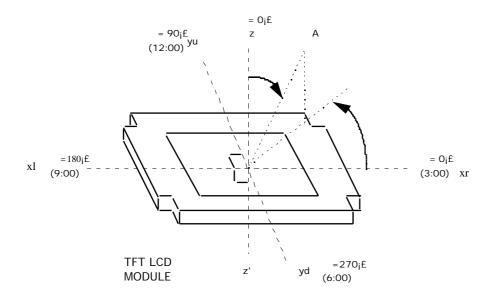


4. The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





5. Viewing angle is the angle at which the contrast ratio is greater than 10.

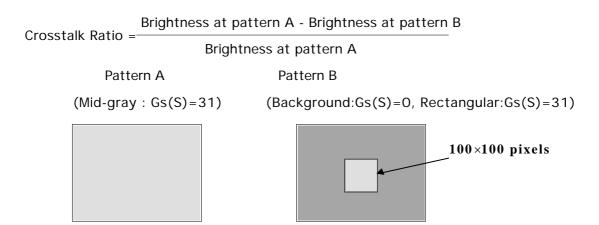


6. Half Luminance Angles

Half Luminance angles are defined as the up, down, left, and right angular boundaries at which the luminance value is 50% of the luminance value measured on-axis.

Measurements shall be done at the center of the display area (Location 1) with an all white image.

7. Cross talk shall be measured at center location.





8. Flicker shall be measured at the center location.

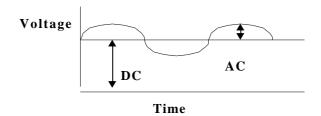
Test pattern : Pixel pattern

Background RGB gray (0, 0, 0) Foreground RGB gray (31, 31, 31)

AC(at 30Hz)

Test equation: 20 log -

DC level



9. Gamma values shall be measured at the center location.

| | - (-) | Relative Bri | ghtness(%) | | | |
|---|-------|--------------|------------|--------|--|--|
| n | Gs(S) | min | max | Remark | | |
| 0 | 0 | - | 0.67 | | | |
| 1 | 7 | - | TBD | | | |
| 2 | 15 | TBD | TBD | | | |
| 3 | 23 | TBD | TBD | | | |
| 4 | 31 | TBD | TBD | | | |
| 5 | 39 | TBD | TBD | | | |
| 6 | 47 | TBD | TBD | | | |
| 7 | 55 | TBD | TBD | | | |
| 8 | 63 | 100 | 100 | | | |



5. Interface Connections

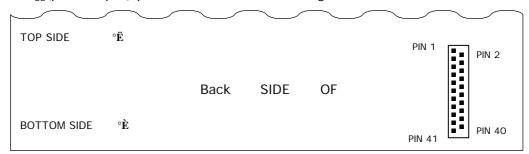
This LCD employs three interface connections, a 41 pin connector is used for the module electronics and two connectors, a three pin connector, are used for the integral backlight system. The electronics interface connector is a model DF9B-41P-1V manufactured by Hirose. The pin configuration for the connector is shown in the table below.

Table 3 MODULE CONNECTOR PIN CONFIGURATION

| Pin | Symbol | Description | Pin | Symbol | Description |
|---|---|--|--|--|--|
| 1 3 5 7 9 11 13 15 17 19 21 23 25 | GND GND V _{sync} GND RO R2 R3 R5 GND G0 G2 G3 G5 | System Ground. Note 1 System Ground V _{sync} . Vertical Sync Signal System Ground Red data 0 (LSB) Red data 2 Red data 3 Red data 5 (MSB) System Ground Green data 0 (LSB) Green data 2 Green data 3 Green data 5 (MSB) | 2 4 6 8 10 12 14 16 18 20 22 24 26 | DCLK H _{sync} GND GND R1 GND R4 GND GND G1 GND G4 GND | Data Input Clock H _{sync} . Horizontal Sync Signal System Ground System Ground Red data 1 System Ground Red data 4 System Ground System Ground Green data 1 System Ground Green data 4 System Ground |
| 25 27 | G5 GND | Green data 5 (MSB) System Ground | | GND | - |
| 29 | ВО | Blue data O (LSB) | 28 30 | GND B1 | System Ground Blue data 1 |
| 31 33 | B2 B3 | Blue data 2 Blue data 3 | 32 34 | GND B4 | System Ground Blue data 4 |
| 35 37 | B5 DE | Blue data 5 (MSB) Data Enable Signal | 36 38 | GND V _{DD} | System Ground No Connection |
| 39 41 | $V_{	extsf{DD}} \ V_{	extsf{DD}}$ | Power Supply for LCD Module No Connection | 40 | V_{DD} | Power Supply for LCD Module |

Notes: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.

2. All V_{DD} (power input) pins should be connected together.



The backlight interface connector is a model BHR-03VS-1, manufactured by JST. The mating connector part number is SM02(8.0)B-BHS-1-TB or equivalent. The pin configuration for the connector is shown in the table below.

Table 4 BACKLIGHT CONNECTOR PIN CONFIGURATION

| Pin | Symbol | Description | Notes |
|-----|--------|------------------|-------|
| 1 | HV | Lamp power input | 1 |
| 2 | NC | No connect | |
| 3 | LV | Ground | 2 |

Notes: 1. The input power terminal is colored pink. Ground pin color is light pink.



2. The backlight ground should be common with Vss.

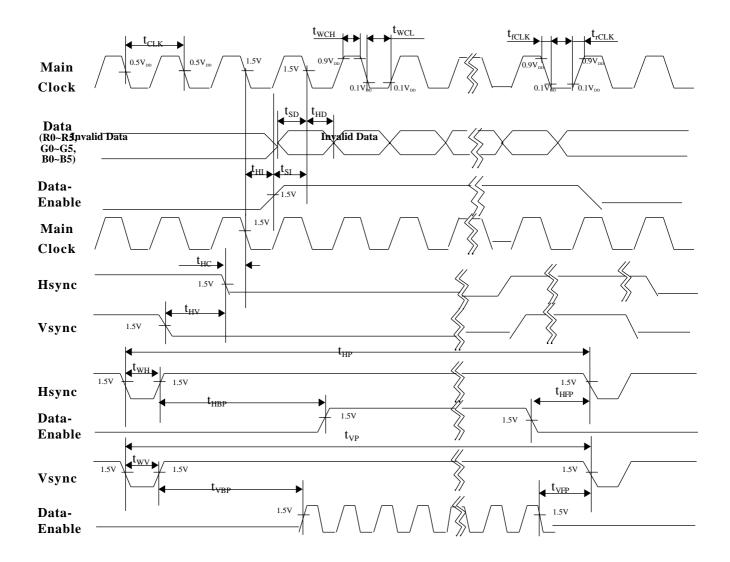


6. Signal Timing Specification

| | Donomotor | Company of | | Value | | l lmit o | Notes |
|---|---|---|--|---|--|---|--|
| | Parameter | Symbol | Min. | Тур. | Max. | Units | Notes |
| Main Clock | Frequency High duration Low duration Rise Time Fall Time | $f_{\text{CLK}} (=1/t_{\text{CL}} \\ \text{k}) \\ t_{\text{wcH}} \\ t_{\text{wcL}} \\ t_{\text{rCLK}} \\ t_{\text{fCLK}}$ | TBD 0.45 t _{CLK} 0.45 t _{CLK} - | 65.0 0.5 t _{CLK} 0.5 t _{CLK} - | TBD 0.55 t _{CLK} 0.55 t _{CLK} 8 | MHz ns ns ns | |
| Data (RGB data including sync) | Set-up duration Hold duration | t _{sD} t _{HD} | 5.0 5.0 | 1 1 | 1 1 | ns ns | for f _{CLK} for f _{CLK} note 1 |
| Hsync | Period Pulse Width | t _{HP} | (TBD) (TBD) (TBD) | 20.7 1344 136 | - (TBD) (TBD) | ß; clock clock | |
| Vsync | Period | t _{VP} | (TBD) | 16.7 806 | - (TBD) | msec lines | |
| | Pulse Width | t _{wv} | (TBD) | 6 | (TBD) | lines | |
| Data Enable | Set-up duration Hold duration Horizontal Back Porch Horizontal Period Horizontal Front porch Vertical Back Porch Vertical Period Vertical Front porch | $egin{array}{l} t_{ m SI} & & & & & & & & & & & & & & & & & & &$ | 5.0 5.0 (TBD) (TBD) (TBD) (TBD) (TBD) (TBD) | - 160 1344 24 29 806 3 | | ns ns clock clock clock lines lines | for f _{CLK} for f _{CLK} |
| Hsync- C | Clock phase difference | t _{HC} | t _{CLK} -10 | - | t_{wcL} | ns | |
| Hsync-V | sync phase difference | t _{HV} | - | - | t _{HP} -t _{WH} | ns | |

Notes: 1. All data input shall be latched at falling edge of data clock.

7. Signal Timimg Waveforms





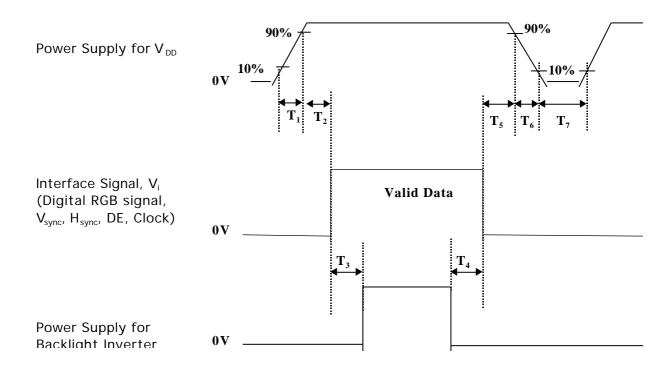
8. Color Input Data Reference

The brightness of each primary color(red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5 COLOR DATA REFERENCE

| | | | | | | | | | | npu | t Co | lor | Data | a | | | | | | |
|--------|------------|--------|-----|-----|----|----|-------|----|----|-------|------|-----|------|------|----|-----|----|----|-----|----|
| | Color | | Red | | | | Green | | | | Blue | | | | | | | | | |
| | | | 1 | MSB | | | LSE | 3 | 1 | MSB L | | | LSE | B MS | | MSB | 3 | | LSE | 3 |
| | | | R5 | R4 | R3 | R2 | R1 | RO | G5 | G4 | G3 | G2 | G1 | GO | В5 | В4 | В3 | В2 | B1 | ВО |
| | Black | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(63) | | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(63) | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| Basic | Blue(63) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| Colors | Cyan | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Magenta | | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Red(0) Da | ark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(1) | | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(2) | | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Red | | | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Red(61) | | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(62) | | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(63) E | 3right | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(O) [| Dark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(1) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(2) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Green | | | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Green(61) | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(62) | | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(63) | Bright | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue(O) [| Dark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue(1) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | Blue(2) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| Blue | | | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Blue(61) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 0 | 1 |
| | Blue(62) | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 |
| | Blue(63) | Bright | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 |

9. Power Sequence



 T_1 , T_6 : 10 ns ~ 20 ms.

n T_2 , T_5 : 50 ms (min.)

n T_3 , T_4 : 300 ms (min.)

 $n = T_7$, : TBD

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{DD} to OV.
- 3. BackLight inverter power must be turn on after power supply for LCD and interface signal are valid.



10. Mechanical Characteristics

The chart below provides general mechanical characteristics for the model LM151X2 LCD. Please refer to appendix 2 regarding the detailed mechanical drawing of the LCD module.

| Parameter | Value | Symbol | Notes |
|--|--|--------|--|
| Outside dimension Width Height Thickness | 352.6 (typ) 264.6 (typ) 16.0 (typ) | mm | Thickness is 18.0mm(typ) at user connector |
| Bezel area Width Height | 311.2 234.4 | mm | |
| Active area Width Height | 307.2 230.4 | mm | |
| Weight | 1500(typ) 1600 (max) | gram | |
| Front surface of LCD | Hard coating 3H. face of LCD Anti-glare treatment of the front polarizer | | |



11. Environmental Specification

| No | Test | ITEM | Conditions | | | | | |
|----|----------------------------------|----------------------------|--|--|--|--|--|--|
| 1 | Temperature | Operating Non-operating | 0°.~ 50° -20°.~ 60° | | | | | |
| 2 | Operating Humidity Non-operating | | 20% ~ 80% RH (non-condensing) 5% ~ 95% RH (38.7°.maximum wet bulb temperature) | | | | | |
| 3 | Alti | tude | Operating: 12,000ft Storage: 40,000ft | | | | | |
| 4 | | ion test perating) | Waveform: Random Vibration level: 1.5G RMS Bandwidth: 10~200Hz Duration: X, Y, Z, 20 min one time each direction | | | | | |
| 5 | 000 | ck test perating) | Shock level: 100G Waveform: half sine wave, 2ms Direction: ±X, ±Y, ±Z one time each direction | | | | | |



12. Incoming Inspection Standard for TFT-LCD Modules

1. Scope

This incoming inspection standard shall be applied to TFT-LCD Modules(hereafter called "LCMs") supplied by LG Electronics Inc. (hereafter called the "Supplier") to Customer.

2. Incoming inspection

The Customer shall have the right to conduct at its own cost and expense, an incoming inspection of the LCMs at the destination specified in the relevant bills of lading in accordance with the LCM's specifications separately agreed upon and the inspection standard set forth in this article.

The Customer shall notify the Supplier in writing of the inspection results (acceptance or rejection) in accordance with the said inspection standard within 40 days after the date of the bills of lading.

Should Customer fail to so notify the Supplier within the said 40 days period, the Customer's right to reject the LCMs shall then lapse, and the said LCMs shall be deemed to have been accepted by the Customer.

3. Method of Incoming inspection

Unless otherwise agreed upon in writing, the sampling inspection method based on MIL-STD-105E shall be applied to the Customer's incoming inspection.

4. Classification of defects

Defects are classified as either a major defect or a minor defect based on the degree of defect defined herein.

a) Major defect

A major defect is a defect that is likely to result in product failure, or reduction in the product's intended usage.

b) Minor defect

A minor defect is a defect that has little bearing on the effective use or operation of the product.

Specific criteria of judgement of major and/or minor defects or other related issues shall be in accordance with the attached "Classification of Defects". (refer to appendix 4)

5. Acceptable Quality Level("AQL")

The AQL for major and minor defects shall be respectively set forth below.

a) Major defects : AQL = 0.65%b) Minor defects : AQL = 1.5%

6. Sampling Inspection Method

a) Lot size : Quantity per shipment lot

b) Sampling type: Normal inspection, Single sampling

c) Inspection level : II

d) Sampling table: Table in MIL-STD-105E

7. Determination of acceptability and subsequent disposal

If the number of defects found in the LCM sampling lot is equal to or less than the applicable acceptance level, the lot shall be accepted.

If the number of defects is greater than the applicable acceptance level, the lot shall be rejected. The Customer shall inform the Supplier of the detailed results of such inspection within the time period stipulated in item #2 "Incoming inspection".

a) Accepted lot

An acceptance under the above incoming inspection shall constitute an acceptance by the Customer of such lot of the LCMs in terms of the landed quality thereof.

b) Rejected lot



If a shipment lot of products is rejected under the above incoming inspection due to any defects for which the Supplier is responsible and such a fact is clearly confirmed by the Supplier through a separate inspection or as otherwise decided, the Supplier shall choose one of the following three options which must be determined by mutual consent. The Supplier shall advise the Customer of its choice not later than 14 working days (Monday to Friday) of receipt of the "Customer's notification of rejected lot":

- -1. The Customer shall return the rejected lot to the place to be designated by the Supplier and the Supplier shall screen all of the products in the lot and repair or replace the defective products.
- -2. The Supplier shall screen all of the products in the lot and repair or replace the defective products within a reasonable time period at the Customer's facility.
- -3. The Customer shall screen the entire lot for the good LCMs at the expense of the Supplier to be separately agreed upon. The rejected products shall be returned to the place designated by the Supplier.
- 8. Inspection Specifications
 - a) General conditions
 - i) The environment conditions
 - Ambient temperature : 20~25°...
 - Humidity : 65° &5 % RH
 - Ambient illumination : Single 20W fluorescent lamp non-directive (About 300 to 700 Lux)
 - ii) Viewing distance

The distance between the LCM and the eyes of the inspector shall be at least 35cm.

III) Inspection zone for external inspection

External inspection should be performed from front side(Display area)

b) External Inspection Standards

| Inspection | on items | Inspection Standards | Remark |
|----------------|------------|---|------------------|
| Dent Dot shape | | D°ብ.5, N°-8 | On the polarizer |
| Scratches | Line shape | 0.01°₩°ਚ.2, 5°±° - 20, N° - 4 | On the polarizer |
| Stain | Dot shape | 0.2°Đ°Đ.5, N°-5 | Operating |
| inclusion | Line shape | 0.05°₩°•0.1, 0.3°±°•3.0, N°•5 | Operating |

W: Width(mm)
L: Length(mm)

D : Average diameter(mm)

Note) Those extraneous substances which can be wiped out easily are acceptable.

- c) Display-on Inspection(Visual inspection)
 - i) Dot Defect
 - Bright Dot Defects : When the LCM is turned on, dots(sub-pixels) which appear bright in the display area at Black(Dark) Mode are bright dot defects.
 - R, G or B 1 dot : 6 Max.
 R, G or B join 2 dots : 1 Max.
 - R, G or B join 3 dots: 0 max.
 - Minimum Distance between defects : D ° 10mm
 - Total amount of Bright dot defects : 6 Max.
 - Black Dot defects : When the LCM is turned on, dots(sub-pixels) which appear black in the display area at White(Bright) Mode are black dot defects.
 - 1 dot : 6 Max.
 - Join 2 dots : 1 Max.



• Join 3 dots: 0 max.

• Minimum Distance between defects : D $^{\circ}$ 10mm • Total amount of Black dot defects : 6 Max.

- Total amount of Dot Defect(Bright and Black Dot): 10 Max. (Combination)

ii) Line Defect

All line defects such as vertical, horizontal or cross line defect are not allowed.

Note 1) Dot Defect: Defected area > 1/2 dot

- 2) 1 Dot : Defect dot is isolated, not attached to other defect dot.
- 3) 3 Join N Dot: N dots are consecutive. (N means the number of defect dots)

9. Other

If any problems arise with the LCM supplied by LGE, the Customer and Supplier will cooperate and make efforts to solve it with mutual confidence and respect.



13. Designation of Lot Mark

a) Lot Mark



A, B : DIVISION CODE C, D, E : MODEL CODE

F: YEAR G: MONTH

H, I, J, K, L: SERIAL NO.

Note: 1. YEAR

| YEAR | 89 | 90 | 91 | 92 | 93 | 94 | 95 | 96 | 97 | 98 | 99 |
|------|----|----|----|----|----|----|----|----|----|----|----|
| Mark | 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 |

2. MONTH

| MONTH | Jan. | Feb. | Mar. | Apr. | May | Jun. | Jun. | Aug. | Sep. | Oct. | Nov. | Dec. |
|-------|------|------|------|------|-----|------|------|------|------|------|------|------|
| Mark | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | Ν | D |

b) Location of Lot Mark

Serial NO. Is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

14. Packing Form

a) Package quantity in one box: 8 pcs

b) Box Size: 587mm°&08mm°&78mm

Note: 1. Please, refer to appendix 3 regarding the detailed packing assembly drawing.



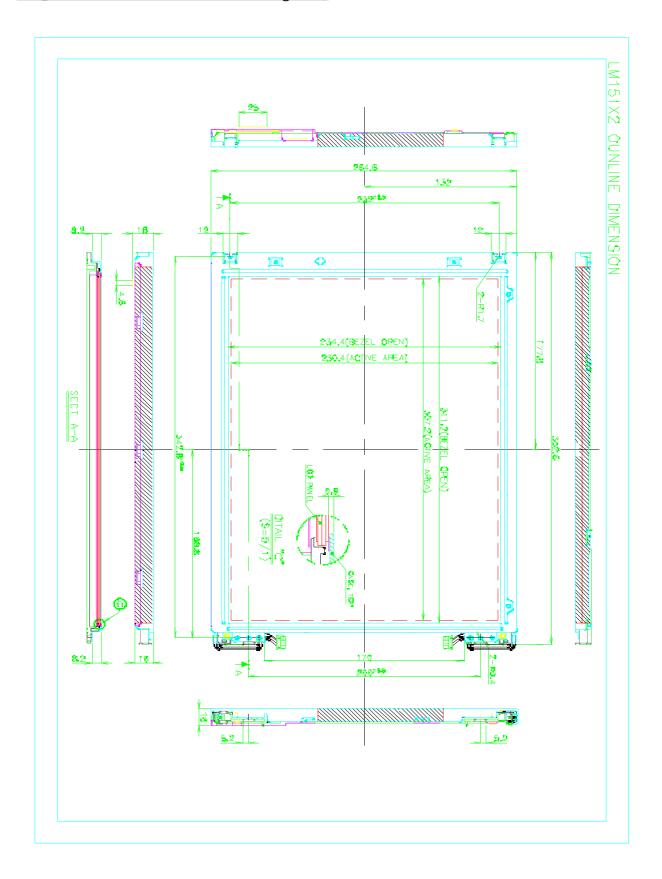
15. PRECAUTIONS

Please pay attention to the followings when you use this TFT-LCD module with Back-light unit.

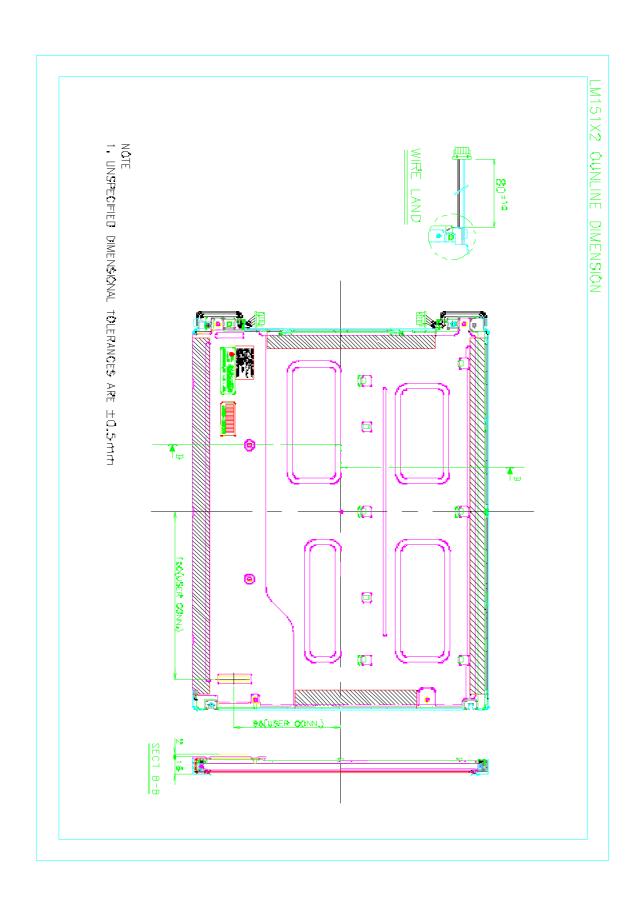
- 1) You must mount Module using mounting holes arranged in 4 corners.
- 2) Be sure to turn off the power when connecting or disconnecting the circuit.
- 3) Note that the polarizers are easily damaged. Pay attention not to scratch or press this surface with any hard object.
- 4) When the LCD surface become dirty, please wipe it off with a soft material. (ie. cotton ball)
- 5) Protect the module from the ESD as it may damage the electronic circuit (C-MOS). Make certain that treatment person's body are grounded through wrist bend.
- 6) Do not disassemble the module and be careful not to incur a mechanical shock that might occur during installation. It may cause permanent damage.
- 7) Do not leave the module in high temperatures, particularly in areas of high humidity for a long time.
- 8) The module not be expose to the direct sunlight.
- 9) Avoid contact with water as it may a short circuit within the module.
- 10) Do not apply invalid signal, especially very high frequency data clock and H_{sync} . Invalid signal causes improper shutdown of DC/DC converter in LCM or permanent damage to LCD module. (
- (If DC/DC converter in LCM is in shutdown state, LCM shows only white screen. Then please turn off and on once LCM power.)
- 11) If the LCM displays the same pattern continuously for a long period of time, it can be the image sticking to the screen.



16. APPENDIX 1 : Outline Drawing

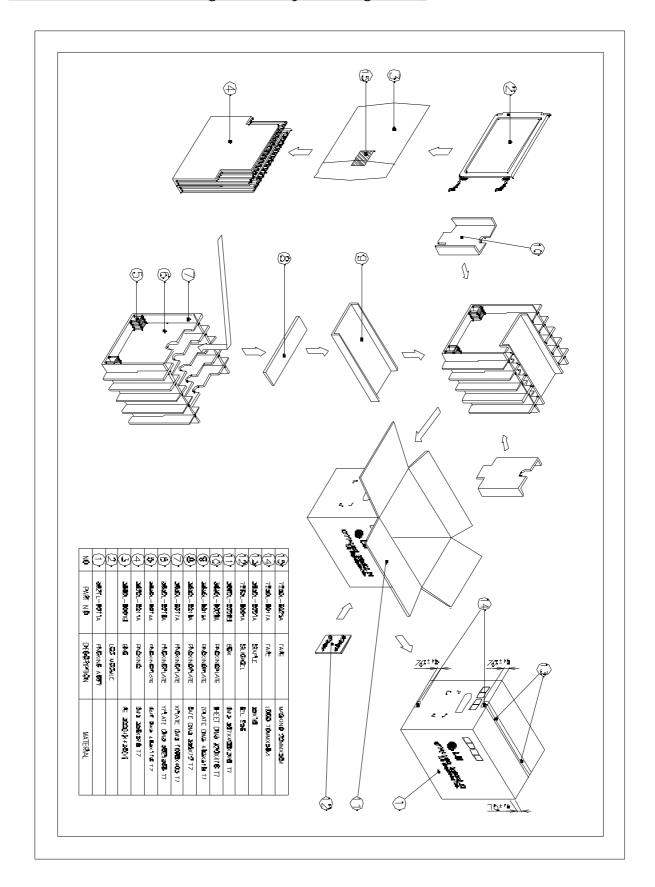




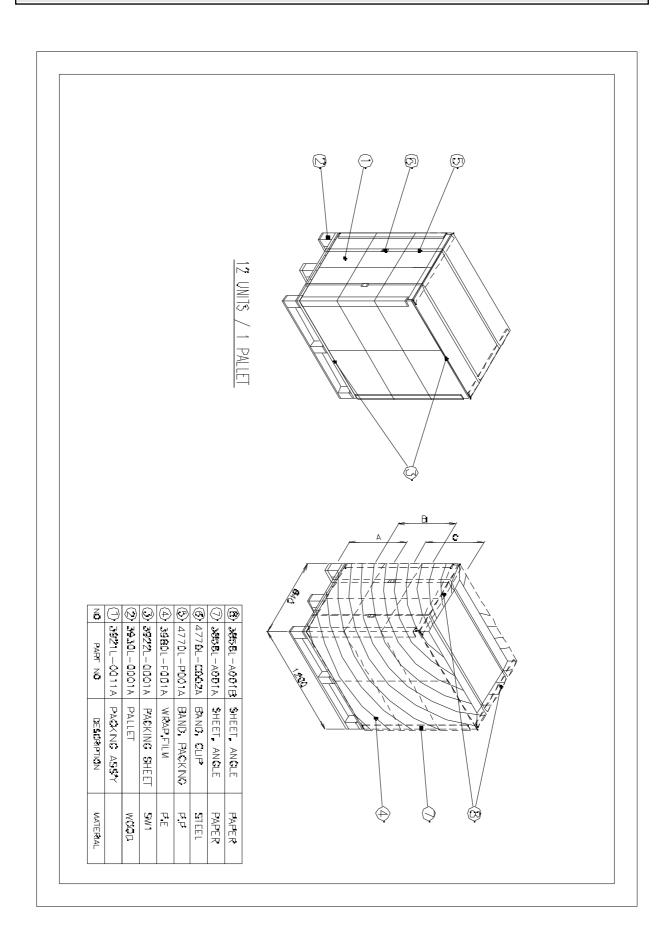




17. APPENDIX 2 : Packing Assembly Drawing









18. APPENDIX 3 : Classification of Defects

| Inspection Item | Criterion for Defect | Туре |
|--------------------------------|---|-------------------------|
| Operating Frequency | Does not meet specified range in the spec. | Major |
| Power Consumption | Does not meet specified range in the spec. | Major |
| Contrast Ratio | Does not meet specified range in the spec. | Major |
| Line Defect | Vertical line defect Horizontal line defect Cross line defect | Major |
| Scratches, Dents or Bubbles | Shall be accordance with the item 8.b) "External inspection standards" on this incoming inspection standard | Minor |
| Extraneous Substance | Shall be accordance with the item 8.b) "External inspection standards" on this incoming inspection standard | Minor |
| Dot Defect | Shall be accordance with the item 8.b) "External inspection standards" on this incoming inspection standard | Minor |
| Bezel Appearance | Deep damage is found on plating, the ground metal is exposed, or rust may occur. (Irregular plating or coating is ignored) | Minor |
| Damaged Parts | a) The lead wire part is disconnectedb) The lead wire part is not yet disconnected, but is damagedc) Function, performance or product value is impaired | Major Minor Major |
| Bezel Claw Forming | a) Bezel claw missing b) The bezel claw is not formed sufficiently | Major Minor |