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TFT LCD Approval Specification

MODEL NO.: M170E6 -L05

Customer : _____

Approved by : _____

Note :

Liquid Crystal Display Division	
QRA Dept.	PDD I Dept.
Approval	Approval
陳永一 92. 9. 23	黃蒐峯 92. 9. 23

Version 2.0

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REVISION HISTORY

Version	Date	Section	Description
Ver 0.0	Apr., 30 '03	-	M170E6-L05 Specifications was first issued.
Ver 1.0	Aug, 13 '03	7.2	Color Chromaticity Red Rx：(0.643)(Typ.)→(0.645)(Typ.)。
			Color Chromaticity Red Ry：(0.353)(Typ.)→(0.352)(Typ.)。
			Color Chromaticity Green Gx：(0.284)(Typ.)→(0.280)(Typ.)。
			Color Chromaticity Green Gy：(0.609)(Typ.)→(0.600)(Typ.)。
			Color Chromaticity Blue Bx：(0.142)(Typ.)→(0.143)(Typ.)。
			Color Chromaticity Blue By：(0.075)(Typ.)→(0.065)(Typ.)。
			Color Chromaticity White Wx：(0.310)(Typ.)→(0.313)(Typ.)。
			Color Chromaticity White Wy：(0.330)(Typ.)→(0.329)(Typ.)。
			Viewing Angle Horizontal θ_x+：(85)(Typ.)→(88)(Typ.)。
			Viewing Angle Horizontal θ_x-：(85)(Typ.)→(88)(Typ.)。
			Viewing Angle Vertical θ_Y+：(85)(Typ.)→(88)(Typ.)。
			Viewing Angle Vertical θ_Y-：(85)(Typ.)→(88)(Typ.)。
Ver 1.1	Sep. 02, '03	6.2	POWER ON/OFF SEQUENCE：Timing　$0 \leqq T1 →$ 0.47ms$\leqq$T1
		7.2	Viewing Angle Horizontal θ_x+：(88)(Typ.)→(85)(Typ.)。
			Viewing Angle Horizontal θ_x-：(88)(Typ.)→(85)(Typ.)。
			Viewing Angle Vertical θ_Y+：(88)(Typ.)→(85)(Typ.)。
			Viewing Angle Vertical θ_Y-：(88)(Typ.)→(85)(Typ.)。
Ver 2.0	Sep.22,'03	all	The same as Ver 1.1。

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1. GENERAL DESCRIPTION

1.1 OVERVIEW

The M170E6-L05 model is a 17.0" TFT-LCD module with a 4-CCFL Backlight Unit and a 30-pin 2ch-LVDS interface. This module supports 1280 x 1024 SXGA mode and displays 16.2M colors. The inverter module for the Backlight Unit is not built in.

1.2 FEATURES

- Wide viewing angle

- High contrast ratio

- Fast response time

- High color saturation (EBU Like Specifications)

- SXGA (1280 x 1024 pixels) resolution

- DE (Data Enable) only mode

- LVDS (Low Voltage Differential Signaling) interface

1.3 APPLICATION

- TFT LCD Monitor

1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Active Area	337.92 (H) x 270.34 (V) (17.0" diagonal)	mm	(1)
Bezel Opening Area	341.9 (H) x 274.4 (V)	mm	
Driver Element	a-Si TFT active matrix	-	-
Pixel Number	1280 x R.G.B. x 1024	pixel	-
Pixel Pitch	0.264 (H) x 0.264 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.2M	color	-
Transmissive Mode	Normally black	-	-
Surface Treatment	Hard coating (3H), Anti-glare (Haze 25)	-	-

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	358.0	358.5	359.0	mm	(1)
	Vertical(V)	296.0	296.5	297.0	mm	
	Depth(D)	-	17.0	17.5	mm	
Weight		-	-	2050	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

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2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	ºC	(1)
Operating Ambient Temperature	T_{OP}	0	+50	ºC	(1), (2)
Shock (Non-Operating)	S_{NOP}	-	50	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	-	1.5	G	(4), (5)

Note (1) Temperature and relative humidity ranges are shown in the figure below.

      (a) 90 %RH Max. (Ta $\leq$ 40 ºC).

      (b) Wet-bulb temperature should be 39 ºC Max. (Ta > 40 ºC).

      (c) No condensation.

Note (2) The temperature of panel surface should be 0 ºC Min. and 60 ºC Max.

Note (3) 11ms, half-sine wave, 1 time for ± X, ± Y, ± Z.

Note (4) 10 ~ 300 Hz, sweep rate 10 min, 30 min for X, Y, Z. axis

Note (5) Upon the Vibration and Shock tests, the fixture used to hold the module must be firm and rigid enough to prevent the module from twisting or bending by the fixture.

Relative Humidity (%RH)

Temperature (ºC)

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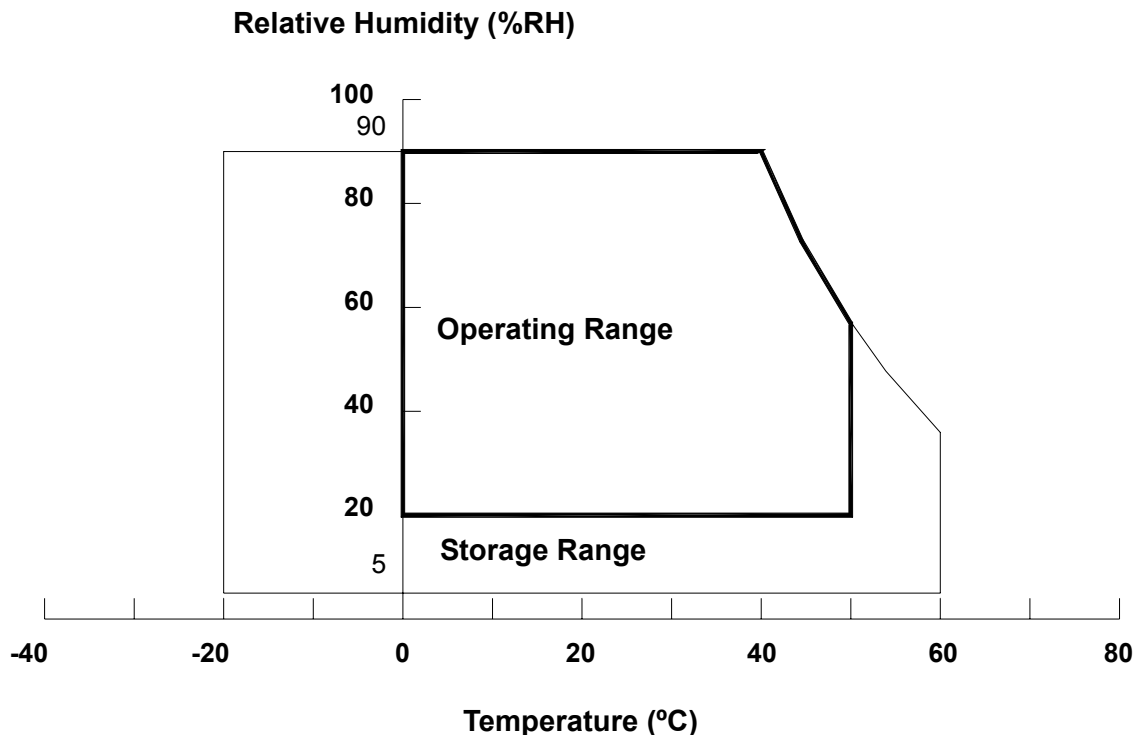
2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	Vcc	-0.3	+6.0	V	(1)
Logic Input Voltage	V_{IN}	-0.3	4.3	V	

2.2.2 BACKLIGHT UNIT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Lamp Voltage	V_L	-	2.5K	V_{RMS}	(1), (2), I_L = 6.5 mA
Lamp Current	I_L	-	7.0	mA_{RMS}	(1), (2)
Lamp Frequency	F_L	-	80	KHz	

Note (1) Permanent damage might occur if the module is operated at conditions exceeding the maximum values.

Note (2) Specified values are for lamps (Refer to 3.2 for further information).

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3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta = 25 ± 2 ºC

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		Vcc	4.5	5.0	5.5	V	-
Ripple Voltage		V_{RP}	-	-	100	mV	-
Rush Current		I_{RUSH}	-	-	3.8	A	(2)
Power Supply Current	White	Icc	-	1030	1350	mA	(3)a
	Black		-	520	700	mA	(3)b
	Vertical Stripe		-	930	1250	mA	(3)c
LVDS differential input voltage		Vid	100	-	600	mV	
LVDS common input voltage		Vic	-	1.2	-	V	
Logic "L" input voltage (SELLVDS)		Vil	Vss	-	0.8	V	

Note (1) The module should always be operated within above ranges.

Note (2) Measurement Conditions:
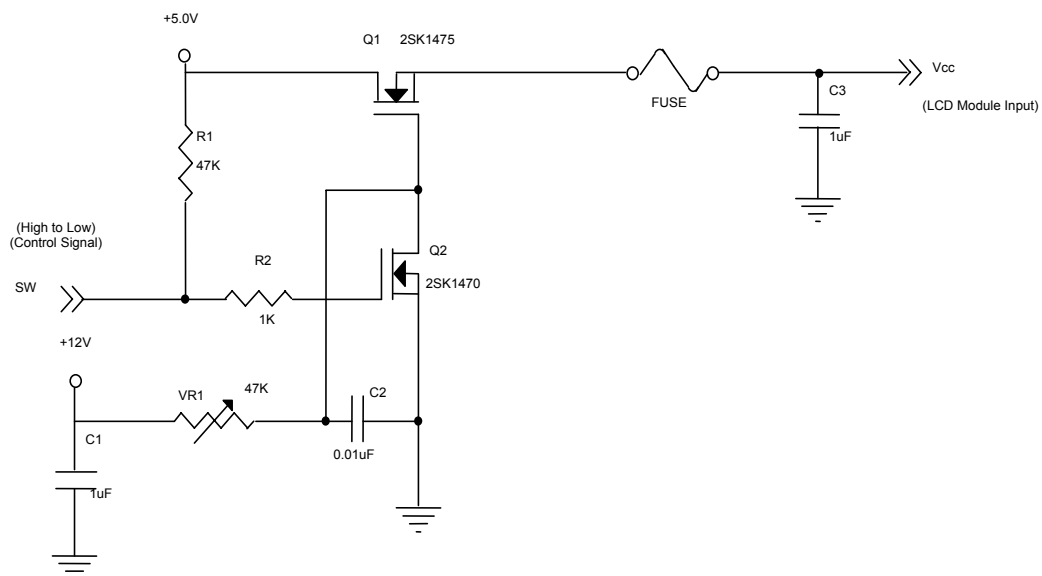
Vcc rising time is 470μs
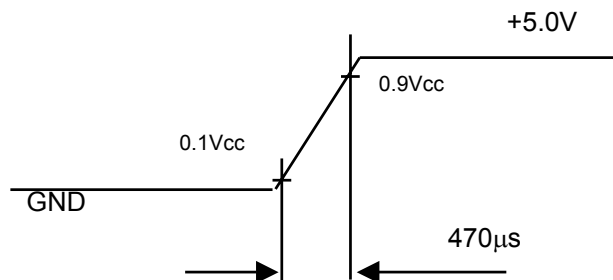
Note (3) The specified power supply current is under the conditions at Vcc = 5.0 V, Ta = 25 ± 2 ºC, f_v = 60

Hz, whereas a power dissipation check pattern below is displayed.

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a. White Pattern
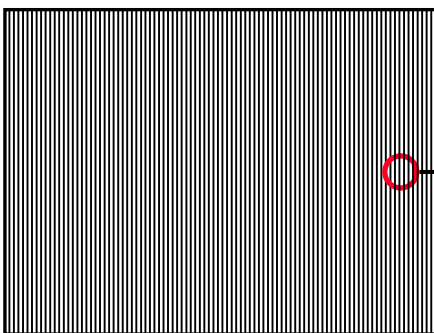
Active Area

b. Black Pattern

Active Area

c. Vertical Stripe Pattern

Active Area
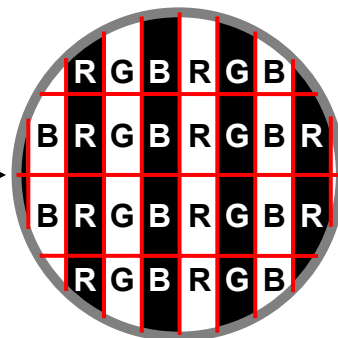
3.2 BACKLIGHT UNIT

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Input Voltage	V_L	585	650	715	V_{RMS}	I_L = 6.5 mA
Lamp Current	I_L	2.0	6.5	7.0	mA_{RMS}	(1)
Lamp Turn On Voltage	V_S	-	-	1290(25 ^{o}C)	V_{RMS}	(2)
		-	-	1500(0 ^{o}C)	V_{RMS}	(2)
Operating Frequency	F_L	40	55	80	KHz	(3)
Lamp Life Time	L_{BL}	50,000	-	-	Hrs	(5)
Power Consumption	P_L	-	16.9	-	W	(4), I_L = 6.5 mA

Note (1) Lamp current is measured by utilizing high-frequency current meters as shown below:

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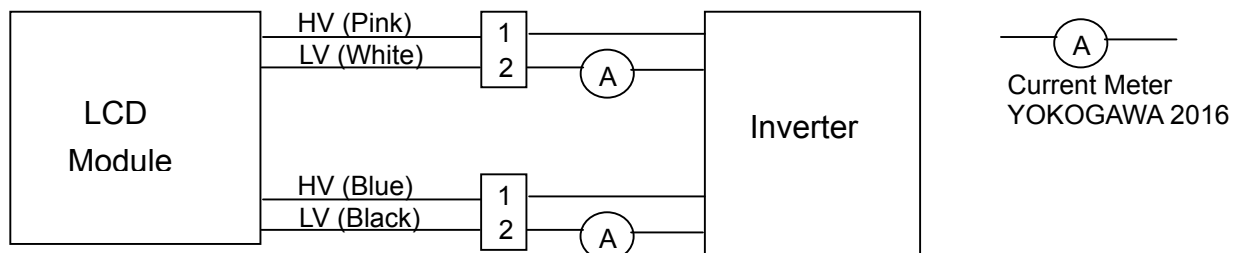
Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise, the lamp may not be turned on.

Note (3) The lamp frequency may produce interference with horizontal synchronization frequency from the display, which might cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronization frequency and its harmonics as far as possible.

Note (4) $P_L = I_L \times V_L$

Note (5) The lifetime of lamp can be defined as the time in which it continues to operate under the condition of Ta = 25 $\pm 2\ ^oC$ and I_L = (2.0) ~ (6.5) mArms until one of the following events occurs:

(a) When the brightness becomes or lower than 50% of its original value.

(b) When the effective ignition length becomes lower than 80% of its original value. (Effective ignition length is defined as an area that has more than 70% of brightness compared to the brightness in the center point.)

Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be designed carefully to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter, please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operate in the same manner when it is installed to your instrument.

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE

4.2 BACKLIGHT UNIT

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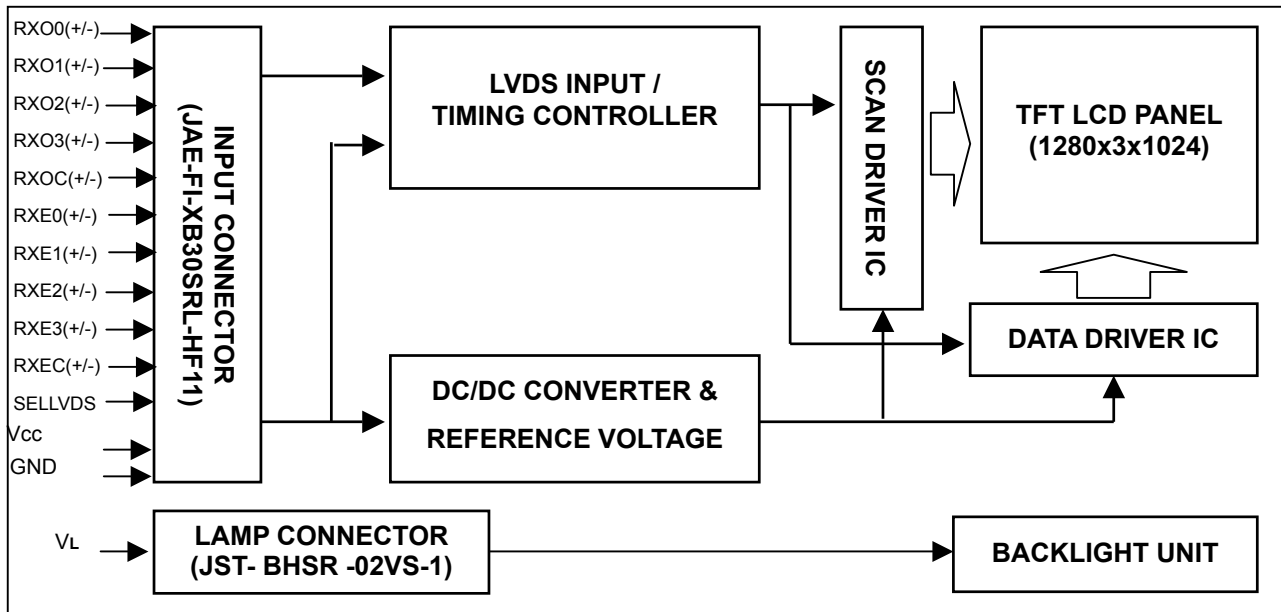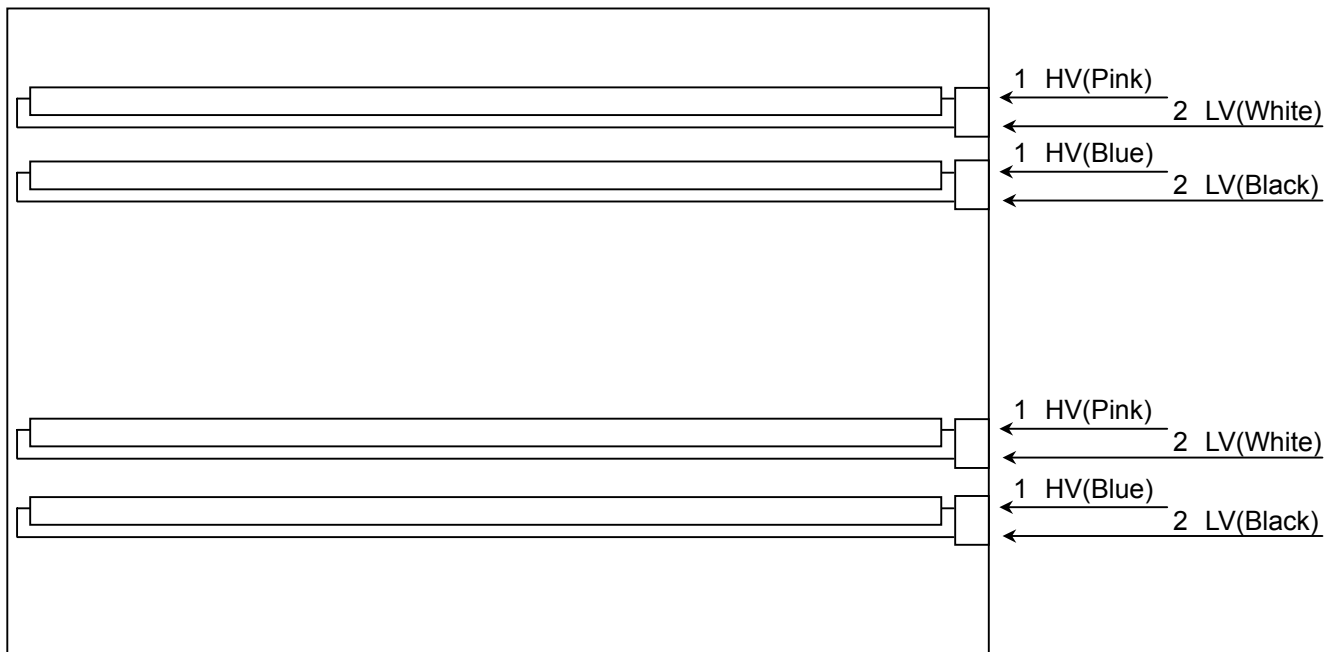
5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

Pin	Name	Description
1	RXE0-	Negative LVDS differential data input. Channel E0 (even)
2	RXE0+	Positive LVDS differential data input. Channel E0 (even)
3	RXE1-	Negative LVDS differential data input. Channel E1 (even)
4	RXE1+	Positive LVDS differential data input. Channel E1 (even)
5	RXE2-	Negative LVDS differential data input. Channel E2 (even)
6	RXE2+	Positive LVDS differential data input. Channel E2 (even)
7	GND	Ground
8	RXEC-	Negative LVDS differential clock input. (even)
9	RXEC+	Positive LVDS differential clock input. (even)
10	RXE3-	Negative LVDS differential data input. Channel E3(even)
11	RXE3+	Positive LVDS differential data input. Channel E3 (even)
12	RXO0-	Negative LVDS differential data input. Channel O0 (odd)
13	RXO0+	Positive LVDS differential data input. Channel O0 (odd)
14	GND	Ground
15	RXO1-	Negative LVDS differential data input. Channel O1 (odd)
16	RXO1+	Positive LVDS differential data input. Channel O1 (odd)
17	GND	Ground
18	RXO2-	Negative LVDS differential data input. Channel O2 (odd)
19	RXO2+	Positive LVDS differential data input. Channel O2 (odd)
20	RXOC-	Negative LVDS differential clock input. (odd)
21	RXOC+	Positive LVDS differential clock input. (odd)
22	RXO3-	Negative LVDS differential data input. Channel O3 (odd)
23	RXO3+	Positive LVDS differential data input. Channel O3 (odd)
24	GND	Ground
25	TEST	Test pin should be tied to ground or open.
26	NC	Not connection.
27	SELLVDS	SELLVDS pin should be tied to ground or open.
28	VCC	+5.0V power supply
29	VCC	+5.0V power supply
30	VCC	+5.0V power supply

Note (1) Connector Part No.: FI-XB30SRL-HF11 (JAE) or MDF76LARW-30S-IH(58) [HIROSE]

Note (2) Mating Connector Part No.:FI-X30H,FI-X30C*,FI-X30M*,FI-X30HL(-T),FI-X30C*L(-T) [JAE]

Note (3) The first pixel is EVEN.

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Note (3) Input signal of even and odd clock should be the same timing.

SELLVDS = Low or Open								
LVDS Channel E0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	EG0	ER5	ER4	ER3	ER2	ER1	ER0
LVDS Channel E1	LVDS output	D18	D15	D14	D13	D12	D9	D8
	Data order	EB1	EB0	EG5	EG4	EG3	EG2	EG1
LVDS Channel E2	LVDS output	D26	D25	D24	D22	D21	D20	D19
	Data order	DE	NA	NA	EB5	EB4	EB3	EB2
LVDS Channel E3	LVDS output	D23	D17	D16	D11	D10	D5	D27
	Data order	NA	EB7	EB6	EG7	EG6	ER7	ER6
LVDS Channel O0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	OG0	OR5	OR4	OR3	OR2	OR1	OR0
LVDS Channel O1	LVDS output	D18	D15	D14	D13	D12	D9	D8
	Data order	OB1	OB0	OG5	OG4	OG3	OG2	OG1
LVDS Channel O2	LVDS output	D26	D25	D24	D22	D21	D20	D19
	Data order	DE	NA	NA	OB5	OB4	OB3	OB2
LVDS Channel O3	LVDS output	D23	D17	D16	D11	D10	D5	D27
	Data order	NA	OB7	OB6	OG7	OG6	OR7	OR6

5.2 BACKLIGHT UNIT

CONN. 1

Pin	Symbol	Description	Remark
1	HV1	High Voltage	Pink
2	LV	Ground	White

CONN. 2

Pin	Symbol	Description	Remark
1	HV1	High Voltage	BLUE
2	LV	Ground	Black

Note (1) Connector Part No.: BHSR -02VS-1 (JST) or equivalent

Note (2) User's connector Part No.: SM02B-BHSS-1-TB (JST) or equivalent

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5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

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6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown in the following table and timing diagram.

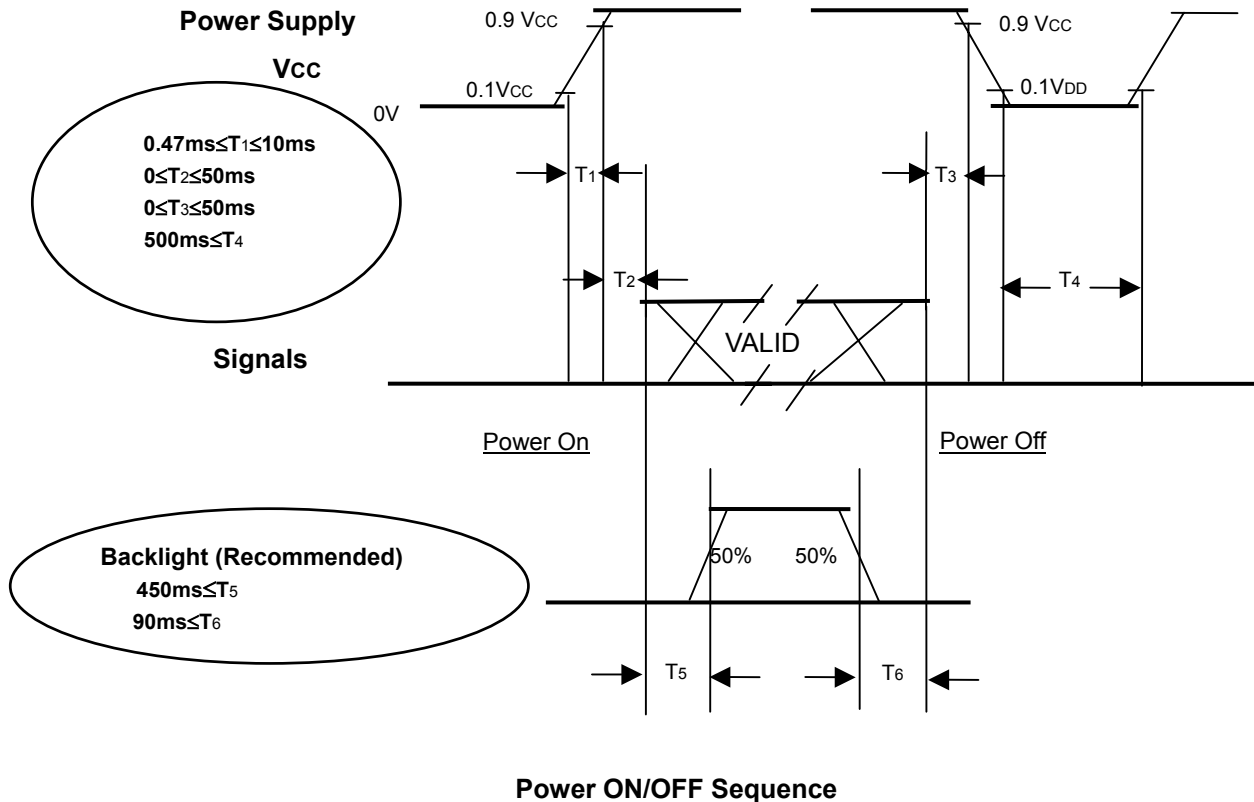| Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	Fc	31	47.3	59.2	MHz	-
	Period	Tc	16.8	21.1	32.3	ns	
	High Time	Tch	-	4/7	-	Tc	-
	Low Time	Tcl	-	3/7	-	Tc	-
LVDS Data	Setup Time	Tlvs	600	-	-	ps	-
	Hold Time	Tlvh	600	-	-	ps	-
Vertical Active Display Term	Frame Rate	Fr	56	60	75	Hz	Tv=Tvd+Tvb
	Total	Tv	1034	1066	1274	Th	-
	Display	Tvd	1024	1024	1024	Th	-
	Blank	Tvb	10	42	Tv-Tvd	Th	-
Horizontal Active Display Term	Total	Th	690	740	844	Tc	Th=Thd+Thb
	Display	Thd	640	640	640	Tc	-
	Blank	Thb	50	100	Th-Thd	Tc	-

Note: For this module is operated under DE-only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM

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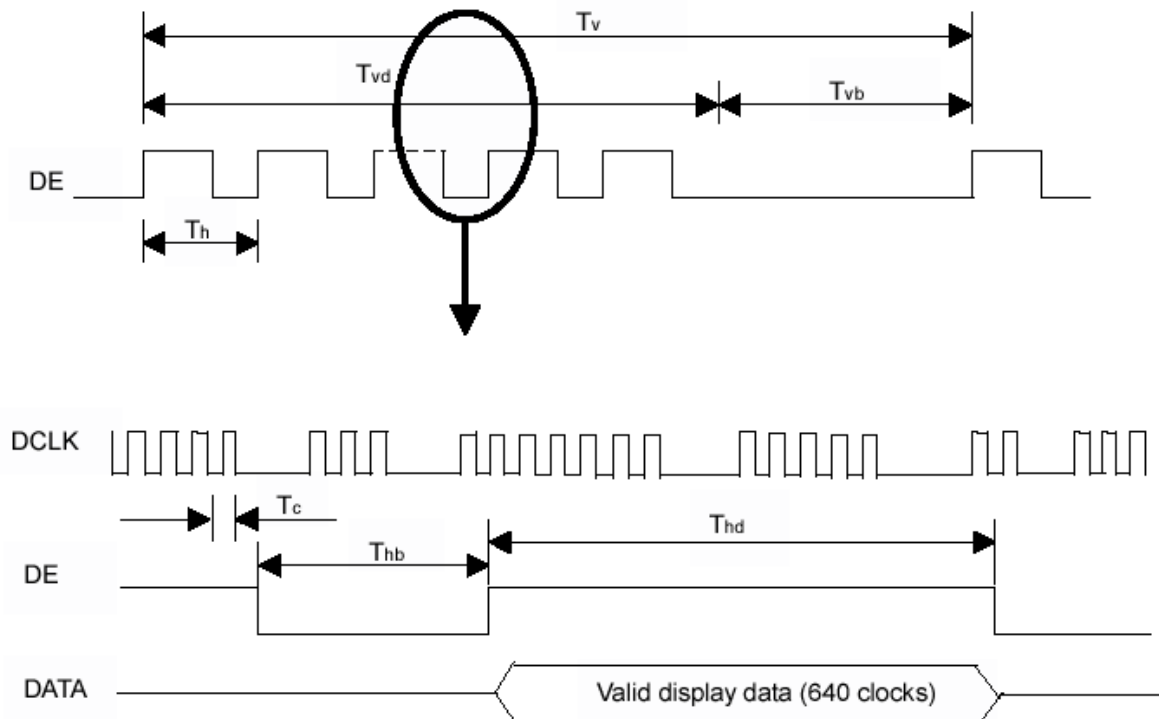
6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should follow the conditions shown in the following diagram.

Power Supply

Vcc

$0.47ms \leq T_1 \leq 10ms$
$0 \leq T_2 \leq 50ms$
$0 \leq T_3 \leq 50ms$
$500ms \leq T_4$

Signals

0.9 Vcc
0.1Vcc
0V

0.9 Vcc
0.1Vdd

T_1 T_3

T_2 T_4

VALID

Power On   Power Off

Backlight (Recommended)
$450ms \leq T_5$
$90ms \leq T_6$

50%   50%

T_5 T_6

Power ON/OFF Sequence

Note.

(1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.

(2) Please apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation of the LCD turns off, the display may, instantly, function abnormally.

(3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.

(4) T4 should be measured after the module has been fully discharged between power on/off periods.

(5) Interface signal should not be kept at high impedance when the power is on.

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7. OPTICAL CHARACTERISTICS

7.1 TESTING CONDITIONS

Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	^{o}C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V_{CC}	5.0	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Inverter Current	I_L	6.5	mA
Inverter Driving Frequency	F_L	50	KHz

7.2 OPTICAL SPECIFICATIONS

The measurement methods for the optical characteristics are explained in this section. The following items should be measured following the testing conditions described in section 7.1 under stable environment shown in Note (6).

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR		350	500	-	-	(2), (6)
Response Time		T_R		-	15	30	ms	(3)
		T_F		-	10	25	ms	
Center Luminance of White		L_C		200	250	-	cd/m^2	(4), (6)
White Variation		δW		-	1.25	1.40	-	(6), (7)
Cross Talk		CT	$\theta_x=0°, \theta_Y=0°$ Viewing Normal Angle	-	-	5	%	(5), (6)
Color Chromaticity	Red	Rx			0.645		-	(1), (6)
		Ry			0.352		-	
	Green	Gx			0.280		-	
		Gy		Typ. - 0.03	0.600	Typ. + 0.03	-	
	Blue	Bx			0.143		-	
		By			0.065		-	
	White	Wx			0.313		-	
		Wy			0.329		-	
Viewing Angle	Horizontal	θ_x+	CR≥10	80	85	-	Deg.	(1), (6)
		θ_x-		80	85	-		
	Vertical	θ_Y+		80	85	-		
		θ_Y-		80	85	-		

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Note (1)   Definition of Viewing Angle (θx, θy):

Normal

$\theta x = \theta y = 0°$

$\theta y-$ $\theta y+$

$\theta X- = 90°$ x-

12 o'clock direction

$\theta y+ = 90°$

y+

$\theta x-$

$\theta x+$

6 o'clock   y-

x+ $\theta X+ = 90°$

$\theta y- = 90°$
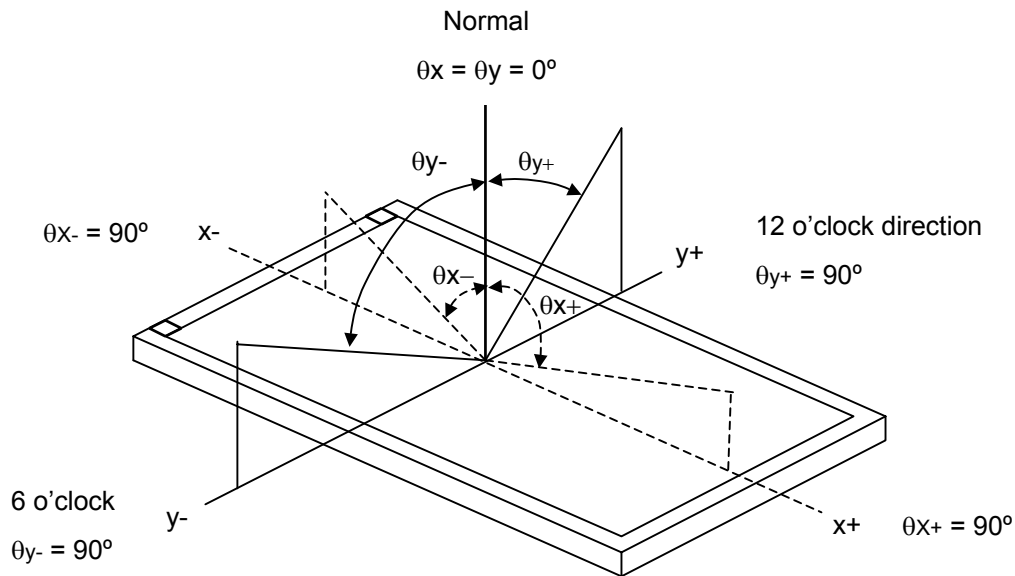
Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.
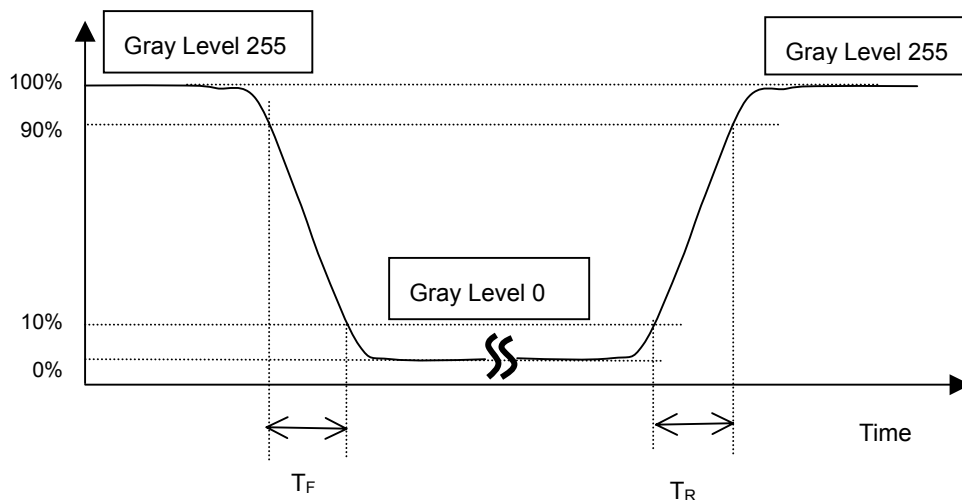
Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (7).

Note (3) Definition of Response Time (T_R, T_F):

Gray Level 255

Gray Level 255

100%

90%

Gray Level 0

10%

0%

Time

T_F

T_R

Note (4) Definition of Luminance of White (L_C):

Measure the luminance of gray level 255 at center point

$L_C = L$ (5)

L (x) is corresponding to the luminance of the point X at Figure in Note (7).

Note (5) Definition of Cross Talk (CT):

$CT = | Y_B - Y_A | / Y_A \times 100$ (%)

Where:

Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)
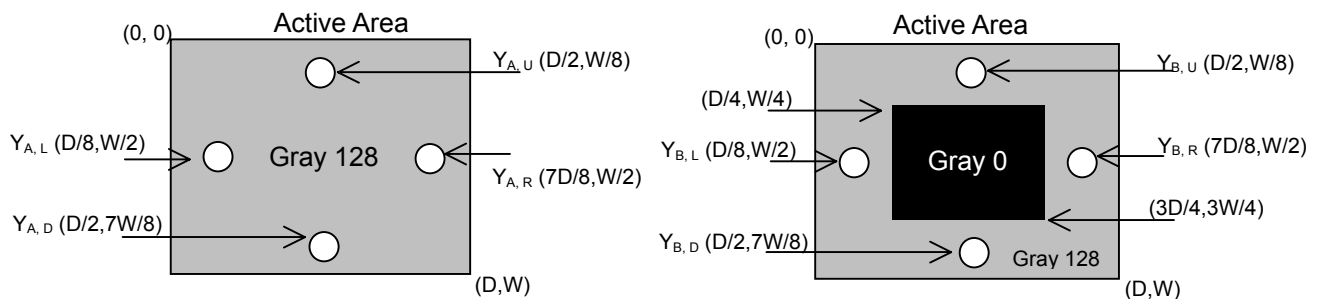

Note (6) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measurement. In order to stabilize the luminance, the measurement should be executed after lighting backlight unit for 20 minutes in a windless room.

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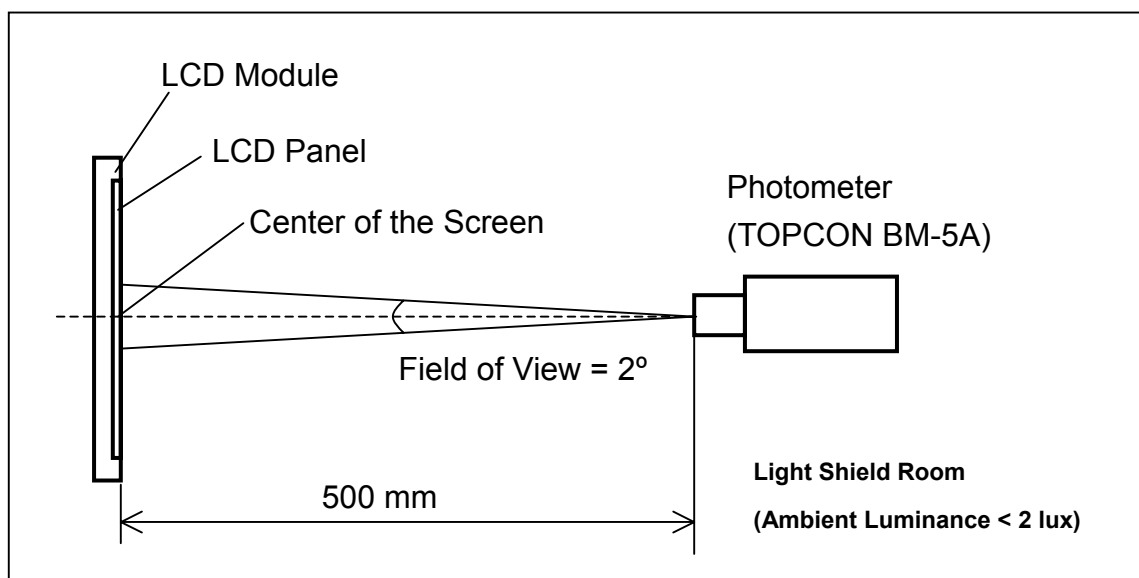
Note (7) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 9 points

$$\delta W = \frac{\text{Maximum } [L(1), L(2), L(3), L(4), L(5), L(6), L(7), L(8), L(9)]}{\text{Minimum } [L(1), L(2), L(3), L(4), L(5), L(6), L(7), L(8), L(9)]}$$

Active Area

Approval
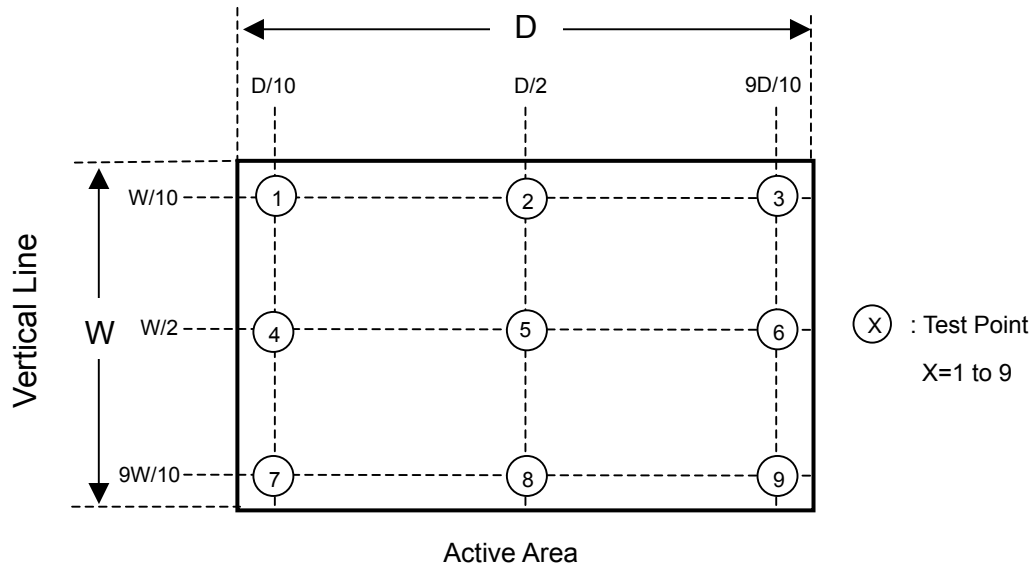
8. PRECAUTIONS

8.1 ASSEMBLY AND HANDLING PRECAUTIONS

(1) Bending or twisting forces to the module during assembly should be avoided.

(2) Please assemble the module into user's systems in clean working areas to prevent dusts and oils from causing electrical short-circuiting and from worsening the polarizer.

(3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.

(4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.

(5) Do not pull the I/F connector in or out while the module is operating.

(6) Do not disassemble the module.

(7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.

(8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.

(9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.

(10) When ambient temperature is lower than 10ºC may reduce the display quality. For example, the response time will become slowly, and the starting voltage of CCFL will be higher than room temperature.
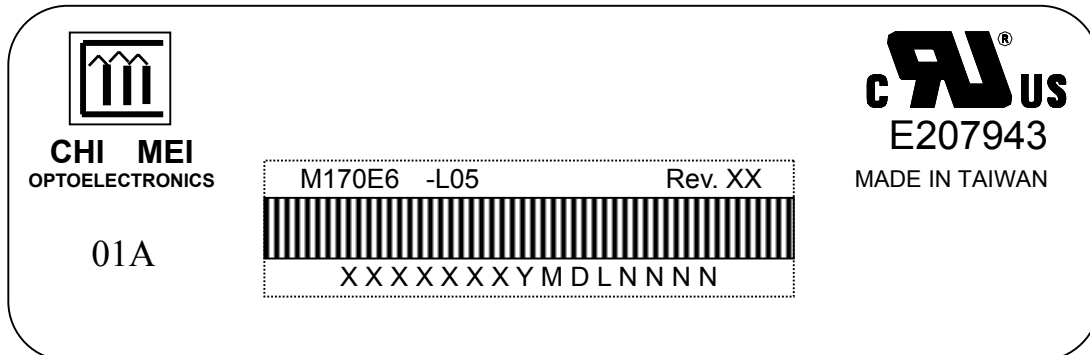
8.2 SAFETY PRECAUTIONS

(1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.

(2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

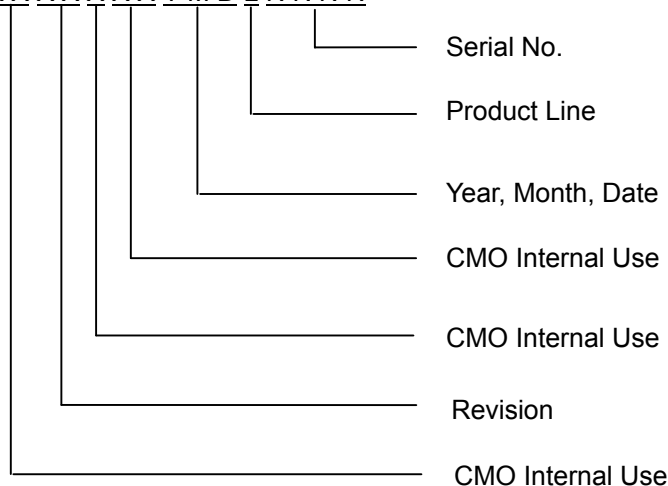(3) After the module's end of life, it is not harmful in case of normal operation and storage.

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9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.

CHI MEI
OPTOELECTRONICS

01A

M170E6 -L05   Rev. XX

X X X X X X X Y M D L N N N N

E207943
MADE IN TAIWAN

(a) Model Name: M170E6 -L05

(b) Revision: Rev. XX, for example: A0, A1… B1, B2… or C1, C2…etc.

(c) Serial ID: X X X X X X X Y M D L N N N N

- Serial No.
- Product Line
- Year, Month, Date
- CMO Internal Use
- CMO Internal Use
- Revision
- CMO Internal Use

Serial ID includes the information as below:

(a) Manufactured Date: Year: 1~9, for 2000~2009

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.

(b) Revision Code: Cover all the change

(c) Serial No.: Manufacturing sequence of product

(d) Product Line: 1 -> Line1, 2 -> Line 2, …etc.

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Approval

10. PACKAGING

10.1 PACKING SPECIFICATIONS

(1) 5 LCD modules / 1 Box

(2) Box dimensions: 537(L) X 316(W) X 462(H) mm

(3) Weight: approximately 12Kg (5 modules per box)

10.2 PACKING METHOD

(1) Carton Packing should have no failure in the following reliability test items.

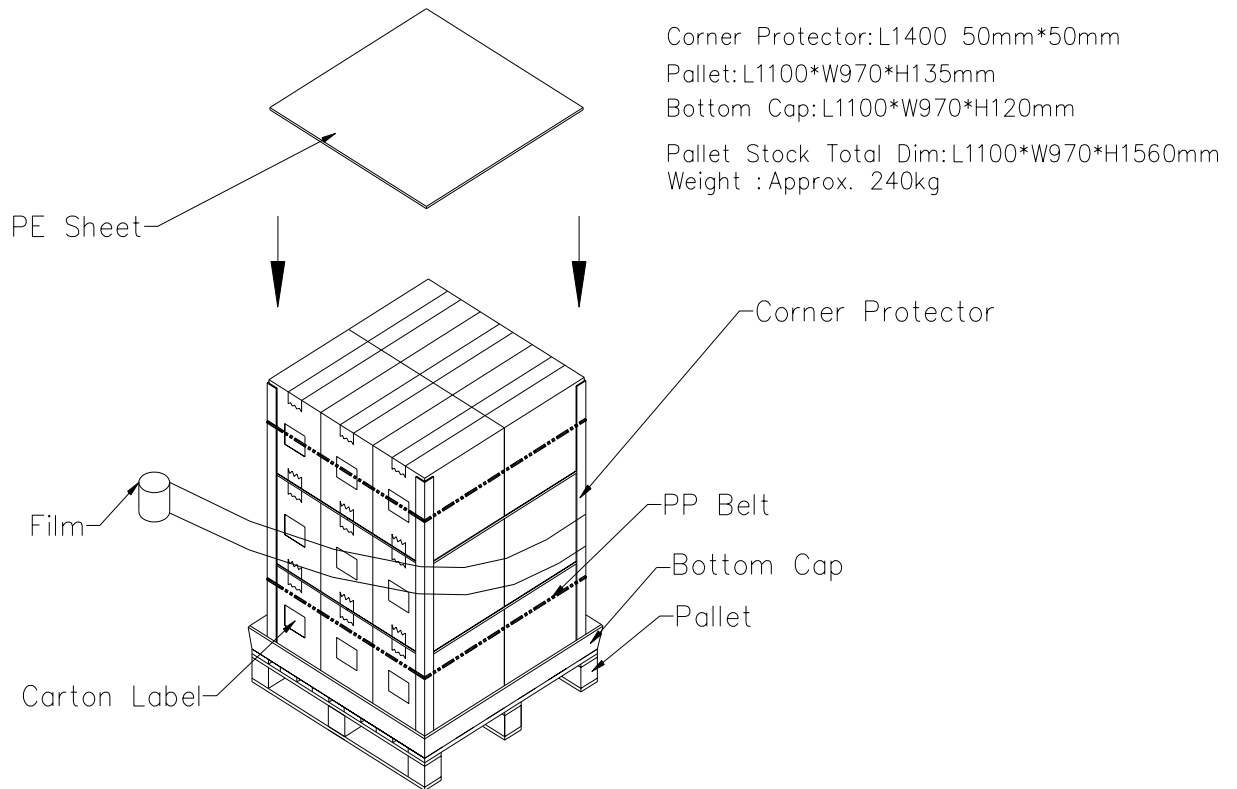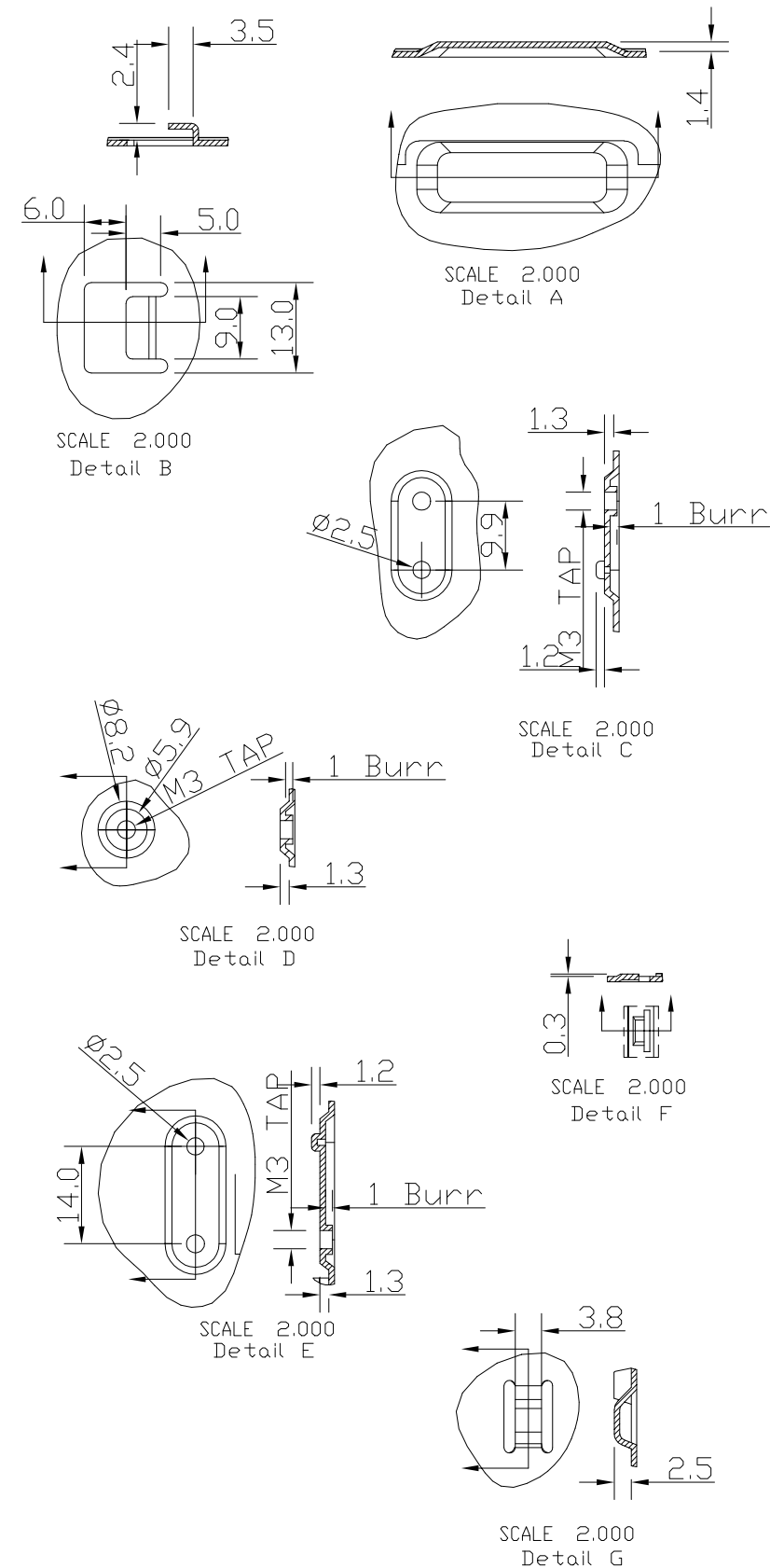| Test Item	Test Conditions	Note
Vibration	ISTA STANDARD Random, Frequency Range: 1 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation
Dropping Test	1 Angle, 3 Edge, 6 Face, 60cm	Non Operation

Box dimensions: 537(L)x316(W)x462(H)mm
Weight: Approx. 12Kg(5modules per 1 box)
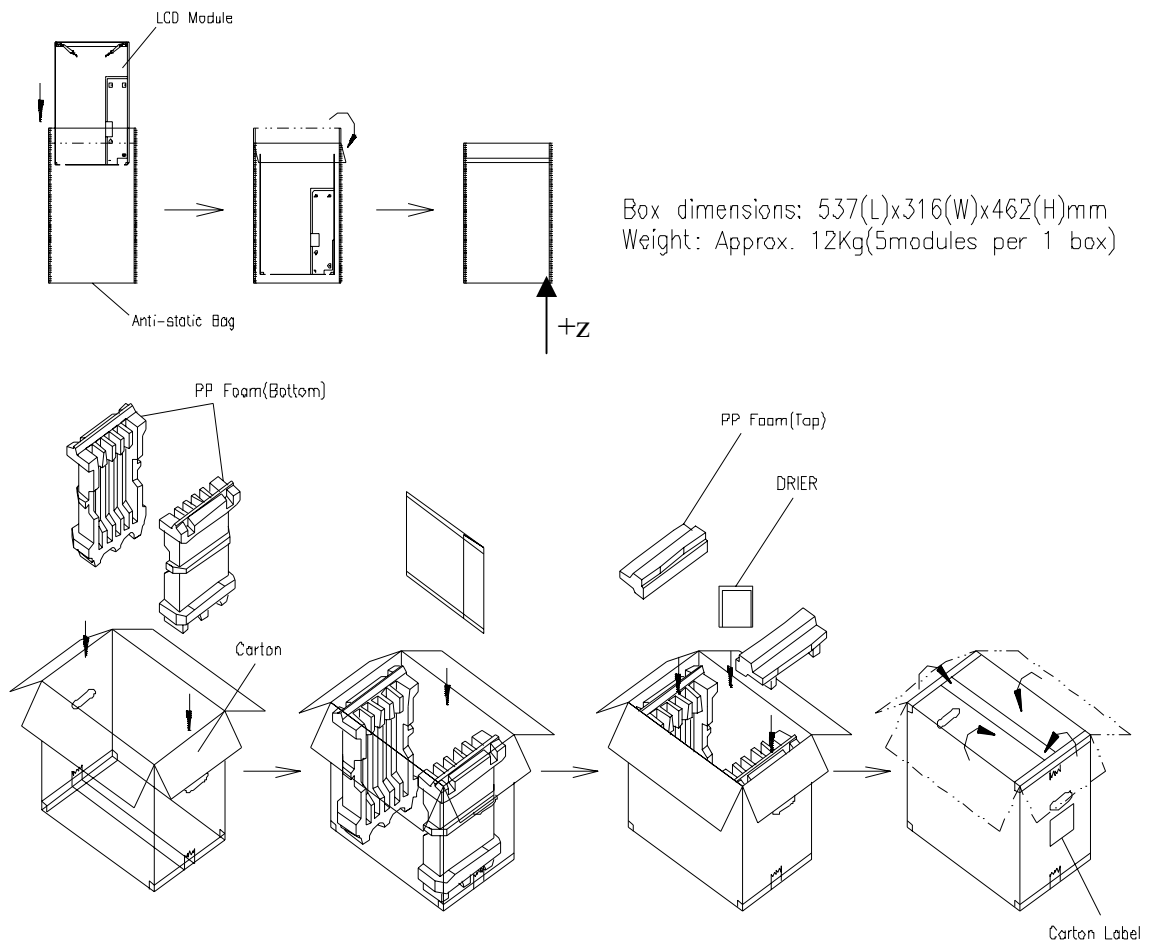
Figure. 10-1 Packing method

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CHI MEI
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Corner Protector: L1400 50mm*50mm
Pallet: L1100*W970*H135mm
Bottom Cap: L1100*W970*H120mm

Pallet Stock Total Dim: L1100*W970*H1560mm
Weight : Approx. 240kg

PE Sheet

Corner Protector

Film

PP Belt

Bottom Cap

Pallet

Carton Label
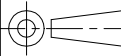
Figure. 10-2 Packing method

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BHSR-02VS-1(JST)
Connector1:
 Pin 1 Pink(HV)
 Pin 2 White(LV)

Connector2:
 Pin 1 Blue(HV)
 Pin 2 Black(LV)

FI-XB30SRL-HF11 or equivalent

Active Center

SCALE 2.000
Detail A

SCALE 2.000
Detail B

SCALE 2.000
Detail C

SCALE 2.000
Detail D

SCALE 2.000
Detail E

SCALE 2.000
Detail F

SCALE 2.000
Detail G

CHI MEI OPTOELECTRONICS CORP. 奇美電子股份有限公司					
APPROVED: Davis Wang	DWG NO. M17074103A				
CHECKED: Jemny Chang	DATE: 2003/04/09				
DESIGNER: Cliff Tsai	PART NO.:				
DRAWER: SW CHEN	REV.: A	UNIT:	SCALE:	SHEET:	
TITLE: OUTLINE M170E6-L03/L05		MM	1:1	2/2	
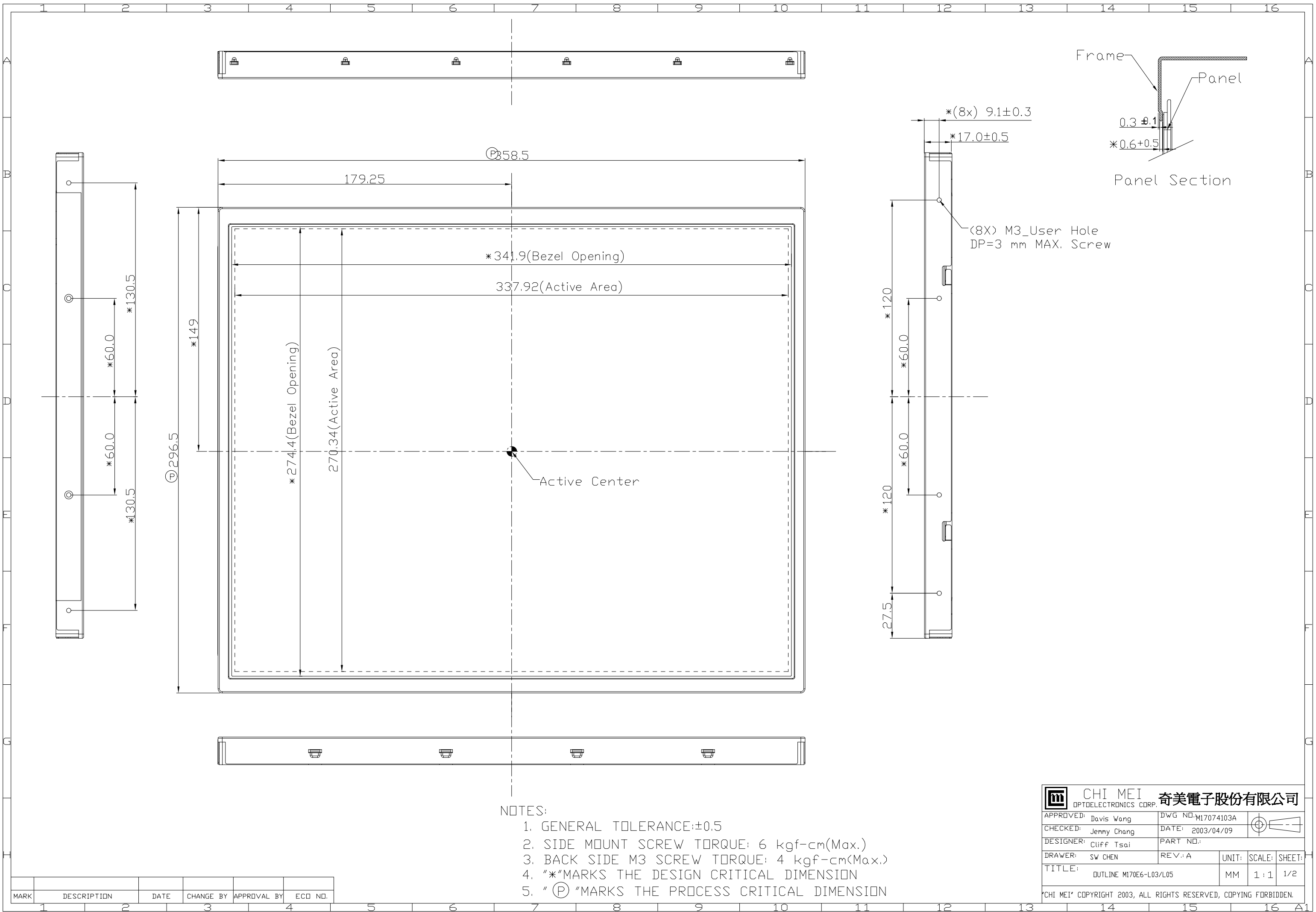
MARK | DESCRIPTION | DATE | CHANGE BY | APPROVAL BY | ECO NO.

Frame

Panel

Panel Section

0.3±0.1

*0.6+0.5

*(8x) 9.1±0.3

*17.0±0.5

(8X) M3_User Hole
DP=3 mm MAX. Screw

Ⓟ358.5

179.25

*341.9(Bezel Opening)

337.92(Active Area)

*274.4(Bezel Opening)

270.34(Active Area)

Active Center

*130.5

*60.0

*149

*60.0

*130.5

Ⓟ296.5

*120

*60.0

*60.0

*120

27.5

NOTES:
1. GENERAL TOLERANCE:±0.5
2. SIDE MOUNT SCREW TORQUE: 6 kgf-cm(Max.)
3. BACK SIDE M3 SCREW TORQUE: 4 kgf-cm(Max.)
4. "*"MARKS THE DESIGN CRITICAL DIMENSION
5. "Ⓟ"MARKS THE PROCESS CRITICAL DIMENSION