SPECIFICATION FOR APPROVAL

() Preliminar	y Specification
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(●) Final Specification

Title	17.1" WUXGA TFT LCD
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Customer	DELL
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.	
*MODEL	LP171WU1	
Suffix	TLA2	

^{*}When you obtain standard approval, please use the above model name without suffix

DATE

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	SIGNATURE
K.K. Jang / S.Manager	
REVIEWED BY	
N.J. Seong / Engineer	
PREPARED BY	
J.H. Kim / Engineer	
Product Engineering LG. Philips LCD Co	



Contents

No	ITEM		
	COVER	1	
	CONTENTS	2	
	RECORD OF REVISIONS	3	
1	GENERAL DESCRIPTION	4	
2	ABSOLUTE MAXIMUM RATINGS	5	
3	ELECTRICAL SPECIFICATIONS		
3-1	ELECTRICAL CHARACTREISTICS	6	
3-2	INTERFACE CONNECTIONS	8	
3-3	SIGNAL TIMING SPECIFICATIONS	9	
3-4	SIGNAL TIMING WAVEFORMS	9	
3-5	COLOR INPUT DATA REFERNECE	10	
3-6	POWER SEQUENCE	11	
4	OPTICAL SFECIFICATIONS	12	
5	MECHANICAL CHARACTERISTICS	16	
6	RELIABLITY	20	
7	INTERNATIONAL STANDARDS		
7-1	SAFETY	21	
7-2	EMC	21	
8	PACKING	l	
8-1	DESIGNATION OF LOT MARK	22	
8-2	PACKING FORM	22	
9	PRECAUTIONS	23	
А	APPENDIX. Enhanced Extended Display Identification Data	25	



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	Nov. 21. 2006	-	First Draft	
0.1	Dec. 6. 2006	4,8,9 17~19	Surface Treatment, Wire color, Signal Timing, Mechanical Drawing	
0.2	Feb. 2. 2007	6	Electrical characteristics	
		25~27	EDID Data	V0.0
0.3	Feb. 5. 2007	12~13	Color Coordinates, Gray Scale	
1.0	Mar. 19. 2007	9	Timing Table : DCLK(79MHz→76MHz)	V1.0
		12	Add the Min. Spec of Viewing Angle	
			Contrast Ration (400 → 500)	
		25~27	Change of EDID Data (SMBUS Step, DCLK)	
 				
 				
 				
[

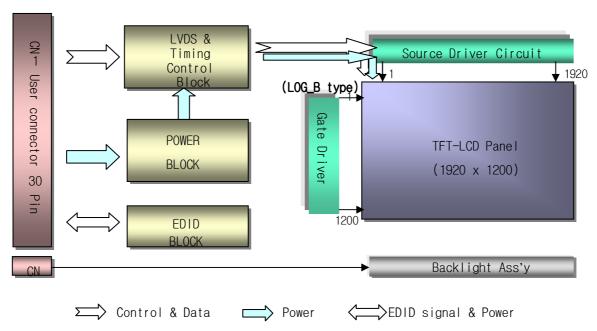


1. General Description

The LP171WU1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 17.1 inches diagonally measured active display area with WUXGA resolution(1920 horizontal by 1200 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP171WU1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP171WU1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP171WU1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	17.1 inches diagonal
Outline Dimension	382.7 (H, max) × 245.0 (V, max) × 6.6(D, max) mm
Pixel Pitch	0.19125 mm × 0.19125 mm
Pixel Format	1920 horiz. by 1200 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	260 cd/m ² (Typ.) , 5 point
Power Consumption	6.99 Watt (Typ.) @ LCM circuit 2.15 Watt(Typ.), B/L input 4.84 Watt(Typ.)
Weight	690g (Typ.) w/o inverter & bracket
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(4H) Glare treatment of the front polarizer

Ver. 1.0 Mar, 19, 2007 4 / 27



2. Absolute Maximum Ratings

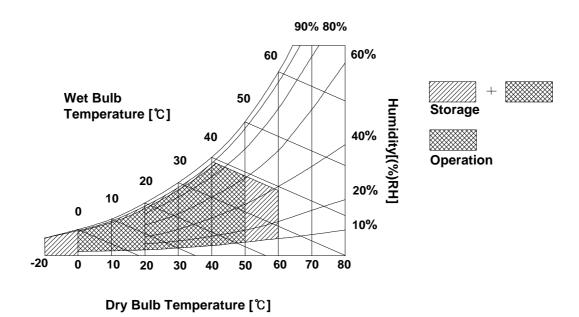
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
i arameter	Symbol	Min	Max	Offics	Notes
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature	Тор	0	50	°C	1
Storage Temperature	Нѕт	-20	60	°C	1
Operating Ambient Humidity	Нор	10	90	%RH	1
Storage Humidity	Нѕт	10	90	%RH	1

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 1.0 Mar, 19, 2007 5 / 27



3. Electrical Specifications

3-1. Electrical Characteristics

The LP171WU1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Symbol Unit Parameter Notes Min Max Тур MODULE: Power Supply Input Voltage VCC 3.3 3.0 3.6 V_{DC} Power Supply Input Current 650 750 mΑ I_{CC} **Power Consumption** Watt Рс 2.15 2.70 1 Differential Impedance 100 110 Ohm 2 Zm 90 LAMP : 710 745 930 Operating Voltage 3 V_{BL} V_{RMS} (6.8mA) (6.5mA)(3.0mA) 3.0 6.5 6.8 **Operating Current** $\mathsf{mA}_{\mathsf{RMS}}$ 4 I_{BL} 9 **Power Consumption** 4.84 P_{BL} 5.26 **Operating Frequency** 60 70 kHz 40 7 f_{BL} Discharge Stabilization Time 3 Min 5 Ts 15,000 Life Time Hrs 6 Established Starting Voltage 8 at 25 ℃ Vs 1300 V_{RMS} at 0 ℃ V_{RMS} 1500

Table 2. ELECTRICAL CHARACTERISTICS

Note)

- 1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The typical operating current $\,$ is for the typical surface luminance (L_{WH}) in optical characteristics.
- 5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.

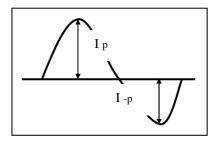
 Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 9. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

Ver. 1.0 Mar, 19, 2007 6 / 27



Note)

- 9. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 - It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

Ver. 1.0 Mar, 19, 2007 7 / 27



3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model FI-XB30SRL-HF11 manufactured by JAE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	BIST	Panel BIST control	1.1 LCD: DTML012(LCD Controller)
6	CIK EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	1.2 System : THC63LVD823A or equivalent
8	0dd_R _{IN} 0-	Negative LVDS differential data input	* Pin to Pin compatible with THINE LVDS
9	Odd_R _{IN} O+	Positive LVDS differential data input	
10	GND	Ground	2. Connector 2.1 LCD : FI-XB30SRL-HF11, JAE or
11	0dd_R _{IN} 1-	Negative LVDS differential data input	MDF76LARW-30S-1H, Hirose
12	0dd_R _{IN} 1+	Positive LVDS differential data input	equivalent. Locking design
13	GND	Ground	2.2 Mating : FI-X30M or equivalent. 2.3 Connector pin arrangement
14	0dd_R _{IN} 2-	Negative LVDS differential data input	2.5 Connector par arrangement
15	0dd_R _{IN} 2+	Positive LVDS differential data input	30 1
16	GND	Ground	_
17	0dd_CLKIN-	Negative LVDS differential clock input	
18	0dd_CLKIN+	Positive LVDS differential clock input	II OD Madala Dana Visusi
19	GND	Ground	[LCD Module Rear View]
20	Even_R _{IN} 0-	Negative LVDS differential data input	
21	Even_R _{IN} 0+	Positive LVDS differential data input	
22	GND	Ground	
23	Even_R _{IN} 1-	Negative LVDS differential data input	
24	Even_R _{IN} 1+	Positive LVDS differential data input	
25	GND	Ground	
26	Even_R _{IN} 2-	Negative LVDS differential data input	
27	Even_R _{IN} 2+	Positive LVDS differential data input	
28	GND	Ground	
29	Even_CLKIN-	Negative LVDS differential clock input	
30	Even_CLKIN+	Positive LVDS differential clock input	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

			. ,
Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored white and the low voltage side terminal is yellow.



3-3. Signal Timing Specifications

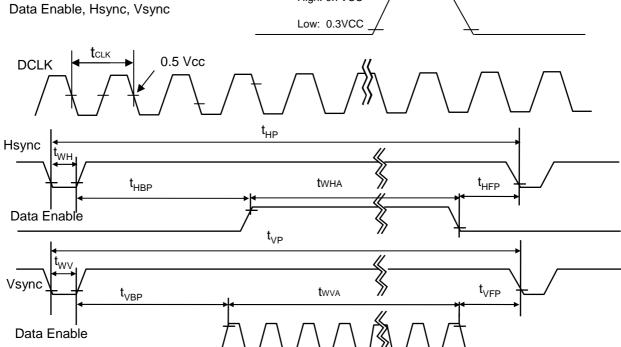
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	73.0	76.0	79.0	MHz	
Hsync	Period	tHP	1008	1040	1072		
	Width	twn	16	16	16	tclk	
	Active	twha	960	960	960		
Vsync	Period	tvp	1213	1235	1278		
	Width	tw∨	6	6	6	tHP	
	Active	twva	1200	1200	1200		
Data	Horizontal back porch	tHBP	24	40	56	tour	
Enable	Horizontal front porch	tHFP	8	24	40	tclk	
	Vertical back porch	tvbp	6	26	48	tup	
	Vertical front porch	tvfp	1	3	24	tHP	

3-4. Signal Timing Waveforms

Condition : VCC =3.3V High: 0.7VCC



Ver. 1.0 Mar, 19, 2007 9 / 27



3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

	Input Color Data																		
	Color			RE	D					GRE	EN					BL	UE		
`	50101	MSE	3				LSB		3				LSB	MSE	3				LSB
	,	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0			0	0	0	0		0	0	0	0	0		0	0	0
	Red	1	1	.1	. 1	. 1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE											 								
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	 1	 1		0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1		 1	1	1
	- (/							<u> </u>											

Ver. 1.0 Mar, 19, 2007 10 / 27



3-6. Power Sequence

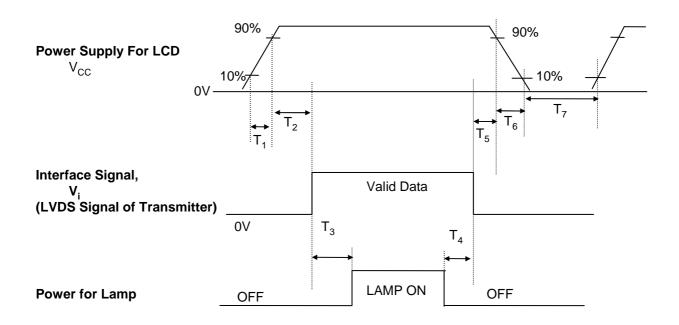


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	-	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	•	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

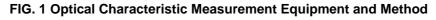
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



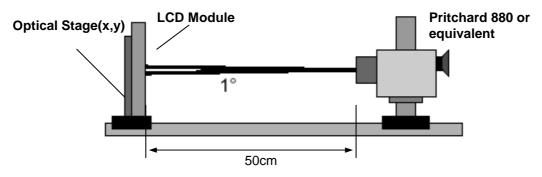


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 76.0MHz, lout = 6.5mA

Parameter	Cumbal		Values		Units	Notes
Parameter	Symbol	Min	Тур	MAx	Units	notes
Contrast Ratio	CR	500				1
Surface Luminance, white	L _{WH}	220	260		cd/m ²	2
Luminance Variation	δ _{WHITE}			2.0		3
Response Time						4
Rise Time+Decay Time	$Tr_{R+}Tr_{D}$		16	25	ms	
Color Coordinates]					±0.03
RED	RX	0.584	0.614	0.644		
	RY	0.323	0.353	0.383		
GREEN	GX	0.291	0.321	0.351		
	GY	0.531	0.561	0.591]	
BLUE	BX	0.122	0.152	0.182]	
	BY	0.094	0.124	0.154]	
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	60	65		degree	
x axis, left (Φ=180°)	Θl	60	65		degree	
y axis, up (Φ=90°)	Θu	50	55		degree	
y axis, down (Φ=270°)	Θd	50	55		degree	
Gray Scale						6

Ver. 1.0 Mar, 19, 2007 12 / 27



Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the 5point (1~5) average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2. When I_{BL} = 6.5mA, L_{WH} =260cd/m²(Typ.)
- 3. Luminance % uniformity is measured for 13 point For more information see FIG 2. δ WHITE = Maximum(LN1,LN2, LN13) ÷ Minimum(LN1,LN2, LN13)
- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

* f_{\/}=60Hz

Gray Level	Luminance [%] (Typ)
LO	0.13
L7	0.43
L15	3.05
L23	
L31	18.3
L39	33.4
L47	54.3
L55	81.1
L63	100

Ver. 1.0 Mar, 19, 2007 13 / 27



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

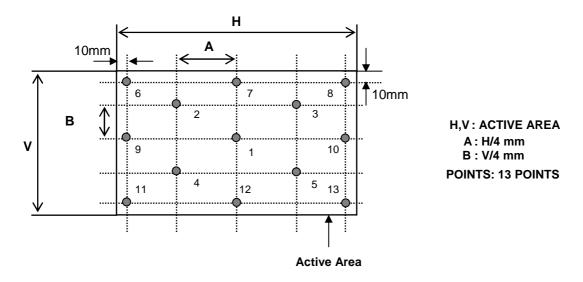


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

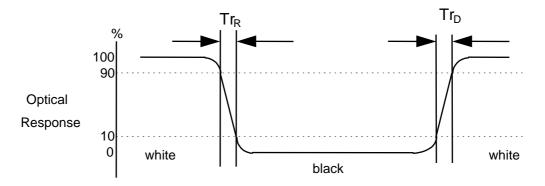
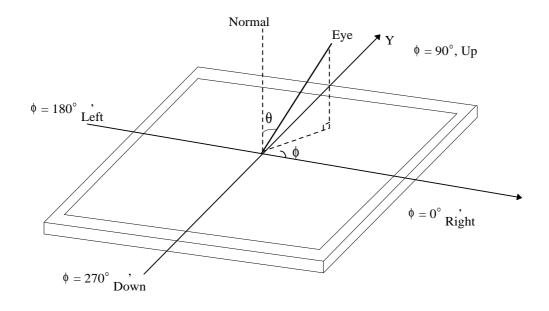




FIG. 4 Viewing angle

<Dimension of viewing angle range>





5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP171WU1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

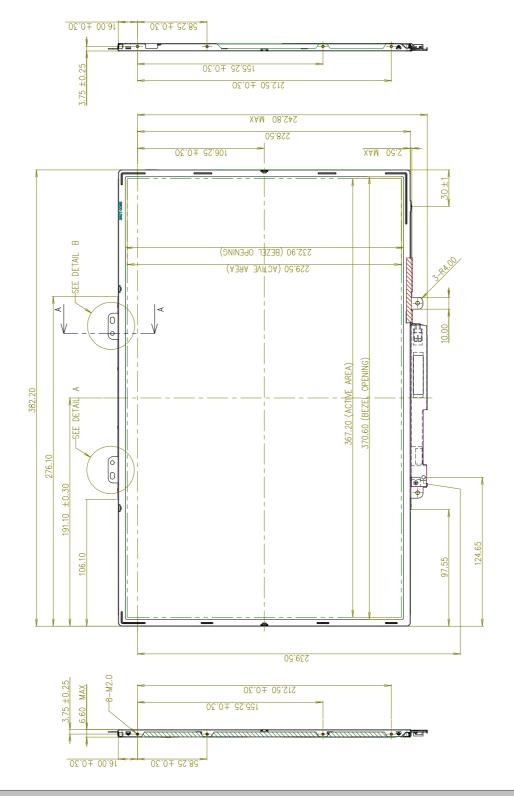
	Horizontal	382.2 ± 0.5mm			
Outline Dimension	Vertical	244.5 ± 0.5mm			
	Depth	$6.3\pm0.3\text{mm}$			
Bezel Area	Horizontal	370.6 ± 0.5mm			
bezei Alea	Vertical	232.9 ± 0.5mm			
Active Diapley Area	Horizontal	367.2 mm			
Active Display Area	Vertical	229.5 mm			
Weight	705g (Max) w/o inverter & bracket				
Surface Treatment	Surface Treatment Hard coating(4H) Glare treatment of the front polarizer				

Ver. 1.0 Mar, 19, 2007 16 / 27



<FRONT VIEW>

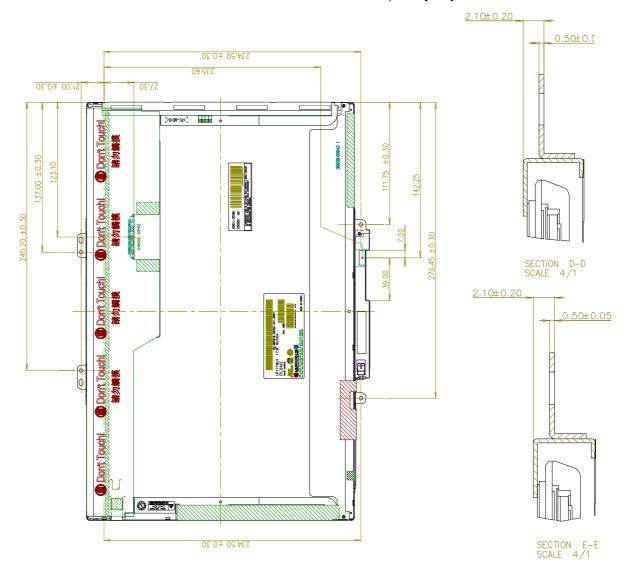
Note) Unit:[mm], General tolerance: \pm 0.5mm





<REAR VIEW>

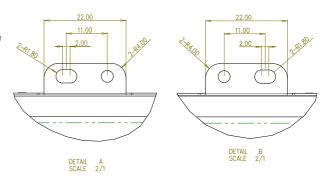
Note) Unit:[mm], General tolerance: ± 0.5mm



**** PPID Label revision:**

It is subject to change with Dell event. Please refer to the below table for detail.

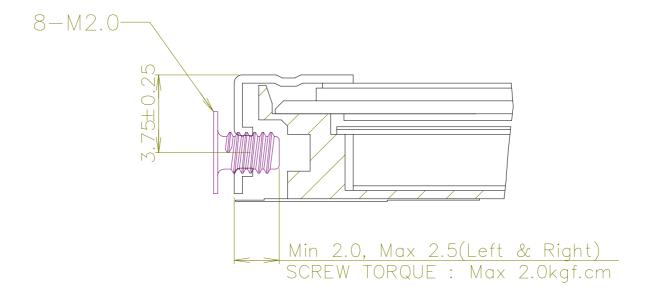
Classification	SSI	PT	ST	QT
No Change	X00	X10	X20	A00
1st Revision	X01	X11	X21	A01
2nd Revision	X02	X12	X22	A02
3rd Revision	X03	X13	X23	A03





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]

*Screw Torque (8 point): Max. 2Kgf.cm



Ver. 1.0 Mar, 19, 2007 19 / 27



6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis					
6	Shock test (non-operating)	 No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays 					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Ver. 1.0 Mar, 19, 2007 20 / 27



7. International Standards

7-1. Safety

a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A1: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+A3: 1995+A1: 1996

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K	L	М
---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH F: FACTORY CODE

G: ASSEMBLY CODE H, I, J, K, L, M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Ī	Mark	1	2	3	4	5	6	7	8	9	Α	В	С

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG		
Mark	K	С	D		

4. SERIAL NO.

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 10 pcs

b) Box Size: 494mm × 281mm × 331mm

Ver. 1.0 Mar, 19, 2007 22 / 27



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 1.0 Mar, 19, 2007 23 / 27



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 1/3

Byte#	Byte#	Field Magnes and Community	Va	lue	Value		
(decimal)	(HEX)	Field Name and Comments	(H	EX)	(bir	nary)	
0	00	Header	0	0	0000	0000	
1	01	Header	F	F	1111	1111	
2	02	Header	F	F	1111	1111	
3	03	Header	F	F	1111	1111	Header
4	04	Header	Ę	E	1111	1111	
5	05	Header	F	F	1111 1111	1111	
<u>6</u> 7	06 07	Header Header	0	0	0000	1111	
8	08	EISA manufacturer code(3 Character ID) = LPL	3	2	0011	0010	
9	09	EISA manufacture code (Compressed ASCII)	0	C	0000	1100	
10	0A	Panel Supplier Reserved - Product code	0	0	0000	0000	
11	0B	Panel Supplier Reserved - Product code	Ē	4	1110	0100	
12	0C	LCD Module Serial No. = 0 (If not used)	0	0	0000	0000	Vender/
13	0D	LCD Module Serial No. = 0 (If not used)	0	0	0000	0000	Product ID
14	0E	LCD Module Serial No. = 0 (If not used)	0	0	0000	0000	FIOUUCLID
15	0F		0	0	0000		
		LCD Module Serial No. = 0 (If not used)		***********		0000	
16	10	Week of Manufacture = 00	0	0	0000	0000	
17	11	Year of Manufacture = 2006	1	0	0001	0000	EDID Vanalas /
18 19	12 13	EDID Structure version # = 1 EDID Revision # = 3	0	3	0000	0001	EDID Version/
20	14	Video Input Definition = Digital I/P.non TMDS CRGB	8	0	1000	0011	Revision
21	15	Max H image size = 36.72cm(37)	2	5	0010	0101	Display
22	16	Max V image size = 22.95cm(23)	1	7	0001	0111	Parameter
23	17	Display gamma = (2.2×100) - 100 = 120	7	8	0111	1000	1 didilicioi
24	18	Feature support (no DPMS, Active off, RGB, timing BLK 1)	0	Ā	0000	1010	
25	19	Red/Green Low bit (RxRy/GxGy)	5	6	0101	0110	
26	1A	Blue/White Low bit (BxBy/WxWy)	3	5	0011	0101	
27	1B	Red X Rx = 0.614	9	D	1001	1101	
28	1C	Red Y Ry = 0.353	5	Α	0101	1010	
29	1D	Green X Gx = 0.321	5	2	0101	0010	Color
30	1E	Green Y Gy = 0.561	8	F	1000	1111	Characteristic
31	1F	Blue X Bx = 0.152	2	7 F	0010	0111	
32	20	Blue Y By = 0.124	1 5	0	0001	1111	
33 34	<u>21</u> 22	White X	5	4	0101	0100	
35	23	Established timings 1 (00h if not used)	0	0	0000	0000	Established
36	24	Established timings 2 (00h if not used)	0	0	0000	0000	Timings
37	25	Manufacturer's timings (00h if not used)	0	0	0000	0000	· · · · · · · · · · · · · · · · · · ·
38	26	Standard Timing Identification 1 was not used	0	1	0000	0000	
39	27	Standard Timing Identification 1 was not used	0	1	0000	0001	
40	28	Standard Timing Identification 2 was not used	0	1	0000	0001	
41	29	Standard Timing Identification 2 was not used	0	1	0000	0001	
42	29 2A	Standard Timing Identification 3 was not used	0	1	0000	0001	
43	2B	Standard Timing Identification 3 was not used Standard Timing Identification 3 was not used	0	1	0000	0001	
44	2C	Standard Timing Identification 3 was not used	0	1	0000	0001	Standard
				4			
45	2D	Standard Timing Identification 4 was not used	0	1	0000	0001	Timing ID
46	2E	Standard Timing Identification 5 was not used	0	1	0000	0001	
47	2F	Standard Timing Identification 5 was not used	0	1	0000	0001	
48	30	Standard Timing Identification 6 was not used	0	1	0000	0001	
49	31	Standard Timing Identification 6 was not used	0	1	0000	0001	
50	32	Standard Timing Identification 7 was not used	0	1	0000	0001	
51	33	Standard Timing Identification 7 was not used	0	1	0000	0001	
52	34	Standard Timing Identification 8 was not used	0	1	0000	0001	
53	35	Standard Timing Identification 8 was not used	0	1	0000	0001	



APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 2/3

Byte#	Byte#	5.111	Va	lue	Va	lue	
(decimal)	(HEX)	Field Name and Comments	(HI	EX)	(bir	nary)	
54	36	Pixel Clock/10,000 (LSB)	6	0	0110	0000	
55	37	Pixel Clock/10,000 (MSB) / 1920 x 1200 @ 60Hz pixel clock =	3	В	0011	1011	
56	38	Horizontal Active = 1920 pixels	8	0	1000	0000	
57	39	Horizontal Blanking = 160 pixels	Α	0	1010	0000	
58	3A	Horizontal Active: Horizontal Blanking = 1920: 160	7	0	0111	0000	
59	3B	Vertical Avtive = 1200 lines	В	0	1011	0000	
60	3C	Vertical Blanking = 35 lines	2	3	0010	0011	Detailed
61	3D	Vertical Active: Vertical Blanking = 1200: 35	4	0	0100	0000	Timing
62	3E	Horizontal Sync. Offset = 48 pixels	3	0	0011	0000	Description
63	3F	Horizontal Sync Pulse Width = 32 pixels	2	0	0010	0000	#1
64	40	Vertical Sync Offset = 3 lines : Sync Width = 6 lines	3	6	0011	0110	
65	41	Horizontal Vertical Sync Offset/Width upper 2bits = 0	0	0	0000	0000	
66	42	Horizontal Image Size =36.720 cm	6	F	0110	1111	
67	43	Vertical image Size = 22.95 cm	E	6	1110	0110	
68	44	Horizontal & Vertical Image Size	1	0	0001	0000	
69	45	Horizontal Border = 0	0	0	0000	0000	
70	<u>46</u>	Vertical Border = 0	0	0	0000	0000	
71	47	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol ne	1	9	0001	1001	
72	48	Pixel Clock/10,000 (LSB)	6	0	0110	0000	
73	49	Pixel Clock/10,000 (MSB) / 1920 x 1200 @ 60Hz pixel clock =	3	В	0011	1011	
74	4A	Horizontal Active = 1920 pixels	8	0	1000	0000	
75	4B	Horizontal Blanking = 160 pixels	<u>A</u>	0	1010	0000	
76	4C	Horizontal Active: Horizontal Blanking = 1920: 160	7	0	0111	0000	
77	4D	Vertical Avtive = 1200 lines	B	0	1011	0000	5
78	4E	Vertical Blanking = 35 lines	2	3	0010	0011	Detailed
79	4F	Vertical Active: Vertical Blanking = 1200: 35	4	0	0100	0000	Timing
80	50	Horizontal Sync. Offset = 48 pixels	3	0	0011	0000	Description
81	51	Horizontal Sync Pulse Width = 32 pixels	2	0	0010	0000	#2
82	52	Vertical Sync Offset = 3 lines : Sync Width = 6 lines	3	6	0011	0110	
83 84	<u>53</u> 54	Horizontal Vertical Sync Offset/Width upper 2bits = 0 Horizontal Image Size =36.720 cm	6	0 F	0000	0000 1111	
	55	Vertical image Size = 22.95 cm	E	6	1110		
85 86	56	Horizontal & Vertical Image Size	1	0	0001	0110 0000	
87	56 	Horizontal Border = 0	0	0	0000	0000	
88	58	Vertical Border = 0	0	0	0000	0000	
89	59	Module "A" Revision = 00	0	0	0000	0000	
90	5A	Flag	0	0	0000	0000	
91	5B	Flag	0	0	0000	0000	
92	5C	Flag	0	0	0000	0000	
93	5D	Dummy Descriptor	F	Ē	1111	1110	
94	5E	Flag	0	0	0000	0000	
95	5F	Dell P/N 1st Character = R	5	2	0101	0010	
96	60	Dell P/N 2nd Character = P	5	0	0101	0000	Detailed
97	61	Dell P/N 3nd Character = 6	3	6	0011	0110	Timing
98	62	Dell P/N 4th Character = 1	3	1	0011	0001	Description
99	63	Dell P/N 5th Character = 6	3	6	0011	0110	#3
100	64	LCD Supplier EEDID Revision # = 0.0	0	0	0000	0000	
101	65	Manufacturer P/N = 1	3	1	0011	0001	
102	66	Manufacturer P/N = 7	3	7	0011	0111	
103	67	Manufacturer P/N = 1	3	1	0011	0001	
104	68	Manufacturer P/N = W	5	7	0101	0111	
105	69	Manufacturer P/N = U	5	5	0101	0101	
106	6A	Manufacturer P/N = 1	3	1	0011	0001	
107	6B	/N(If <13 char, then terminate with ASCII code 0Ah, set remai	0	Α	0000	1010	



APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte#	Byte#	Field Name and Comments		lue	Value		
(decimal)	(HEX)	Field Name and Comments	(HEX)		(binary)		
108	6C	Flag	0	0	0000	0000	
109	6D	Flag	0	0	0000	0000	
110	6E	Flag	0	0	0000	0000	
111	6F	Data Type Tag: ASCII String	F	E	1111	1110	
112	70	Flag	0	0	0000	0000	
113	71	SMBUS Value = 10 nits	1	Ε	0001	1110	
114	72	SMBUS Value = 17 nits	2	D	0010	1101	Detailed
115	73	SMBUS Value = 24 nits	3	7	0011	0111	Timing
116	74	SMBUS Value = 30 nits	3	E	0011	1110	Description
117	75	SMBUS Value = 60 nits	5	Α	0101	1010	#4
118	76	SMBUS Value = 110 nits	9	D	1001	1101	
119	77	SMBUS Value = 180 nits	С	E	1100	1110	
120	78	SMBUS Value = max nits (Typically = FFh, 260 nits)	F	F	1111	1111	
121	79	Number of LVDS receiver chips = 1 or 2	0	2	0000	0010	
122	7A	BIST Enable: Yes = '01' No = '00'	0	1	0000	0001	
123	7B	char, then terminate with ASCII code 0Ah, set remaining cha	0	Α	0000	1010	
124	7C	(If<13 char, then terminate with ASCII code 0Ah)	2	0	0010	0000	
125	7D	(If<13 char, then terminate with ASCII code 0Ah)	2	0	0010	0000	
126	7E	Extension flag = 00	0	0	0000	0000	Extension Flag
127	7F	Checksum	3	Е	0011	1110	Checksum

Ver. 1.0 Mar, 19, 2007 27 / 27