# SPECIFICATION FOR APPROVAL

(	)	Preli	minai	ry Sp	ecifi	cation
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### ( ● ) Final Specification

Title 17.1" WUXGA TFT LCD	
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BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.	
*MODEL	LP171WU1	
Suffix	A4	

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

SIGNATURE	DATE
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Ver. 0.1 2. SEP. 2004 1 / 27



# Contents

No	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	
3-1	ELECTRICAL CHARACTREISTICS	6
3-2	INTERFACE CONNECTIONS	7
3-3	SIGNAL TIMING SPECIFICATIONS	9
3-4	SIGNAL TIMING WAVEFORMS	9
3-5	COLOR INPUT DATA REFERNECE	10
3-6	POWER SEQUENCE	11
4	OPTICAL SFECIFICATIONS	12
5	MECHANICAL CHARACTERISTICS	16
6	RELIABLITY	20
7	INTERNATIONAL STANDARDS	]
7-1	SAFETY	21
7-2	EMC	21
8	PACKING	<u> </u>
8-1	DESIGNATION OF LOT MARK	22
8-2	PACKING FORM	22
9	PRECAUTIONS	23
Α	APPENDIX. Enhanced Extended Display Identification Data	25
В	APPENDIX. Inspection Criteria	28

Ver. 0.1 2. SEP. 2004 2 / 27



### **RECORD OF REVISIONS**

Revision No	Revision Date	Page	Description	Note
0.1	2.AUG.2004	22	FACTORY CODE added (HEESUNG)	
0.1	2.AUG.2004	25	EISA manufacturer code(3 Character ID) = LGP => LPL	
0.1	2.AUG.2004	25	Panel Supplier Reserved - Product code(10/11:5E/B1 => 00/00)	
0.1	2.AUG.2004	25	Checksum (91 => 81)	
			( The new-established EDID by LPL standard)	
			( Add the Bist function in the EDID, $7A(HEX): 00 \rightarrow 01$ )	
0.2	3.SEP.2004	25	Check sum (81 => 80)	
			(Change the EDID Format by Dell)	
0.3	10.SEP.2004	25	Changing hexa & binary code format in the EDID	
0.4	08.OCT.2004	25	Changing Hsync timing in the EDID	
			( Add the Jitter issue improvement )	
0.5	18.OCT.2004	25	Changing Timing #2 Block in the EDID	
				······
				·····

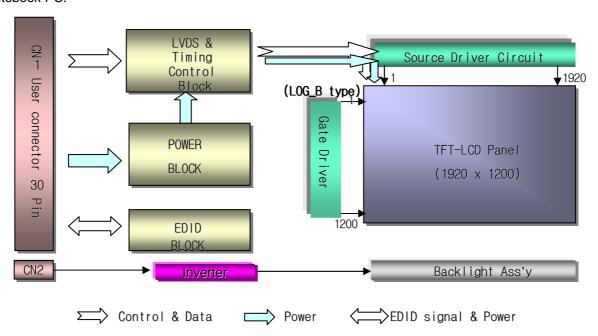


### 1. General Description

The LP171WU1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 17.1 inches diagonally measured active display area with WUXGA resolution(1920 horizontal by 1200 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP171WU1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP171WU1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP171WU1(A4) characteristics provide an excellent flat display for office automation products such as Notebook PC.



#### **General Features**

Active Screen Size	17.1 inches diagonal
Outline Dimension	382.7 (H) × 245.0 (V) × 6.6(D, max) mm
Pixel Pitch	0.191 mm × 0. 191 mm
Pixel Format	1920 horiz. by 1200 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	190 cd/m²(Typ.) , 5 point
Power Consumption	Total 6.81 Watt(Typ.) @ LCM circuit 2.01 Watt(Typ.), B/L input 4.8 Watt(Typ.)
Weight	705 g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

Ver. 0.1 2. SEP. 2004 4 / 27



### 2. Absolute Maximum Ratings

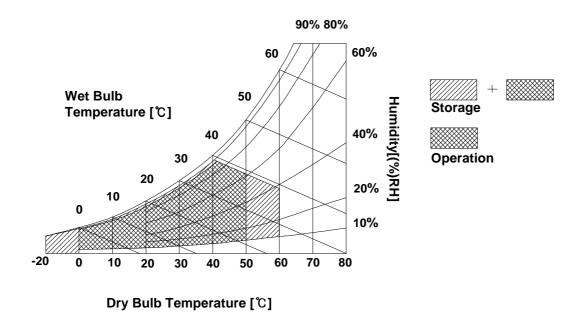
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Offics	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 0.1 2. SEP. 2004 5 / 27



### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

The LP171WU1(A4)requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Unit Parameter Symbol Notes Min Max Тур MODULE: Power Supply Input Voltage VCC 3.3 3.0 3.6  $V_{DC}$ 540 Power Supply Input Current 600 660 mΑ  $I_{CC}$ 2.01 Watt **Power Consumption** Рс 1 Differential Impedance 100 Ohm 2 Zm 90 110 LAMP : 735 920 714 Operating Voltage 3  $V_{BL}$  $V_{\mathsf{RMS}}$ (6.8mA) (6.5mA)(3.0mA)4 3.0 6.5 6.8  $\mathsf{mA}_{\mathsf{RMS}}$ **Operating Current** I<sub>BL</sub> 9 **Power Consumption** 4.8 4.9  $\mathsf{P}_\mathsf{BL}$ Operating Frequency 60 70 kHz 40 7 f<sub>BL</sub> 3 Discharge Stabilization Time Min 5 Ts 10,000 Life Time Hrs 6 Established Starting Voltage 8 at 25 ℃ Vs 1300  $V_{RMS}$ at 0 ℃  $V_{\mathsf{RMS}}$ 1500

Table 2. ELECTRICAL CHARACTERISTICS

#### Note)

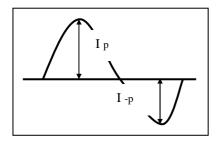
- 1. The specified current and power consumption are under the Vcc = 3.3V,  $25^{\circ}C$ , fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is  $\pm$  10%.
- 4. The typical operating current  $\,$  is for the typical surface luminance ( $L_{WH}$ ) in optical characteristics.
- 5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.
  Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 9. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

Ver. 0.1 2. SEP. 2004 6 / 27



#### Note)

- 9. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
  - It shall help increase the lamp lifetime and reduce leakage current.
    - a. The asymmetry rate of the inverter waveform should be less than 10%. b. The distortion rate of the waveform should be within  $\sqrt{2 \pm 10\%}$ .
      - \* Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

Ver. 0.1 2. SEP. 2004 7 / 27



#### 3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	Reserved for supplier test point	1.1 LCD: KZ4E053G23(LCD Controller)
6	CIK EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	(THINE, THC63LVD824) 1.2 System : THC63LVDF823A or equivalent
8	R <sub>IN</sub> 0-	Odd channel differential data input	1.2 Gyotom: Theodev Brozer, or equivalent
9	R <sub>IN</sub> 0+	Odd channel differential data input	
10	GND	Ground	2. Connector 2.1 LCD : FI-XB30SRL-HF11(JAE) or
11	R <sub>IN</sub> 1-	Odd channel differential data input	its compatibles (Hirose)
12	R <sub>IN</sub> 1+	Odd channel differential data input	2.2 Mating : FI-X30M or equivalent.
13	GND	Ground	2.3 Connector pin arrangement
14	R <sub>IN</sub> 2-	Odd channel differential data input	30 
15	R <sub>IN</sub> 2+	Odd channel differential data input	
16	GND	Ground	
17	CLKIN-	Odd channel differential clock input	[LCD Module Rear View]
18	CLKIN+	Odd channel differential clock input	
19	GND	Ground	
20	RA2-	Even channel differential data input	
21	RA2+	Even channel differential data input	
22	GND	Ground	
23	RB2-	Even channel differential data input	
24	RB2+	Even channel differential data input	
25	GND	Ground	
26	RC2-	Even channel differential data input	
27	RC2+	Even channel differential data input	
28	GND	Ground	
29	RCLK2-	Even channel differential clock input	
30	RCLK2+	Even channel differential clock input	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible [(1674817-2(AMP)].

The mating connector part number is SM02B-BHSS-1-TB or equivalent [1-1565647-3(AMP)].

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

Table 9: BACKEICHT CONNECTION THE CONTROL (03)						
Pin	Symbol	Description	Notes			
1	HV	Power supply for lamp (High voltage side)	1			
2	LV	Power supply for lamp (Low voltage side)	1			

Notes: 1. The high voltage side terminal is colored pink and the low voltage side terminal is yellow.



### 3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

**Table 6. TIMING TABLE** 

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	80	81	82	MHz	
Hsync	Period	tHP	1030	1080	1170		
	Width	twn	8	52	104	tclk	
	Active	twha	960	960	960		
Vsync	Period	t∨P	1207	1250	1400		
	Width	tw∨	1	3	6	tHP	
	Active	twva	1200	1200	1200		
Data	Horizontal back porch	tHBP	8	-	•	tour	
Enable	Horizontal front porch	tHFP	8	-	1	tCLK	
	Vertical back porch	tvbp	5	-	-	tup	
	Vertical front porch	tvfp	1	-	-	tHP	

## 3-4. Signal Timing Waveforms (Normal status) Cautions

- Case1: BIST status

VCC =3.3V, No video signal

- Case 2: System Power On ⇔ Maker Logo ⇔ Window logo ⇔ Log On

DE Blanking time( tWV+ tVBP+ tVFP) ≤ 71 tHP

If not, LCD will abnormally display

High: 0.7VCC (ex: There is brief flash when booting windows) Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc DCLK t<sub>HP</sub> Hsync **t**WHA  $t_{HFP}$ t<sub>HBP</sub> Data Enable  $t_{VP}$  $t_{VFP}$  $t_{VBP}$ **t**wva Data Enable

 Ver. 0.1
 2. SEP. 2004
 9 / 27



### 3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

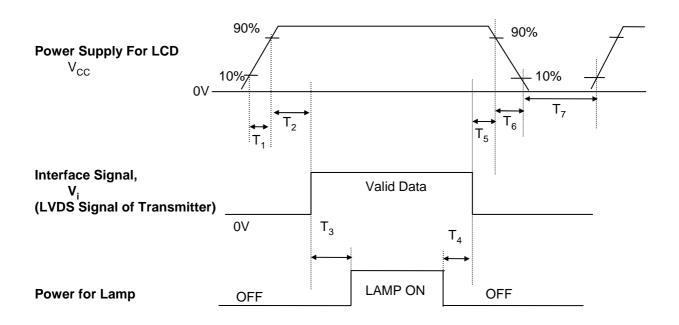
Table 7. COLOR DATA REFERENCE

									Inp	ut Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	30101	MSI	3				LSB	MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	1	1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																	 		••••
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	1	1
	1 ' '	I																	

Ver. 0.1 2. SEP. 2004 10 / 27



### 3-6. Power Sequence



**Table 8. POWER SEQUENCE TABLE** 

Parameter		Value		Units
	Min.	Тур.	Max.	
T <sub>1</sub>	-	•	10	(ms)
T <sub>2</sub>	0	-	50	(ms)
T <sub>3</sub>	200	-	-	(ms)
T <sub>4</sub>	200	-	-	(ms)
T <sub>5</sub>	0	-	50	(ms)
T <sub>6</sub>	0	-	10	(ms)
T <sub>7</sub>	400	-	-	(ms)

### Note)

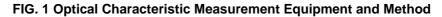
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

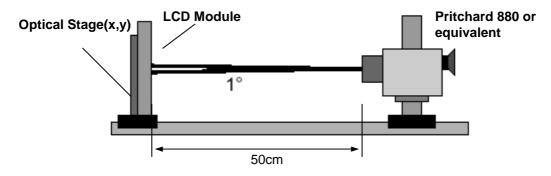


### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\Theta$  equal to  $0^{\circ}$ .

FIG. 1 presents additional information concerning the measurement equipment and method.





**Table 9. OPTICAL CHARACTERISTICS** 

Ta=25°C, VCC=3.3V,  $f_{V}$ =60Hz,  $f_{CLK}$ = 68.9MHz, lout = 6.5mA

Parameter	Symbol		Values		Units	Notes
Parameter	Symbol	Min	Тур	Max	Units	notes
Contrast Ratio	CR	350	<b>-</b>	-		1
Surface Luminance, white	L <sub>WH</sub>	160	190	-	cd/m <sup>2</sup>	2
Luminance Variation	$\delta_{ \text{WHITE}}$	-	-	2.0		2
Response Time						3
Rise Time+Decay Time	$Tr_{R+}Tr_{D}$	-	25	40	ms	
Color Coordinates						±0.03
RED	RX	0.566	0.596	0.628		
	RY	0.311	0.341	0.371		
GREEN	GX	0.297	0.327	0.357	]	
	GY	0.519	0.549	0.579		
BLUE	ВХ	0.134	0.164	0.194		
	BY	0.119	0.149	0.179		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	Θr	65	-		degree	
x axis, left (Φ=180°)	Θl	65	-	-	degree	
y axis, up (Φ=90°)	Θu	50	-		degree	
y axis, down (Φ=270°)	Θd	50	-		degree	
Gray Scale						6

Ver. 0.1 2. SEP. 2004 12 / 27



#### Note)

Contrast Ratio(CR) is defined mathematically as
 Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the 5point (1~5) average across the LCD surface 50cm from the surface with all pixels displaying white (6.5mA). For more information see FIG 2. When  $I_{BL}$ = 6.0mA,  $L_{WH}$ =175cd/m²(typ.)
- 3. Luminance % uniformity is measured for 13 point For more information see FIG 2. δ WHITE = Maximum(LN1,LN2, ..... LN13) ÷ Minimum(LN1,LN2, ..... LN137)
- 4. Response time is the time required for the display to transition from white to black (rise time,  $Tr_R$ ) and from black to white(Decay Time,  $Tr_D$ ). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

\*  $f_{V} = 60Hz$ 

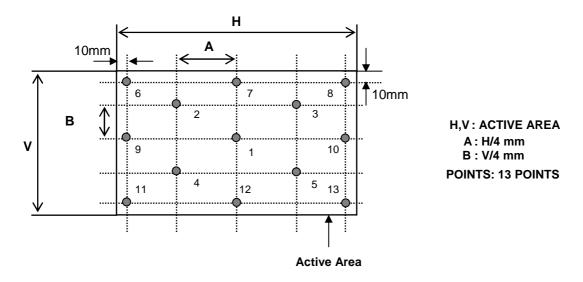
Gray Level	Luminance [%] (Typ)
LO	0.14
L7	0.44
L15	2.83
L23	9.10
L31	
L39	
L47	67.6
L55	89.4
L63	100

Ver. 0.1 2. SEP. 2004 13 / 27



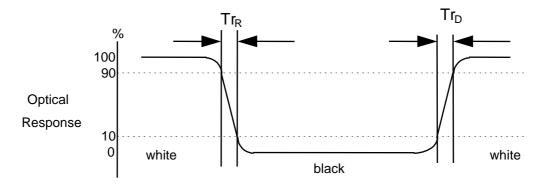
#### FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>



### FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

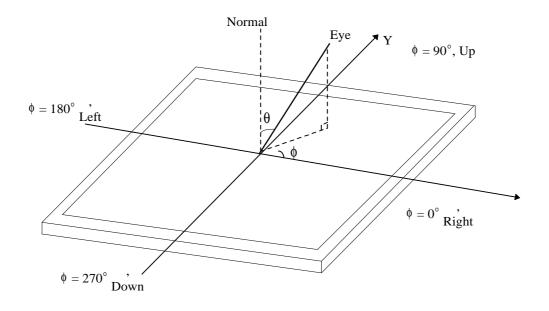


Ver. 0.1 2. SEP. 2004 14 / 27



### FIG. 4 Viewing angle

### <Dimension of viewing angle range>



Ver. 0.1 2. SEP. 2004 15 / 27



### 5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP171WU1(A4). In addition the figures in the next page are detailed mechanical drawing of the LCD.

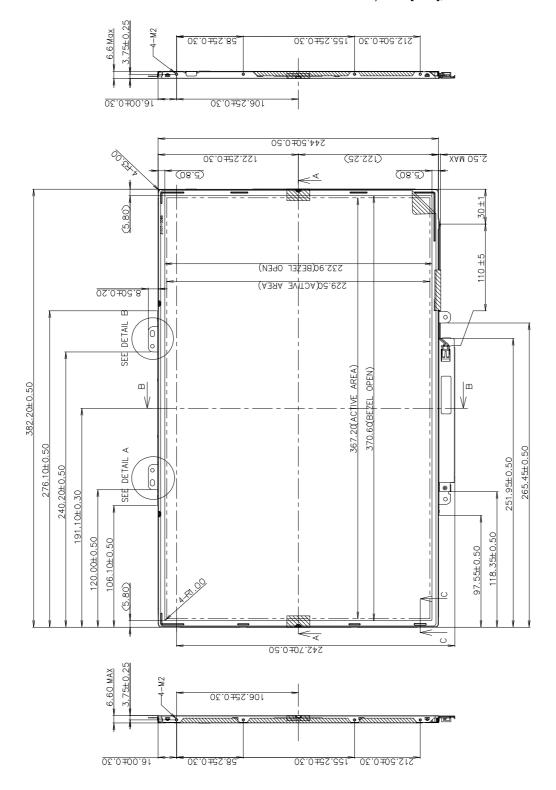
	Horizontal	382.2.0 ± 0.5mm		
Outline Dimension	Vertical	244.5 ± 0.5mm		
	Depth (Max)	6.6mm		
Bezel Area	Horizontal	370.6 ± 0.5mm		
bezei Alea	Vertical	232.9 ± 0.5mm		
Active Display Area	Horizontal	367.2 mm		
Active Display Area	Vertical	229.5 mm		
Weight	705g (MAX)			
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer			

Ver. 0.1 2. SEP. 2004 16 / 27



<FRONT VIEW>

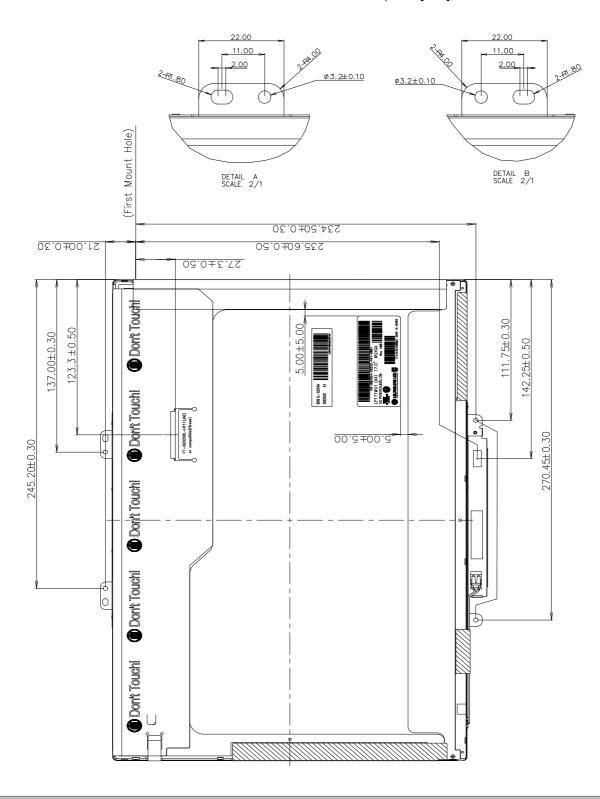
Note) Unit:[mm], General tolerance: ± 0.5mm





<REAR VIEW>

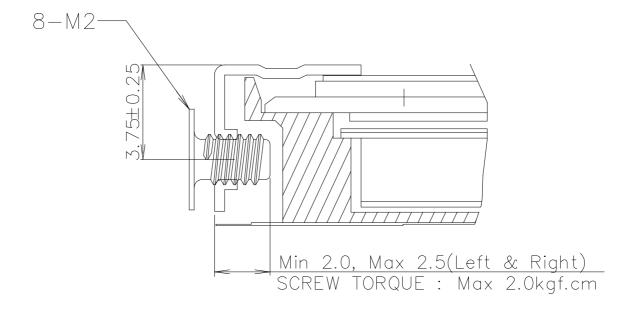
Note) Unit:[mm], General tolerance: ± 0.5mm





[ DETAIL DESCRIPTION OF SIDE MOUNTING SCREW ]

## \*Screw Torque (8 point):



Ver. 0.1 2. SEP. 2004 19 / 27



### 6. Reliability

#### **Environment test condition**

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis					
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 200 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 260 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

<sup>{</sup> Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Ver. 0.1 2. SEP. 2004 20 / 27



#### 7. International Standards

### 7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

c) EN 60950 : 2000, Third Edition

IEC 60950: 1999, Third Edition

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A4: 2000)

Ver. 0.1 2. SEP. 2004 21 / 27



### 8. Packing

### 8-1. Designation of Lot Mark

#### a) Lot Mark

A   B   C   D   E   F   G   H   I   J
---------------------------------------

A,B,C: SIZE(INCH)

D: YEAR E: MONTH

F : PANEL CODE G : FACTORY CODE H : ASSEMBLY CODE I,J,K,L,M : SERIAL NO.

#### Note

#### 1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

#### 3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	Н

### 4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG
Mark	K	С	D

### 5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

#### b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

### 8-2. Packing Form

a) Package quantity in one box: 10 pcs

b) Box Size: 490mm ×277mm × 322mm

Ver. 0.1 2. SEP. 2004 22 / 27



#### 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

#### 9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
  Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 0.1 2. SEP. 2004 23 / 27



#### 9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

#### 9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
  - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Ver. 0.1 2. SEP. 2004 24 / 27



# APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte	Field Name and Comments	Value	Value
Header	(hex)		(hex)	(binary)
	0	Header	00	00000000
	1	Header	FF	11111111
	2	Header	FF	11111111
	3	Header	FF	11111111
<del> </del>	4	Header	FF	11111111
	5	Header	FF	11111111
	6	Header	FF	11111111
	7	Header The American Control of	00	00000000
	8	EISA manufacture code = 3 Character ID = LPL	32	00110010
	9	EISA manufacture code (Compressed ASCII)	0C 00	00001100
ti _	0A	Panel Supplier Reserved – Product Code	00	00000000
Vendor / Product EDID Version	OB	Panel Supplier Reserved – Product Code	00	00000000
or ers	OC OD	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
~ >	0E	LCD module Serial No - Preferred but Optional ("0" if not used)	00	
ᅙᇶᄋ	0F	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
ă C	10	LCD module Serial No - Preferred but Optional ("0" if not used)  Week of manufacture	00	00000000
> "	11	Year of manufacture	0E	00001110
	12	EDID structure version # = 1	01	00000001
	13	EDID revision # = 3	03	0000001
<u>S</u>	14	Video I/P definition = Digital I/P (80h)	80	10000000
Display Parameters	15	Max H image size = 36.72cm(37)	25	00100101
lds m	16	Max V image size = 22.95cm(23)	17	00010111
	17	Display gamma = $(2.2 \times 100) - 100 = 120$	78	01111000
<u>~</u>		1 20	0A	
	18 19	Feature support ( no DPMS, Active off, RGB, timing BLK 1)	9E	00001010
	19 1A	Red/Green Low bit (RxRy/GxGy) Blue/White Low bit (BxBy/WxWy)	15	10011110 00010101
	1B	Red X Rx=0.596	98	10011000
<u>10</u>	1C	Red Y $Ry = 0.341$	57	01010111
Panel Color Coordinates	1D	Green X $Gx = 0.327$	53	01010111
<u>d</u> : <u>e</u>	1E	Green Y Gy = $0.527$	8C	10001100
an	1F	Blue X	2A	00101010
a o	20	Blue Y By = 0.149	26	00100110
	21	White X Wx = 0.313	50	01010000
	22	White Y Wy = 0.329	54	01010100
shed	23	Established timings 1 (00h if not used)	00	00000000
Established Timings	24	Established timings 2 (00h if not used)	00	00000000
ы П	25	Manufacturer's timings (00h if not used)	00	00000000
	26	Standard timing ID1 (01h if not used)	01	00000001
	27	Standard timing ID1 (01h if not used)	01	00000001
	28	Standard timing ID2 (01h if not used)	01	00000001
	29	Standard timing ID2 (01h if not used)	01	00000001
$\Box$	2A	Standard timing ID3 (01h if not used)	01	00000001
Standard Timing ID	2B	Standard timing ID3 (01h if not used)	01	00000001
Ë	2C	Standard timing ID4 (01h if not used)	01	00000001
įΞ	2D	Standard timing ID4 (01h if not used)	01	00000001
5	2E	Standard timing ID5 (01h if not used)	01	00000001
da	2F	Standard timing ID5 (01h if not used)	01	00000001
an	30	Standard timing ID6 (01h if not used)	01	00000001
လ	31	Standard timing ID6 (01h if not used)	01	00000001
	32	Standard timing ID7 (01h if not used)	01	00000001
	33	Standard timing ID7 (01h if not used)	01	00000001
	34	Standard timing ID8 (01h if not used)	01	00000001
	35	Standard timing ID8 (01h if not used)	01	00000001

Ver. 0.1 2. SEP. 2004 25 / 27



## APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte	Field Name and Comments	Value	Value
	(hex)		(hex)	(binary)
	36	Pixel Clock/10,000 162Mhz (LSB)	48	01001000
	37	Pixel Clock/10,000 162Mhz (MSB)	3F	00111111
	38	Horizontal Active = 1920 pixels (lower 8 bits)	80	10000000
_	39	Horizontal Blanking (Thbp) = 240 pixels (lower 8 bits)	F0	11110000
	3A	Horizontal Active/Horizontal blanking (Thbp) (upper4:4 bits)	70 B0	01110000
#	3B 3C	Vertical Active = 1200 lines	32	10110000
Ē	3D	Vertical Blanking (Tvbp) = 50 lines (DE Blanking typ. for DE only panels)  Vertical Active: Vertical Blanking (Tvbp) (upper4:4 bits)	40	00110010 01000000
- <u>E</u>	3E	Horizontal Sync, Offset (Thfp) = 48 pixels	30	00110000
Sec	3F	Horizontal Sync, Oliset (Thip) = 46 pixels  Horizontal Sync, Pulse Width = 32 pixels	20	0010000
ă	40	Vertical Sync, Offset (Tvfp) = 1 lines Sync Width = 3 lines	13	00010011
ρc	41	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
Timing Descripter #1	42	Horizontal Image Size =36.720 cm	6F	01101111
įΞ	43	Vertical image Size = 22.95 cm	E6	11100110
	44	Horizontal Image Size / Vertical image size	10	00010000
	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
		Non-interlaced, Normal, no stereo, Separate sync, H/V pol Negatives, DE only note: LSB is set to		
	47	"1" if panel is DE-timing only. H/V can be ignored.	19	00011001
	48	Not used	00	00000000
	49	Not used	00	00000000
	4A	Not used	00	00000000
	4B	Not used	00	00000000
#5	4C	Not used	00	00000000
Fiming Descripter #2	4D 4E	Not used Not used	00	00000000
pte	4E 4F	Not used	00	0000000
CII	50	Not used	00	0000000
es	51	Not used	00	00000000
D D	52	Not used	00	00000000
ĿĔ	53	Not used	00	00000000
_≟	54	Not used	00	00000000
	55	Not used	00	00000000
	56	Not used	00	00000000
	57	Not used	00	00000000
	58	Not used	00	00000000
	59	Module "A" Revision = 00 Example: 00, 01, 02, 03, etc.	00	00000000
	5A	Flag	00	00000000
	5B	Flag	00	00000000
	5C	Flag	00	00000000
	5D	Dummy Descriptor	FE	11111110
	5E	Flag	00	00000000
3 Ou				
#3 atic	5F	Dell P/N 1 <sup>st</sup> Character = W	57	01010111
T ter	60	Dell P/N 2 <sup>nd</sup> Character = 4	34	00110100
Timing Descripter #3 Dell specific information	61	Dell P/N 3 <sup>rd</sup> Character = 5	35	00110101
	62	Dell P/N 4 <sup>th</sup> Character = 5	35	00110101
	63	Dell P/N 5 <sup>th</sup> Character = 4	34	00110100
)g Sec	64	LCD Supplier EEDID Revision #	05	00000101
mi I sp		<b>1</b>	31	
i i	65	Manufacturer P/N = 1		00110001
	66	Manufacturer P/N = 7	37	00110111
	67	Manufacturer P/N = 1	31	00110001
	68	Manufacturer P/N = W	57	01010111
	69	Manufacturer P/N = U	55	01010101
	6A	Manufacturer P/N = 1	31	00110001
	6B	Manufacturer P/N (If $<$ 13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	0A	00001010
	, ,,,	( and the state of		



# APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte	Field Name and Comments	Value	Value
	(hex)	Field Name and Comments	(hex)	(binary)
	6C	Flag	00	00000000
	6D	Flag	00	00000000
	6E	Flag	00	00000000
	6F	Data Type Tag:	FE	11111110
	70	Flag	00	00000000
44	71	SMBUS Value = 10 nits	C8	11001000
er#	72	SMBUS Value = 17 nits	B4	10110100
escripter #4	73	SMBUS Value = 24 nits	A8	10101000
SCI	74	SMBUS Value = 30 nits	A0	10100000
	75	SMBUS Value = 60 nits	7C	01111100
ng	76	SMBUS Value = 110 nits	58	01011000
Timing	77	SMBUS Value = 150 nits	3D	00111101
	78	SMBUS Value = max nits (Typically = 00h, XXX nits)	00	00000000
	79	Number of LVDS receiver chips = '01' or '02'	02	0000010
	7A	BIST Enable: Yes = '01' No = '00'	01	00000001
	7B	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	0A	00001010
	7C	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
	7D	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
Checksum				
	7E	Extension flag (# of optional 128 EDID extension blocks to follow, Typ = 0)	00	00000000
	7F	Checksum (The 1-byte sum of all 128 bytes in this EDID block shall = 0)	BD	10111101

Ver. 0.1 2. SEP. 2004 27 / 27

```
ERROR: syntaxerror
OFFENDING COMMAND: --nostringval--
STACK:
/Title
( )
/Subject
(D:20100507092513+08'00')
/ModDate
( )
/Keywords
(PDFCreator Version 0.9.5)
/Creator
(D:20100507092513+08'00')
/CreationDate
(USER)
/Author
-mark-
```