Specification of FUJITSU TFT-LCD module

FLC51UXC8V-10

	Approval	
Date:		
By :		

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

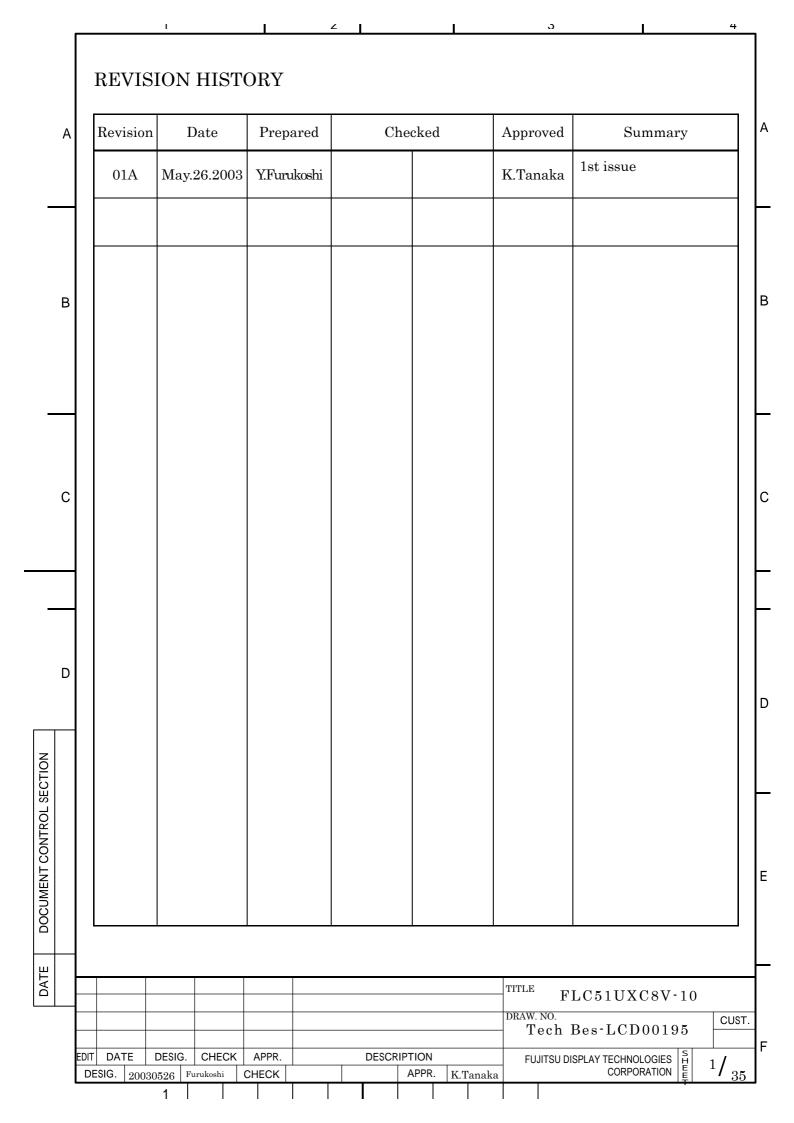
Specification No.: Tech Bes LCD-00195

Issue Date : May 26, 2003

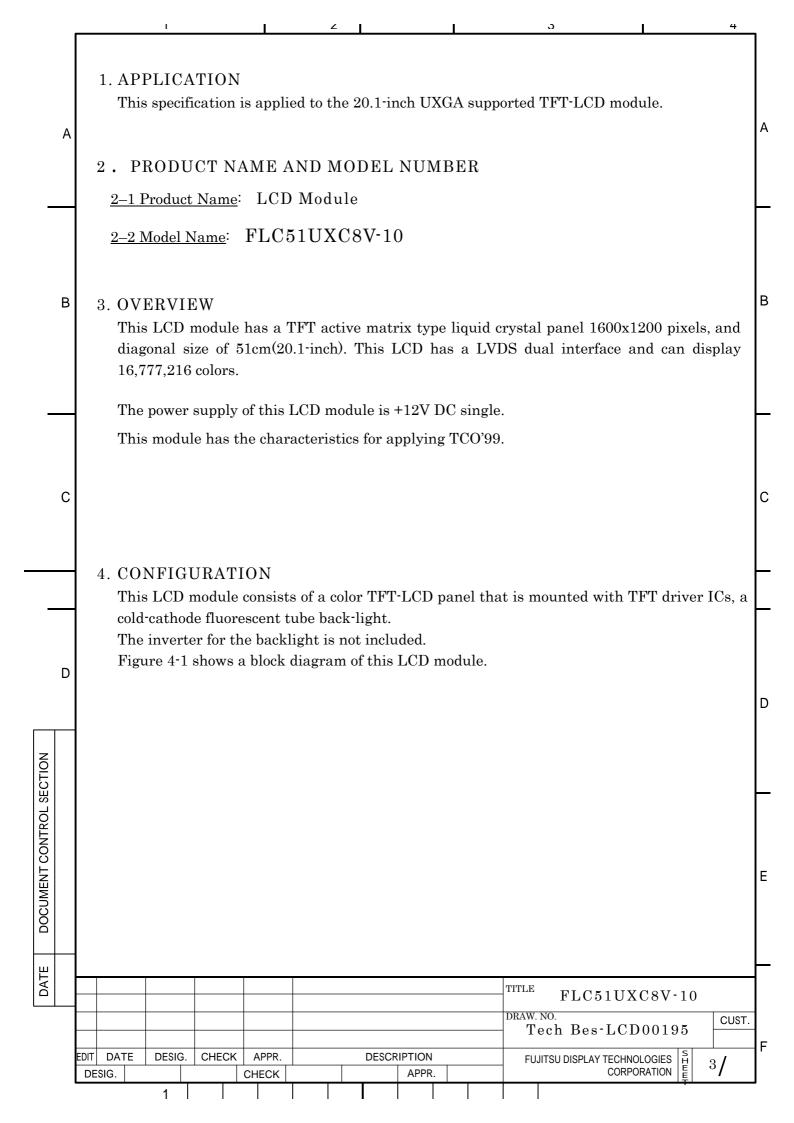
Issued by: \an

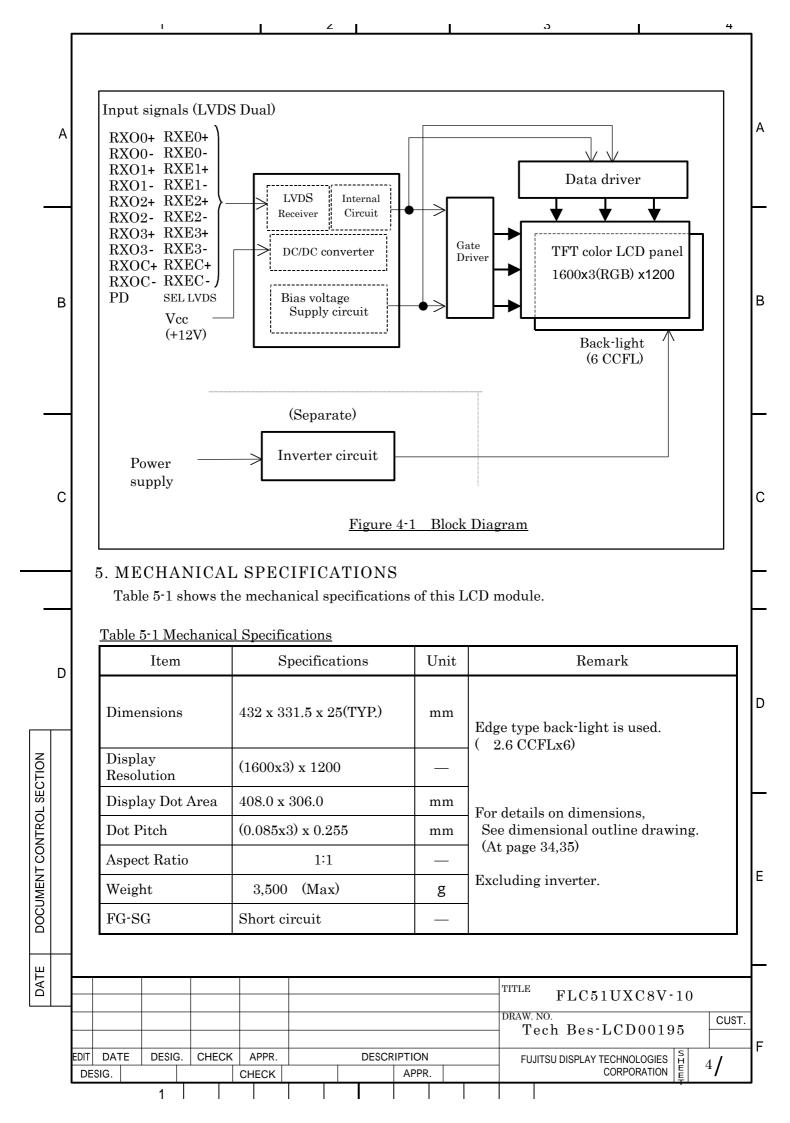
K. Tanaka

Director
Design Dep.
Technology Div.



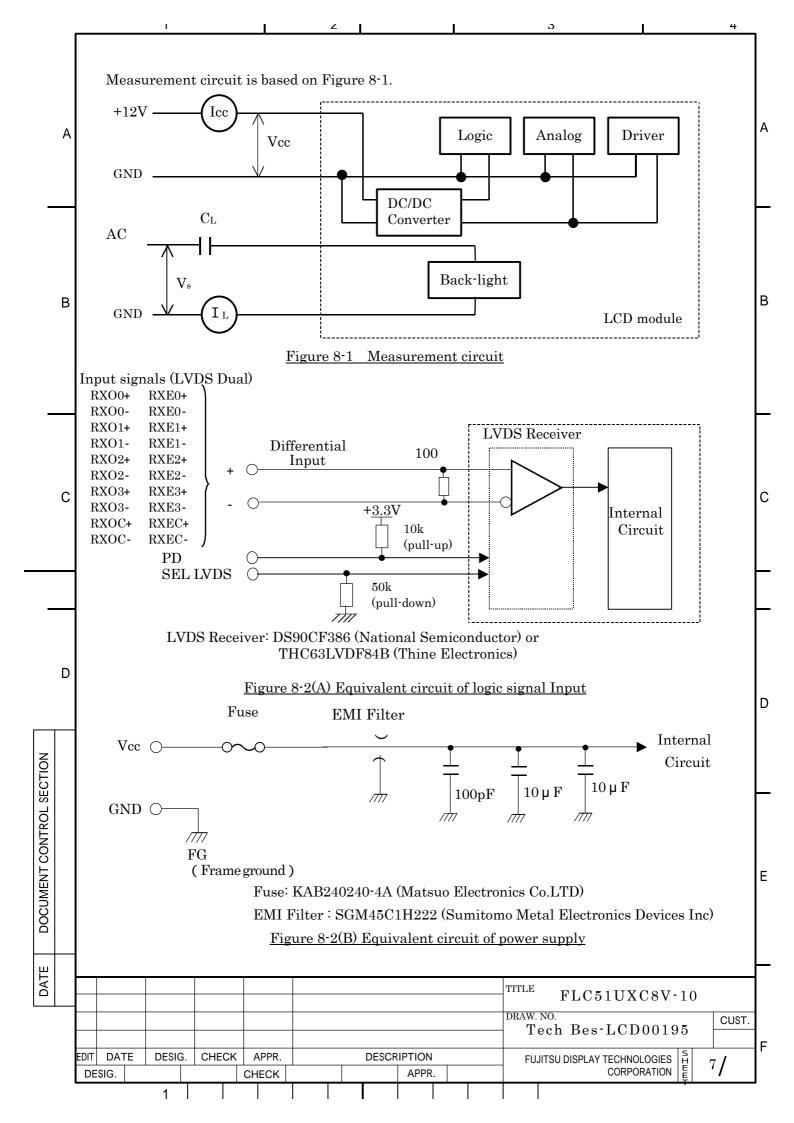
			TAB	LE OF	CONT	ENTS						
			1.	APPLICA	ATIONS						3	
	A		2	2-1.Produ	ct Name	es					3	
			3.	OVERVI	EW						3	
			4.	CONFIG	URATIO)N					3	
			5.	MECHA	NICAL S	SPECIFIC	CATION	S			4	
	В		6.	ABSOLU	JTE MAX	XIMUM F	RATING	(_ 5	
			7.	RECOM	MENDE	D OPERA	ATING (CONDITIO	ONS		5	
_												
				INTERFA 1-1.Signa								
							t				14	
	С		10)-4. Input	Signal	Timing					15	
			10)-5. Corre	esponden	ice betwe						
			10)-6.Power	Supply	Sequence				 	17	
			11.	BACK-LI	GHT SP	PECIFICA	TIONS				18	
			11	-1.Pin co		ions for E						
_				-2.CCFL							18	-
			11	-3.Life							18	
			12.	APPEAR	ANCE S	PECIFIC					19	
	D			2-1.Appea							13	
			12	2-2.Dot de	efects						20	
			13 .	ENVIRO	NMENT	AL SPEC	IFICAT	TIONS			21	
			14.	INDICAT	TIONS						22	
SECTION			15	PACKAG	ING						99	
												-
				5-2.Packii								
NTR			16 .	WARRAN	VTY						27	
DOCUMENT CONTROL			17	PRECAL	ITIONS						27	
MEN												
			18.	OTHERS	}						33	
DATE	ļ									TITLE FLC51UXC8	V-10	
	\dashv	\perp								DRAW. NO.	v - 10	
	ŀ	\dashv								Tech Bes-LCD00	195	CUST.
			DATE	DESIG.	CHECK			DESC	RIPTION APPR.	FUJITSU DISPLAY TECHNOLOGI CORPORATI		2/
			SIG.		1	CHECK		1			/ NI I 🗀 I	-,



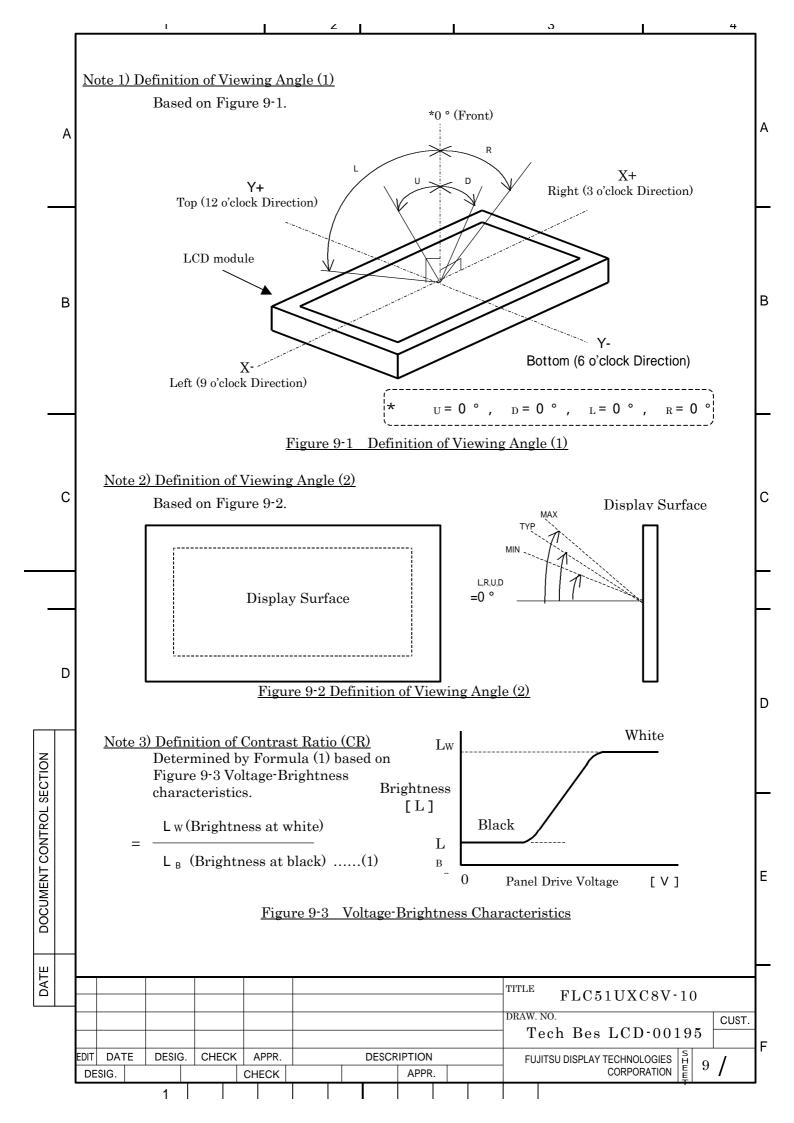


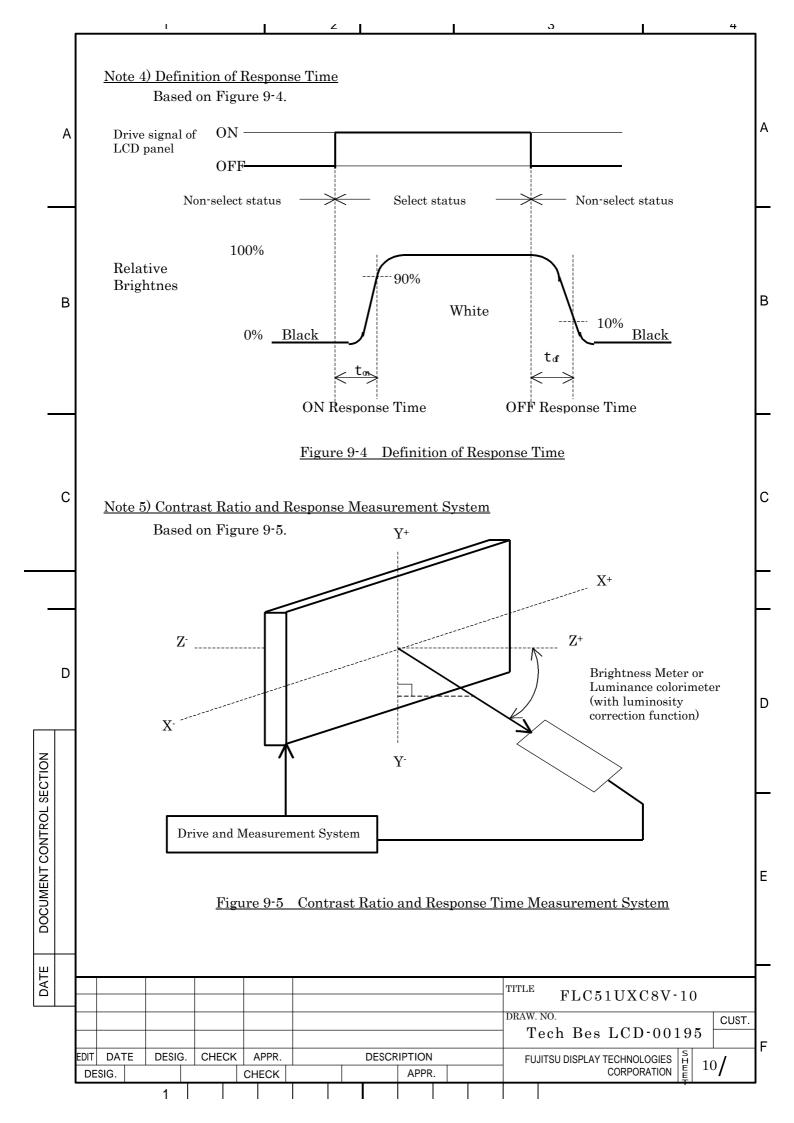
6. ABSOLUTE MAXIMUM RATING Α Table 6-1 shows the absolute maximum rating of this LCD module. Table 6-1 Absolute Maximum Rating Item Symbol Condition MIN. TYP. MAX. Unit Supply Voltage $Ta=25^{\circ}C$ V $V_{\rm CC}$ -0.314.0 В В Input Signal Voltage (LVDS signal, PD, $V_{\rm IN}$ $Ta=25^{\circ}C$ -0.33.6 V SEL LVDS) 7. RECOMMENDED OPERATING CONDITIONS Table 7-1 shows the recommended operating conditions of this LCD module. Table 7-1 Recommended Operating Conditions С TYP. MAX. Item Symbol MIN. Unit Supply Voltage (Logic) $V_{\rm CC}$ 11.5 12.0 12.5V Ripple Voltage $V_{\rm CC}$ V_{RP} 0.1V D DOCUMENT CONTROL SECTION Ε DATE TITLE FLC51UXC8V-10 DRAW. NO. CUST. Tech Bes-LCD00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 5/ CORPORATION DESIG. CHECK APPR

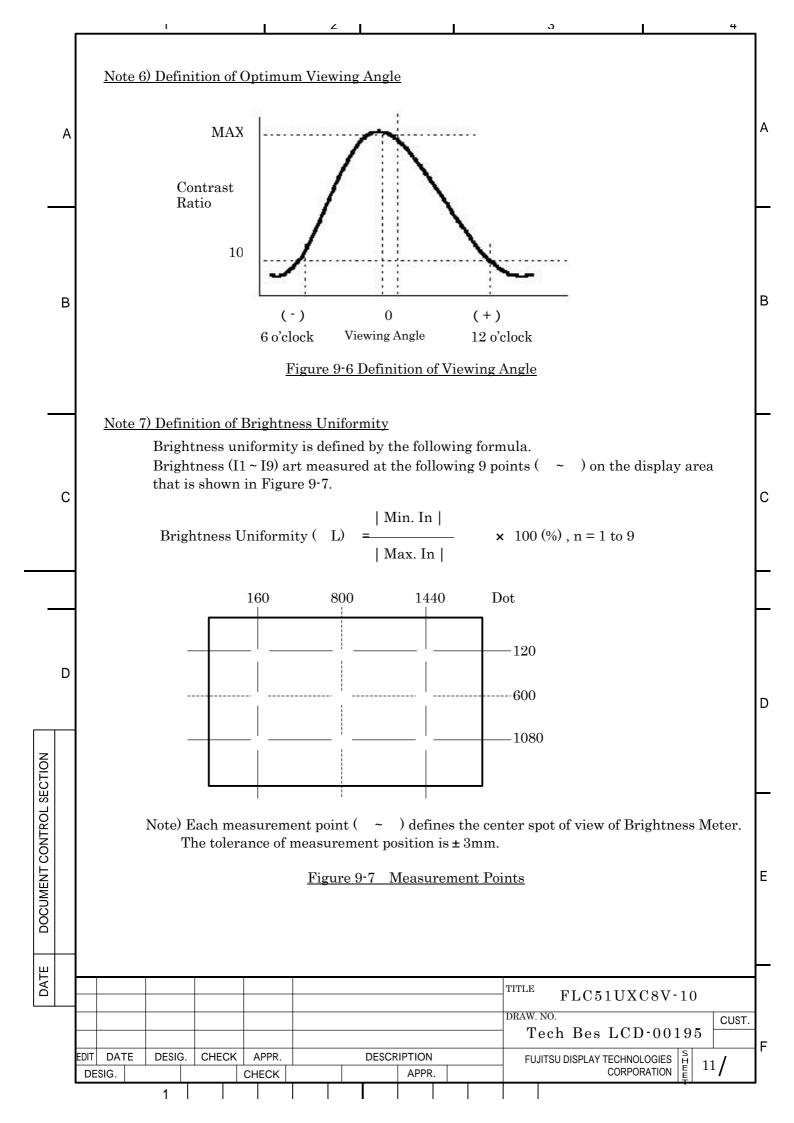
8. ELECTRICAL SPECIFICATIONS Table 8-1 shows the electrical specifications of this LCD module. Figure 8-1 shows the measurement circuit. Figure 8-2(A) shows the equivalent circuit of the logic signal input area. Α Figure 8-2(B) shows the equivalent circuit of the supply voltage Input area. <u>Table 8-1 Electrical Specifications</u> Symbol Condition MIN. TYP. MAX. Unit Item Remark Differential-input 100 V_{IH} mVVoltage (High) $V_{CM}=+1.2V$ Differential-input $V_{\rm IL}$ -100mV Voltage (Low) В В Supply Current *1 1000 I_{CC} 500 mΑ $V_{CC} = +12.0 \pm 0.5 V$ $V_{SS}=0V$ Supply Rush Current Iscc 5.8 Α DCLK=81MHz *2 60HzSupply Rush Current Tscc 0.2. msDuration (1A excess) $f_L=50kHz$, *4 1800 1600 Vrms В $Ta=25^{\circ}C$ CCFL Turn on $V_{\rm S}$ Voltage $f_L=50kHz$, *4 1850 Vrms Ta=0°C C $f_L=50kHz$ Lighting Voltage $V_{\rm L}$ 800 Vrms I_L=6mA Ι G $V_L=800Vrms$ KHzLighting Frequency $f_{\rm L}$ 30 50 60 Η (*\f3) $f_L=50kHz$ *3 **Tube Current** 3 7 $I_{\rm L}$ 6 mArms $V_L=800Vrms$ (*1) Typical current situation : Color bar pattern. Vcc=12.0VMaximum current situation: White screen. Vcc=11.5V Without rush current. D (*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of Vcc is not prescribed. (*3) Tube current (I_L) shows the value of the current that is consumed at one lamp. This LCD module has 6 lamps. Each 3 lamps are placed at upper side and DOCUMENT CONTROL SECTION lower side of the display. 3 lamps are connected in parallel. Each low voltage terminals are connected with separate Cable to Back-light connecter. (*4) The voltage above this value should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on. Ε DATE FLC51UXC8V-10 CUST. Tech Bes-LCD00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 6/ CORPORATION DESIG. APPR CHECK



9. OPTICAL SPECIFICATIONS Table 9-1 shows the optical specifications of this LCD module. Table 9-1 Optical Specifications Ta=25□ C Α Remark Specifications Item Symbol Condition Unit MIN. TYP. MAX. Note $_{\text{U}}$, $_{\text{D}}\!=\!0$ ° Horizontal deg 85 L , R CR 10 Visual (1)(2)Vertical $_{\text{L}}$, $_{\text{R}}\!=\!0$ ° 85 deg U.D (3)(5)Angle All Direction 80 deg (6)White/ (1)(2)Contrast Ratio CR $_{\text{L,R,U,D}}$ =0 $^{\circ}$ 400 600 (3)(5)Black В В Ta=25□ C 15 30 Response ms L,R, Time (ON) (1) t on U,D =0 ° (B W) (4) Ta=0□ C 50 100 ms (5)Ta=25□ C 25 10 ms Response L,R, Time (OFF) t_{off} U,D =0 ° (W B) Та=0□ С 100 50 ms $_{\text{L}}$, $_{\text{R}}$, $_{\text{U}}$, $_{\text{D}}\!=\!0$ ° Ι 200 cd/m^2 (1)(5)**Brightness** 250 $V_{cc} = 12.0V$ Brightness (1)(5)С I_L=6mA Ι % 70 White Uniformity (7)*1 Х 0.283 0.313 0.343W 0.299 0.329 0.359У (1) Red (0.641, 0.350) Typ. (5)Chromaticity R (0.287,0.595) Typ. G (x,y)Green В Blue (0.142,0.070) Typ. TFT Color LCD Panel Type D Display Mode Normally Black Wide Viewing Angle Technology **MVA** (6)Optimum Viewing Angle (Symmetry) DOCUMENT CONTROL SECTION 16,777,216 (8-bit color) Display Color Color of non-display area Black Anti-glare Surface Treatment (Haze value: (25%), 2H) (*1) Value at 15 ~ 20 minutes after lighting on. Ε (Note) · CS-1000 (MINOLTA Co. Ltd.), BM-5A (Topcon) and the like should be used as a luminance colorimeter. Field=1 $^{\circ}$, L=500mm •Back-light current = 6mA, Dark room condition (1 lux or less) DATE TITLE FLC51UXC8V-10 DRAW. NO. CUST. Tech Bes LCD-00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES CORPORATION DESIG. APPR CHECK







10. INTERFACE SPECIFICATIONS

10-1 Signal descriptions

Α

В

С

D

DOCUMENT CONTROL SECTION

Table 10-1 shows the description and configuration of interface signals (CN1).

Table 10-1 Interface signals (CN1)

Pin No.	Symbol	I/O	Function
1	Vec		+12V power supply
2	Vcc		+12V power supply
3	Vcc		+12V power supply
4	TST		Test pin *2
5	PD	I	LVDS Core Power Down
6	SEL LVDS	I	Select LVDS data order *1
7	GND		Ground
8	RxE3+	I	Positive differential input
9	RxE3-	I	Negative differential input
10	RxEC+	I	Positive differential input
11	RxEC-	I	Negative differential input
12	RxE2+	I	Positive differential input
13	RxE2-	I	Negative differential input
14	GND		Ground
15	RxE1+	I	Positive differential input
16	RxE1-	I	Negative differential input
17	GND		Ground
18	RxE0+	I	Positive differential input
19	RxE0-	I	Negative differential input
20	RxO3+	I	Positive differential input
21	RxO3-	I	Negative differential input
22	RxOC+	I	Positive differential input
23	RxOC-	I	Negative differential input
24	GND		Ground
25	RxO2+	I	Positive differential input
26	RxO2-	I	Negative differential input
27	RxO1+	I	Positive differential input
28	RxO1-	I	Negative differential input
29	RxO0+	I	Positive differential input
30	RxO0-	I	Negative differential input

Connector : FI-X30S-HF (Japan Aviation Electronics) User's connector : FI-X30M (Japan Aviation Electronics)

> FI-X30H FI-X30C

*1: 3.3V CMOS Signal input. (High or Low)

*2: Keep open. (Internal test use only.)

		_	- I I.				 J .,						
											TITLE	FLC51UXC8V-10	
											DRAW.	ech Bes LCD-00195	т.
EDIT	DAT	ΓE	DESIG	. CHE		APPR. HECK		DESCR	APPR		FU	JITSU DISPLAY TECHNOLOGIES 12/	٦٢
	oiG.		1		0	TECK		Ī	AFFIX			T Some State of the left of th	

10-2 LVDS Data Assignment Table 10-2 shows the LVDS Data Assignment. Table 10-2 LVDS Data Assignment

	А	Inp	out signal	l *1		ansmitter CF383,C385	Interfa	ce coni	nector		Receiver S90CF386		input LVDS)	
		SEL LVDS	Low	High	pin	INPUT	System side	LC pin	D module	pin	OUTPUT	Low	High	
-			RO2 RO3 RO4 RO5	RO0 RO1 RO2 RO3	51 52 54 55	TxIN0 TxIN1 TxIN2 TxIN3	Tx OUT0+	2	RxO0+	27 29 30 32	RxOUT0 RxOUT1 RxOUT2 RxOUT3	RO2 RO3 RO4 RO5	RO0 RO1 RO2 RO3	
			RO6 RO7 GO2 GO3	RO4 RO5 GO0 GO1	56 3 4 6	TxIN4 TxIN6 TxIN7 TxIN8	Tx OUT0-	1	RxO0-	33 35 37 38	RxOUT4 RxOUT6 RxOUT7 RxOUT8	RO6 RO7 GO2 GO3	RO4 RO5 GO0 GO1	
	В		GO4 GO5 GO6 GO7	GO2 GO3 GO4 GO5	7 11 12 14	TxIN9 TxIN12 TxIN13 TxIN14	Tx OUT1+	3	RxO1+	39 43 45 46	RxOUT9 RxOUT12 RxOUT13 RxOUT14	GO4 GO5 GO6 GO7	GO2 GO3 GO4 GO5	В
		LVDS Odd	BO2 BO3 BO4 BO5	BO0 BO1 BO2 BO3	15 19 20 22	TxIN15 TxIN18 TxIN19 TxIN20	Tx OUT2+		RxO2+	47 51 53 54	RxOUT15 RxOUT18 RxOUT19 RxOUT20	BO2 BO3 BO4 BO5	BO0 BO1 BO2 BO3	
-		Odd	BO6 BO7 RSVD RSVD	BO4 BO5 RSVD RSVD	23 24 27 28	TxIN21 TxIN22 TxIN24 TxIN25	Tx OUT2-	6 5	RxO2-	55 1 3 5	RxOUT21 RxOUT22 RxOUT24 RxOUT25	BO6 BO7 Not use Not use	BO4 BO5 Not use Not use	_
			ENAB RO0 RO1 GO0 GO1	ENAB RO6 RO7 GO6 GO7	30 50 2 8 10	TxIN26 TxIN27 TxIN5 TxIN10 TxIN11	Tx OUT3+	11	RxO3+	6 7 34 41 42	RXOUT26 RXOUT27 RXOUT5 RXOUT1 RXOUT11	ENAB RO0 RO1 GO0 GO1	ENAB RO6 RO7 GO6 GO7	
	С		BO0 BO1 RSVD	BO6 BO7 RSVD	16 18 25	TxIN16 TxIN17 TxIN23	Tx OUT3-	10	RxO3-	49 50 2	RxOUT16 RxOUT17 RxOUT23	BO0 BO1 Not use	BO6 BO7 Not use	С
			DCLK	1	31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	9 8	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	DCLK		
			RE2 RE3 RE4 RE5	RE0 RE1 RE2 RE3	51 52 54 55	TxIN0 TxIN1 TxIN2 TxIN3	Tx OUT0+	13	RxE0+	27 29 30 32	RxOUT0 RxOUT1 RxOUT2 RxOUT3	RE2 RE3 RE4 RE5	RE0 RE1 RE2 RE3	
			RE6 RE7 GE2 GE3	RE4 RE5 GE0 GE1	56 3 4 6	TxIN4 TxIN6 TxIN7 TxIN8	Tx OUT0-	12	RxE0-	33 35 37 38	RxOUT4 RxOUT6 RxOUT7 RxOUT8	RE6 RE7 GE2 GE3	RE4 RE5 GE0 GE1	
	D		GE4 GE5 GE6 GE7	GE2 GE3 GE4 GE5	7 11 12 14	TxIN9 TxIN12 TxIN13 TxIN14	Tx OUT1+	16	RxE1+	39 43 45 46	RxOUT9 RxOUT12 RxOUT13 RxOUT14	GE4 GE5 GE6 GE7	GE2 GE3 GE4 GE5	D
		LVDS	BE2 BE3 BE4 BE5	BE0 BE1 BE2 BE3	15 19 20 22	TxIN15 TxIN18 TxIN19 TxIN20	Tx OUT1-	15	RxE1-	47 51 53 54	RxOUT15 RxOUT18 RxOUT19 RxOUT20	BE2 BE3 BE4 BE5	BE0 BE1 BE2 BE3	
SECTION		Even	BE6 BE7 RSVD RSVD	BE4 BE5 RSVD RSVD	23 24 27 28	TxIN21 TxIN22 TxIN24 TxIN25	Tx OUT2+ Tx OUT2-	19	RxE2+	55 1 3 5	RxOUT21 RxOUT22 RxOUT24 RxOUT25	BE6 BE7 Not use Not use	BE4 BE5 Not use Not use	L
DOCUMENT CONTROL (RSVD RE0 RE1 GE0 GE1	RSVD RE6 RE7 GE6 GE7	30 50 2 8 10	TxIN26 TxIN27 TxIN5 TxIN10 TxIN11	Tx OUT3+	23	RxE3+	6 7 34 41 42	RxOUT26 RxOUT27 RxOUT5 RxOUT10 RxOUT11	Not use RE0 RE1 GE0 GE1	Not use RE6 RE7 GE6 GE7	
UMENT (BE0 BE1 RSVD	BE6 BE7	16 18 25	TxIN16 TxIN17 TxIN23	Tx OUT3-	22	RxE3-	49 50 2	RxOUT16 RxOUT17 RxOUT23	BE0 BE1 Not use	BE6 BE7 Not use	Е
DOC			DCLK		31	TxCLK IN	TxCLK OUT+ TxCLK OUT-	21 20	RxCLK IN+ RxCLK IN-	26	RxCLK OUT	Not use		
		*	1 · RS	VD (res	erved) pin on a	transmitter	shoul	d be connect	ed wit	h Ground.			

DATE

• Input odd or even data depending on the display position of the LCD module. TITLE FLC51UXC8V-10 DRAW. NO.

CUST. Tech Bes LCD-00195 FUJITSU DISPLAY TECHNOLOGIES CORPORATION EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** 13/ DESIG.

CHECK

10-3 Color Data Assignment

Α

В

С

D

DOCUMENT CONTROL SECTION

Table 10-3 shows the Color Data Assignment.

Table 10-3 Color Data Assignment

Col	or				RΙ	npı	ıt d	lata	ì			(G In	npu	ıt d	lata	ì]	B Iı	npı	ıt d	ata	ì	
	Odd		R7	R6	R5	R4	R3	R2	R	RO	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	B4	ВЗ	B2	В1	В0
	Even		R7	R6	R5	R4	R3	R2	R	RO	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	В3	B2	B1	B0
_	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Blue		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
0]	Green		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
C	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
U	Red		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Si	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
a	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
В	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
e d		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
R	Û	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter 2	253	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	J 2	254	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red 2	255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
⊏	仓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
e e		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
_ _	Û	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
ū	0	253	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Ţ 2	254	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green 2	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
o	仓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
⊃		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
3 1	Û	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
В	0	253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Ţ 2	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note.1) Definition of gray scale:Color (n)..."n" indicates gray scale level.

Larger number means brighter level.

Note.2) Data; 1:High, 0:Low

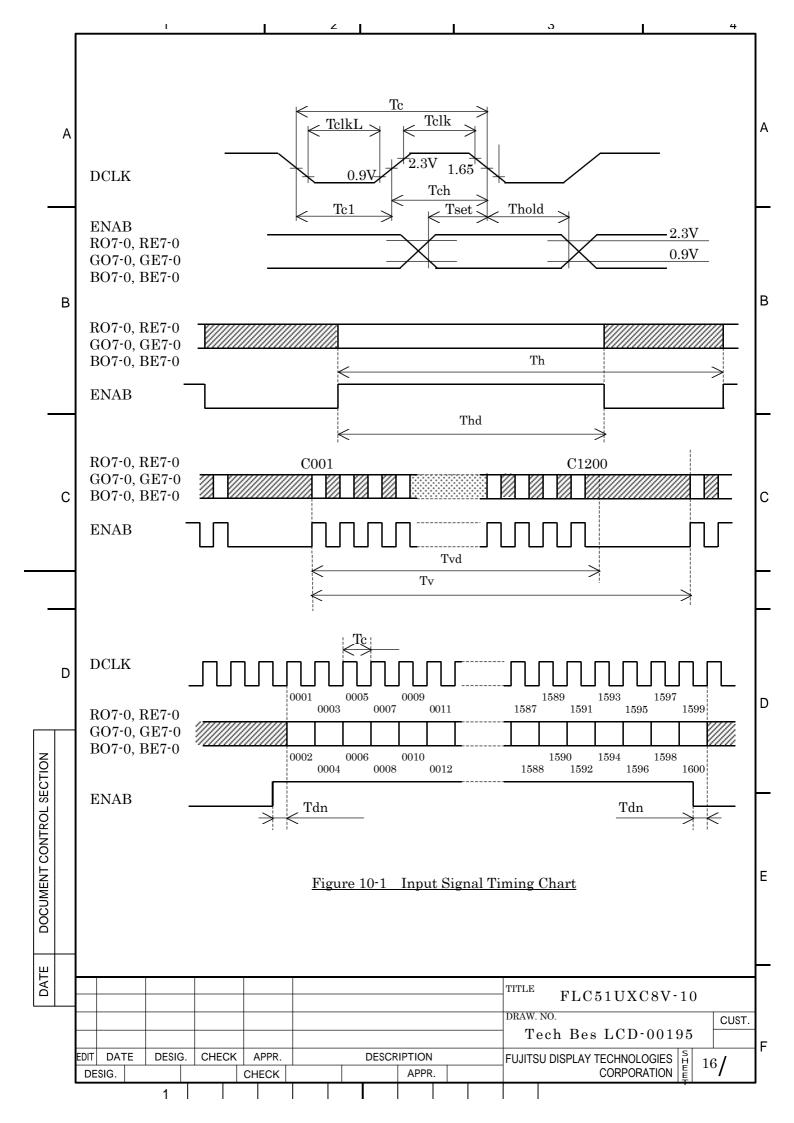
Note 3) Color data consist of 8 bit red, green and blue data of odd and even number pixel data. Total data number is 48 signals. This module is able to display 16,777,216 colors because each red, green and blue data is controlled independently.

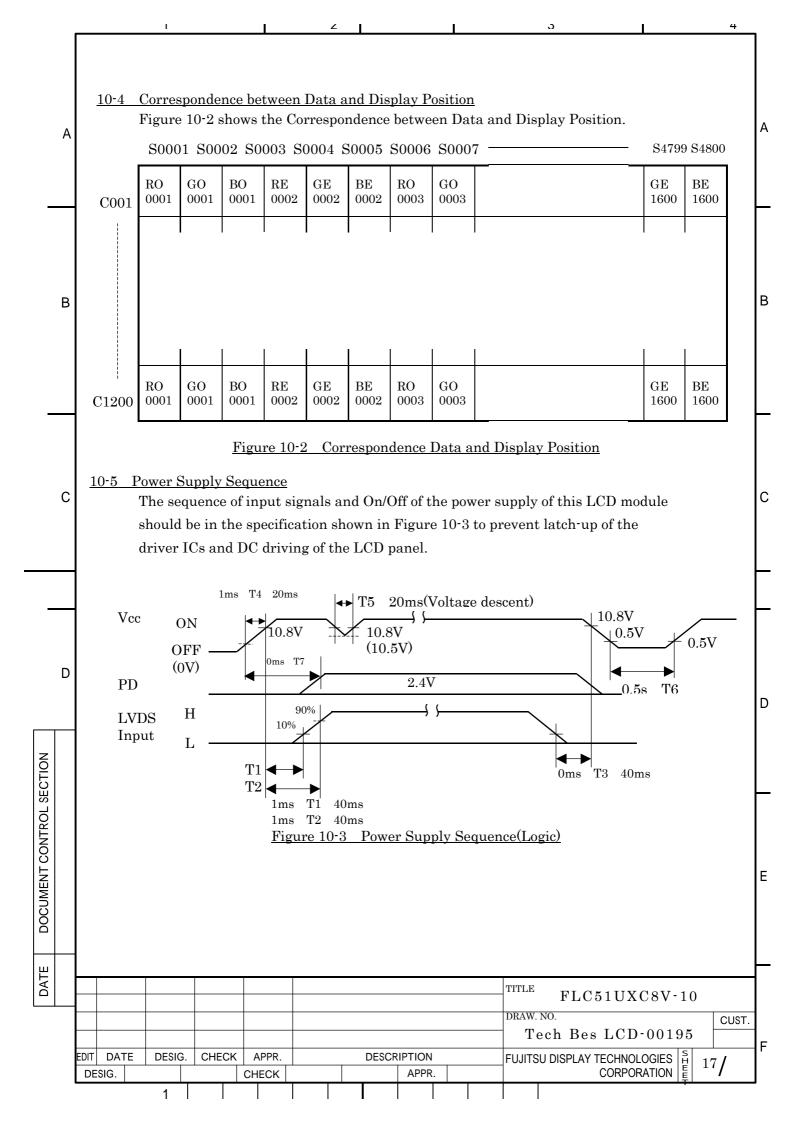
Ε

															—
												TITLE	FLC51UXC8V-10		
												DRAW.	ech Bes LCD-00195	CUST.	_
	EDIT	DAT	ΓE	DESIG	. CHE	 APPR.		DESCR	IPTION	-		FU	JITSU DISPLAY TECHNOLOGIES S H E 14	/	٢
ļ	DE	SIG.				CHECK		1	APPR	·	Г		CONFORMION E	<u>′</u>	

Α	<u>Table 10-4 </u>	Ciming Characteristic	<u>s</u>			(Ta=	0~45□ C,	Vcc=12±0.	5V)
		Item	Symbol	Min.	Typ.	Max.	Unit	Remarl	k
	DCLK	Period Frequency	Tc 1/Tc	11.765 50.000	12.345 81.000	20.000 85.000	ns MHz		
	signal (Clock)	Duty High time	Tch/Tc TclkH	$45 \\ 3.5$	50	55	% ns		
		Low time	TclkL	3.5	_		ns		
	DCLK-Data Timing	Setup time Hold time	Tset Thold	3 2		<u> </u>	ns ns		
В		Horizontal Period Hor. Period	Th Th	$865^{*1} \\ 13.0$	1080 13.3	$1130^{*1} \\ 14.65$	DCLK µs		
		Hor. Display period Vertical Period	Thd	800	800	800	DCLK Th	*2	
	ENAB signal	Ver. Frequency	Tv 1/Tv	1207*1 50	1250 60	1280*1 62	Hz Hz		
		Ver. Display period	Tvd	1200	1200	1200	Hz Th		
\dashv		Data-ENAB timing	Tdn	_	0	_	DCLK	*3	
		gh" level period of ENAB does not synchronize with rea.			black color i		at the res	_	ay a
)	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	_	ау ғ
)	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	_	ay a
-	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	_	ay ε
	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	_	ау ғ
0	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	_	ау ғ
	*3)•If ENAB (does not synchronize with			black color i	s displayed	at the res	fit to the	ay ε
D	*3)•If ENAB (does not synchronize with			black color i	s displayed play position	at the res	_	ay
D	*3)•If ENAB (does not synchronize with			black color i	s displayed play position the state of the s	at the res	fit to the	CU

ی ک





11. BACK-LIGHT SPECIFICATION

11-1 Pin configuration for Back-light

Α

В

C

D

Table 11-1 shows the description and Pin assignment of the connectors (CN-A to D) for the Back-light of this LCD module.

Table 11-1 Pin Assignment of CN-A to CN-D

	Pin No.	Symbol	Function	Cable Color
	1	$V_{\rm H1}$	Power supply (High voltage)	Pink
	2	$V_{ m H2}$	Power supply (High voltage)	Orange
CN-A	3	-	NC	
	4	V_{L^1}	Power supply (Low voltage)	Blue
	5	V _{L 2}	Power supply (Low voltage)	Gray
CN-B	1	V _{H3}	Power supply (High voltage)	Red
OND	2	V _{L 3}	Power supply (Low voltage)	White
	1	$V_{ m H4}$	Power supply (High voltage)	Pink
	2	$V_{ m H5}$	Power supply (High voltage)	Orange
CN-C	3	-	NC	
	4	V _{L 4}	Power supply (Low voltage)	Blue
	5	V _L 5	Power supply (Low voltage)	Gray
CN-D	1	V _{H6}	Power supply (High voltage)	Red
CN-D	2	V _L 6	Power supply (Low voltage)	White

Connector : Housing(CN-A, CN-C):BHR-05VS-1

(CN-B, CN-D):BHSR-02VS-1

Contact : SBH-001T-P0.5

SBHS-002T-P0.5

User's Connector: Post with base: SM04(9-E2)B-BHS-1

SM02B-BHSS-1-TB

Supplier : Japan Solder less Terminal manufacturing Company LTD. (J.S.T.)

11-2 CCFL

Supplier: SANKEN ELECTRONICS Co.LTD. Part No.: SS26E4175E8550C2882710S

11-3 Life

The life of the back-light is a minimum of 50,000 hours at the following conditions.

(1) Working conditions

Ambient temperature: 25 ± 5

Tube current (I_L) : (6mA or less)

(2) Definition of life

Brightness becomes 50% or less than the minimum brightness value shown in Table 9-1. Flashing.

Ε

11-4 <u>Lamp assembly set (for replacement)</u>

Lamp assembly set (with charge) is prepared for replacing old lamp to new one.

This set consists of an upper lamp assembly and a lower lamp assembly.

Type number: FLCL-26

F												TITLE	FLC51UXC8V-10	
												DRAW.	NO. ech Bes LCD-00195	ST.
EDI	T DA	TE	DESIG	. CHE		APPR.						FUJITS	SU DISPLAY TECHNOLOGIES 18/	
	2010.		1			I				7				

DOCUMENT CONTROL SECTION

DATE

12. APPEARANCE SPECIFICATIONS 12-1 Appearance 12-1-1 Appearance specification Α No. Item Judgment method and standard Bright spot (high and Low) (Note 1) <6 dots 2 Bright spot connection (Note 1) ≤2 pair (high and low) Total of bright spot 3 <u><6</u> dots (Note 2) 4 Dark spot ≤10 dots (Note 2) 5 Dark spot connection ≤4pairs 6 Total of dark spot <10 dots (Note 2) Total of dot defect В < 10 dots (bright and dark) Distance of high-hgh >15mm bright spot ≥ 5 mm others 9 Distance of dark spot ≥ 5mm 10 Scratch on polarizer, W≤0.03 Ignore line shape L≤6 Ignore $0.03 < W \le 0.05$ 6<L<u>≤</u>12 ≤7 12<L 0 L<u><</u>0.6 Ignore 0.6<L<u>≤</u>5 $0.05 < W \le 0.10$ <5 5<L 0 C 0.10<W 0 D≤0.3 11 Dent on polarizer, Ignore dot shape $0.3 < D \le 0.4$ <u><</u>9 0.4<D 0 D<0.3 Ignore 12 Bubble in polarizer 0.3<D≤0.5 <u>≤</u>4 0.5 < D0 D≤0.15 Ignore 13 Black white spot $0.15 < D \le 0.5$ <u><</u>5 (Foreign circular matter) D 0.5 < D0 D<0.3 Ignore 14 Light leakage by foreign articles $0.3 < D \le 0.6$ **≤**5 0.6<D 0 W≤0.03 Ignore Lints. 15 DOCUMENT CONTROL SECTION L≤6 Ignore black/white line $0.03 < W \le 0.05$ 6<L≤12 <u>≤</u>6 12<L 0 Ignore L≤0.6 0.05<W<u><</u>0.10 0.6<L<u><</u>5 **≤**3 5<L 0 (W+L)/2=D0.10<W Conform to No.13 D:Average diameter [mm], W:Width [mm], L:Length [mm], S=(bright spot size)/(dot size) DATE TITLE FLC51UXC8V-10 DRAW. NO. CUST. Tech Bes LCD-00195 FUJITSU DISPLAY TECHNOLOGIES H EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** 19/ DESIG. CHECK APPR CORPORATION

12-1-2 Light leakage outside the active area. The light leakage that occurs minimum of 2.0mm outside the peripheral of the active area is overlooked. A leakage of light may occur between the BM section and the bezel at the peripheral of the 12-2 Dot defects (Bright spots, Dark spots) 12-2-1 Zone ·Inside display dot area (408.0 X 306.0mm) ·Display dot area means active area. ·One pixel consists of 3 dots (red, green and blue). · Foreign particle and scratch unharmful to display image, such as the foreign particle В under polarizer film but outside of the display area and scratch on metal bezel, backlight module or polarizer film out of the display area, etc., are not counted. 12-2-2 Bright spots (1) Bright spots by the defect of TFT. ·Visible under bias of 2% ND filter...... High bright spot R • G ·Visible under 5% but invisible under 2% ND filter...... Low bright spot R·G·B (2) Bright spots by the light passing through tears, breaks, etc in color filter. C · Exceed size of a half dot High bright spot (3) Bright spots by the light passing through tears, breaks, etc in chromium mask. · Exceed 50µm High bright spot 12-2-3 Test condition D ·Inspector must observe the LCD screen from the normal direction under the illumination by a single 20W fluorescent lamp. The distance between the LCD screen and the inspector should be a height of 50cm above the worktable. The vertical illuminance is 300 to 600lux (reference value). ·Bright spot should be counted under entire black screen. DOCUMENT CONTROL SECTION ·Dark spot should be counted under entire white screen. ·Input signal timing should be typical value. (Note1) Please do not mistake a single bright spot for a bright spot connection due to Cs(supplemental capacitance) line at the center of each dot. (Note2) If a pixel is dark partially, it connects into the number of dark spots in accordance with following rule. (a) A<1/3 : Not count. Only one of 4 dark connection is allowed. : Considered as 0.5 dot. (b) $1/3 \le A < 2/3$ (c) 2/3 < A: Considered as 1 dot. (A=Dark spot size/dot size) DATE TITLE FLC51UXC8V-10 DRAW. NO. CUST. Tech Bes LCD-00195 FUJITSU DISPLAY TECHNOLOGIES H EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** 20 / DESIG. CORPORATION CHECK

13. ENVIRONMENTAL SPECIFICATIONS Table 13-1 show the environmental specifications. <u>Table 13-1 Environmental specifications</u> Item Condition Operation 0~45°C Temperature on surface of Temperature LCD panel (display area.) $-20 \sim 60^{\circ} \text{C}$ Storage Maximum wet-bulb temperature Operation 20~85%RH Humidity should not exceed 29°C. 5~85%RH Storage No condensation. В 10~500Hz, 1octave/20minute, Vibration Non-operation 2G, 1.5mm max, 1hour each X, For single module Y and Z directions without package. 30G, 6ms, 1time each Shock Non-operation $\pm X$, $\pm Y$ and $\pm Z$ directions. С

NOTE: Table 13-2 and Figure 13-1 show the shock resistance standard when module is packaged.

Remark

В

Ε

Table 13-2 Shock resistance standard when module is packaged

D

DOCUMENT CONTROL SECTION

Dropping location	Dropping height	Count
A~J	60cm	1 time

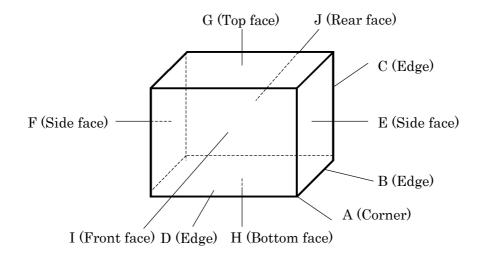
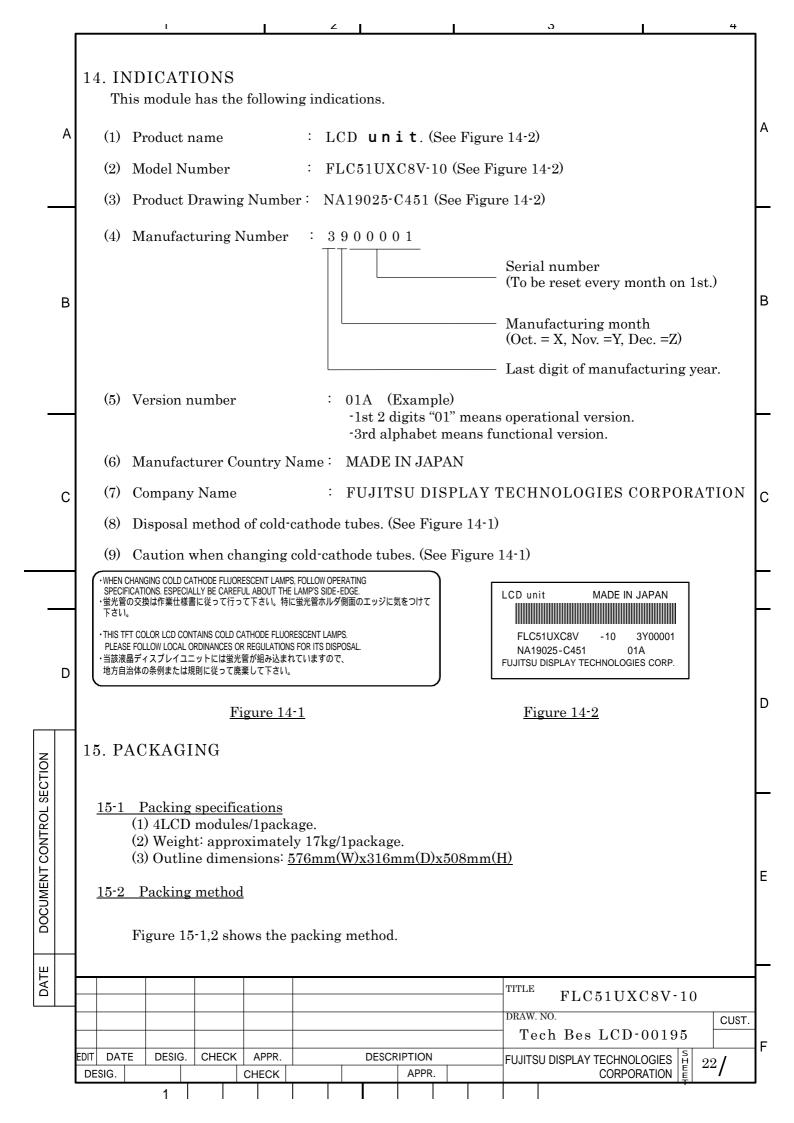
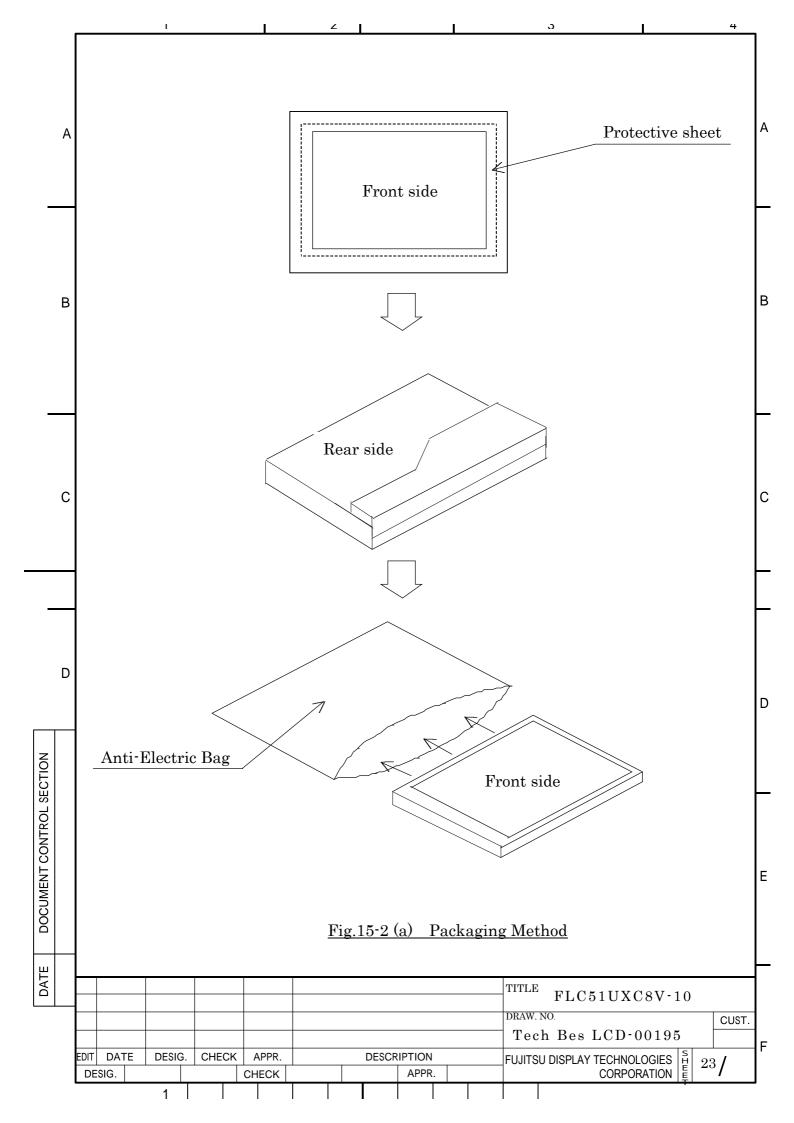
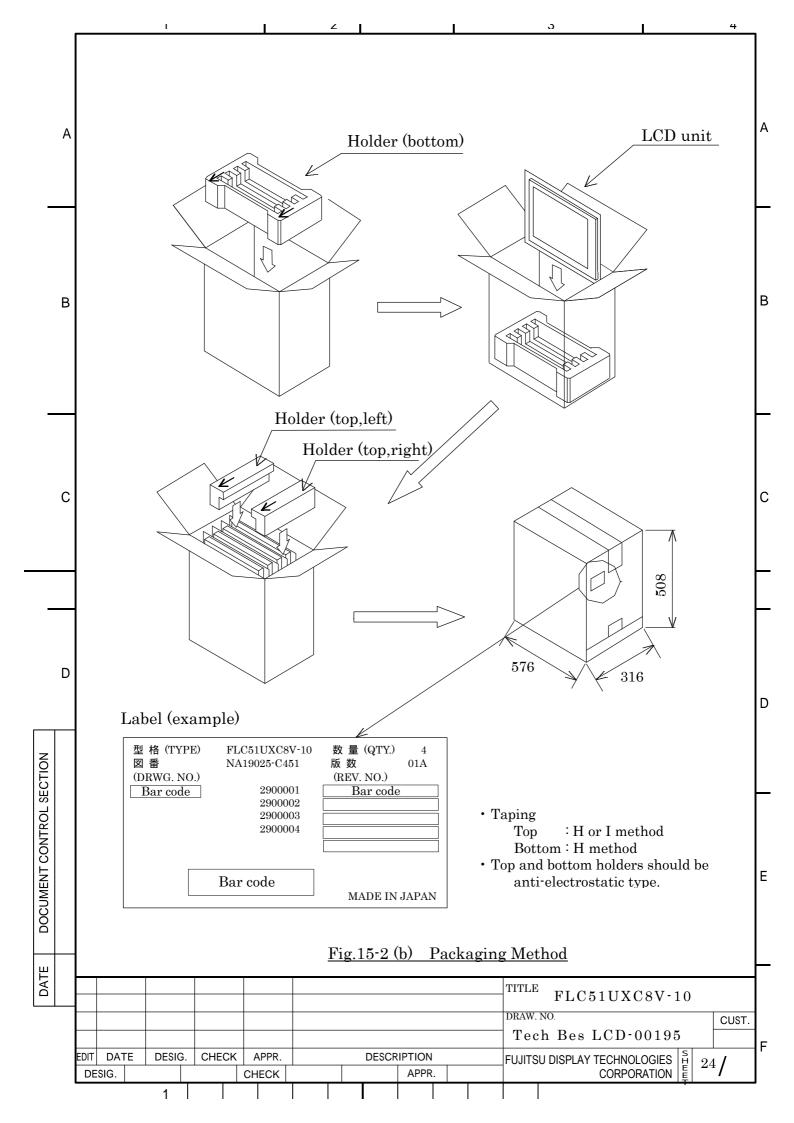


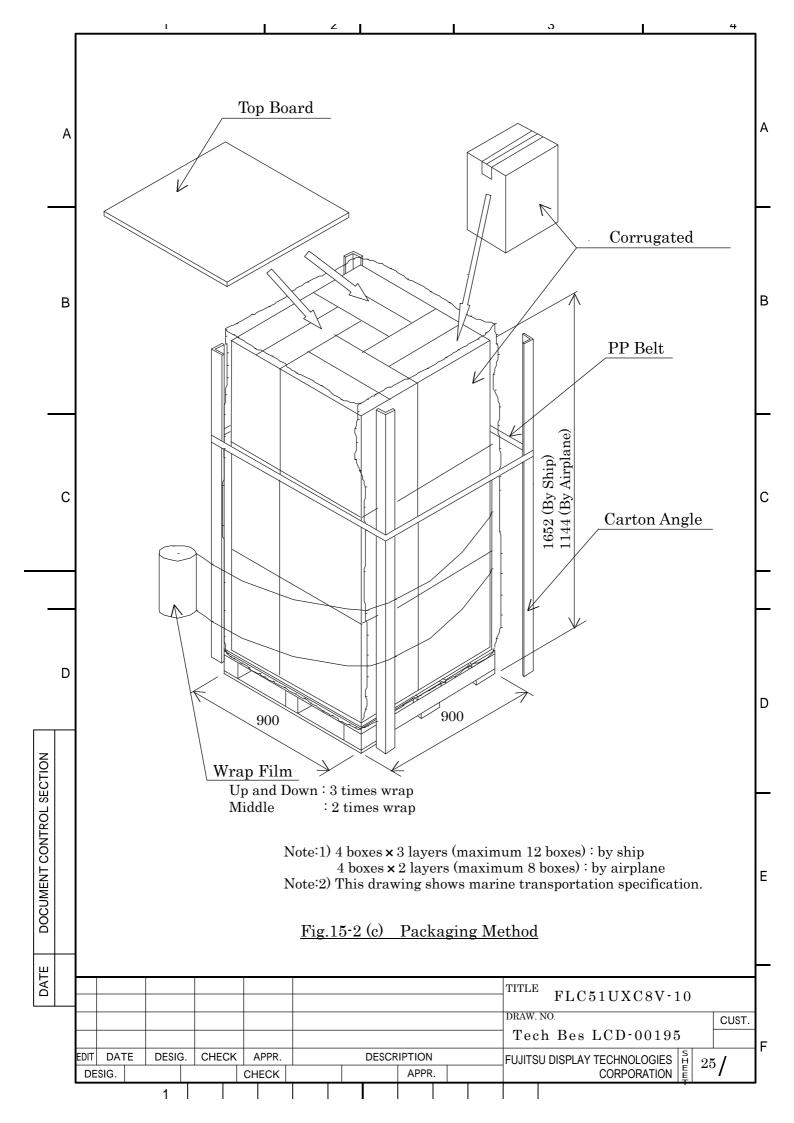
Figure 13-1 Direction to apply shock to package

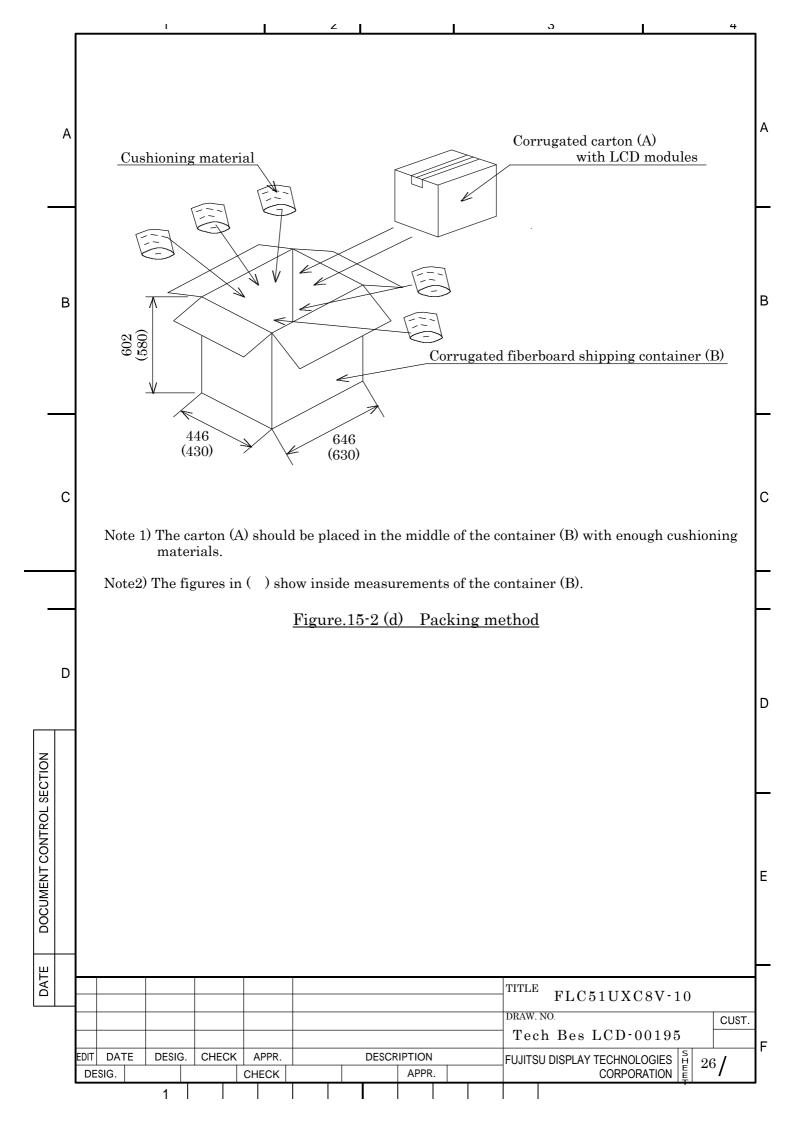
											1
											H
								TITLE	FLC51UXC8V-10		
								DRAW.		CUST.	
									ech Bes LCD-00195		F
EDIT	DATE	DESIG	. CHECK	APPR.	DES	CRIPTION		FUJITS	SU DISPLAY TECHNOLOGIES S 21	/	ľ
DE	SIG.			CHECK		APPR.			CORPORATION E	/	l
		1									











16.WARRANTY The warranty period is one year after shipping. Products which fail during this period are repaired or replaced without charge, unless the failure is caused by user. 17.PRECAUTIONS Adhere to the following precautions to use this LCD module properly. (1) Handling of LCD panel Do not apply any strong mechanical shock to the LCD panel. В Since the LCD panel is made of glass, excessive shock may damage the panel or cause a malfunction. Do not press hard on the LCD panel surface. In the LCD panel, the gap between two glass plates is kept perfectly even to maintain display properties and reliability. The hard pressure on the LCD panel may cause the following problems. Ununiformity of color Disorder of orientation of liquid crystal returns to normal condition after a while. Problem Problem returns to normal condition by turning the power off and turning on again. C However these operations should be avoided to insure reliability. Do not scratch the polarizer film on the LCD panel surface. • Do not press or rub the display surface with a hard tool, tweezers, etc. • For handling, use cotton or conductive gloves so that the display surface is not soiled. • If dust or dirt soils the display surface, clean it as follows with a soft cloth (deerskin, etc.) [Dust] Wipe off with a soft cloth. (do not rub.) [Dirt] Apply clear water to a soft cloth and squeeze hard out of water drops, then lightly wipe off the specified parts. Only if the dirt is hardly wiped off, use isopropyl alcohol D or ethanol. Be careful not to splash the water or the solvents on the edge of polarizer and in the The polarizer possibly exfoliates due to the solvent and water penetrated between DOCUMENT CONTROL SECTION the polarizer and the LCD panel. Do not use unspecified solvent such as ketone (acetone, etc.) and aromatics (xylene, toluene, etc.) (Caution) Be careful not to allow the water or solvent to enter the module. • If saliva or water drops are left for a long period of time, the part may become Ε deformed or discolored. Wipe off immediately in the same way as for dirt. • Do not allow oil to adhere to the module since excessive oil is hard to clean. DATE TITLE FLC51UXC8V-10 CUST. Tech Bes LCD-00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 27 / DESIG. CHECK **CORPORATION**

Do not place or contact objects on the display surface for a long period of time. This may make some parts of the LCD module distorted and the quality of display may deteriorate. (2) Handling of LCD module Do not pull the cold-cathode tube cable strongly. If the cable is pulled with the strength of 2kg or more, the cable may be damaged or may lose reliability. В В Assemble the module into user's system in a dust free environment. Conductive foreign matter adheres to the module may cause failures. Take anti-electrostatic measures for assembling the module. Since the LCD module contains CMOS-ICs, the following points should be observed. · For assembling the module, operator should be grounded and wear cotton or conductive gloves. · Floor of work area and work table to assemble the LCD module should be covered with electrostatic shielding in order to discharge static electricity via an earth wire. · If necessary, ground operation tools (soldering iron, radio pliers, tweezers, etc.). С • Do not take the module out of the conductive bag until the module is assembled. • Do not assemble the module under low humidity (50%RH or less). Do not pull the connecting cable on the rear face of the LCD module strongly. Do not disassemble or remodel the LCD module. Disassembly or remodeling of the LCD module may result in malfunctions or deterioration of the display quality and reliability. (3) Precautions in regards of operating the LCD module D Adhere to the specified power supply sequence. If not followed, the CMOS-IC may cause a latch-up, or DC voltage may be applied to the liquid crystal, which cause a failure or serious deterioration in display quality. DOCUMENT CONTROL SECTION Do not operate the LCD module when condensation occurs. If the LCD module is operated when condensation is on the terminals of the LCD panel, the terminals cause electrochemical reaction, and may reach disconnection. Condensation easily occurs especially when the module is moved from cold environment to warm environment. Ε DATE TITLE FLC51UXC8V-10 CUST. Tech Bes LCD-00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 28/DESIG. CHECK CORPORATION

The following troubles occur when the LCD module is not used under recommended Α temperature. • Operation under high temperature(>50): Display colors shift to blue. · Storage under high temperature(>60): The polarizer film deteriorates and contrast decreases. • Operation under low temperature (< 0): The response speed decreases considerably. · Storage under low temperature (<-20): The liquid crystal may solidify and become В damaged. Be sure to input the control signals at the correct timing. If control signals (DCLK, ENAB) are not input, or if the timing is out of the specified timing, DC voltage may be applied to the liquid crystal and, as a result, cause image sticking or deterioration of contrast. (4) Precautions in regards of designing module mounting Excessive force should not be applied to the screen or the rear side of the LCD modul Excessive pressure on the screen caused by the installation of the LCD module may C deteriorate display quality and reliability. Brightness uniformity and the reliability of CCFL may decrease if the pressure is applied to the backlight module. Avoid twisting and bending the LCD module. Excessive twist and bend may damage display quality and reliability. Avoid extending the power cable between the LCD module and inverter. This may cause the backlight to flicker or not to light. Keep the backlight cable apart from the metal enclosure of the LCD module. D When frequency current for backlight driving leak to the metal enclosure, the desired brightness may not be assured. When Mounting LCD module with MB screws (x4), tighten the screws with torque below 29.4Nm(3kgf). DOCUMENT CONTROL SECTION (5) Storage method Do not store the LCD module in an atmosphere of organic solvent or corrosive gas. In an organic solvent atmosphere, the polarizer film discolors and display quality deteriorates. In a corrosive gas environment, various parts of the module may corrode or deteriorate. E Store the LCD module in a Fujitsu package. At storing, Fujitsu packages can be stacked up to 3 boxes. The LCD module is in an anti-static bag. Keep the module in that status. DATE TITLE FLC51UXC8V-10 CUST. Tech Bes LCD-00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 29 / DESIG. CORPORATION CHECK

The LCD module is recommended to be stored in humidity controlled, cool and dark Recommended storage environment Place : Dark (avoid direct sunlight) • Temperature : $10 \sim 35$ Humidity $: 50 \sim 60\% RH$ Note) If the module is left in an environment of 60 and above for a long period of time, optical characteristics may deteriorate. (6) Disposal Method В LCD module The components of this LCD module can be grouped into metal, resin, glass and so on. As the backlight contains CCFL which includes mercury, it must be disposed according to the local ordinance or regulations. Package All the packages are made of recyclable papers except the anti-ESD bag. (7) Others If the LCD panel is damaged, do not inhale and do not swallow the liquid crystal. C If the liquid crystal adhere to the body or cloths, wash it off with soap immediately. Follow regular precautions for electronic components. Flux residue on the printed circuit board is harmless to the quality and reliability of LCD module. Fujitsu has adopted non-wash technology on module assembly process. D DOCUMENT CONTROL SECTION TITLE FLC51UXC8V-10 DRAW. NO. CUST. Tech Bes LCD-00195 EDIT DATE DESIG. CHECK APPR. **DESCRIPTION** FUJITSU DISPLAY TECHNOLOGIES 30 / DESIG. CORPORATION CHECK

