

PREPARED BY :      DATE	<h1 style="text-align: center;">SHARP</h1> <p style="text-align: center;">Mobile Liquid Crystal Display Group SHARP CORPORATION</p> <h2 style="text-align: center;">SPECIFICATION</h2>	SPEC No. LD-17437
		FILE No.
APPROVED BY :      DATE		ISSUE : Nov. 05. 2005
		PAGE : 39 pages
		APPLICABLE GROUP Mobile Liquid Crystal Display Group

DEVICE SPECIFICATION FOR

# TFT-LCD Module






MODEL No.

## LQ170K1LW01




### (FLC43XWC8V-06A)

**英文仕様書確認印**

モバイル液晶事業本部    第1設計センター    第4開発部

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ユーザー名：

備考：17.0型WXGAカラーモジュール

☐ CUSTOMER'S APPROVAL

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**TFT-LCD Module**  
 MODEL No.  
**LQ170K1LW01**  
**(FLC43XWC8V-06A)**

**CUSTOMER :** \_\_\_\_\_

☐ CUSTOMER'S APPROVAL

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SHARP Corporation

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05	20041130	Sekido	Takahashi	T.Ito	Revised P3,8	LQ170K1LW01 (FLC43XWC8V-06A)						
04	20040908	Sekido	Takahashi	T.Ito	Revised P6,12							
07	20051105	S.Fukutoku		T.Ito	Revised P1, P32	LD17437					CUST.	
06	20050509	Sekido		T.Ito	Add 02A, Revised P1,3,19,24,31,39							
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	DESIG.	20040623	Sekido	CHECK	Takahashi		APPR.	Ito				

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A	<b>1. APPLICATIONS</b> This specification is applied to the 17.0 in. XGA-WIDE supported TFT-LCD module.											A																																																																																		
	<b>2. PRODUCT NAME AND MODEL NAME</b>  <u>2-1. Product Name</u> : LCD Module  <u>2-2. Model Number</u> : LQ170K1LW01 (FLC43XWC8V-06A)  <del><u>2-3. Drawing Number</u> : NA19027-C052</del>											B																																																																																		
C	<b>3. OVERVIEW</b> This LCD module has a TFT active matrix type liquid crystal panel 1280×768 pixels, and diagonal size of 43cm (17.0-inch). This module supports 1280×768 XGA-WIDE mode (Non-interlace). This LCD has a XGA-WIDE-capable 1ch-LVDS interface and can display 16.2 million colors. By timing control signal, images are displayed on the screen at given value under specified timing. Control signals to this LCD module are ENAB (Data enable signal), DCLK signal and RGB data signal. Data signal is transmitted with single port. (8 bit for each R, G, B) Internal control signal of Timing control IC to Data-IC are 1ch-RSDS signal. The power supply of this LCD module is +5V DC. This module has the characteristics that apply to TCO'03 and EBU100%.											C																																																																																		
	<b>4. CONFIGURATION</b> This LCD module consists of a color TFT-LCD panel that mounted with TFT driver ICs, a cold-cathode fluorescent tube back-light, a LVDS interface PCB, a structure and a bezel. And these components are integrated in a LCD module. Cold-cathode fluorescent tube and lamp holder are replaceable. Fig.4-1 shows a block diagram of this LCD module.											D																																																																																		
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A	<div><div><div>Input signal</div><div>RxCLK RxCLK+ Rx0 Rx0+ Rx1 Rx1+ Rx2 Rx2+ Rx3 Rx3+</div></div><div>Power supply + 5 V DC</div><div>LVDS</div><div><div>Interface circuit</div><div>DC/DC converter</div><div>Bias power supply circuit</div></div><div>DC</div><div>(Separate)</div><div>Inverter</div><div>RSDS</div><div>Data Driver</div><div>Gate Driver</div><div>L C D panel 1280 × 3(RGB) × 768</div></div>					A																																				
B						B																																				
C	<div>Figure 4-1. Block Diagram</div>					C																																				
<div>5. MECHANICAL SPECIFICATIONS</div> <div>Table 5-1 shows the mechanical specifications of this LCD module.</div> <div>Table 5-1. Mechanical Specifications</div> <table><tr><th>Item</th><th>Specifications</th><th>Unit</th><th>Remark</th></tr><tr><td>Dimensions</td><td>413.8×259.0×15.0(TYP.)</td><td>mm</td><td rowspan="4">Edge type backlight is used. (φ2.6 CCFL×4)  Outward Appearance is shown at last page.</td></tr><tr><td>Display Resolution</td><td>(1280×3) ×768</td><td>—</td></tr><tr><td>Display Dot Area</td><td>369.6×221.76</td><td>mm</td></tr><tr><td>Dot Pitch</td><td>(0.09625×3) ×0.28875</td><td>mm</td></tr><tr><td>Aspect Ratio</td><td>1 : 1</td><td>—</td><td></td></tr><tr><td>Weight</td><td>2,000 max</td><td>g</td><td></td></tr><tr><td>FG-SG</td><td>Separate</td><td>—</td><td></td></tr></table>						Item	Specifications	Unit	Remark	Dimensions	413.8×259.0×15.0(TYP.)	mm	Edge type backlight is used. (φ2.6 CCFL×4)  Outward Appearance is shown at last page.	Display Resolution	(1280×3) ×768	—	Display Dot Area	369.6×221.76	mm	Dot Pitch	(0.09625×3) ×0.28875	mm	Aspect Ratio	1 : 1	—		Weight	2,000 max	g		FG-SG	Separate	—		D							
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6. ABSOLUTE MAXIMUM RATINGS

Table 6-1 shows the absolute maximum rating of this LCD module.

Table 6-1. Absolute Maximum Ratings

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>CC</sub>	- 0.3	—	6.0	V
Input Voltage	V <sub>IN</sub>	- 0.3	—	V <sub>CC</sub> +0.3	V

B

7. RECOMMENDED OPERATING CONDITIONS

Table 7-1 shows the recommended operating conditions of this LCD module.

Table 7-1. Recommended Operating Conditions

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>CC</sub>	4.75	5.0	5.25	V
Ripple Voltage (V <sub>CC</sub> )	V <sub>RP</sub>	—	—	100	mVp-p

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8. ELECTRICAL SPECIFICATIONS

Table 8-1 shows the electrical specifications of this LCD module.

Table 8-1. Electrical Specifications

Item	Symbol	Condition	MIN.	TYP.	MAX.	Unit	Remark	
Supply Current	I <sub>CC</sub>	V <sub>CM</sub> =+1.2V	—	700	1000	mA	*1	
Differential Input Voltage (High)	V <sub>IH</sub>		—	—	100	mV		
Differential Input Voltage (Low)	V <sub>IL</sub>		-100	—	—	mV		
"H" Level Data Mapping Select Input Voltage	V <sub>SH</sub>	V <sub>CC</sub> =+5.0±0.25V V <sub>SS</sub> =0V	3.0	3.3	3.6	V	*2	
"L" Level Data Mapping Select Input Voltage	V <sub>SL</sub>	DCLK=32.498MHz	GND	—	0.9	V	*2	
BACK LIGHT	CCFL Turn on Voltage	V <sub>S</sub>	f <sub>L</sub> =50kHz, T <sub>a</sub> =25°C	—	1230	1600	V <sub>rms</sub>	
			f <sub>L</sub> =50kHz, T <sub>a</sub> =0°C	—	—	1600		
	Lighting Voltage	V <sub>L</sub>	f <sub>L</sub> =50kHz I <sub>L</sub> =10.5mA	590	630	670	V <sub>rms</sub>	*4
	Lighting Frequency	f <sub>L</sub>	V <sub>L</sub> =630V <sub>rms</sub>	40	50	60	kHz	
	*3 Tube Current	I <sub>L</sub>	f <sub>L</sub> =50kHz V <sub>L</sub> =630V <sub>rms</sub>	9.5	10.5	11.0	mArms	*4

(\*1) Typical current value is measured when gray scale (vertical 256 levels) is displayed at V<sub>cc</sub>=5.0V.  
Maximum current value is measured when V stripes with respect to each RGB dot are displayed at V<sub>cc</sub>=5.0V.  
Without rush current.

(\*2) ~~Timing control circuit input voltage~~ No.20 Pin of I/F connector.

(\*3) Tube current (I<sub>L</sub>) shows the value of the current and voltage that is consumed at one lamp.  
(4 tubes/unit)  
This LCD module has 4 lamps. Each 2 lamps are placed at upper and lower side of the display.  
2 lamps are connected in parallel. Each low voltage terminals (GND side) are bound into 1 line cable.  
(See 11-1. Pin configuration for backlight)

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04	20040908					Specified V <sub>IH</sub> , V <sub>IL</sub> , V <sub>SH</sub> , V <sub>SL</sub>	
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### Note 1) Measurement Circuit

Based on Fig.8-1.

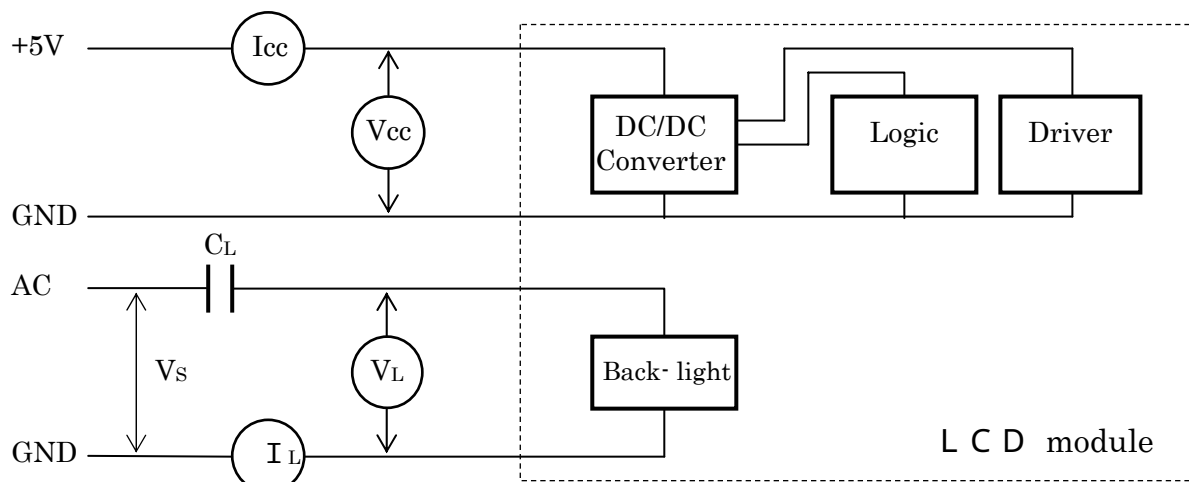


Fig.8-1. Measurement Circuit

### Note 2) Equivalent Circuit

Based on Fig.8-2 (a), (b).

Input signals(LVDS-1ch)

RxCLK+  
RxCLK-  
Rx0+  
Rx0-  
Rx1+  
Rx1-  
Rx2+  
Rx2-  
Rx3+  
Rx3-

Differential Input

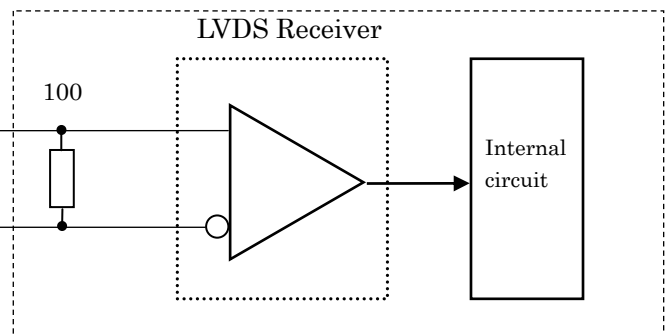


Fig.8-2 (a) Equivalent Circuit of Logic Signal Input

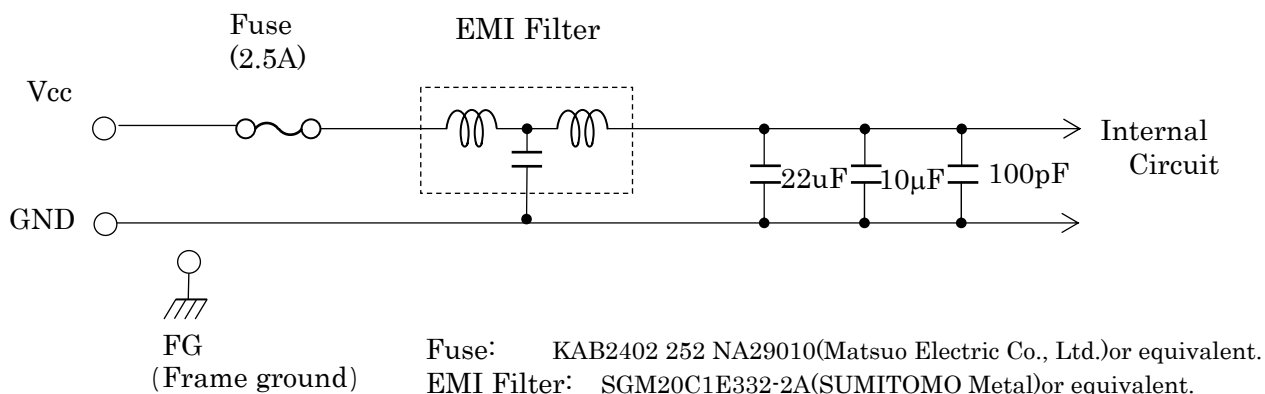


Fig.8-2 (b) Equivalent Circuit of Power Supply

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## 9. OPTICAL SPECIFICATIONS

Table 9-1 shows the optical specifications of this LCD module.

### Table 9-1. Optical Specifications

Signal timing=Typ, Ta=25°C

Item		Symbol	Condition		Specifications			Unit	Remark	
					MIN.	TYP.	MAX			Note
Visual Angle	Horizontal	θ <sub>L,R</sub>	CR≥10	θ <sub>U,D</sub> =0°	85	89	—	deg		(1)(2)
	Vertical	θ <sub>U,D</sub>		θ <sub>L,R</sub> =0°	85	89	—	deg		(3)(5)
	all direction	θ <sub>all</sub>			—	80	—	deg		(6)
Contrast Ratio		CR	θ <sub>L,R,U,D</sub> =0°		350	600	—	—	White/Black	(1)(2)(3)(5)
Response Time (rise+fall) (B W B)		rise + fall	θ <sub>L,R,U,D</sub> =0°	Ta=25°C	—	16	—	ms		(1)(4)(5)
				Ta=0°C	—	34	—	ms		
Response Time (Rise or Fall) (All gray scale)		AVG	θ <sub>L,R,U,D</sub> =0°	Ta=25	—	10	—	ms	Average of Response Time, *2	
Brightness		I	θ <sub>L,R,U,D</sub> =0° V <sub>CC</sub> =5V, I <sub>L</sub> =10.5mA (at maximum brightness)		350	450	—	cd/m <sup>2</sup>	White*1	(1)(5)
Brightness Uniformity		ΔI			75	—	—	%		(1)(5)(7)
Chromaticity	W	X			0.283	0.313	0.343	—		(1)(5)
		Y			0.299	0.329	0.359	—		
	R G B	(x, y)	R,G,B signal=All "H"	Red	(0.64, 0.35 Typ.)					
				Green	(0.29, 0.59 Typ.)					
Blue				(0.14, 0.08 Typ.)						
γ-curve					2.4 Typ.					
LCD Panel Type					TFT Color					
Display Mode					Normally Black VA					
Wide Viewing Angle Technology					MVA-Premium					
Optimum Viewing Angle					— (symmetry)					(6)
Display Color					16.2 million (each 6-bit+2-bit FRC)					
Color of non-display area					Black					
Surface Treatment					Anti-glare (Haze value:25%, 3H(at weight of 300g))					

(\*1) Specified value is measured in 20~30 minutes after lighting on (LCD module single).

A required value may not be achieved on condition that LCD module is built in the cabinet because of its radiation.

All items without “Brightness Uniformity” are measured at the center of display board.

(\*2) In case of applying optimized over driving method.

(Note1) \*CS-1000 (MINOLTA Co., Ltd.), Field=2°, L=500mm

\*Be careful that the luminance meter, which you use, may not be able to get correct brightness  
If it's not set correctly.

[illegible]

Note 1) Definition of Viewing Angle (1)

Based on Fig.9-2.

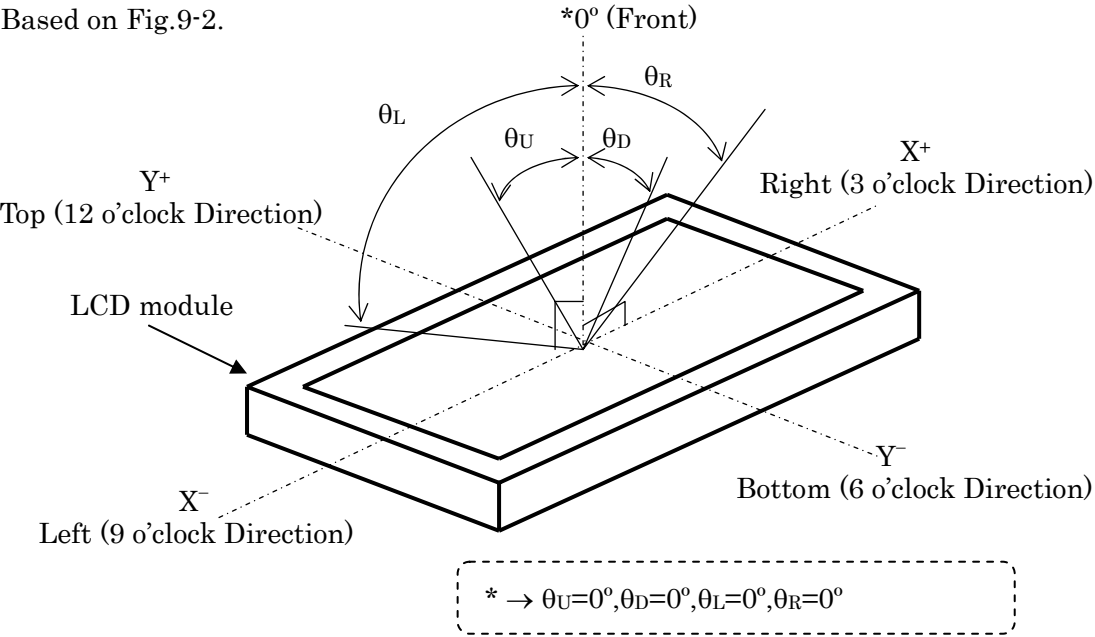


Fig.9-2. Definition of Viewing Angle (1)

Note 2) Definition of Viewing Angle (2)

Based on Fig.9-3.

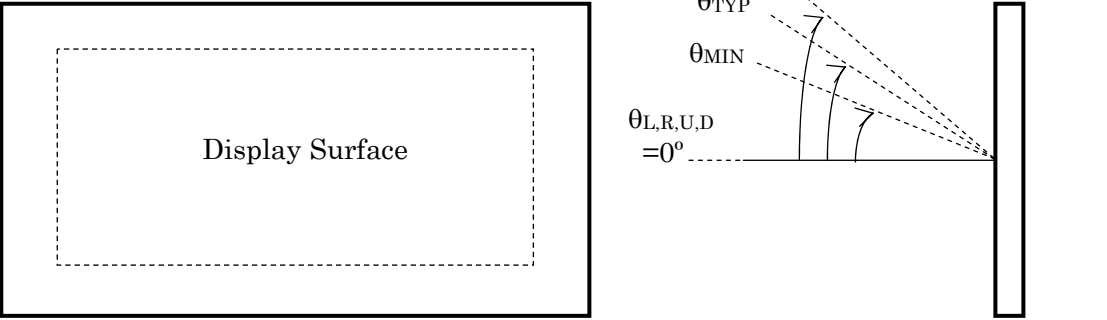


Fig.9-3. Definition of Viewing Angle (2)

Note 3) Definition of Contrast Ratio (CR)

Determined by Formula (1) based on Fig.9-4.Voltage-Brightness Characteristics.

$$= \frac{L_W \text{ (Brightness at white)}}{L_B \text{ (Brightness at black)}} \quad \text{----- (1)}$$

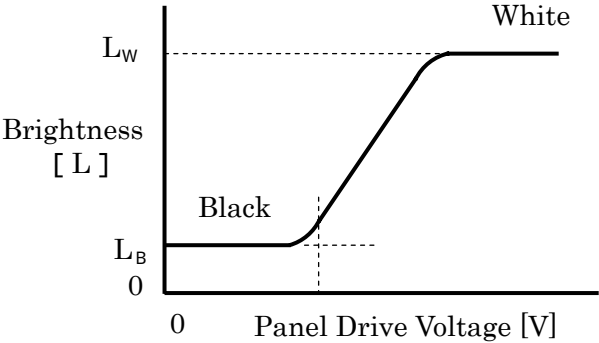


Fig.9-4. Voltage-Brightness Characteristics

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Note 4) Definition of Response Time  
Based on Fig.9-5.

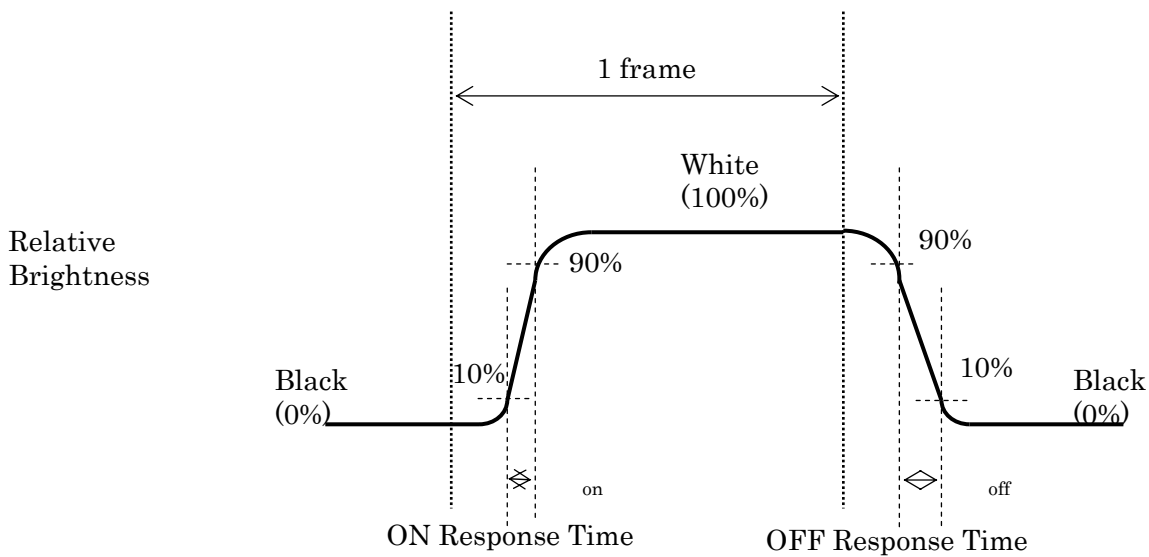


Fig.9-5. Definition of Response Time

Note 5) Contrast Ratio and Response Measurement System  
Based on Fig.9-6.

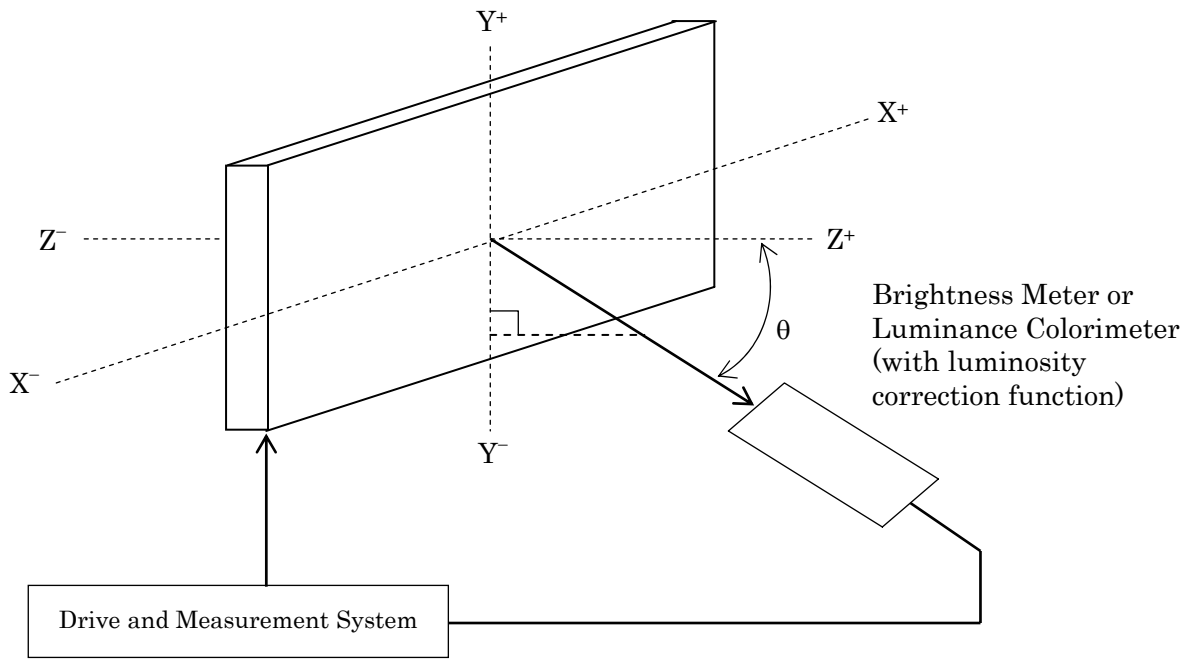


Fig.9-6. Contrast Ratio and Response Time Measurement System

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Note 6) Definition of Optimum Viewing Angle

Based on Fig.9-7.

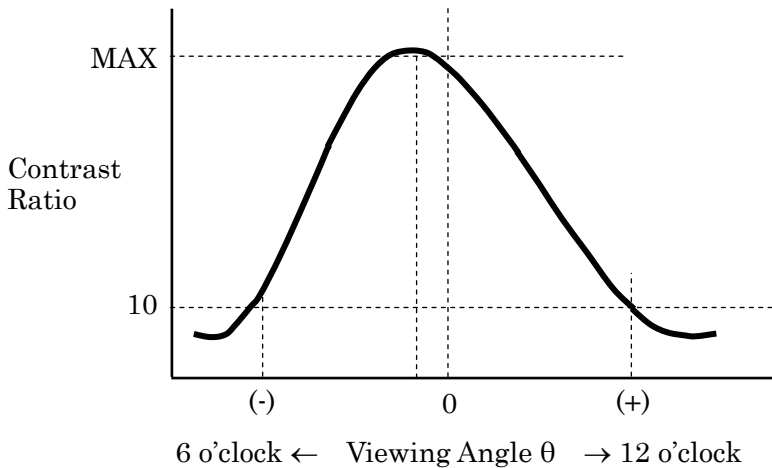
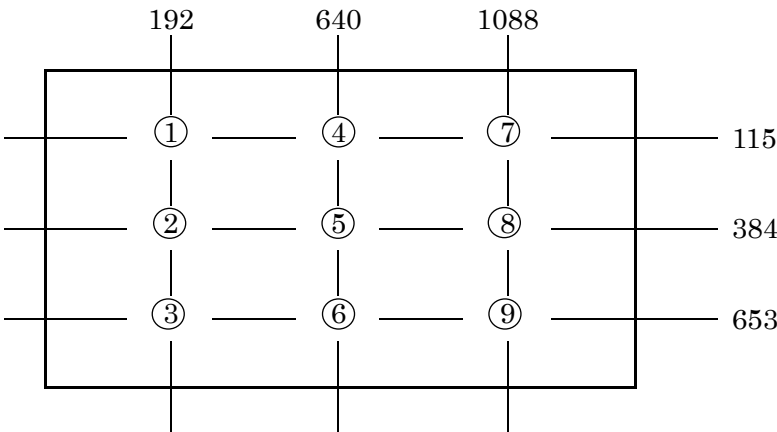


Fig.9-7. Definition of Viewing Angle

Note 7) Definition of Brightness Uniformity

Brightness uniformity is defined by the following formula.  
Brightness (I1~I9) are measured at the following 9 points ( ① ~ ⑨ ) on the display area shown in Fig.9-8.

Brightness Uniformity ( $\Delta L$ ) =  $\frac{| \text{Min. In} |}{| \text{Max. In} |} \times 100 (\%), n=1 \text{ to } 9$



Note) Each measurement point ( ① ~ ⑨ ) defines the center spot of Brightness Meter view.  
The tolerance of measurement position is  $\pm 3\text{mm}$ .

Fig.9-8. Measurement Points

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### 10-1-2. LVDS Data Mapping 1

Table 10-1-2 shows the LVDS data mapping 1.(DataMappingSelectInput = Low)

Table 10-1-2. LVDS Data Mapping 1

Transmitter (DS90CF385)		Symbol	Interface connector		Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin LCD module	Pin	OUTPUT	
51	TxIN0	R 0	TxOUT0- TxOUT0+	5 6 RX0- RX0+	27	RxOUT0	I R 0
52	TxIN1	R 1			29	RxOUT1	I R 1
54	TxIN2	R 2			30	RxOUT2	I R 2
55	TxIN3	R 3			32	RxOUT3	I R 3
56	TxIN4	R 4			33	RxOUT4	I R 4
2	TxIN5	R 7	TxOUT3- TxOUT3+	17 18 RX3- RX3+	34	RxOUT5	I R 7
3	TxIN6	R 5	TxOUT0- TxOUT0+	5 6 RX0- RX0+	35	RxOUT6	I R 5
4	TxIN7	G 0			37	RxOUT7	I G 0
6	TxIN8	G 1	TxOUT1- TxOUT1+	8 9 RX1- RX1+	38	RxOUT8	I G 1
7	TxIN9	G 2			39	RxOUT9	I G 2
8	TxIN10	G 6	TxOUT3- TxOUT3+	17 18 RX3- RX3+	41	RxOUT10	I G 6
10	TxIN11	G 7			42	RxOUT11	I G 7
11	TxIN12	G 3	TxOUT1- TxOUT1+	8 9 RX1- RX1+	43	RxOUT12	I G 3
12	TxIN13	G 4			45	RxOUT13	I G 4
14	TxIN14	G 5			46	RxOUT14	I G 5
15	TxIN15	B 0			47	RxOUT15	I B 0
16	TxIN16	B 6	TxOUT3- TxOUT3+	17 18 RX3- RX3+	49	RxOUT16	I B 6
18	TxIN17	B 7			50	RxOUT17	I B 7
19	TxIN18	B 1	TxOUT1- TxOUT1+	8 9 RX1- RX1+	51	RxOUT18	I B 1
20	TxIN19	B 2	TxOUT2- TxOUT2+	11 12 RX2- RX2+	53	RxOUT19	I B 2
22	TxIN20	B 3			54	RxOUT20	I B 3
23	TxIN21	B 4			55	RxOUT21	I B 4
24	TxIN22	B 5			1	RxOUT22	I B 5
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18 RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12 RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED			5	RxOUT25	Not use
30	TxIN26	ENAB			6	RxOUT26	E N A B
50	TxIN27	R 6	TxOUT3- TxOUT3+	17 18 RX3- RX3+	7	RxOUT27	I R 6
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15 RXCLK- RXCLK+	26	RxCLKOUT	D C L K

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### 10-1-3. LVDS Data Mapping 2

Table 10-1-3 shows the LVDS data mapping 2.(DataMappingSelectInput = Open or Hi)

**Table 10-1-3. LVDS Data Mapping 2**

Transmitter (DS90CF385)		Symbol	Interface connector			Receiver (DS90CF386)		LCD Control input
Pin	INPUT		System side	Pin	LCD module	Pin	OUTPUT	
51	TxIN0	R 2	TxOUT0- TxOUT0+	5 6	RX0- RX0+	27	RxOUT0	IR 2
52	TxIN1	R 3				29	RxOUT1	IR 3
54	TxIN2	R 4				30	RxOUT2	IR 4
55	TxIN3	R 5				32	RxOUT3	IR 5
56	TxIN4	R 6				33	RxOUT4	IR 6
2	TxIN5	R 1	TxOUT3- TxOUT3+	17 18	RX3- RX3+	34	RxOUT5	IR 1
3	TxIN6	R 7	TxOUT0- TxOUT0+	5 6	RX0- RX0+	35	RxOUT6	IR 7
4	TxIN7	G 2				37	RxOUT7	IG 2
6	TxIN8	G 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	38	RxOUT8	IG 3
7	TxIN9	G 4				39	RxOUT9	IG 4
8	TxIN10	G 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	41	RxOUT10	IG 0
10	TxIN11	G 1				42	RxOUT11	IG 1
11	TxIN12	G 5	TxOUT1- TxOUT1+	8 9	RX1- RX1+	43	RxOUT12	IG 5
12	TxIN13	G 6				45	RxOUT13	IG 6
14	TxIN14	G 7				46	RxOUT14	IG 7
15	TxIN15	B 2				47	RxOUT15	IB 2
16	TxIN16	B 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	49	RxOUT16	IB 0
18	TxIN17	B 1				50	RxOUT17	IB 1
19	TxIN18	B 3	TxOUT1- TxOUT1+	8 9	RX1- RX1+	51	RxOUT18	IB 3
20	TxIN19	B 4	TxOUT2- TxOUT2+	11 12	RX2- RX2+	53	RxOUT19	IB 4
22	TxIN20	B 5				54	RxOUT20	IB 5
23	TxIN21	B 6				55	RxOUT21	IB 6
24	TxIN22	B 7				1	RxOUT22	IB 7
25	TxIN23	RESERVED	TxOUT3- TxOUT3+	17 18	RX3- RX3+	2	RxOUT23	Not use
27	TxIN24	RESERVED	TxOUT2- TxOUT2+	11 12	RX2- RX2+	3	RxOUT24	Not use
28	TxIN25	RESERVED				5	RxOUT25	Not use
30	TxIN26	ENAB				6	RxOUT26	E N A B
50	TxIN27	R 0	TxOUT3- TxOUT3+	17 18	RX3- RX3+	7	RxOUT27	IR 0
31	TxCLKIN	DCLK	TxCLKOUT- TxCLKOUT+	14 15	RXCLK- RXCLK+	26	RxCLKOUT	D C L K

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## 10-2. Color Data Assignment

Table 10-2 shows the color data assignment.

**Table 10-2. Color Data Assignment**

A

B

D

E

F

Color and Brightness		Input data ( 0 : Low level , 1 : High level )																							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	254	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	255	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	254	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0
	Green	255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	Darker	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	↑	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	...	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Brighter	253	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue	255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0

Note.1) Definition of gray scale : Color (n) ---"n" indicates gray scale level.

The gray scale is brighter as the number is larger.

Note.2) Data; 1: High, 0: Low

Note.3) By inputting 8-bit data signal for each red, green and blue, this module can display 256 gray scale independently for each color.

Therefore, the module is able to display 16 million colors. Color data are 24 lines.

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### 10-3. Input Signal Timing

Table 10-3 and Fig.10-3 shows the input signal timing.

**Table 10-3. Timing Characteristics**

(T=0~50°C, Vcc=5±0.25V)

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK signal (Clock)	Period	Tc	12.195	15.382	20.000	ns	fc=1/Tc *1
	Frequency	fc	50.00	65.01	82.00	MHz	
	Duty	Tch/Tc	45	50	55	%	
	High time	TclkH	5.0	—	—	ns	
	Low time	TclkL	5.0	—	—	ns	
	Rise time	Tclkr	—	—	5.0	ns	
	Fall time	Tclkf	—	—	5.0	ns	
DCLK-Data Timing	Setup time	Tset	4	—	—	ns	
	Hold time	Thold	4	—	—	ns	
Data-ENAB timing	Horizontal	Period	Th	1310	1344	1688	DCLK
		Frequency	fh	35.0	48.3	75.0	kHz
		Display period	Thd	1280	1280	1280	DCLK
	Vertical	Period	Tv	776	806	806	Th
		Frequency	1/Tv	50	60	75	Hz
		Display period	Tvd	768	768	768	Th
	Data-ENAB timing		Tdn	0	0	0	DCLK

\*1) DCLK signal input must be valid while power supply is applied.

\*2) Display position is specified by the ENAB signal only.

· Horizontal display position is specified by the rise of ENAB signal. The data which is latched by the falling edge of 1st DCLK right after the rise of ENAB, is displayed on the left edge of the screen.

· Vertical display position is specified by the rise of ENAB after a “Low” level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of ENAB is displayed at the top line of screen.

\*3) If a period of ENAB “High” is less than 1280 DCLK or less than 768 lines, the rest of the screen displays black.

\*4) The display position does not fit to the screen if a period of ENAB “High” and the effective data period do not synchronize with each other.

\*5) The specifications of all are not provided individually.

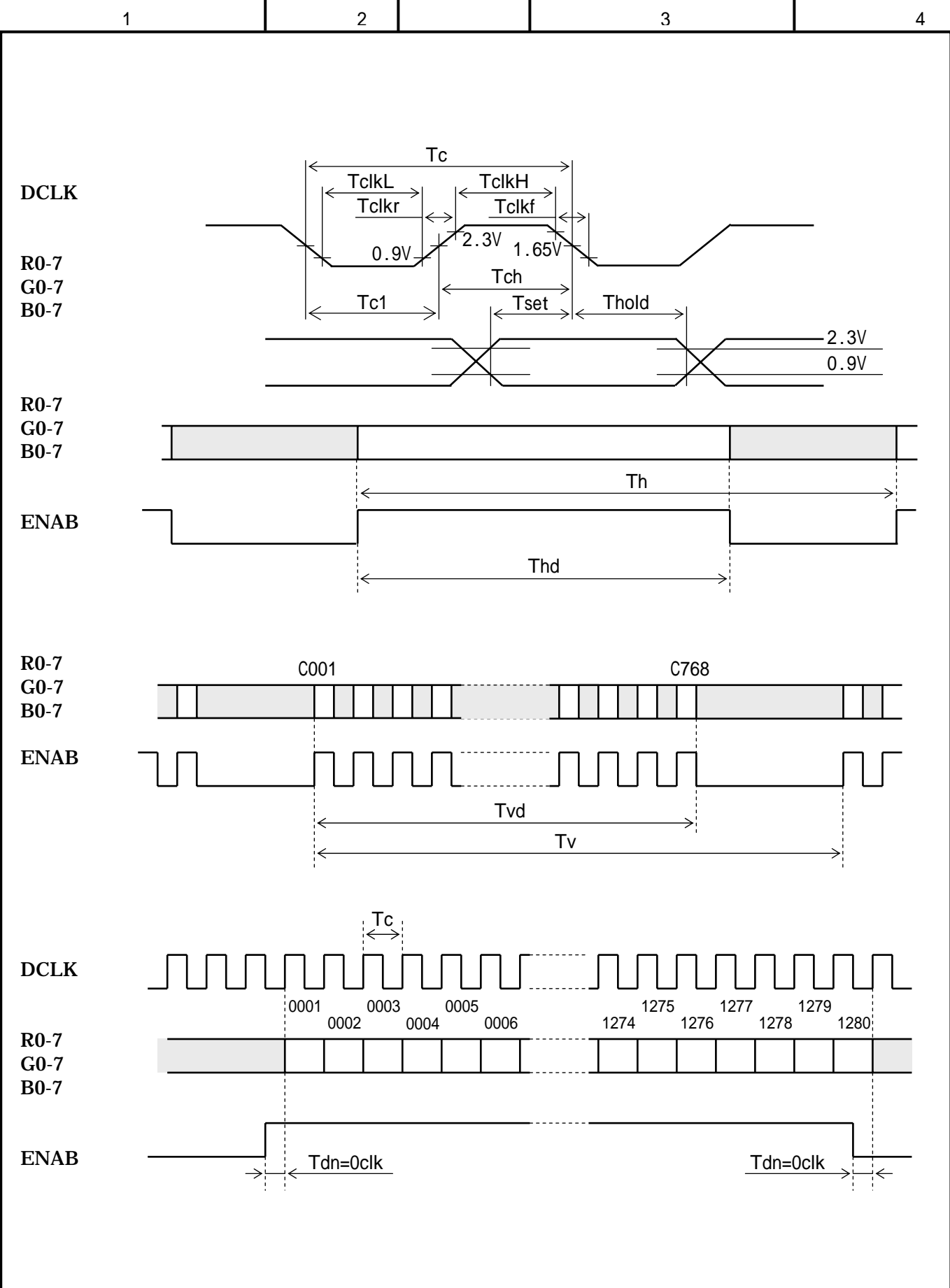
It is necessary that all of those specifications are satisfied at the same time.

\*6) The display quality is guaranteed by the Typ timing.

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**Fig.10-3.Input Signal Timing Chart**

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## 12. APPEARANCE SPECIFICATIONS

### 12-1. Zone

- Inside display dot area (369.6× 221.8mm)
- Display dot area means active area.
- One pixel consists of 3 dots (red, green and blue).
- Foreign particle and scratch unharmed to display image, such as the foreign particle under polarizer film but outside of the display area and scratch on metal bezel, backlight module or polarizer film out of the display area, etc., are not counted.

### 12-2. Bright spots

#### (1) Bright spots by the defect of TFT.

- Visible under bias of 2% ND filter .....High bright spot R•G
- Visible under 5% but invisible under 2% ND filter.....Low bright spot R•G•B
- Invisible under bias of 5% ND filter .....Not counted

#### (2) Bright spots by the light passing through tears, breaks, etc in color filter.

- Exceed size of a half dot.....High bright spot
- A half dot or less.....Not counted

#### (3) Bright spots by the light passing through tears, breaks, etc in chromium mask.

- Exceed 50μm .....High bright spot
- 50μm or less .....Not counted

### 12-3. Test condition

- Inspector must observe the LCD screen from the normal direction under the illumination by a single 20W fluorescent lamp. The distance between the LCD screen and the inspector should be a height of 50cm above the worktable.  
The vertical illuminance is 300 to 600lux (reference value).
- Bright spot should be counted under entire black screen.
- Dark spot should be counted under entire white screen.
- Frame frequency should be 60Hz.

### 12-4. Specifications

Table 12-4 shows the appearance standard.

(Note1) Please do not mistake a single bright spot for a bright spot connection due to Cs(supplemental capacitance) line at the center of each dot.

(Note2) If a pixel is dark partially, it connects into the number of dark spots in accordance with following rule.

(a)  $S < 1/3$  : Not count. Only one of 4 dark connection is allowed.

(b)  $1/3 \leq S < 2/3$  : Considered as 0.5 dot.

(c)  $2/3 \leq S$  : Considered as 1 dot.

(S=Dark spot size/dot size)

(Note3) Min. distance between foreign particles : 100mm

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		1		2		3		4																						
A	No.		Item		Judgment method and standard																									
	1		Bright spot (high and Low)		≤3 dots (Note 1)																									
	2		Bright spot connection (high and low)		≤1 pair (2 dot connection in horizontal) (Note 1)																									
	3		Total of bright spot		≤3 dots																									
	4		Dark spot		≤5 dots (Note 2)																									
	5		Dark spot connection		≤2 pairs (2 dot connection in horizontal) (Note 2)																									
	6		Total of dark spot		≤ 5dots (Note 2)																									
	7		Total of dot defect(bright and dark)		≤ 8 dots																									
	8		Distance of bright spot		high-high high		≥15mm																							
					others		≥ 5mm																							
B	9		Distance of dark spot		≥ 5mm																									
	10		Scratch on polarizer		<table border="1"> <tr> <td>W 0.03</td> <td></td> <td>Not count</td> </tr> <tr> <td rowspan="3">0.03 &lt; W 0.05</td> <td>L 6</td> <td>Not count</td> </tr> <tr> <td>6 &lt; L 12</td> <td>5</td> </tr> <tr> <td>12 &lt; L</td> <td>0</td> </tr> <tr> <td rowspan="4">0.05 &lt; W 0.10</td> <td>L 0.6</td> <td>Not count</td> </tr> <tr> <td>0.6 &lt; L 5</td> <td>3</td> </tr> <tr> <td>5 &lt; L, W × L &lt; 0.5</td> <td>1</td> </tr> <tr> <td>5 &lt; L, 0.5 W × L</td> <td>0</td> </tr> <tr> <td>0.10 &lt; W</td> <td></td> <td>0</td> </tr> </table>					W 0.03		Not count	0.03 < W 0.05	L 6	Not count	6 < L 12	5	12 < L	0	0.05 < W 0.10	L 0.6	Not count	0.6 < L 5	3	5 < L, W × L < 0.5	1	5 < L, 0.5 W × L	0	0.10 < W	
W 0.03		Not count																												
0.03 < W 0.05	L 6	Not count																												
	6 < L 12	5																												
	12 < L	0																												
0.05 < W 0.10	L 0.6	Not count																												
	0.6 < L 5	3																												
	5 < L, W × L < 0.5	1																												
	5 < L, 0.5 W × L	0																												
0.10 < W		0																												
C	11		Dent on polarizer		<table border="1"> <tr> <td>D 0.3</td> <td>Not count</td> </tr> <tr> <td>0.3 &lt; D 0.4</td> <td>6</td> </tr> <tr> <td>0.4 &lt; D</td> <td>0</td> </tr> </table>					D 0.3	Not count	0.3 < D 0.4	6	0.4 < D	0															
	D 0.3	Not count																												
	0.3 < D 0.4	6																												
0.4 < D	0																													
12		Bubble in polarizer		<table border="1"> <tr> <td>D 0.5</td> <td>Not count</td> </tr> <tr> <td>0.5 &lt; D</td> <td>0</td> </tr> </table>					D 0.5	Not count	0.5 < D	0																		
D 0.5	Not count																													
0.5 < D	0																													
D	13		Spot (black/white) by foreign particle		<table border="1"> <tr> <td>D &lt; 0.5</td> <td>5</td> </tr> <tr> <td>0.5 D</td> <td>0</td> </tr> </table>					D < 0.5	5	0.5 D	0																	
	D < 0.5	5																												
	0.5 D	0																												
	14		Bright spot by fiber foreign particle (under the polarizer. i.e. between the polarizer and the glass) (Note 3 )		<table border="1"> <tr> <td>D 0.2</td> <td>Not count</td> </tr> <tr> <td>0.2 &lt; D 0.3</td> <td>2</td> </tr> <tr> <td>0.3 &lt; D 0.35</td> <td>1</td> </tr> <tr> <td>0.35 &lt; D</td> <td>0</td> </tr> </table>					D 0.2	Not count	0.2 < D 0.3	2	0.3 < D 0.35	1	0.35 < D	0													
D 0.2	Not count																													
0.2 < D 0.3	2																													
0.3 < D 0.35	1																													
0.35 < D	0																													
15		Line (black /white), scratch by fiber foreign particle		<table border="1"> <tr> <td>W 0.03</td> <td></td> <td>Not count</td> </tr> <tr> <td rowspan="3">0.03 &lt; W 0.05</td> <td>L 6</td> <td>Not count</td> </tr> <tr> <td>6 &lt; L 12</td> <td>5</td> </tr> <tr> <td>12 &lt; L</td> <td>0</td> </tr> <tr> <td rowspan="3">0.05 &lt; W 0.1</td> <td>L 0.6</td> <td>Not count</td> </tr> <tr> <td>0.6 &lt; L 5</td> <td>3</td> </tr> <tr> <td>5 &lt; L</td> <td>0</td> </tr> <tr> <td>0.1 &lt; W</td> <td>(W+L)/2 = D</td> <td>Count as spot (black/white) of No.13</td> </tr> </table>					W 0.03		Not count	0.03 < W 0.05	L 6	Not count	6 < L 12	5	12 < L	0	0.05 < W 0.1	L 0.6	Not count	0.6 < L 5	3	5 < L	0	0.1 < W	(W+L)/2 = D	Count as spot (black/white) of No.13		
W 0.03		Not count																												
0.03 < W 0.05	L 6	Not count																												
	6 < L 12	5																												
	12 < L	0																												
0.05 < W 0.1	L 0.6	Not count																												
	0.6 < L 5	3																												
	5 < L	0																												
0.1 < W	(W+L)/2 = D	Count as spot (black/white) of No.13																												
16		Mura		Invisible under 6%ND filter from center of display. (Display pattern : Black, White, 50%gray)																										
D:Average diameter [mm], W:Width [mm], L:Length [mm], S=(bright spot size)/(dot size)																														
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Applied area	Item	Condi tion	Specification																				
Display dot area	Display location	Non-lighting	<p>As for the display position in the display dot area, the variation of tolerance between window frame of the upper bezel and dot display area (right, left, up and down) is shown in the following table.</p> <table><thead><tr><th colspan="2">Tolerance (X direction)</th><th colspan="2">Tolerance (Y direction)</th></tr></thead><tbody><tr><td>  E-F  </td><td>1.0mm</td><td>  A-B  </td><td>1.0mm</td></tr><tr><td>  E-H  </td><td>0.7mm</td><td>  A-D  </td><td>0.7mm</td></tr><tr><td>  F-G  </td><td>0.7mm</td><td>  B-C  </td><td>0.7mm</td></tr><tr><td>  G-H  </td><td>1.0mm</td><td>  C-D  </td><td>1.0mm</td></tr></tbody></table> <p>*The dimension of effective display area, dot display area and display frame should follow the dimensions described in each specification.</p>	Tolerance (X direction)		Tolerance (Y direction)		E-F	1.0mm	A-B	1.0mm	E-H	0.7mm	A-D	0.7mm	F-G	0.7mm	B-C	0.7mm	G-H	1.0mm	C-D	1.0mm
Tolerance (X direction)		Tolerance (Y direction)																					
E-F	1.0mm	A-B	1.0mm																				
E-H	0.7mm	A-D	0.7mm																				
F-G	0.7mm	B-C	0.7mm																				
G-H	1.0mm	C-D	1.0mm																				

						LQ170K1LW01 (FLC43XWC8V-06A)	
						LD17437	CUST.
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION	SHARP CORPORATION	
DESIG.			CHECK		APPR.		22 /



### 13. ENVIRONMENTAL SPECIFICATIONS

Table 13-1 shows the environmental specifications.

**Table 13-1. Environmental Specifications**

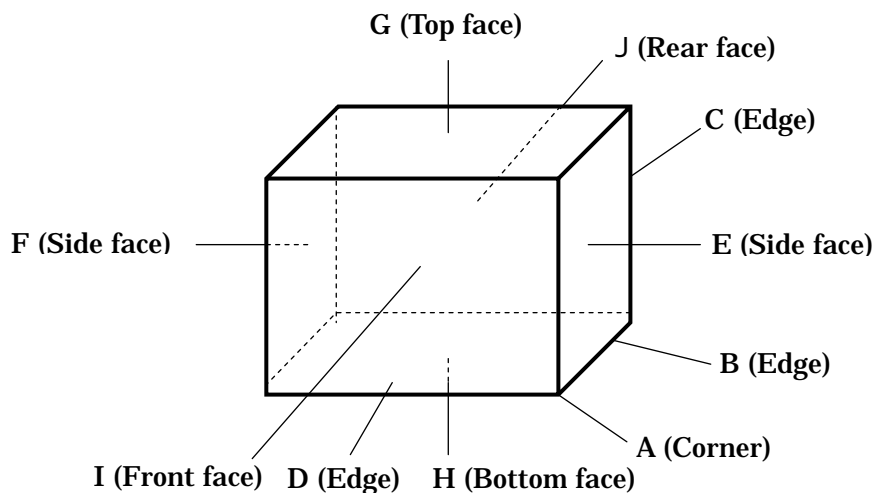
Item	Condition		Remark
Temperature	Operation	0~57°C (Note1)	Temperature on surface of LCD panel (display area.)
	Storage	-20~60°C	
Humidity	Operation	20~85%RH	Maximum wet-bulb temperature should not exceed 29°C. No condensation.
	Storage	5~85%RH	
Vibration	Non-operation	10~500Hz, 1 cycle/20minute, 2G, 1.5mm max, 2hour each X, Y and Z directions	For single module without package.(Note2)
Shock	Non-operation	30G, 6ms, 1time each $\pm X$ , $\pm Y$ and $\pm Z$ directions.	

Note1: Temperature on surface of LCD panel should be under 57 .

Note2: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged.

**Table 13-2. Shock Resistance Standard when Module is Packaged**

Dropping location	Dropping height	Count
A ~ J	60cm	1 time



**Fig.13-1. Direction to apply shock to package**

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DESIG.				CHECK			APPR.		
SHARP CORPORATION								23/	

## 14. INDICATIONS

This module has the following indications.

(1) Product name : LCD unit

(2) SHARP Model number : LQ170K1LW01

(3) Model number : FLC43XWC8V-06A

(4) Manufacturing number : 5 7 0 0 0 0 1

Serial number  
(To be reset every month on 1st.)

Manufacturing month  
(Oct. = X, Nov. =Y, Dec. =Z)

Last digit of manufacturing year.

(5) Version number : 01A (Example)  
-1st 2 digits "01" means operational version.  
-3rd alphabet means functional version.

(6) Country of origin : MADE IN TAIWAN

(7) Company name : SHARP CORPORATION

(8) Caution when changing cold-cathode tubes and disposal method of them. (See Fig. 14-1)

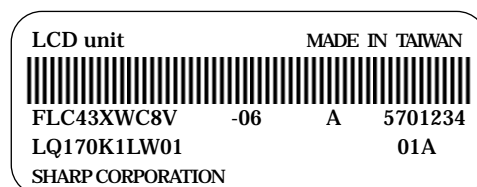


Fig.14-2 Product label (example)

DOCUMENT CONTROL SECTION

DATE

						LQ170K1LW01 (FLC43XWC8V-06A)	
						LD17437	
						CUST.	
06 20050509						Change product label format	
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION		
DESIG.		CHECK		APPR.		SHARP CORPORATION	24/

## 15. PACKAGING

Separately specified in packaging specifications.

### 15-1. Packaging specifications

- (1) 5 LCD modules / 1 package.
- (2) Weight: approximately 13kg / 1 package.
- (3) Outline dimensions : 509mm(W) × 250(D) × 398mm(H)

### 15-2. Packaging method

Fig.15-2 (a),(b),(c),(d),(e),(f),(g) show the packing method.

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	DESIG.			CHECK			APPR.					

Fix backlight cable to the rear side of module by scotch tape.

A

B

C

D

A

B

C

D

E

F

LCD module

Rear side

Scotch tape  
(3M: #421)

Detail of cable section

Put LCD module into an anti-static bag

Front side

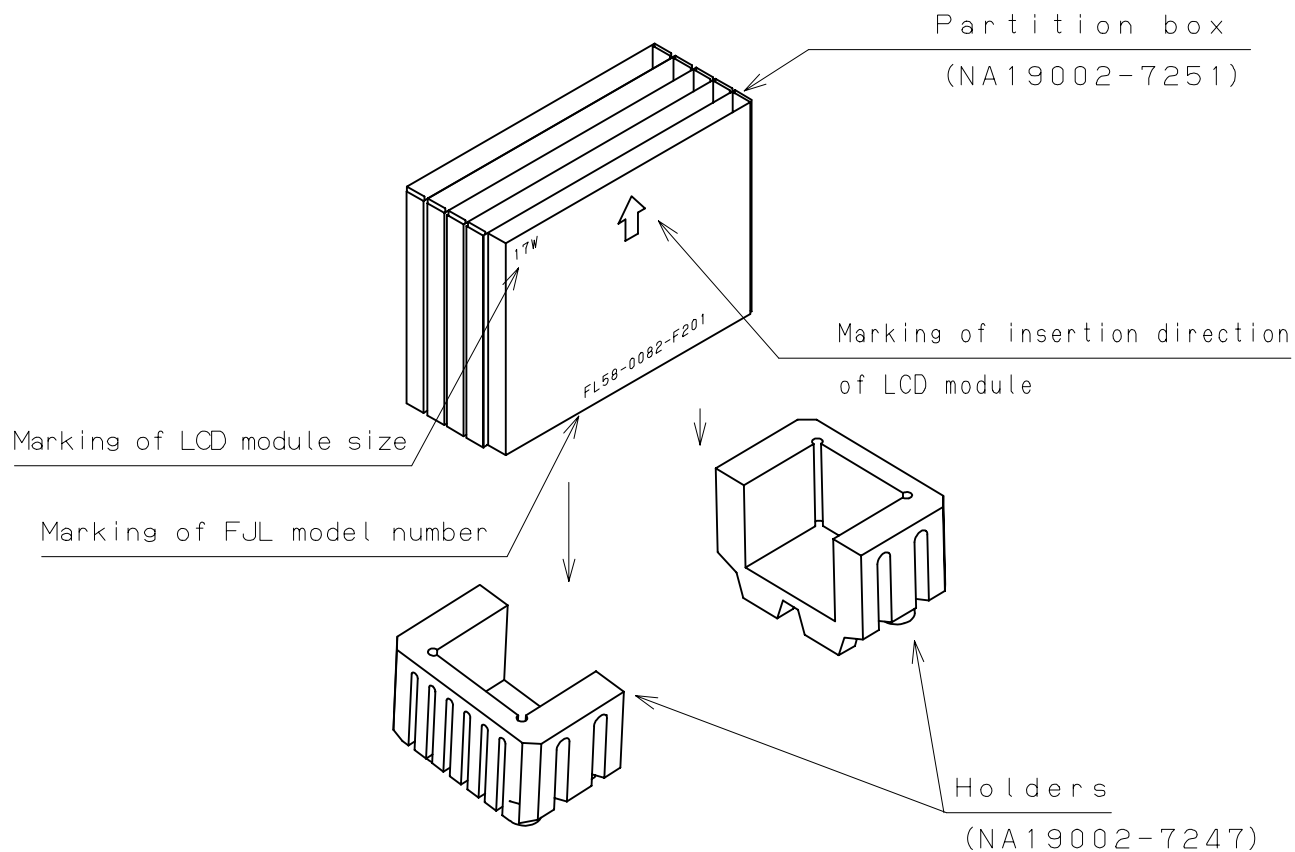
Fig.15-2 (a) Packaging Method

DATE DOCUMENT CONTROL SECTION

					TITLE		LQ170K1LW01 (FLC43XWC8V-06A)	
					DRAW. NO.		LD17437	CUST.
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DESIG.			CHECK	APPR.			26 /	

## Packing

Set a corrugated carton onto bottom holders (2 pieces)



Set the holders so that the insertion side of partition box faces up.

Fig.15-2 (b) Packaging Method

DATE DOCUMENT CONTROL SECTION

					TITLE LQ170K1LW01 (FLC43XWC8V-06A)		
					DRAW. NO. LD17437		CUST.
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DESIG.			CHECK	APPR.		27/	

Place the partition box with holders into the corrugated carton.

A

B

C

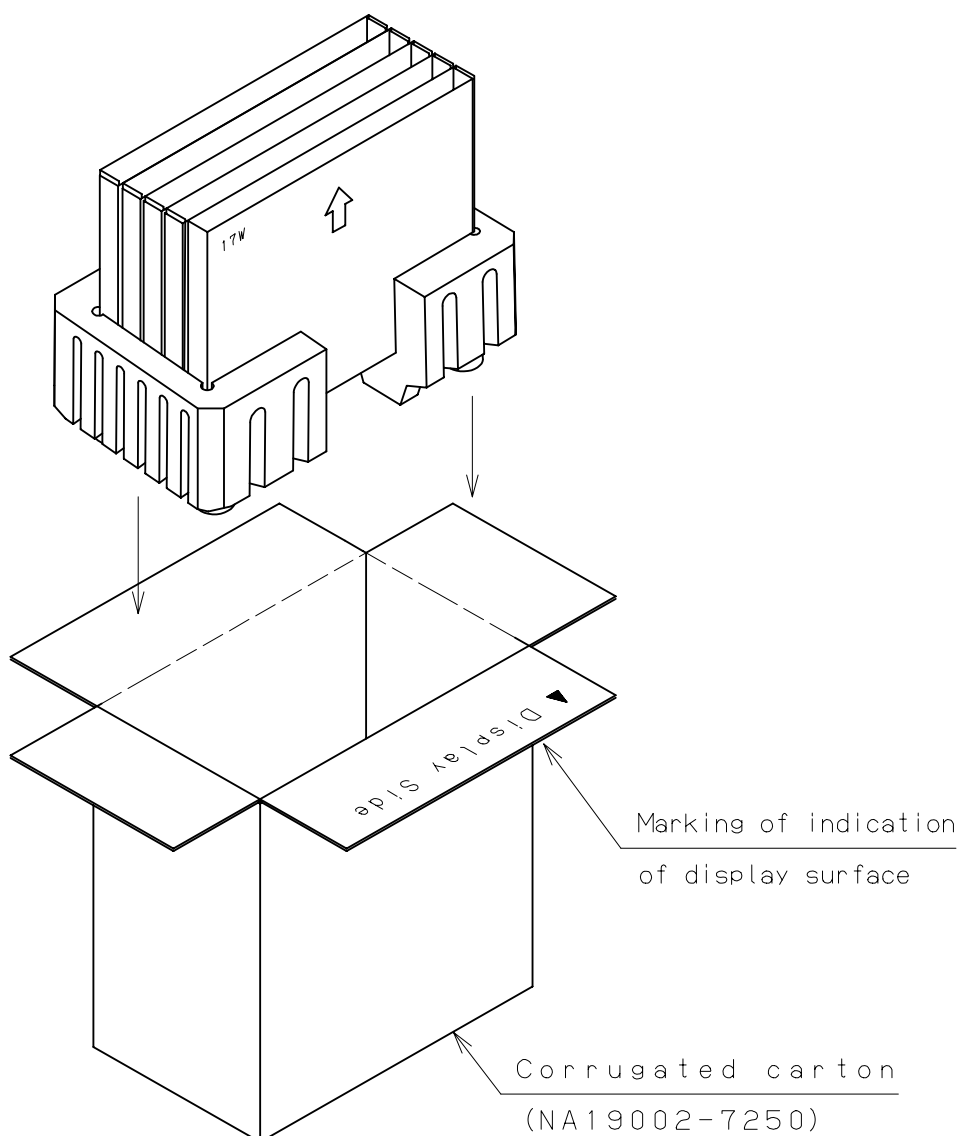
D

A

B

C

D

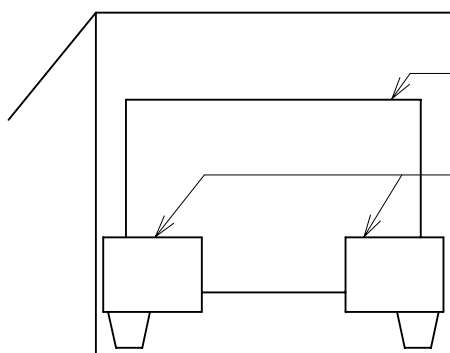


Cross section after insertion

Corrugated carton

Partition carton

Holders



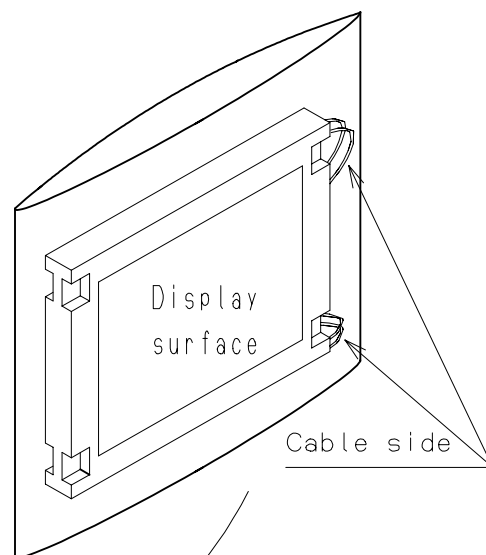
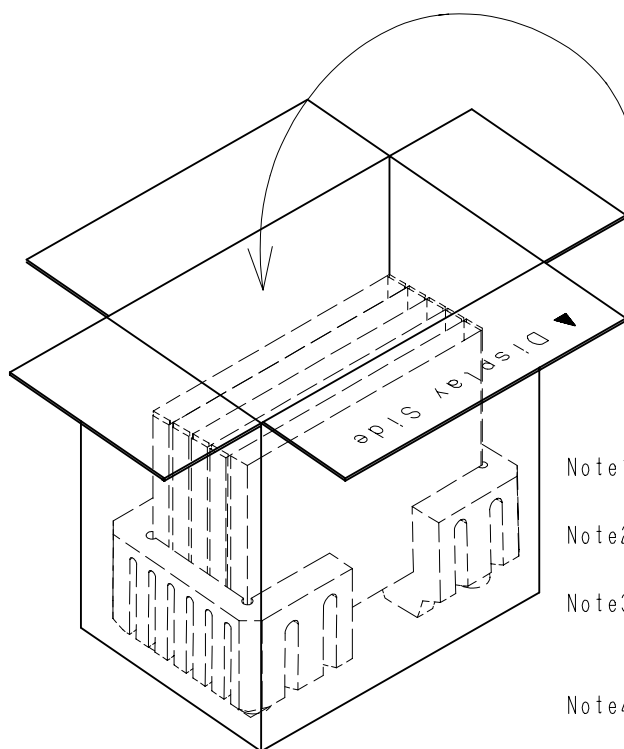
Note1) Insert the partition box so that its marking side will corresponds with the direction mark of display side on the inside of the corrugated carton.

Fig.15-2 (c) Packaging Method

DATE DOCUMENT CONTROL SECTION

					TITLE LQ170K1LW01 (FLC43XWC8V-06A)		
					DRAW. NO. LD17437		CUST.
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION	SHARP CORPORATION		
DESIG.			CHECK	APPR.			
					SHEET 28 /		

Place the unit-packed product in the corrugated carton one by one.



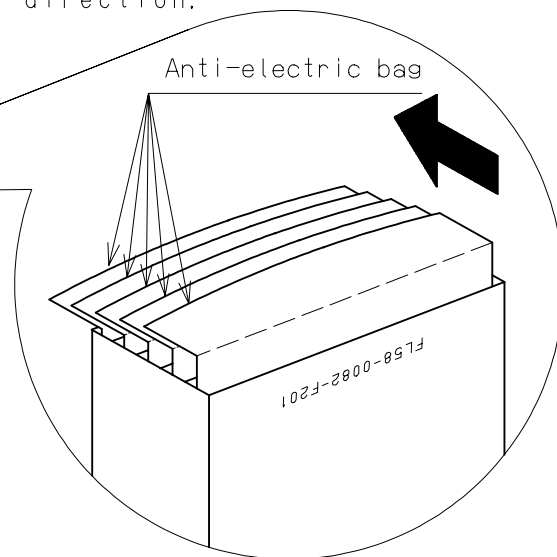
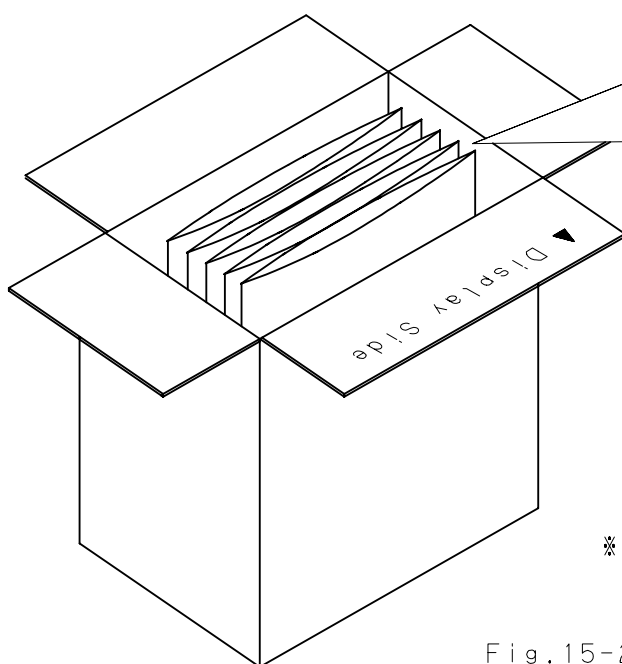
Note1) When modules are shown from the display side, the cable is located on the right.

Note2) Insert them with care so that the cable will not be stuck in the partition box.

Note3) The display side of LCD module and the side of the partition box where the model number is marked should face the same direction.

Note4) Please confirm that the display side of LCD module and the indication of the display side on the corrugated carton face the same direction.

Fold the anti-electric bag in the same direction.



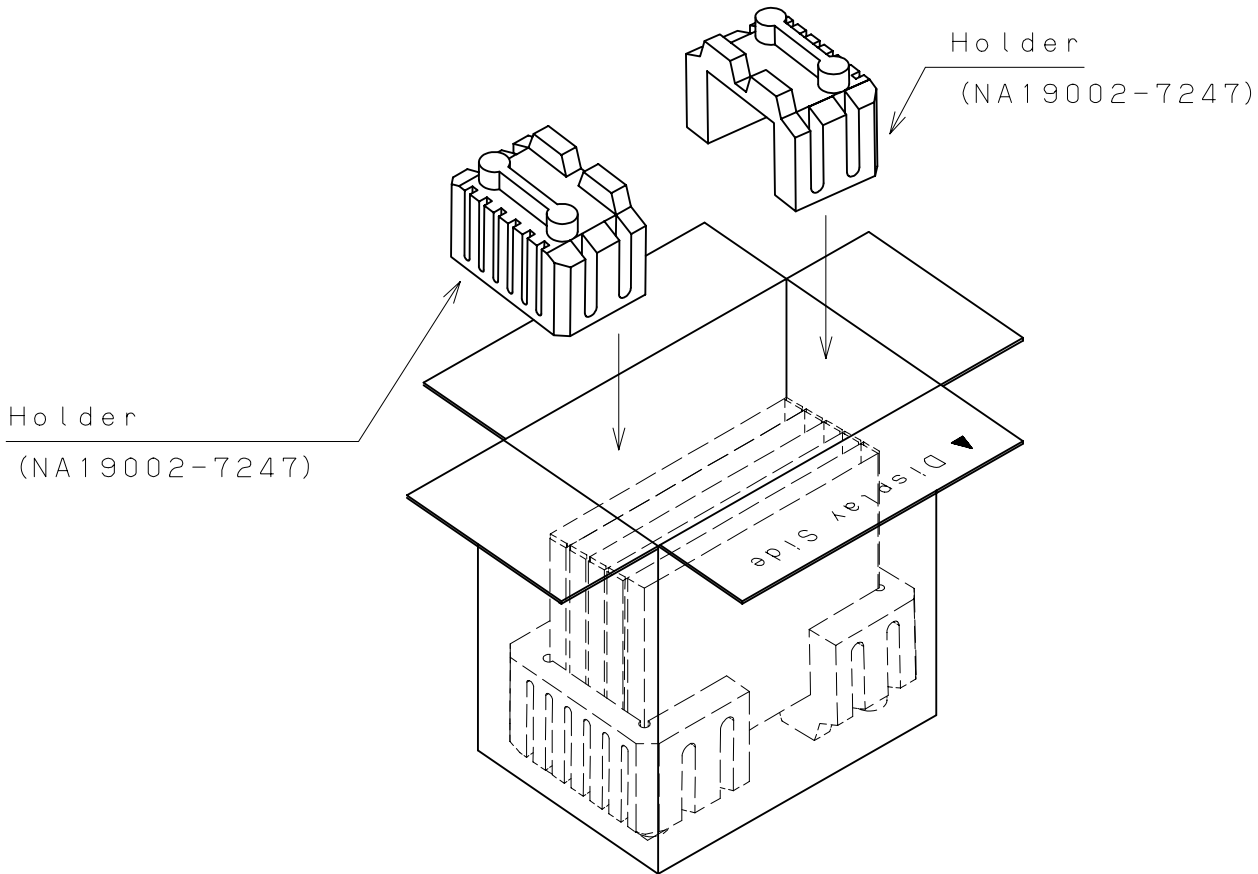
※ Fold each anti-electric bag together to a certain direction as the arrow.

Fig.15-2 (d) Packaging Method

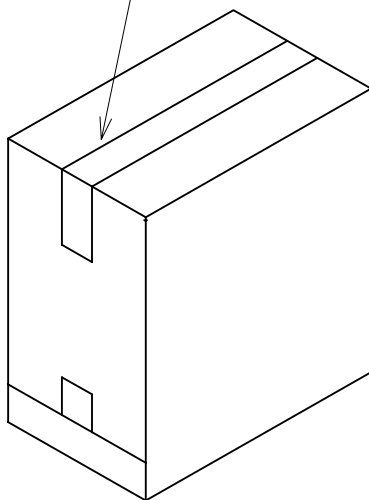
DATE DOCUMENT CONTROL SECTION

					TITLE		LQ170K1LW01 (FLC43XWC8V-06A)	
					DRAW. NO.		LD17437	CUST.
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION	SHARP CORPORATION		SHEET	29/
DESIG.			CHECK	APPR.				

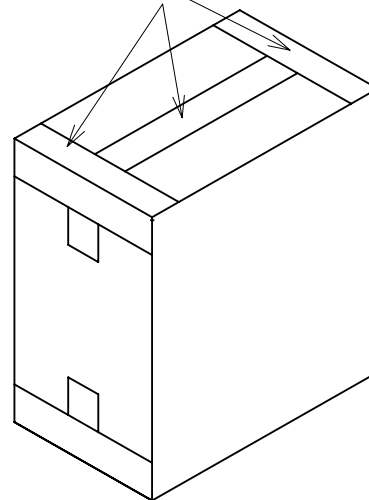
Place two upper holders on the partition box and close the inner lid.



After putting the cover, seal the carton with OPP tape or vinycloth tape.  
OPP tape (equivalent to FL01-0020-E311:FJL)  
Vinycloth tape (equivalent to No.750:SEKISUI)



•When using OPP tape  
Bottom: H method  
Top: I method



•When using vinycloth tape  
Top, Bottom: H method

Note) Make sure to stick the side edge with the tape of 10cm or more.

Fig.15-2 (e) Packaging Method

DATE DOCUMENT CONTROL SECTION

				TITLE		LQ170K1LW01 (FLC43XWC8V-06A)	
				DRAW. NO.		LD17437	CUST.
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION		SHARP CORPORATION	
DESIG.			CHECK		APPR.	30 /	



Shipping Label

State the following items on shipping label.  
Stick the label according to the locating mark on  
a side face of corrugated carton.

- (1) Item to state
- Model No. section : indicate the product model number
  - Drawing No. section : indicate the product drawing number
  - Quantity section : indicate the quantity of packing
  - Revision No. section : indicate the product revision number
  - Serial No. of the same packing unit

(2) Position of sticking shipping label

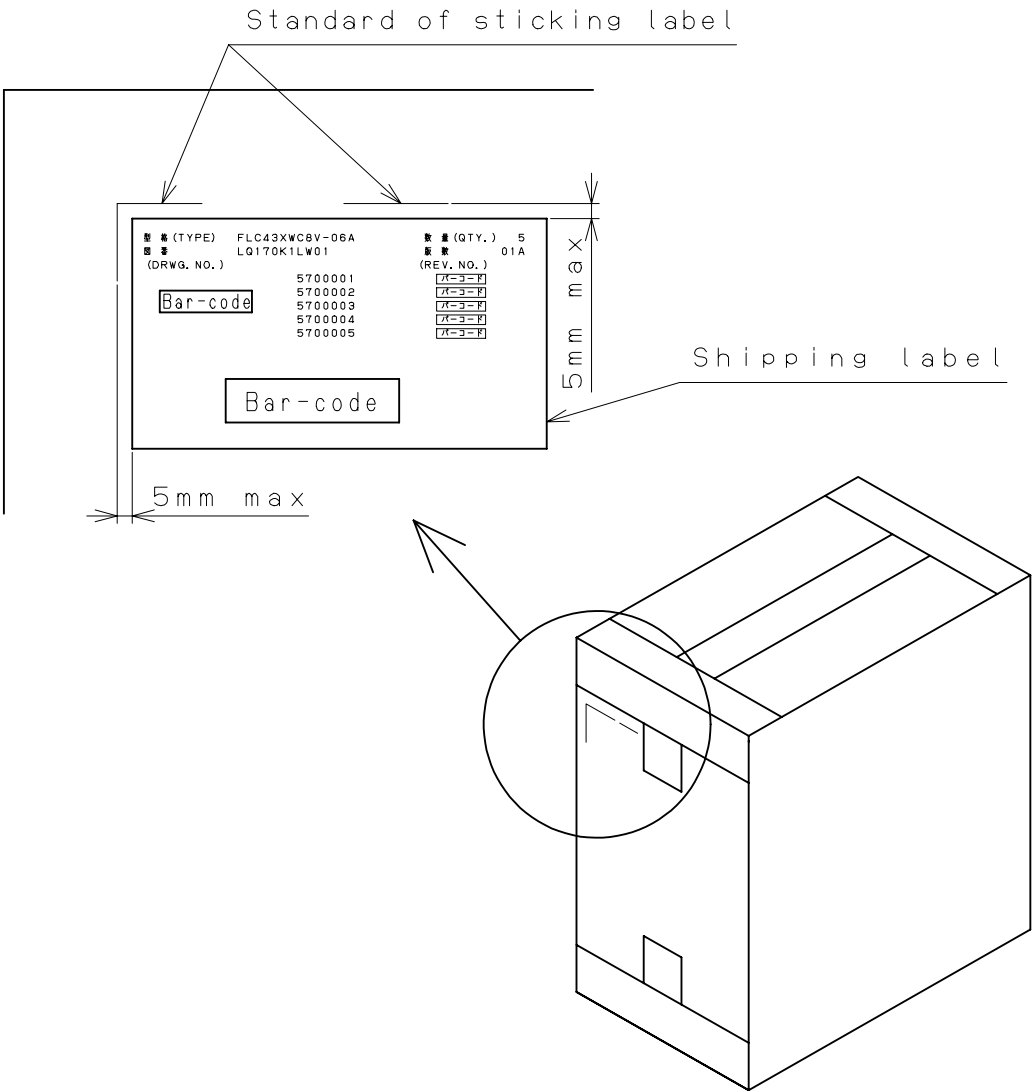


Fig.15-2 (f) Packaging Method

DATE	DOCUMENT CONTROL SECTION										
	↑										
TITLE LQ170K1LW01 (FLC43XWC8V-06A)											
DRAW. NO. LD17437 CUST.											
SHARP CORPORATION SHEET 31 /											
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION							
DESIG.			CHECK			APPR.					
1											

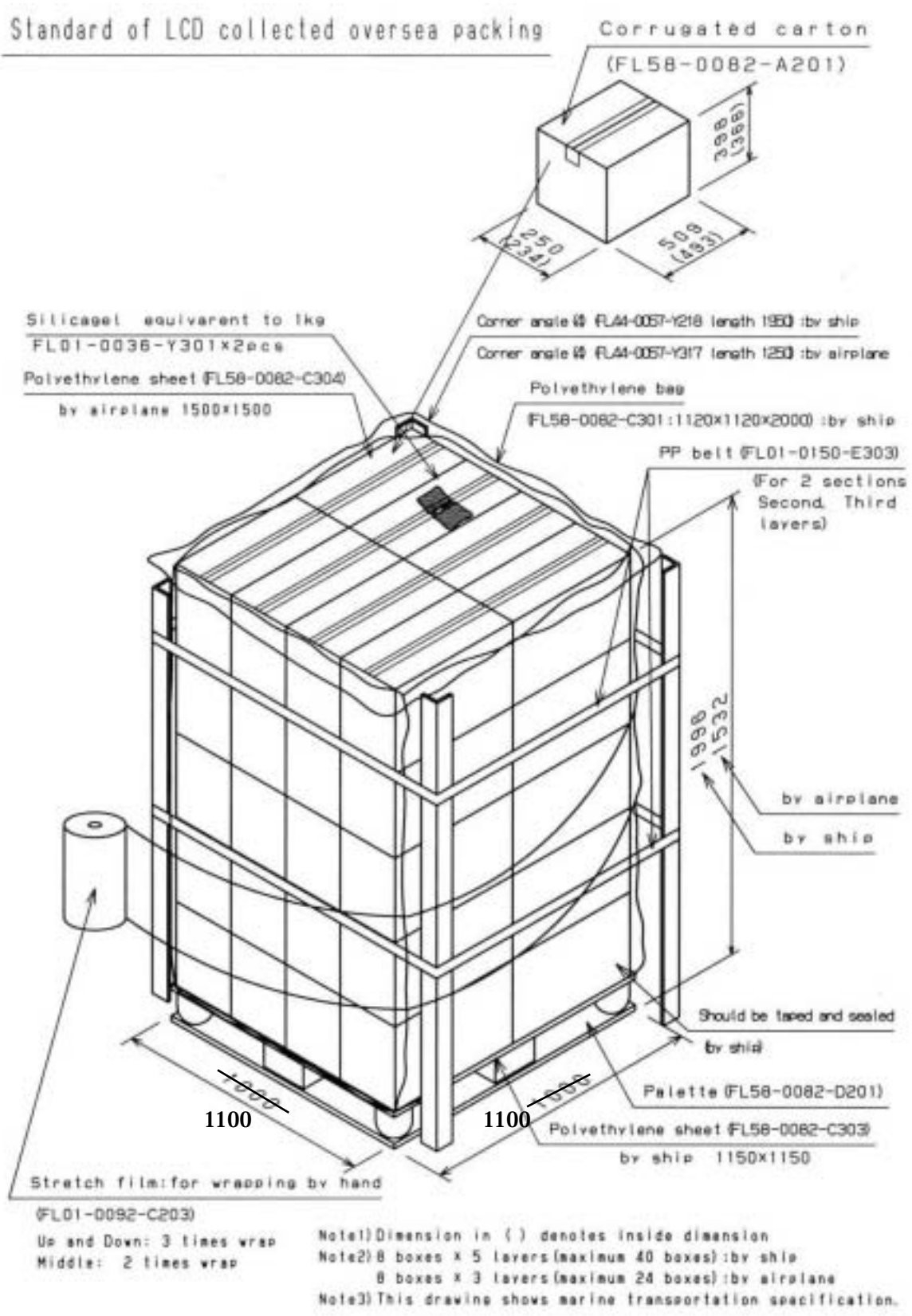


Figure.15-2(g) Packaging Method

DATE	DOCUMENT CONTROL SECTION					LQ170K1LW01 (FLC43XWC8V-06A)										
						LD17437										
						CUST.										
07	20051104	Pallet size correction.					SHARP CORPORATION									
EDIT	DATE	DESIG.	CHECK	APPR.	DESCRIPTION					32 /						
DESIG.				CHECK					APPR.							

## A

The warranty period is one year after manufacturing. Products which fail during this period are repaired or replaced without charge, unless the failure is caused by user.

## B

**Adhere to the following precautions to properly use this LCD module.**

## C

**LCD module has an inherent chance of failure. Customers must protect against injury, damage or loss from such failures by incorporating safety design measures into your facility and equipment such as redundancy, fire protection, and prevention of over-current levels and other abnormal operating conditions.**

## D

**Do not apply any strong mechanical shock to the LCD panel.**

Since the LCD panel is made of glass, excessive shock may damage the panel or cause a malfunction.

**Do not press hard on the LCD panel surface. (Max 2.0kg/cm<sup>2</sup>)**

In the LCD panel, the gap between two glass plates is kept precisely and uniformly to maintain display's characteristics and reliability.

If the pressure is over  $2.0\text{kg/cm}^2$ , the problem don't return to normal condition.

- (a) Ununiformity of color  
(b) Orientation of liquid crystal becomes disordered

Problem (a) returns to normal after a while. Problem (b) returns to normal if power is shut off once then turned on again.

**However these operations should be avoided to insure reliability.**

**Do not scratch the polarizer film on the LCD panel surface.**

- Do not press or rub the display surface with a hard tool, pincet, etc.
- For handling, use cotton or conductive gloves so that the display surface is not stained.
- For If the display surface is stained by dust or dirt, clean it as follows with a soft cloth (deer skin, etc.)

**[Dust] Wipe off with a soft cloth. (do not rub.)**

[Dirt ] Wipe off lightly with a soft cloth after soaking in the clear water and squeezing hard out of water drops. Only if the dirt is hardly wiped off, use isopropyl alcohol or ethanol.

Be careful not to splash the water or the solvent and water penetrated between the polarizer and the LCD panel.

Do not use solvents such as ketone (acetone, etc.) and aromatics (xylene, toluene, etc.)

- If saliva or water drops are left for long time, it may deform partial deformation or discolored. Wipe off immediately in the same way as for dirt.
- Do not allow oil to adhere to the module, since the cleaning of oil is difficult.

**Do not place or contact objects on the display surface for a long period of time.**

That's because this may make some parts of the LCD module distorted and the display quality may decline.

DATE	DOCUMENT CONTROL SECTION

						LQ170K1LW01 (FLC43XWC8V-06A)									
						LD17437						CUST.			
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	DESIG.			CHECK			APPR.								





## (8) CAUTION IN DESIGNING INVERTER

Fluorescent lamps driven by high voltage are included in this LCD module. Please stand to the instructions below when designing inverter that lights the fluorescent lamps. Otherwise it may lead to FATAL FAILURE, such as SMOKING or FIRING.

APPLY PROTECTIVE CIRCUIT in preparation for lamp breaking, wire breaking and short circuit. The protective circuit should also detect half open circuit and wire breaking in narrow gap etc.. Otherwise it may lead to fatal failure.

KEEP ENOUGH CURRENT CAPACITY of inverter output for leakage current, which leaks from lamps and wire to surrounding metal material. Usually output current of about 1.5 times as same as the lamp current is necessary. But it sometimes varies due to characteristics of the inverter itself. So before determining design, please check characteristics of the inverter by connecting it to the LCD module.

KEEP ENOUGH TEMPERATURE MARGIN for each parts mounted on inverter. Temperature of the parts becomes higher when they are mounted in the final products due to heating inside. The temperature of each parts MUST NOT increase over the guaranteed temperature.

DOCUMENT CONTROL SECTION

DATE

						LQ170K1LW01 (FLC43XWC8V-06A)							
						LD17437				CUST.			
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	DESIG.			CHECK			APPR.						

## A

B

## C

B

C

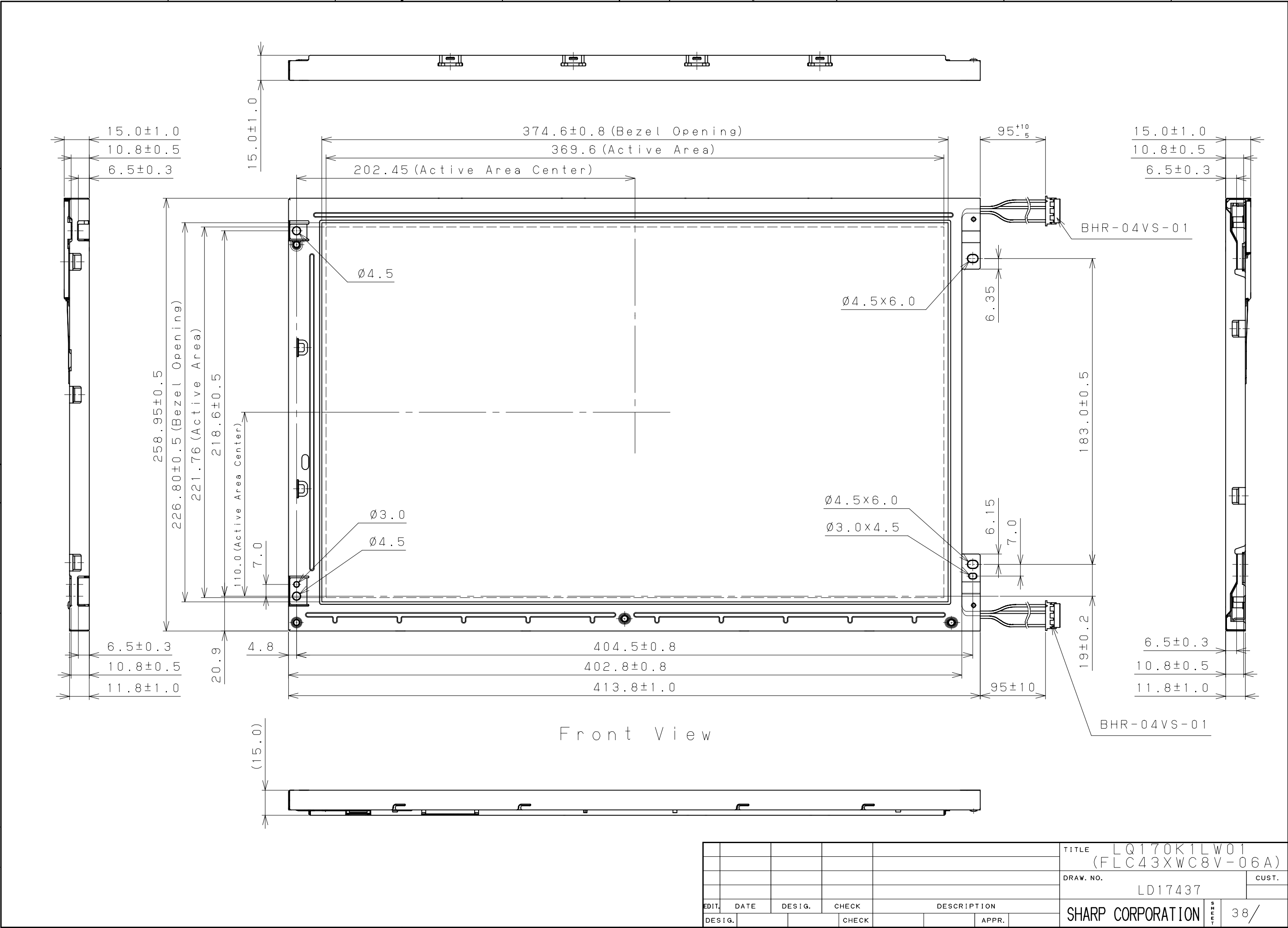
D

D

E

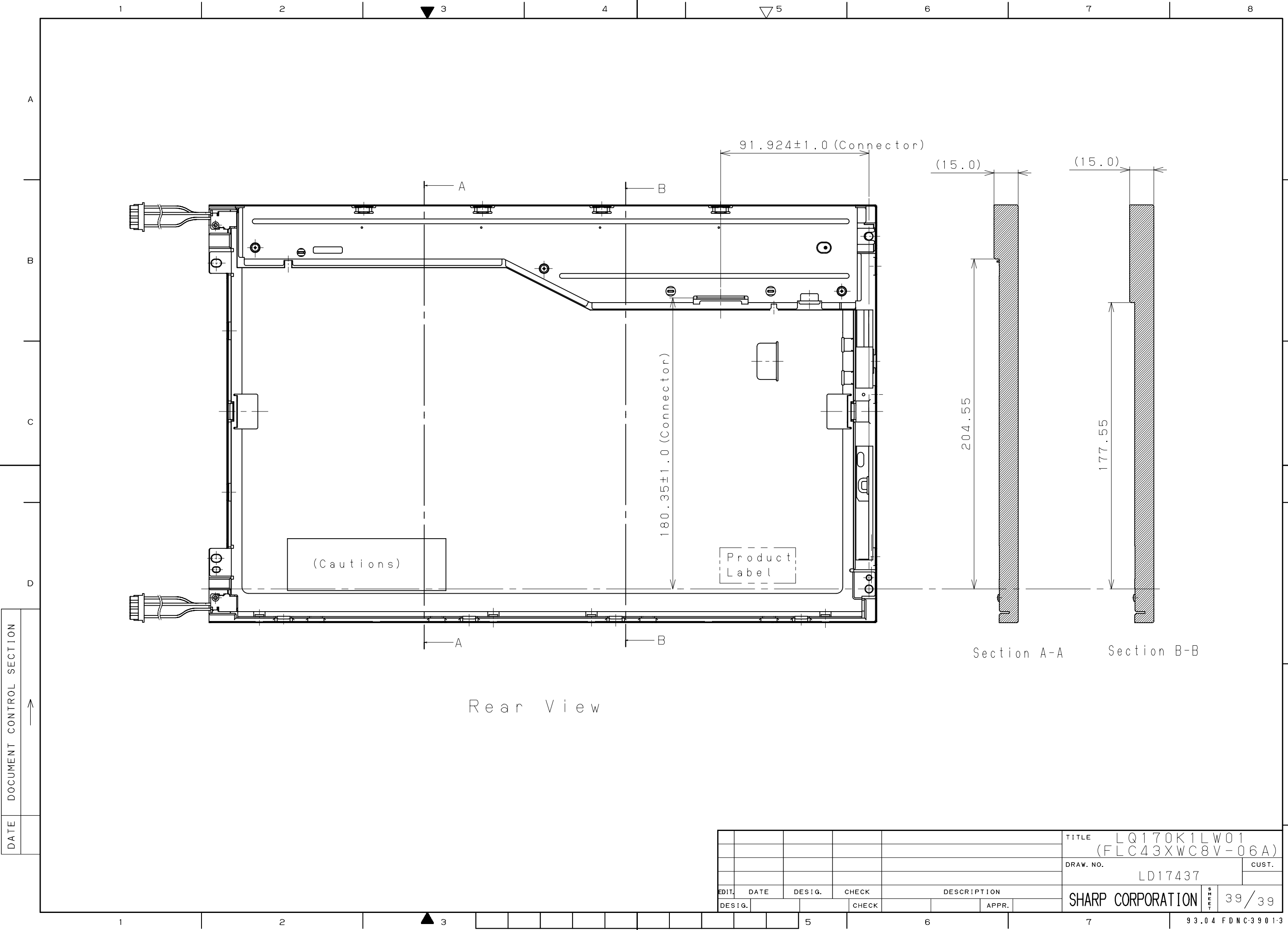
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1



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					CUST.			
					SHARP CORPORATION		38/	
					93.04 F D N C-3901-3			





DATE DOCUMENT CONTROL SECTION

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					DRAW. NO.				LD17437				CUST.	
EDIT.	DATE	DESIG.	CHECK	DESCRIPTION										
DESIG.			CHECK			APPR.		SHARP CORPORATION				SHEET	39 / 39	

SHARP CORPORATION 39/39