

**PROPRIETARY NOTE**

THIS SPECIFICATION IS THE PROPERTY OF BOE CQ AND SHALL NOT BE REPRODUCED OR COPIED WITHOUT THE WRITTEN PERMISSION OF BOE CQ AND MUST BE RETURNED TO BOE CQ UPON ITS REQUEST

TITLE : NV133FHM-N63
Preliminary Product Specification
Rev. 2

Chongqing BOE Optoelectronics Technology Co., Ltd

SPEC. NUMBER

PRODUCT GROUP
TFT-LCD

Rev.
2

ISSUE DATE
2016.9.2

PAGE
1 OF 31

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9.2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 2 OF 31
REVISION HISTORY				
REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
0	-	Initial release	2016.5	Gu Cheng
1	-	Label/EDID update X10 to X20	2016.7.28	Gu Cheng
2	-	Label/EDID update X20 to A00	2016.9.2	Gu Cheng

2

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 3 OF 31

Contents

No.	Items	Page
	REVISION HISTORY	2
	CONTENTS	3
1.0	General Description	4
2.0	Absolute Maximum ratings	6
3.0	Electrical specifications.	7
4.0	Optical specifications.	10
5.0	Interface Connection	15
6.0	Signal Timing Specification	18
7.0	Horizontal Timing Waveforms	20
8.0	Input Signals, Basic Display Colors & Gray Scale Of Colors	21
9.0	Power Sequence	22
10.0	Reliability Test	24
11.0	Handling & Cautions.	24
12.0	Label	25
13.0	Packing information	27
14.0	Mechanical Outline Dimension	28
15.0	EDID Table	30

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 4 OF 31

1.0 General Description

1.1 Application

- Notebook PC Without Touch function

1.2 General Specification

1.2.1.General LCM Specification(Table 1.)

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	293.76 (H) x 165.24 (V)	mm	
Number of pixels	1920 (H) ×1080 (V)	pixels	
Pixel pitch	0.153 (H) X 0.153 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	262K	colors	
Display mode	Normally Black		
Dimensional outline	305.35 +/-0.5(H)*188.45 +/-0.5(V) (W/PCB)*2.85(Max) 305.35 +/-0.5(H)*178.11 +/-0.5(V) (WO/PCB) 2.85(Max)	mm	
Surface treatment	AG		
Weight	270(max)	g	
Back-light	Lower Down side, 1-LED Lighting Bar type		Note 1
Power consumption	P _D : 1.2(max)	W	@mosaic pattern
	P _{BL} :3.5(max)	W	
	4.7	W	4

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 5 OF 31

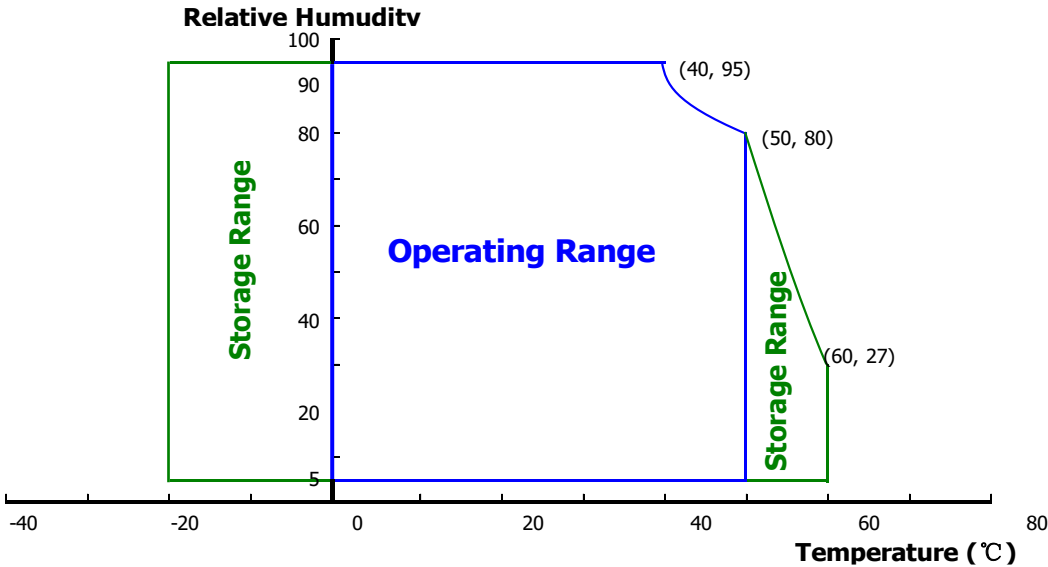
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.5	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{ss} -0.3	V _{DD} +0.3	V	
Operating Temperature	T _{OP}	0	+50	°C	Note 2
Storage Temperature	T _{ST}	-20	+60	°C	

- Notes : 1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
2. Temperature and relative humidity range are shown in the figure below.
 95 % RH Max. (40 °C ≥ Ta)
 Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 6 OF 31

3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

Ta=25+/-2°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V _{RF}	-	-	100	mV	At V _{DD} = 3.3V
Power Supply Current	I _{DD}	-	303	-	mA	Note 1
Differential Input Voltage	V _{ID}	120	-	1200	mV	
Power Consumption	P _D	-	1.0	1.2	W	Note 1
	P _{BL}	-	-	3.5	W	Note 2
	P _{total}	-	-	4.7	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for 3.3V at 25°C.
a) Mosaic Pattern

2. Calculated value for reference (V_{LED} × I_{LED})

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 7 OF 31

3.2 Backlight Unit

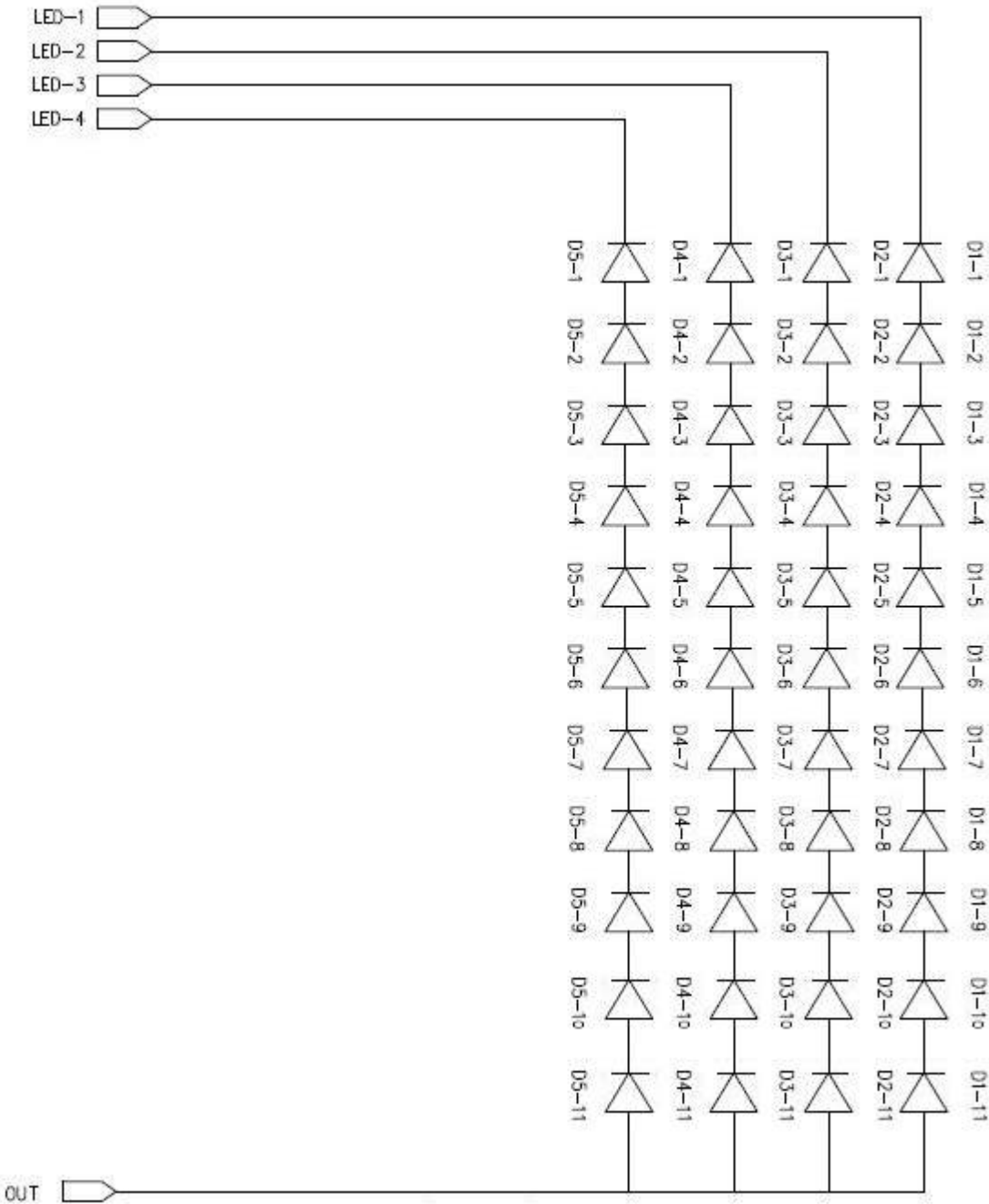
< Table 4. LED Driving guideline specifications > Ta=25+/-2°C

Parameter			Min.	Typ.	Max.	Unit	Remarks
LED Forward Voltage		V _F	-	-	3.0	V	-
LED Forward Current		I _F	-	22.7	-	mA	-
LED Power Consumption		P _{LED}		-	3.5	W	Note 1
LED Life-Time		N/A	15,000	-	-	Hour	I _F = 22.7mA
Power supply voltage for LED Driver		V _{LED}	6	12	21	V	
EN Control Level	Backlight on		2.0		5.0	V	
	Backlight off		0		0.8	V	
PWM Control Level	PWM High Level		2.0		5.0	V	
	PWM Low Level		0		0.8	V	
PWM Control Frequency		F _{PWM}	200	-	10,000	Hz	
Duty Ratio		-	1	-	100	%	Note3

- Notes : 1. Power supply voltage12V for LED Driver
 Calculator Value for reference I_F × V_F ×44 / efficiency = P_{LED}
 2. The LED Life-time define as the estimated time to 50% degradation of initial luminous.
 3. 1% duty cycle is achievable with a dimming frequency less than 1KHz.

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			8 OF 31

3.3 LED structure



PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			9 OF 31

4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2^{\circ}\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta\varnothing=0$ ($=\theta_3$) as the 3 o'clock direction (the “right”), $\theta\varnothing=90$ ($=\theta_{12}$) as the 12 o'clock direction (“upward”), $\theta\varnothing=180$ ($=\theta_9$) as the 9 o'clock direction (“left”) and $\theta\varnothing=270$ ($=\theta_6$) as the 6 o'clock direction (“bottom”). While scanning θ and/or \varnothing , the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be 3.3+/- 0.3V at 25°C . Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ_3	CR > 10	80	-	-	Deg.	Note 1
		Θ_9		80	-	-	Deg.	
	Vertical	Θ_{12}		80	-	-	Deg.	
		Θ_6		80	-	-	Deg.	
Luminance Contrast ratio		CR	$\Theta = 0^\circ$	600	800	-	-	Note 2
Luminance of White	5 Points	Y_w	$\Theta = 0^\circ$ $I_{LED} = 22.7mA$	300	350	-	-	Note 3
White Luminance uniformity	5 Points	$\Delta Y5$		80%	-	-	-	Note 4
	13 Points	$\Delta Y13$		65%	-	-	-	
White Chromaticity		x_w	$\Theta = 0^\circ$	0.283	0.313	0.343	-	Note 5
		y_w		0.299	0.329	0.359	-	
Reproduction of color	Red	x_R	$\Theta = 0^\circ$	-0.03	0.651	+0.03	-	
		y_R			0.345		-	
	Green	x_G			0.328		-	
		y_G			0.622		-	
	Blue	x_B			0.151		-	
		y_B			0.057		-	
Gamut		-	-	68	72	-	%	
Response Time (Rising + Falling)		T_{RT}	$Ta= 25^\circ C$ $\Theta = 0^\circ$	-	30	35	Ms	Note 6
Cross Talk		CT	$\Theta = 0^\circ$	-	-	2.0	%	Note 7

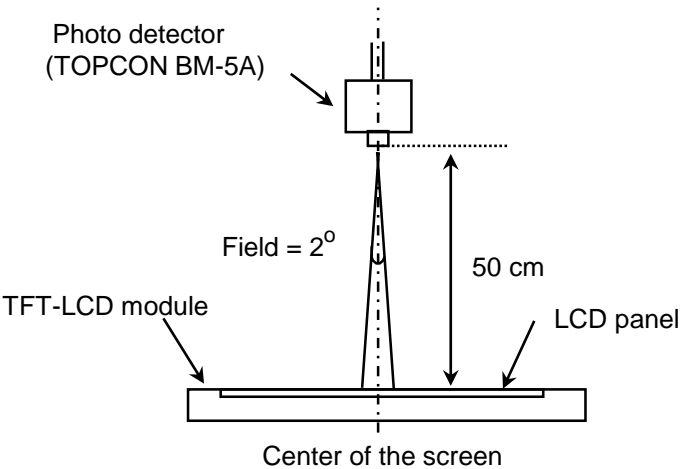
PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 10 OF 31

- Notes :
- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
 - 2. Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state .
(see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.
$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$
 - 3. Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
 - 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = \text{Minimum Luminance of 5(or 13) points} / \text{Maximum Luminance of 5(or 13) points}$.
(see FIGURE 2 and FIGURE 3).
 - 5. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
 - 6. The electro-optical response time measurements shall be made as FIGURE 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
 - 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark.
(See FIGURE 5).

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			11 OF 31

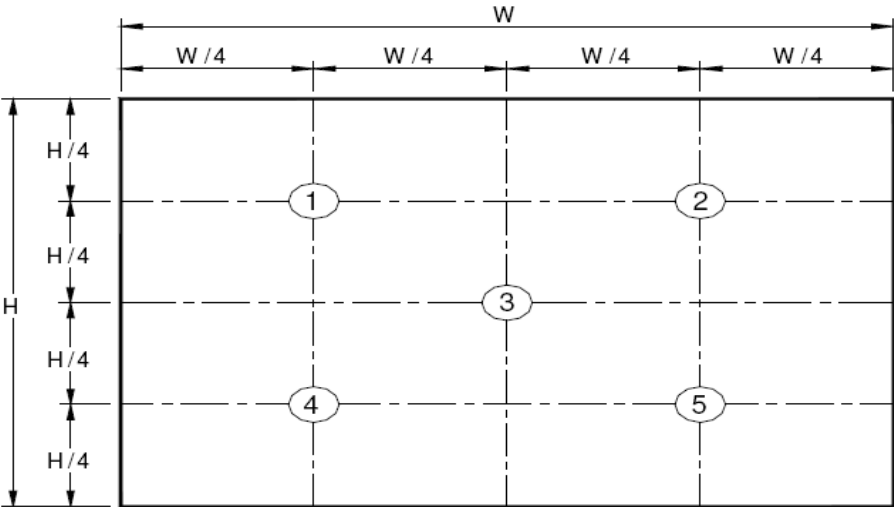
4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

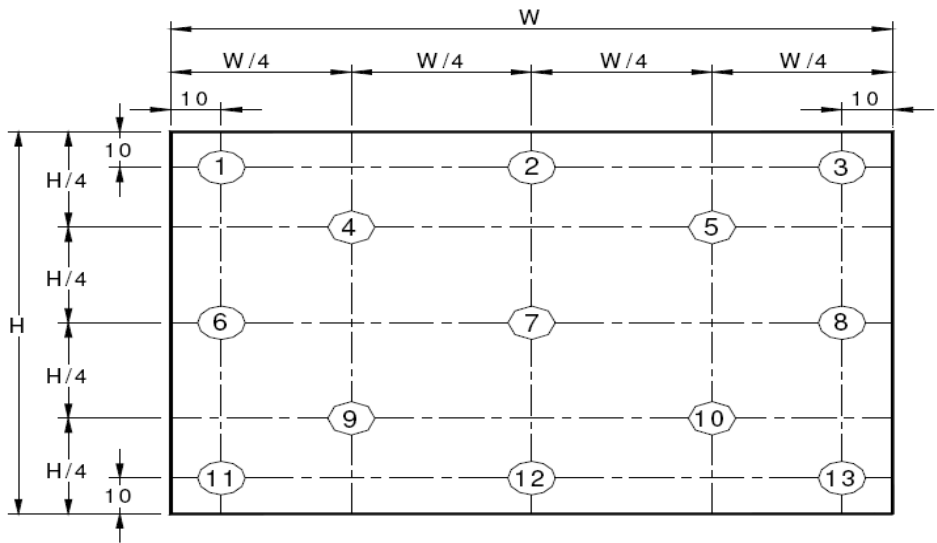
Figure 2. White Luminance and Uniformity Measurement Locations (5 points)



Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

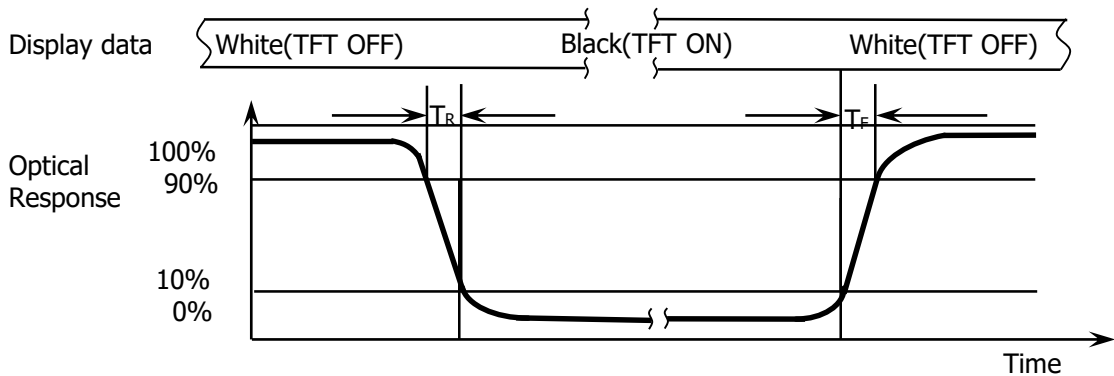
PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			12 OF 31

Figure 3. Uniformity Measurement Locations (13 points)



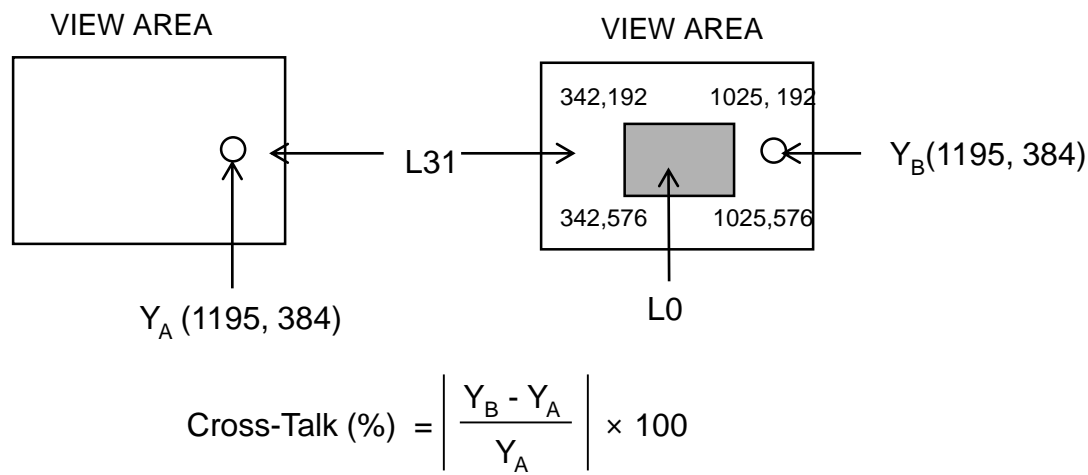
The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5$ = Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2) , $\Delta Y13$ = Minimum Luminance of 13 points /Maximum Luminance of 13 points (see FIGURE 3).

Figure 4. Response Time Testing



The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

Figure 5. Cross Modulation Test Description



Where:

Y_A = Initial luminance of measured area (cd/m²)

Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 14 OF 31

5.0 INTERFACE CONNECTION.

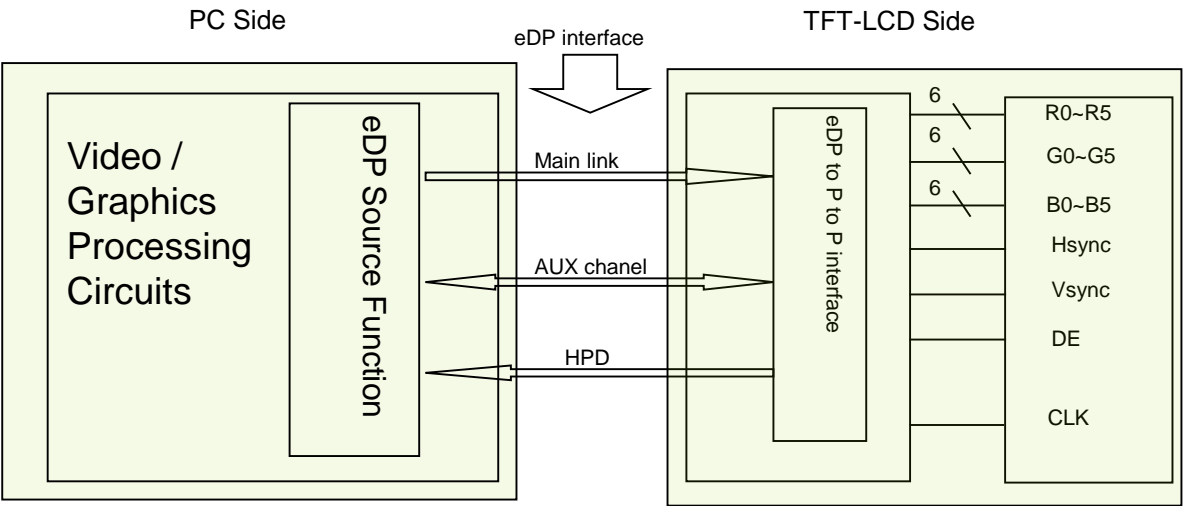
5.1 Electrical Interface Connection

The electronics interface connector is UJU IS050-L30B-C10 or Compatible.
The connector interface pin assignments are listed in Table 6.

<Table 6. Pin Assignments for the Interface Connector>

Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	CABC	CABC
2	H-GND	Ground
3	LAN1_N	Complement Signal Link _Lane1
4	LAN1_P	True Signal Link _Lane1
5	H-GND	Ground
6	LAN0_N	Complement Signal Link _Lane0
7	LAN0_P	True Signal Link _Lane0
8	H-GND	High Speed Ground
9	AUXP	True Signal Link _Auxiliry Channel
10	AUXN	Complement Signal Link _Auxiliry Channel
11	H-GND	Ground
12	LCD_VCC	Power Supply, 3.3V (typ.)
13	LCD_VCC	Power Supply, 3.3V (typ.)
14	BIST	Reserved(BIST function)
15	H-GND	Ground
16	H-GND	Ground
17	HPD	HPD(Hot Plug Detect) Signal Pin
18	BL_GND	High Speed Ground
19	BL_GND	High Speed Ground
20	BL_GND	High Speed Ground
21	BL_GND	High Speed Ground
22	BL_EN	Backlight on/off Control pin
23	BL_PWM	Back light PWM Dimming
24	NC	Reserved
25	NC	Reserved
26	BL_PWR	Backlight power
27	BL_PWR	Backlight power
28	BL_PWR	Backlight power
29	BL_PWR	Backlight power
30	NC	Reserved

5-2. eDP Interface



Note. Transmitter : Parade DP501 or equivalent
Transmitter is not contained in Module.

5.3.eDP Input signal

Lane 0	Lane 1
R0-5:0 G0-5:4	R1-5:0 G1-5:4
G0-3:0 B0-5:2	G1-3:0 B1-5:2
B0-1:0 R2-5:0	B1-1:0 R3-5:0
G2-5:0 B2-5:4	G3-5:0 B3-5:4
B2-3:0 R4-5:2	B3-3:0 R5-5:2
R4-1:0 G4-5:0	R5-1:0 G5-5:0
B4-5:0 R6-5:4	B5-5:0 R7-5:4
R6-3:0 G6-5:2	R7-3:0 G7-5:2
G6-1:0 B6-5:0	G7-1:0 B7-5:0

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 16 OF 31

5.4 Back-light & LCM Interface Connection

<Table 7. Pin Assignments for the BLU & LCM Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED	LED cathode connection	6	GND	Ground
2	LED	LED cathode connection	7	NC	No Connection
3	LED	LED cathode connection	8	Vout	LED anode connection
4	LED	LED cathode connection	9	Vout	LED anode connection
5	NC	No Connection	10	Vout	LED anode connection

6.0 SIGNAL TIMING SPECIFICATION

6.1 The NV133FHM-N63 is operated by the DE only.

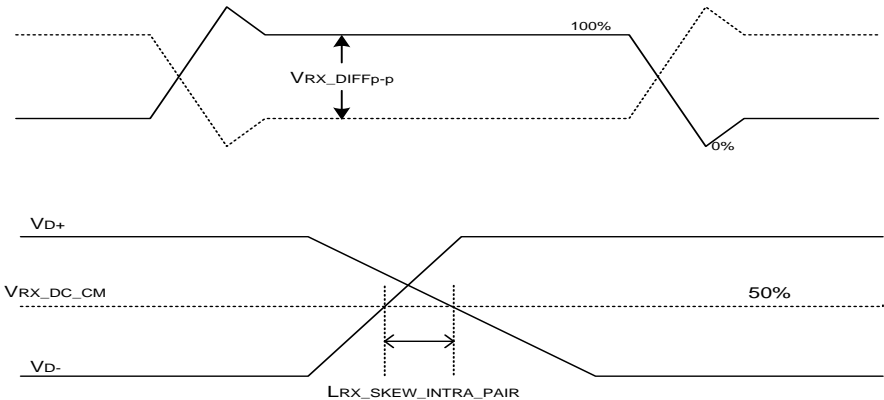
Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	100	147.8	150	MHz
Frame Period		Tv	1112	1120	1238	lines
			-	60	-	Hz
			25	16.67	16.39	ms
Vertical Display Period		Tvd	-	1080	-	lines
One line Scanning Period		Th	2080	2200	2400	clocks
Horizontal Display Period		Thd	-	1920	-	clocks

6.2 eDP Rx Interface Timing Parameter

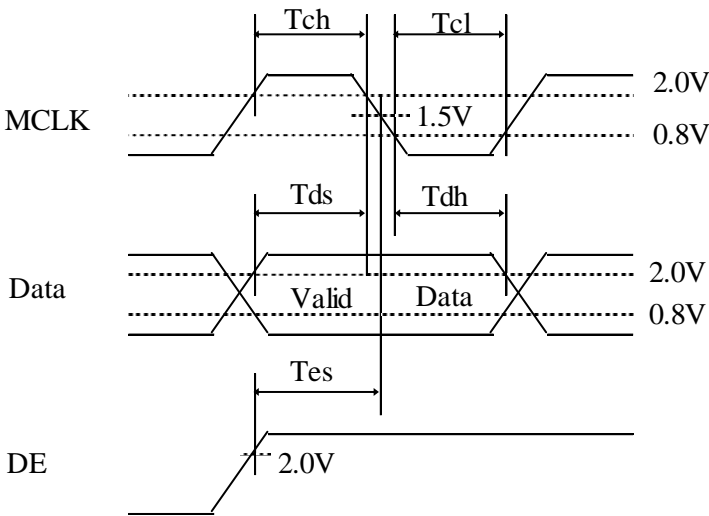
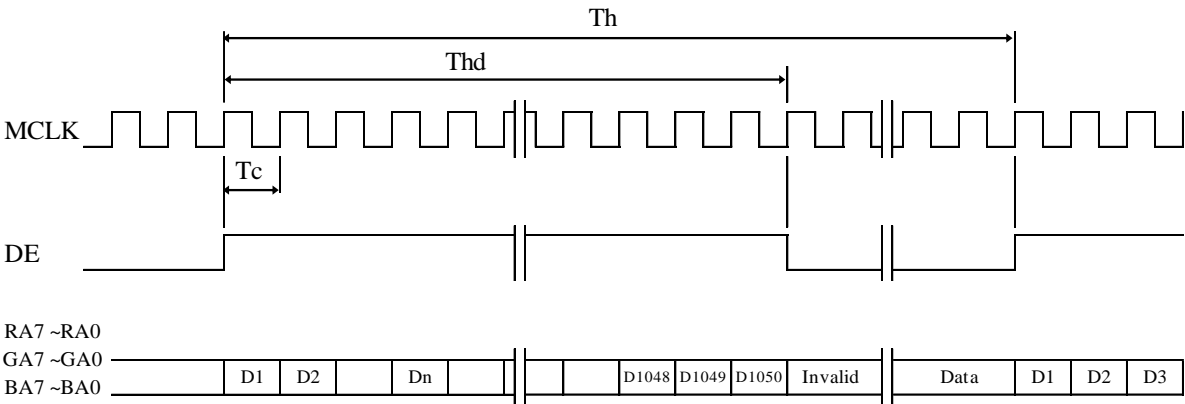
The specification of the eDP Rx interface timing parameter is shown in Table 8.

<Table 8. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock	ssc		0.5		%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	120	0	1200	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	-	V	
Differential termination resistance	RRX-DIFF	90	100	110	Ω	
Single-ended termination resistance	RRX-SE	45	50	55	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_INTRA_PAIR	-	-	100	ps	



7.0 Horizontal Timing Waveforms



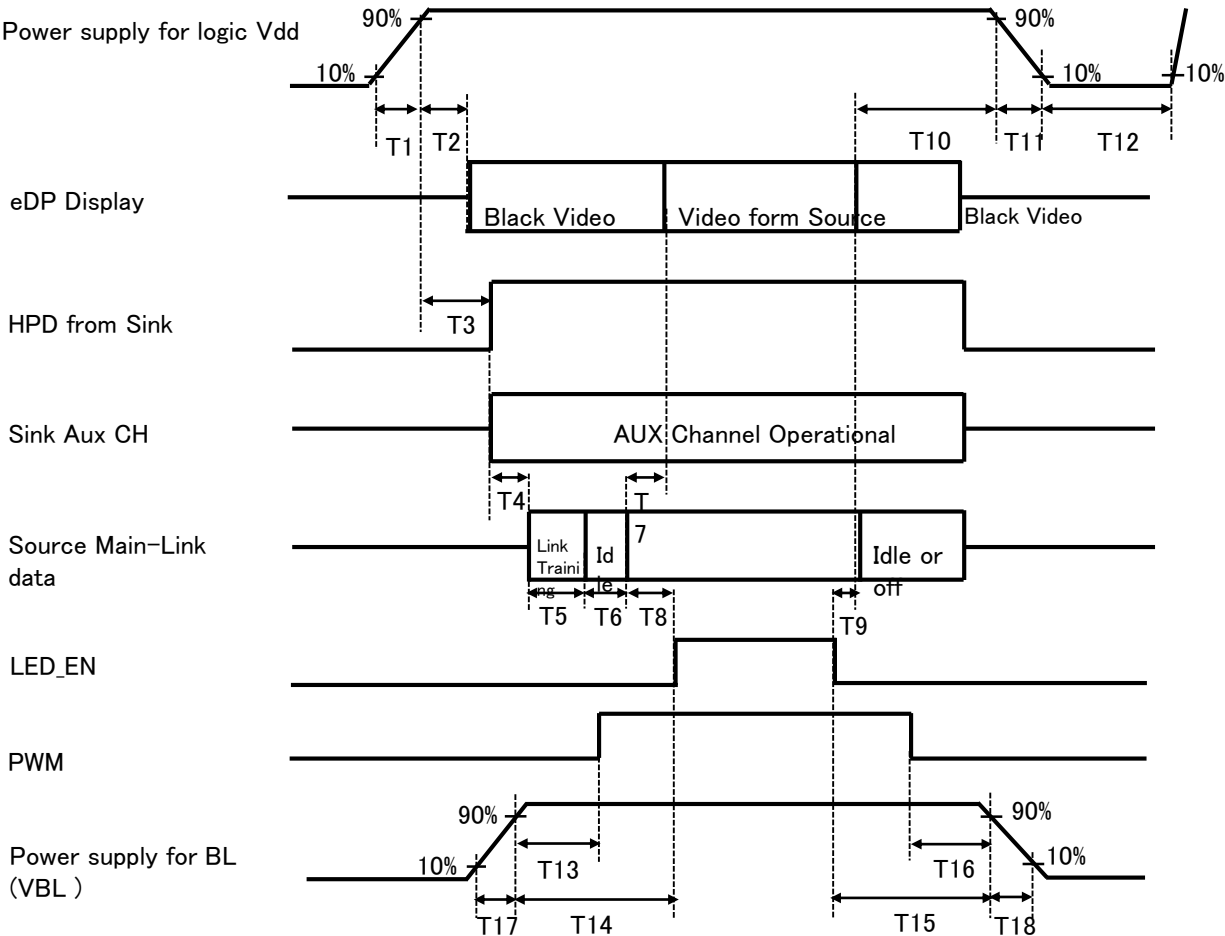
8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color & Gray Scale		RED DATA								GREEN DATA								BLUE DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of RED	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of GREEN	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of BLUE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Gray Scale of WHITE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	0	1	1
	▽	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			21 OF 31

9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- 0.5ms ≤ T1 ≤ 10 ms
 - 0ms ≤ T2 ≤ 200 ms
 - 0ms ≤ T3 ≤ 200 ms
 - 10ms ≤ T13
 - 20ms ≤ T14
 - 0.5ms ≤ T17 ≤ 20ms
- 0ms ≤ T7 ≤ 50ms
 - 0ms ≤ T10 ≤ 500 ms
 - 3ms ≤ T11 ≤ 10 ms
 - 500ms ≤ T12
 - 20ms ≤ T15
 - 10ms ≤ T16
 - 0.5ms ≤ T18 ≤ 20ms

- Notes:
- When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
 - Do not keep the interface signal high impedance when power is on.
 - Back Light must be turn on after power for logic and interface signal are valid.

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 22 OF 31

10.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 10. Reliability test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60 °C, 240 hrs
2	Low temperature storage test	Ta = -20 °C, 240 hrs
3	High temperature & high humidity operation test	Ta = 40 °C, 90%RH, 240 hrs
4	High temperature operation test	Ta = 50 °C, 240 hrs
5	Low temperature operation test	Ta = 0 °C, 240 hrs
6	Thermal shock	Ta = -40 °C ↔ 80 °C (0.5 hr), 100 cycle
7	Drop (non-operating)	60cm/1 corner/3 edges/6 faces
8	Shock test (non-operating)	220G, Half Sine Wave 2msec ±X, ±Y, ±Z Once for each direction
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV

11.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation

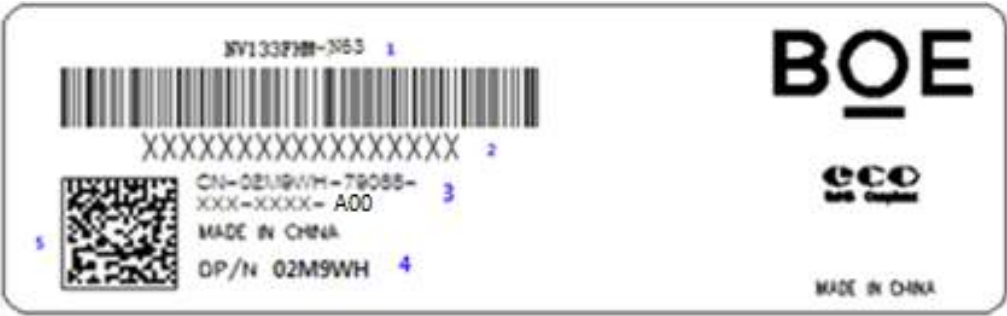
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 23 OF 31

- (4) Cautions for the atmosphere
- Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
- Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
- Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

12.0 LABEL


(1) LCM label



- 1. BOE module name
- 2. BOE module ID
- 3. PPID
- 4. Dell DPN
- 5. PPID Quick Response code

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 24 OF 31

(2) High voltage caution label




HIGH VOLTAGE
CAUTION

RISK OF ELECTRIC SHOCK.
DISCONNECT THE ELECTRIC
POWER BEFORE SERVICING

COLD CATHODE FLUORESCENT LAMP IN LCD
PANEL CONTAINS A SMALL AMOUNT
OF MERCURY. PLEASE FOLLOW LOCAL OR-
DINANCES OR REGULATIONS FOR DISPOSAL.

(3) Box label

 京东方
BOE

HEFEI BOE OPTOELECTRONICS
Technology Co., LTD

MODEL: NV133FHM-N63①

Q'TY: XX②

SERIAL NO: XXXXXXXXXXXXXXX③

DATE: 20XX / XX / XX④



2M9WH⑤

XXXX⑥



蓝色字体为后打印标识, 说明如下:

- 1. FG-CODE
- 2. Box 产品数量
- 3. Box ID, 编码规则如下
- 4. Box Packing 日期
- 5. 产品物料号(客户端)
- 6. FG-CODE 后四位

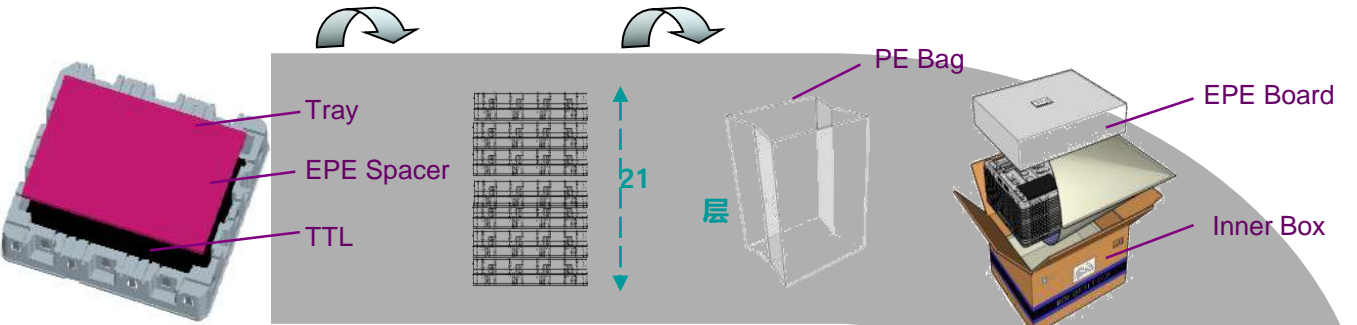
Box ID 编码规则

序号号	1	2	3	4	5	6	7	8	9	10	11	12	13
代码	S	L	S	T	1	4	3	D	0	0	1	H	D
描述	GBN代码		等级	TM1	年份		月	Rev	Serial Number				

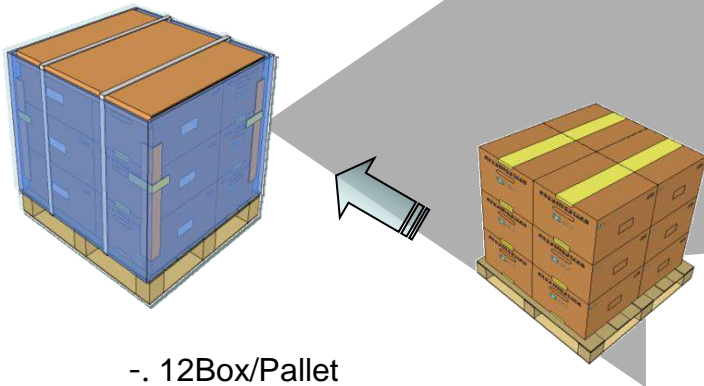
PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE			PAGE
	NV133FHM-N63 Preliminary Product Specification			25 OF 31

13.0 PACKING INFORMATION

13.1 Packing order



- Put 1pcs TTL in Tray and 1pcs Spacer on TTL
- Put 26 Tray and 25 pcs TTL in PE Bag
- 25pcs TTL/26 Tray
- Put PE Bag with 2 EPE Board in the inner Box
- 25pcs TTL/Box



- 12Box/Pallet
- 300pcs TTL/Pallet

13.2 Notes

- Box Dimension: TBD
- Package Quantity in one Box: 25pcs
- Total Weight: TBD

PRODUCT GROUP		REV	ISSUE DATE	BOE
LCM PRODUCT		P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE NV133FHM-N63 Preliminary Product Specification			PAGE 28 OF 31

15.0 EDID Table

Address (HEX)	Function	Hex	Dec	Input values.	Notes
00	Header	00	0	0	EDID Header
01		FF	255	255	
02		FF	255	255	
03		FF	255	255	
04		FF	255	255	
05		FF	255	255	
06		FF	255	255	
07		00	0	0	
08	ID Manufacturer Name	09	9	BOE	ID = BOE
09		E5	229		
0A	ID Product Code	BE	190	1726	ID = 1726
0B		06	6		
0C	32-bit serial No.	00	0		
0D		00	0		
0E		00	0		
0F		00	0		
10	Week of manufacture	01	1	1	
11	Year of Manufacture	1A	26	2016	Manufactured in 2016
12	EDID Structure Ver.	01	1	1	EDID Ver 1.0
13	EDID revision #	04	4	4	EDID Rev. 0.4
14	Video input definition	95	149	-	digital signal/DP input
15	Max H image size	1D	29	29	29 cm (Approx)
16	Max V image size	11	17	17	17 cm (Approx)
17	Display Gamma	78	120	2.2	Gamma curve = 2.2
18	Feature support	0A	10		RGB display, Preferred Timming mode
19	Red/Green low bits	9C	156	-	Red / Green Low Bits
1A	Blue/White low bits	A2	162	-	Blue / White Low Bits
1B	Red x high bits	A6	166	0.651	Red (x) = 10100110 (0.651)
1C	Red y high bits	58	88	0.345	Red (y) = 01011000 (0.345)
1D	Green x high bits	54	84	0.328	Green (x) = 01010100 (0.328)
1E	Green y high bits	9F	159	0.622	Green (y) = 10011111 (0.622)
1F	Blue x high bits	26	38	0.151	Blue (x) = 00100110 (0.151)
20	BLue y high bits	0E	14	0.057	Blue (y) = 00001110 (0.057)
21	White x high bits	4D	77	0.301	White (x) = 01001101 (0.301)
22	White y high bits	55	85	0.334	White (y) = 01010101 (0.334)
23	Established timing 1	00	0	-	
24	Established timing 2	00	0	-	

PRODUCT GROUP			REV	ISSUE DATE	BOE
LCM PRODUCT			P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE				PAGE
	NV133FHM-N63 Preliminary Product Specification				29 OF 31
25	Established timing 3	00	0	-	
26	Standard timing #1	01	1		Not Used
27		01	1		
28	Standard timing #2	01	1		Not Used
29		01	1		
2A	Standard timing #3	01	1		Not Used
2B		01	1		
2C	Standard timing #4	01	1		Not Used
2D		01	1		
2E	Standard timing #5	01	1		Not Used
2F		01	1		
30	Standard timing #6	01	1		Not Used
31		01	1		
32	Standard timing #7	01	1		Not Used
33		01	1		
34	Standard timing #8	01	1		Not Used
35		01	1		
36	Detailed timing/monitor descriptor #1	BC	188	147.8	147.8MHz Main clock
37		39	57		
38		80	128	1920	Hor Active = 1920
39		18	24	280	Hor Blanking = 280
3A		71	113	-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		38	56	1080	Ver Active = 768
3C		28	40	40	Ver Blanking = 40
3D		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E		30	48	48	Hor Sync Offset = 48
3F		20	32	32	H Sync Pulse Width = 32
40		36	54	3	V sync Offset = 3 line
41		00	0	6	V Sync Pulse width : 6 line
42		26	38	294	Horizontal Image Size = 294 mm (Low 8 bits)
43		A5	165	165	Vertical Image Size = 165 mm (Low 8 bits)
44		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0	0	Hor Border (pixels)
46		00	0	0	Vertical Border (Lines)
47		1A	26		Refer to right table

29

PRODUCT GROUP				REV	ISSUE DATE	BOE
LCM PRODUCT				P2	2016.9. 2	
SPEC. NUMBER	SPEC. TITLE					PAGE
	NV133FHM-N63 Preliminary Product Specification					30 OF 31
48	Detailed timing/monitor descriptor #2	36	54	118.3	118.3MHz Main clock	
49		2E	46			
4A		80	128	1920	Hor Active = 1920	
4B		18	24	280	Hor Blanking = 280	
4C		71	113	-	4 bits of Hor. Active + 4 bits of Hor. Blanking	
4D		38	56	1080	Ver Active = 768	
4E		28	40	40	Ver Blanking = 40	
4F		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking	
50		30	48	48	Hor Sync Offset = 48	
51		20	32	32	H Sync Pulse Width = 32	
52		36	54	3	V sync Offset = 3 line	
53		00	0	6	V Sync Pulse width : 6 line	
54		26	38	294	Horizontal Image Size = 294 mm (Low 8 bits)	
55		A5	165	165	Vertical Image Size = 165 mm (Low 8 bits)	
56		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size	
57		00	0	0	Hor Border (pixels)	
58		00	0	0	Vertical Border (Lines)	
59		1A	26			
5A	Detailed timing/monitor descriptor #3	00	0		ASCII Data Sting Tag	
5B		00	0			
5C		00	0			
5D		FE	254			
5E		00	0		D/PN:2M9WH	
5F		32	50	2		
60		4D	77	M		
61		39	57	9		
62		77	119	w	EDID:A00	
63		48	72	H		
64		80	128	10000000		
65		4E	78	N		
66		56	86	V	BOE PN	
67		31	49	1		
68	33	51	3			
69	46	70	F			
6A	48	72	H			
6B	4D	77	M			

30

6C	Detailed timing/monitor descriptor #4	00	0		Product Name Tag (ASCII)
6D		00	0		
6E		00	0		
6F		00	0		
70		00	0		
71		00	0	00000000	6-bit Color Depth & no FRC
72		41	65	01000001	WLED & singal light bar & one light bar
73		21	33	00100001	SDRRS Support & Frame rate 40Hz~65Hz
74		A3	163	10100011	Light Controller:PWM & Max. Luminance 350
75		00	0	00000000	Front Surface: Anti Glare & RGB v-stripe
76		10	16	00010000	NTSC & DBC
77		00	0	00000000	no Motion Blur & no Active Gamma
78		00	0	00000000	no Wireless Enhancement & no In-Cell Scanner
79		0A	10	00001010	2 lane edp1.3,no Over Driving
7A		01	1	00000001	Built-In Self Test
7B	Extension flag	0A	10		
7C		20	32		
7D		20	32		
7E		00	0		
7F	Checksum	F7	247	-	