

(V) Preliminary Specifications () Final Specifications

Module	15 Inch Color TFT-LCD
Model Name	G150XTN06.3

Customer Date	Approved by Date
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Note: This Specification is subject to change without notice.	General Display Business Division / AU Optronics corporation



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Version and Date	Page	Old description	New Description
0.0 2015/04/25	All	First Edition	



1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED Reflector edge. Instead, press at the far ends of the LED Reflector edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials without flammability grade are used in the TFT-LCD module. The TFT-LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950-1 or UL60950-1), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time.
- 14) Continuous operating TFT-LCD Module under high temperature environment may accelerate LED light bar exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when TFT-LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or moving content periodically if fixed pattern is displayed on the screen.



2. General Description

G150XTN06.3 is a Color Active Matrix Liquid Crystal Display composed of a TFT-LCD display, a driver circuit, and a backlight system. The screen format is intended to support XGA (1024(H) x 768(V)) screen and 16.2M or 262K colors. All input signals are LVDS interface compatible. G150XTN06.3 is designed for industrial display applications.

2.1 Display Characteristics

Items	Unit	Specifications
Screen Diagonal	[inch]	15
Active Area	[mm]	304.128(H) x 228.096(V)
Pixels H x V		1024x3(RGB)x768
Pixel Pitch	[mm]	0.297 x 0.297
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN, Normally White
Nominal Input Voltage VDD	[Volt]	3.3 typ.
Typical Power Consumption	[Watt]	27.89W(Typ.)
Weight	[Grams]	1320g
Physical Size	[mm]	326.5(H)x 253.5(V) x 13.0(D) (max.)
Electrical Interface		LVDS
Surface Treatment		Anti-Reflection, Hardness 2H
Support Color		16.2M/262K
Temperature Range Operating Storage (Non-Operating)	[°C]	-30 to +70 -30 to +70
RoHS Compliance		RoHS Compliance
Light Bar Unit		LED, Non-replaceable



2.2 Optical CharacteristicsThe optical characteristics are measured under stable conditions at 25°C (Room Temperature):

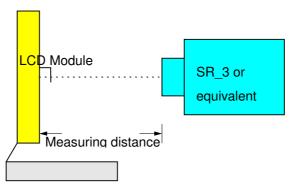
Item	Unit	Conditions Conditions	Min.	Typ.	Max.	Note
White Luminance	[cd/m2]		1280	1600	-	1
Uniformity	%	9 Points	75	80	-	1, 2, 3
Contrast Ratio			400	800	-	4
Cross talk	%		-	1.2	1.5	5
	[msec]	Rising	-	5.7		
Response Time	[msec]	Falling	-	2.3		6
	[msec]	Raising + Falling	-	8		
Viewing Angle	[degree]	Horizontal (Right)	70	80	-	
	[degree]	CR = 10 (Left)	70	80	-	_
	[degree]	Vertical (Upper)	60	70	-	7
	[degree]	CR = 10 (Lower)	70	80	-	
		Red x	0.567	0.617	0.667	
		Red y	0.296	0.346	0.396	
		Green x	0.286	0.336	0.386	
Color / Chromaticity Coordinates		Green y	0.562	0.612	0.662	
(CIE 1931)		Blue x	0.109	0.159	0.209	
		Blue y	0.015	0.065	0.115	
		White x	0.261	0.311	0.361	
		White y	0.272	0.322	0.372	
Color Gamut	%			60	-	

Note 1: Measurement method

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

1° with 50cm viewing distance **Aperture**

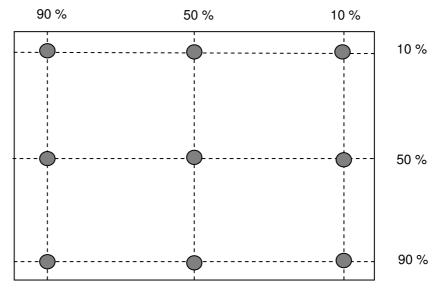
Test Point Center Environment < 1 lux



Module Driving Equipment



Note 2: Definition of 9 points position (Display active area: 304.128(H) x 228.096(V))



Note 3: The luminance uniformity of 9 points is defined by dividing the minimum luminance values by the maximum test point luminance

 $\delta_{W9} = \frac{\text{Minimum Brightness of nine points}}{\text{Maximum Brightness of nine points}}$

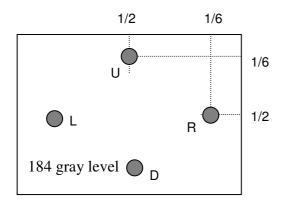
Note 4: Definition of contrast ratio (CR):

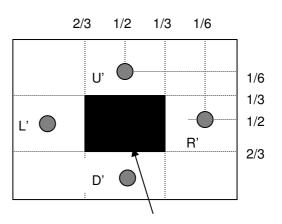
Note 5: Definition of cross talk (CT)

 $CT = max(\mid Y' - Y \mid / \mid Y \times 100 \text{ (\%) }), \ Y = U/D/L/R$ Where

Y = Luminance of measured location without gray level 0 pattern (cd/m2)

Y' = Luminance of measured location with gray level 0 pattern (cd/m2)

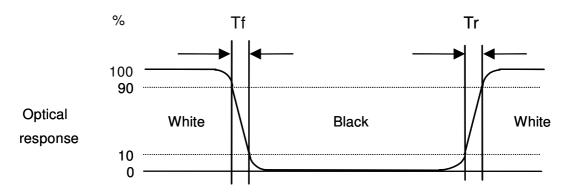






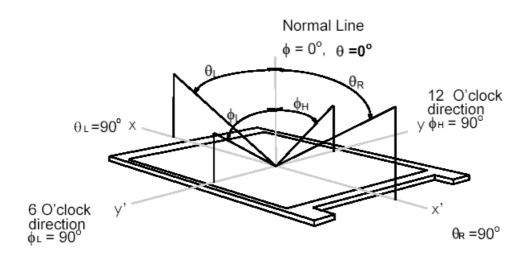
Note 6: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



Note 7: Definition of viewing angle

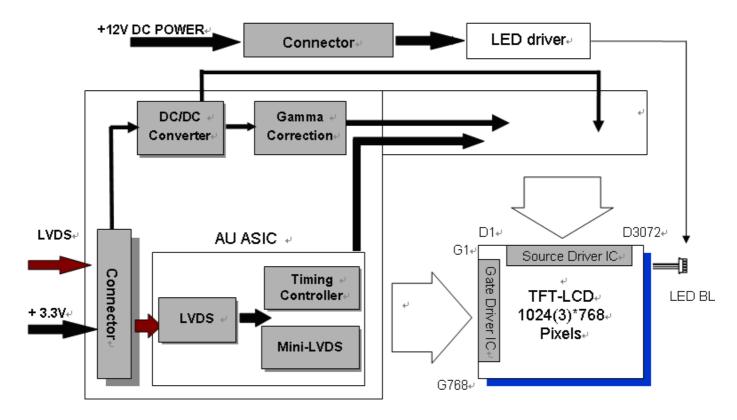
Viewing angle is the measurement of contrast ratio \geq 10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° (θ) horizontal left and right, and 90° (Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.





3. Functional Block Diagram

The following diagram shows the functional block of the 15 inch color TFT/LCD module:





4. Absolute Maximum Ratings

4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit
Logic/LCD Drive Voltage	Vin	-0.3	+3.6	[Volt]

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	8	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	8	90	[%RH]



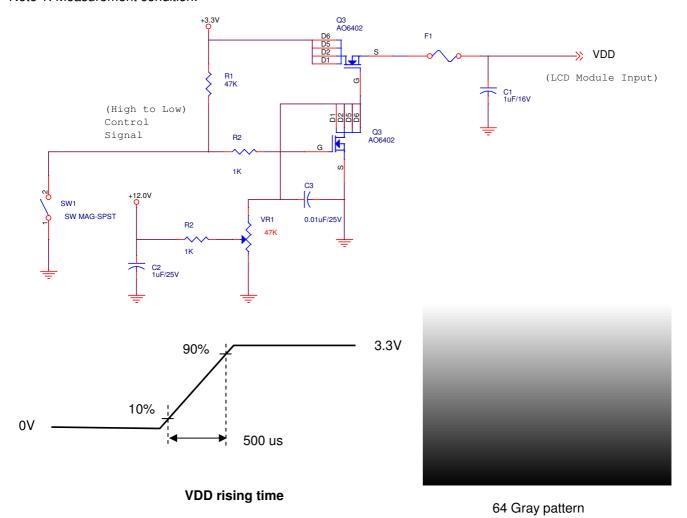
5. Electrical Characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

Symbol	Parameter	Min	Тур	Max	Units	Remark
VDD	Logic/LCD Drive Voltage	3.0	3.3	3.6	[Volt]	± 10%
IDD	VDD Current	-	450	750	[mA]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)
Irush	LCD Inrush Current	-	-	2	[A]	Note 1
PDD	VDD Power	-	1.485	2.475	[Watt]	64 Gray Bar Pattern (VDD=3.3V, at 60Hz)

Note 1: Measurement condition:

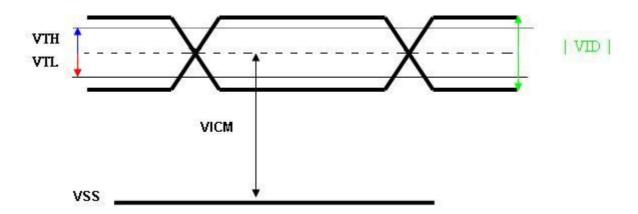




5.1.2 Signal Electrical CharacteristicsInput signals shall be low or Hi-Z state when VDD is off.

Symbol	Item		Тур.	Max.	Unit	Remark
VTH	Differential Input High Threshold		-	100	[mV]	VCM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V
VID	Input Differential Voltage		400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.15	1.2	1.45	[V]	VTH/VTL=±100mV

Note: LVDS Signal Waveform.





5.2.1 Parameter guideline for LED

Temperature).

Symbol	Parameter	Min	Тур	Max	Unit	Remark
Vcc	Input Voltage	10.8	12	13.2	Volt	
lvcc	Input Current	-	2.2	-	Α	100% Dimming
PLED	Power Consumption	-	26.4	-	Watt	100% Dimming
Irush	Inrush Current	-		2	Α	
\/\ FD/- ((On Control Voltage	2.0		5.5	Volt	
VLED on/off Off Control Voltage		-		0.8	Volt	
	PWM Dimming Frequency	90	180	240	Hz	
	High Voltage	2.0		3.3	Volt	
FPWM	FPWM Low Voltage			0.8	Volt	
	Dimming Duty Cycle	5	-	100	%	
I _F	LED Forward Current	-	80	84	mA	Ta = 25°C
LED Life time		50000	-	-	Hrs	Ta = 25°C

Note 1: Ta means ambient temperature of TFT-LCD module.

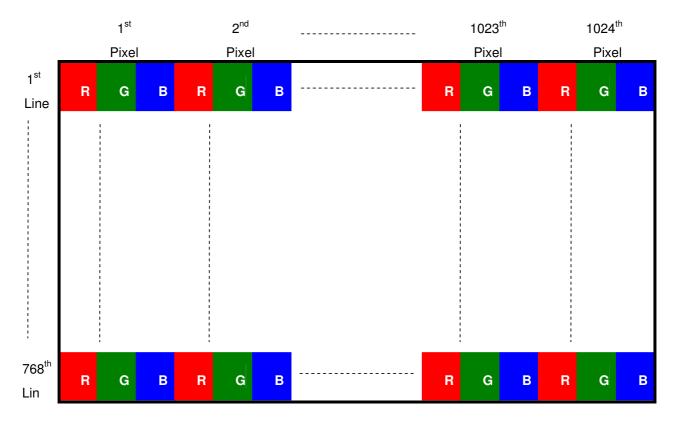
Note 2: If G150XTN06.3 module is driven at high ambient temperature & humidity condition. The operating life will be reduced.

Note 3: LED life time means brightness goes down to 50% initial brightness. Min. LED life time is estimated data.



6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.



6.2 Scanning Direction

The following figures show the image seen from the front view. The arrow indicates the direction of scan.





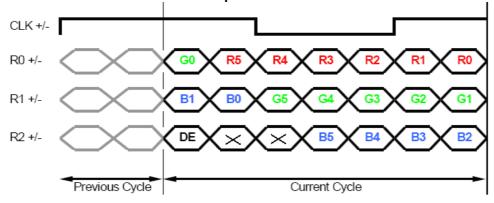
LVDS is a differential signal technology for LCD interface and high speed data transfer device.

Pin No.	Symbol	Description			
1	VDD	Power Supply, 3.3V (typical)			
2	VDD	Power Supply, 3.3V (typical)			
3	NC	No Connect			
4	NC	No Connect			
5	Rin1-	- LVDS differential data input			
6	Rin1+	+ LVDS differential data input			
7	VSS	Ground			
8	Rin2-	- LVDS differential data input			
9	Rin2+	+ LVDS differential data input			
10	VSS	Ground			
11	Rin3-	- LVDS differential data input			
12	Rin3+	+ LVDS differential data input			
13	VSS	Ground			
14	ClkIN-	- LVDS differential clock input			
15	ClkIN+	+ LVDS differential clock input			
16	VSS	Ground			
17	Rin4-	- LVDS differential data input			
18	Rin4+	- LVDS differential data input			
19	VSS	Ground			
20	SEL LVDS	H or NC: 6bit/L: 8bit			

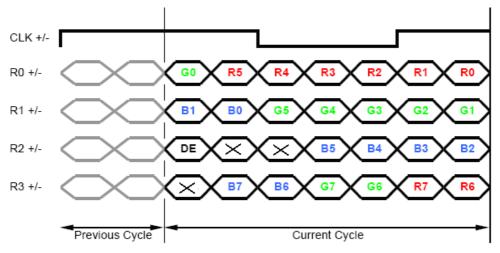


6.4 The Input Data Format

SEL LVDS = "H" or NC for 6 bits LVDS Input



SEL LVDS ="L" for 8 bits LVDS Input



Note1: Please follow PSWG.

Note2: R/G/B data 7:MSB, R/G/B data 0:LSB



6.5 Interface Timing

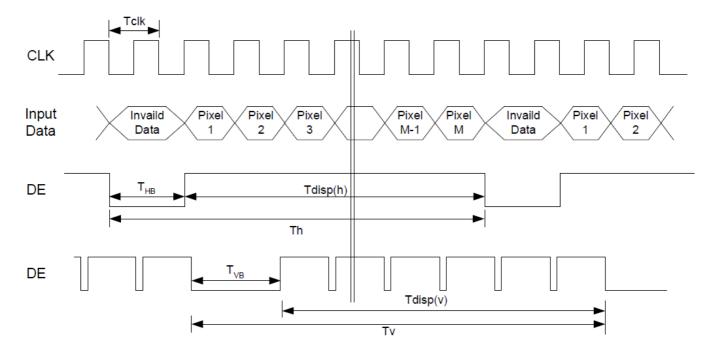
6.5.1 Timing Characteristics

Signal	Parameter Clock frequency		Symbol	Min.	Тур.	Max.	Unit
Clock Timing			1/ T _{Clock}	50	65	80	MHz
Vsync Timing	Vertical	Period	T_V	776	806	990	Vsync
	Section	Active	T_VD	-	768	-	Timing
		Blanking	T_VB	8	38	222	
Hsync Timing	Horizontal	Period	T _H	1094	1344	1720	Hsync
	Section	Active	T_{HD}	-	1024	-	Timing
		Blanking	T _{HB}	70	320	696]
Frame Rate		F	50	60	75	Hz	

Note: DE mode only.

Note: Typical value refer to VESA STANDARD

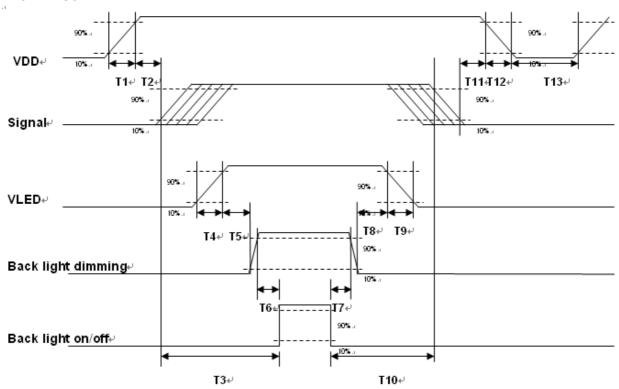
6.5.2 Input Timing Diagram





6.6 Power ON/OFF Sequence

VDD power and LED on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Davamatav		Heita		
Parameter	Min.	Тур.	Max.	Units
T1	0.5	-	10	[ms]
T2	30	40	50	[ms]
Т3	220	-	-	[ms]
T4	0.5	-	10	[ms]
T5	10	-	-	[ms]
Т6	10	-	-	[ms]
T7	0	-	-	[ms]
Т8	10	-	-	[ms]
Т9	100	-	-	[ms]
T10	110	-	-	[ms]
T11	0	16	50	[ms]
T12	-	-	10	[ms]
T13	1000	_	-	[ms]

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



7. Connector & Pin Assignment

Physical interface is described as for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

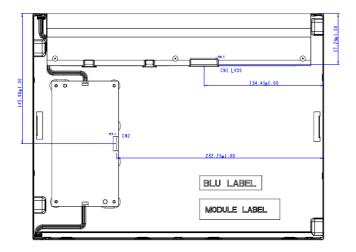
7.1 TFT LCD Module: LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	STM or compatible
Connector Model Number	MSB240420E or compatible
Mating Model Number	P240420 or compatible

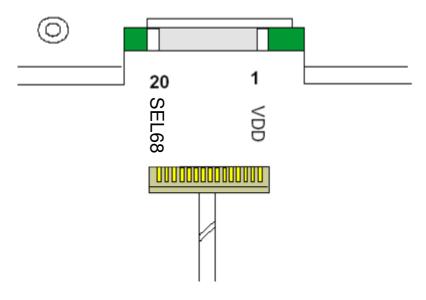
Pin#	Signal Name	Pin#	Signal Name
1	VDD	2	VDD
3	NC	4	NC
5	Rin1-	6	Rin1+
7	VSS	8	Rin2-
9	Rin2+	10	VSS
11	Rin3-	12	Rin3+
13	VSS	14	CIkIN-
15	ClkIN+	16	VSS
17	Rin4-	18	Rin4+
19	VSS	20	SEL68



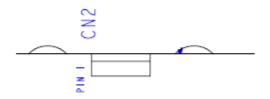
7.1.1 Connector Illustration



LVDS (CN1):



BLU Power in (CN2):





7.2 Backlight Unit: LED Driver Connector

Connector Name / Designation	LED Driver Connector
Manufacturer	ENTERY
Connector Model Number	3806K-X12X-03X or compatible
Mating Model Number	H208K-P12N-02B or compatible

7.3 LED Connector Pin Assignment

Pin#	Symbol Signal Name	
1	VCC	Supply Voltage 12V
2	VCC	Supply Voltage 12V
3	VCC	Supply Voltage 12V
4	VCC	Supply Voltage 12V
5	GND	Ground
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	NC	NA
10	DET	Normal (GND) Abnormal (Open Collector)
11	Enable BL ON/OFF	
12	External PWM Control Dimming (HW mode)	



8. Reliability Test Criteria

Items	Required Condition	Note
Temperature Humidity Bias	50℃/80%,300 hours	
High Temperature Operation	85℃,300 hours	
Low Temperature Operation	-30°C ,300 hours	
Hot Storage	85°C,300 hours	
Cold Storage	-30°C,300 hours	
Thermal Shock Test	-20°C/30 min ,60°C/30 min ,100cycles	
Shock Test (Non-Operating)	50G,20ms,Half-sine wave,(±X, ±Y, ±Z)	
Vibration Test (Non-Operating)	1.5G, (10~200Hz, P-P)	
On/off test	30 mins/axis (X, Y, Z) On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact Discharge: \pm 8KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point Air Discharge: \pm 15KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point	Note 1

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost . Self-recoverable. No hardware failures.

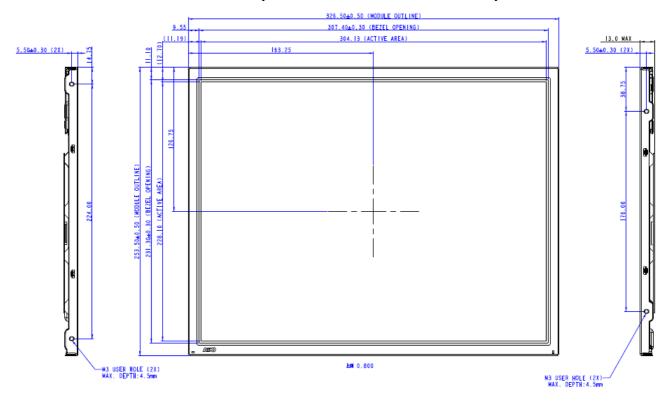
Note 2:

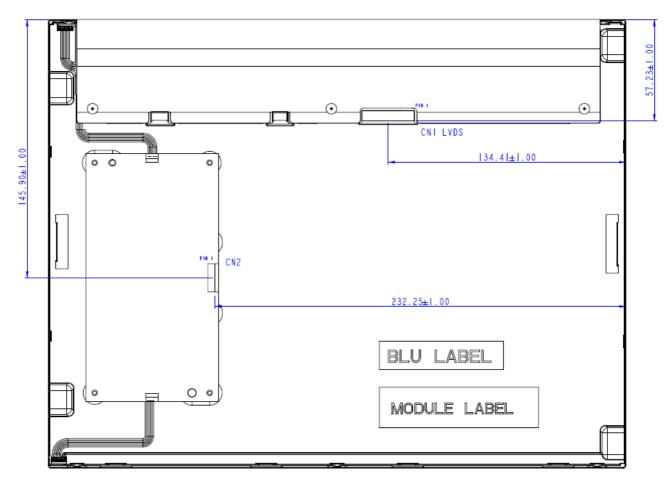
- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.
- No function failure occurs.



9. Mechanical Characteristics

9.1 LCM Outline Dimension (Front View and back view)





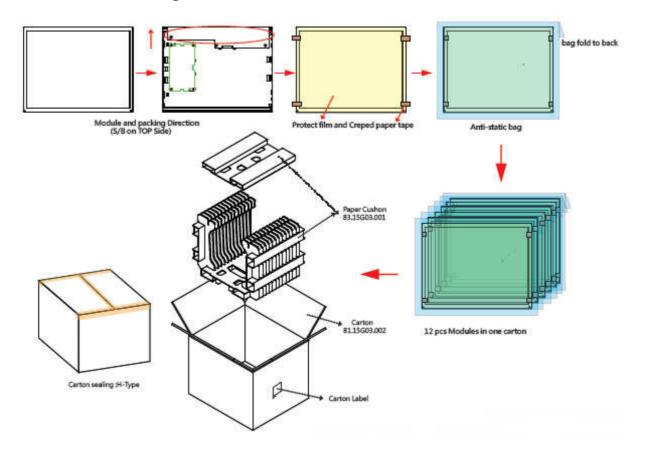


10. Label and Packaging

10.1 Shipping Label (on the rear side of TFT-LCD display)



10.2 Carton Package





11 Safety

11.1 Sharp Edge Requirements

There will be no sharp edges or comers on the display assembly that could cause injury.

11.2 Materials

11.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO toxicologist.

11.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

11.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

11.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 60950-1, Second Edition

U.S.A. Information Technology Equipment