

SPECIFICATION FOR APPROVAL

() Pre	eliminary	/ Spe	cification
---	-------	-----------	-------	------------

	(•	.)	Final	Spe	cific	ation
--	-----	-----	--------------	-----	-------	-------

Customer	LENOVO
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LP156WH3
Suffix	TLA3

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
Please return 1 copy for you your signature and commen	

APPROVED BY	SIGNATURE		
K. S. Kwon / S.Manager			
REVIEWED BY			
N. J. Seong / Manager			
PREPARED BY			
S. S. Han / Engineer			
D. J. Lee / Engineer	_		
Products Engineeri LG Display Co.			

Ver. 1.0 Sep. 10, 2009 1 / 27



Contents

No	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	
3-1	ELECTRICAL CHARACTREISTICS	6
3-2	INTERFACE CONNECTIONS	7
3-3	LVDS SIGNAL TIMING SPECIFICATION	8-9
3-4	SIGNAL TIMING SPECIFICATIONS	10
3-5	SIGNAL TIMING WAVEFORMS	10
3-6	COLOR INPUT DATA REFERNECE	11
3-7	POWER SEQUENCE	12~13
4	OPTICAL SFECIFICATIONS	14-16
5	MECHANICAL CHARACTERISTICS	17-19
6	RELIABLITY	20
7	INTERNATIONAL STANDARDS	
7-1	SAFETY	21
7-2	EMC	21
8	PACKING	
8-1	DESIGNATION OF LOT MARK	22
8-2	PACKING FORM	22
9	PRECAUTIONS	23-24
А	APPENDIX. Enhanced Extended Display Identification Data	25-27



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	May. 13. 2009	-	First Draft (Preliminary Specification)	-
1.0	Sep. 10. 2009	10	Final Specification Update the Signal Timing Specification for WWAN improvement	1.0
		15 25 ~ 27	Update the Gray Scale Specification Update the EDID information	

Ver. 1.0 Sep. 10, 2009 3 / 27

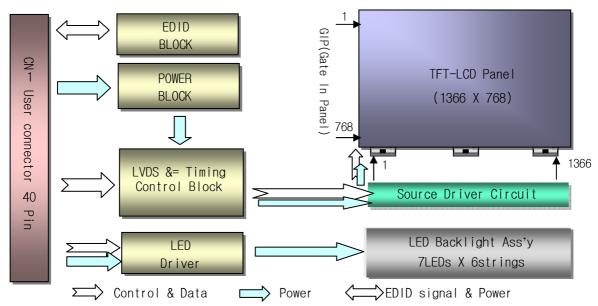


1. General Description

The LP156WH3 is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.6 inches diagonally measured active display area with HD resolution(768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP156WH3 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP156WH3 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP156WH3 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.5(H, typ) × 217.1(V, typ) × 3.8(D,max) [mm] (with PCB Board)
Pixel Pitch	0.252mm × 0.252 mm
Pixel Format	1366 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.5 point)
Power Consumption	Total 4.17 Watt(Typ.) @ LCM circuit 1.32 Watt (TypMosaic), B/L2.85Watt(Typ.)
Weight	420g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard Coating(3H), Glare treatment of the front polarizer
RoHS Comply	Yes

Ver. 1.0 Sep. 10, 2009 4 / 27



2. Absolute Maximum Ratings

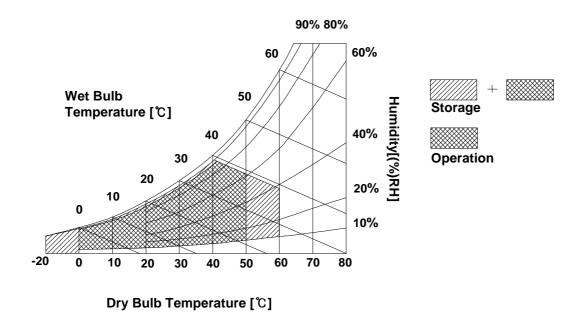
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
i arameter	Symbol	Min	Max	Offics		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 1.0 Sep. 10, 2009 5 / 27



3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WH3 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL.with LED Driver.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter		umb al	Values			Unit	Notes
		ymbol	Min	Тур	Max	Onit	Notes
LOGIC:							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	
Power Supply Input Current	Icc	Mosaic	340	400	460	mA	1
Fower Supply Input Current	ICC	Black	415	490	565	mA	
Power Consumption		Pcc	-	1.32	1.52	W	1
Power Supply Inrush Current		ICC_P	-	-	1500	mA	
LVDS Impedance		ZLVDS	90	100	110	Ω	2
BACKLIGHT : (with LED Driver)							
LED Power Input Voltage		VLED	7.0	12.0	20.0	V	
LED Power Input Current		ILED	-	18	-	mA	3
LED Power Consumption		PLED	-	2.85	3.02	W	3
LED Power Inrush Current	I	LED_P	-	-	1000	mA	
PWM Dimming (Duty) Ratio		-	12.5	-	100	%	4
PWM Impedance		Z _{PWM}	20	40	60	kΩ	
PWM Frequency	I	- PWM	200	-	1000	Hz	5
PWM High Level Voltage	V	PWM_H	3.0	-	5.3	V	
PWM Low Level Voltage		PWM_L	0	-	0.5	V	
LED_EN High Voltage		ED_EN_H	3.0	-	5.3	V	
LED_EN Low Voltage		ED_EN_L	0	-	0.5	V	
Life Time			12,000			Hrs	6

Note)

- 1. The specified Icc current and power consumption are under the Vcc = 3.3V , 25℃, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The specified LED current and power consumption are under the Vled = 12.0V , 25 ℃, Dimming of Max luminance whereas White pattern is displayed and fv is the frame frequency.
- 4. The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 5. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 6. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value at Table 9. These LED backlight has 6 strings on it and the typical current of LED's string is base on typical current at Table 2.

Ver. 1.0 Sep. 10, 2009 6 / 27



3-2. Interface Connections

This LCD employs one interface connections, a 40 pin connector is used for the module electronics interface and LED Driver.

The electronics interface connector is a model IS050-L40B-C1 manufactured by UJU.

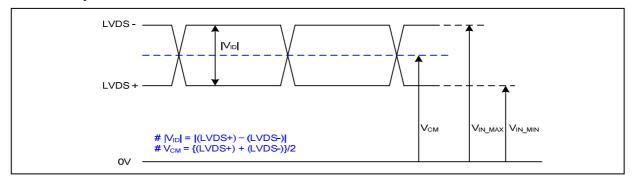
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	NC	No connection	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	No Connection	1.1 LCD: SW, SW0624 (LCD Controller)
6	Clk EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	1.2 System : THC63LVDF823A
8	Odd_R _{IN} 0-	Negative LVDS differential data input	or equivalent * Pin to Pin compatible with LVDS
9	Odd_R _{IN} 0+	Positive LVDS differential data input	Till to Till compatible mail 2000
10	GND	Ground	2. Connector
11	Odd_R _{IN} 1-	Negative LVDS differential data input	2.1 LCD:CABLINE-VS RECE ASS'Y, I-PEX GT05Q-40S-H10, LSM
12	Odd_R _{IN} 1+	Positive LVDS differential data input	IS050-L40B-C10, UJU
13	GND	Ground	or equivalant
14	Odd_R _{IN} 2-	Negative LVDS differential data input	2.2 Mating : CABLINE-VS PLUG CABLE
15	Odd_R _{IN} 2+	Positive LVDS differential data input	ASS'Y or equivalent 2.3 Connector pin arrangement
16	GND	Ground	40
17	Odd_CLKIN-	Negative LVDS differential clock input	I П П I
18	Odd_CLKIN+	Positive LVDS differential clock input	
19	GND	Ground	
20	NC	No Connection	[LCD Module Rear View]
21	NC NC	No Connection	
22	GND	Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	NC	No Connection	
35	BLIM	PWM for Luminance control	
36	BL_On	Backlight On/Off Control	
37	NC	No Connection (Reserved)	
38	VLED	LED Power Supply (7V-20V)	
39	VLED	LED Power Supply (7V-20V)	
40	VLED	LED Power Supply (7V-20V)	



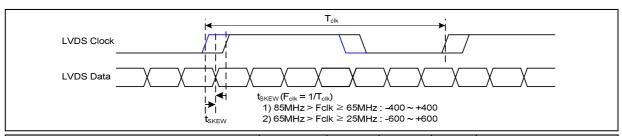
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

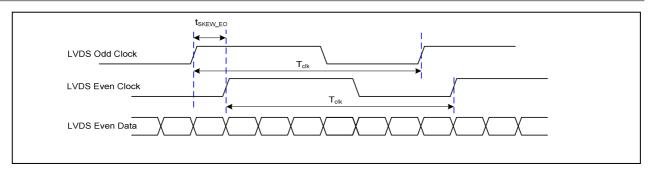
3-3-2. AC Specification



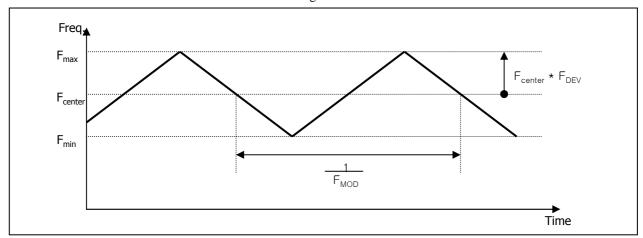
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

Ver. 1.0 Sep. 10, 2009 8 / 27





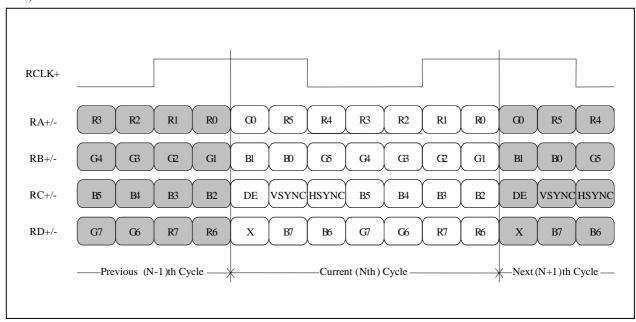
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

1) LVDS 1 Port



< LVDS Data Format >

Ver. 1.0 Sep. 10, 2009 9 / 27

Condition: VCC =3.3V



Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	75.1	-	MHz	
	Period	t _{HP}	1470	1526	1600		
Hsync	Width	t _{wH}	23	32	42 tCLK		
	Width-Active	t _{WHA}	1366	1366	1366		
	Period	t _{VP}	779	800	804		
Vsync	Width	t _{wv}	2	5	8	tHP	
	Width-Active	t _{WVA}	768	768	768		
	Horizontal back porch	t _{HBP}	72	118	138	+CLV	
Data	Horizontal front porch	t _{HFP}	8	48	54	tCLK	
Enable	Vertical back porch	t _{VBP}	8	24	24	+UD	
	Vertical front porch	t _{VFP}	1	3	4	tHP	

3-5. Signal Timing Waveforms

Ver. 1.0

High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc DCLK t_{HP} Hsync **t**WHA t_{HFP} t_{HBP} Data Enable Vsync t_{VFP} twva t_{VBP} Data Enable 10 / 27

Sep. 10, 2009



3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	ut Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	30101	MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	1	. 1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																	· · · · · ·		
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																	 		••••
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	1	 1	1	1
	. ,	1																	

Ver. 1.0 Sep. 10, 2009 11 / 27



3-7. Power Sequence

3-7-1. Logic Power and LVDS Signal Sequence

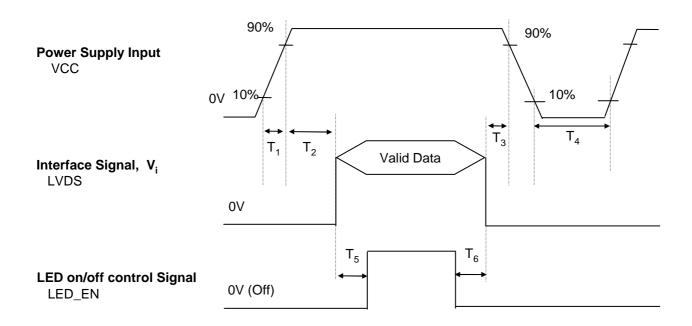


Table 6. POWER SEQUENCE TABLE

Parameter		Value	Units		
Parameter	Min.	Тур.	Max.	Units	
T ₁	0.5	-	10	ms	
T ₂	0	-	50	ms	
T_3	0	-	50	ms	
T ₄	400	-	-	ms	
T ₅	200	-	-	ms	
T ₆	200	-	-	ms	

Note)

- 1. Valid Data has to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.

3-7-2. LED_EN, PWN and LED Power Sequence

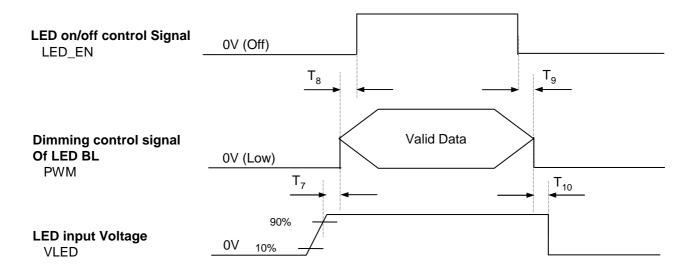


Table 7. LED POWER SEQUENCE TABLE

Parameter		Value	Units	
Parameter	Min.	Тур.	Max.	Offics
T ₇	10	-	-	ms
T ₈	0	-	-	ms
T ₉	0	-	-	ms
T ₁₀	10	-	-	ms

Note)

1. Valid Data of Control signal has to meet "3-1. Electrical Characteristics"

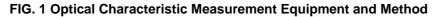
Ver. 1.0 Sep. 10, 2009 13 / 27



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



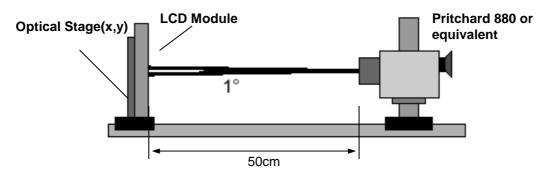


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = **75.1MH**z, I_{LED} = 18 mA

Parameter	Symbol		Values	Units	Notes	
Parameter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio	CR	400	500	-		1
Surface Luminance, white	L_WH	170	200	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6]	3
Response Time	Tr_{R} + Tr_{D}	-	16	25	ms	4
Color Coordinates]	
RED	RX	0.560	0.590	0.620	1	
	RY	0.320	0.350	0.380		
GREEN	GX	0.300	0.330	0.360		
	GY	0.525	0.555	0.585		
BLUE	BX	0.123	0.153	0.183		
	BY	0.089	0.119	0.149		
WHITE	WX	0.283	0.313	0.343		
l	WY	0.299	0.329	0.359	<u>.</u>	
Viewing Angle						5
x axis, right(Φ=0°)	Θr	40	-		degree	
x axis, left (Φ =180°)	Θl	40	-		degree	
y axis, up (Φ =90°)	Θu	10	-		degree	
y axis, down (Φ=270°)	Θd	30	-	-	degree	
Color Gamut	%	-	45	-]	
Gray Scale						6

Ver. 1.0 Sep. 10, 2009 14 / 27



Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{13})}{\text{Minimum}(\textbf{L}_{1}, \textbf{L}_{2}, \ \dots \ \textbf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_{V} = 60Hz$$

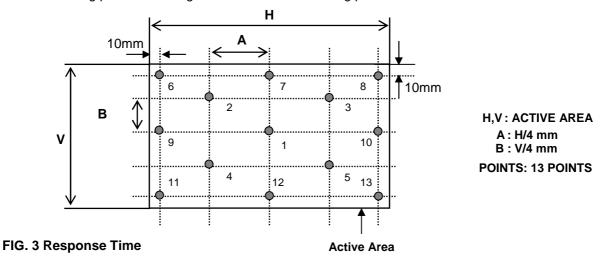
Gray Level	Luminance [%] (Typ)
LO	0.12
L7	1.45
	5.36
L23	12.2
L31	21.0
	34.8
L47	52.5
L55	76.5
L63	100

Ver. 1.0 Sep. 10, 2009 15 / 27

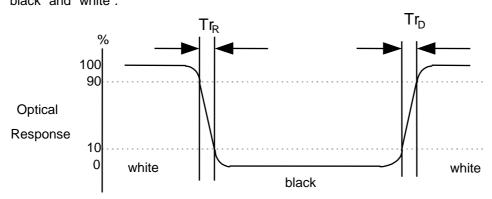


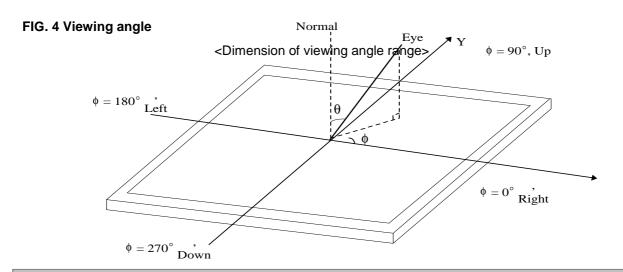
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





Ver. 1.0 Sep. 10, 2009 16 / 27



5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP156WH3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

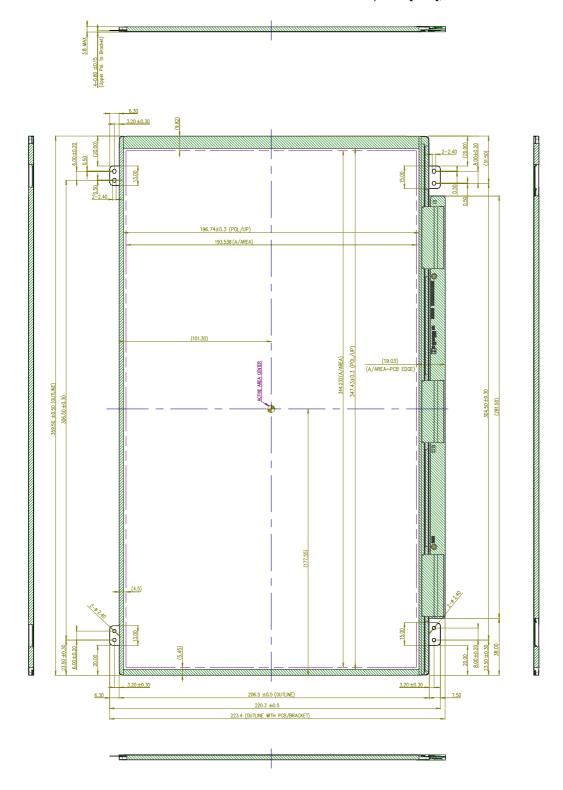
	Horizontal	359.5 ± 0.5mm		
Outline Dimension	Vertical	217.1 ± 0.5mm		
	Thickness	3.8mm (max)		
Bezel Area	Horizontal	347.5 ± 0.5mm		
bezei Alea	Vertical	196.8 ± 0.5mm		
Active Diepley Area	Horizontal	344.23 mm		
Active Display Area	Vertical 193.54 mm			
Weight	420g (Max.)			
Surface Treatment	Hard Coating(3H), Glare treatment of	of the front polarizer		

Ver. 1.0 Sep. 10, 2009 17 / 27



<FRONT VIEW>

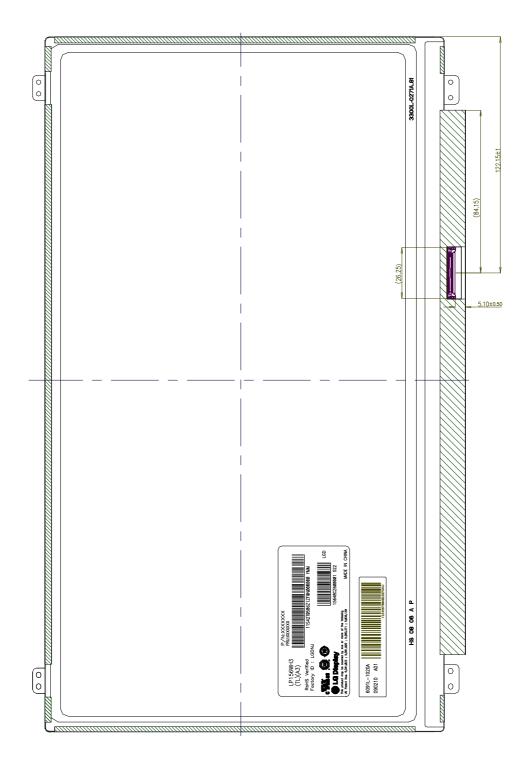
Note) Unit:[mm], General tolerance: \pm 0.5mm





<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





6. Reliability

Environment test condition

No.	Test Item	Conditions							
1	High temperature storage test	Ta= 60°C, 240h							
2	Low temperature storage test	Ta= -20°C, 240h							
3	High temperature operation test	Ta= 50°C, 50%RH, 240h							
4	Low temperature operation test	Ta= 0°C, 240h							
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis							
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays							
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr							

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

Ver. 1.0 Sep. 10, 2009 20 / 27



7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

Ver. 1.0 Sep. 10, 2009 21 / 27



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

		А	В	С	D	Е	F	G	Н	I	J	К	L	М
--	--	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20pcs

b) Box Size: 476mm X 370mm X 292mm

Ver. 1.0 Sep. 10, 2009 22 / 27



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 1.0 Sep. 10, 2009 23 / 27



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Ver. 1.0 Sep. 10, 2009 24 / 27



APPENDIX A. Enhanced Extended Display Identification Data (EEDIDTM) 1/3 EDID Data for Lenovo_Ver1.0 2009.09.10.

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (B in)
	0	00	Header	00	00000000
	1	01	Header	FF	11111111
	2	02	Header	FF	11111111
Header	3	03	Header	FF	11111111
ea	4	04	Header	FF	11111111
H	5	0.5	Header	FF	11111111
	6	06	Header	FF	11111111
	7	07	Header	00	00000000
	8	08	EISA manufacture code (3 Character ID) LGD	30	00110000
EDID	9	09	EISA manufacture code (Compressed ASCII)	E4	11100100
ED	10	0 A	Panel Supplier Reserved - Product Code 0222h	22	00100010
	11	0B	(Hex. LSB first)	02	00000010
~	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
roduct Version	13	0D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
ers	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
Pre V	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00	00000000
~	16	10	Week of Manufacture 00 weeks	00	00000000
Vendor / Product Version	17	11	Year of Manufacture 2009 years	13	00010011
en	18	12	EDID structure version # = 1	01	00000001
>	19	13	EDID revision # = 3	03	00000011
5	20	14	Video input Definition = Digital signal	80	10000000
Display Parameters	21	15	Max H image size (Rounded cm) = 35 cm	23	00100011
Display arameter	22	16	Max V image size (Rounded cm) = 19 cm	13	00010011
)is _i	23	17	Display gamma = $(gamma*100)-100 = Example:(2.2*100)-100=120 = 2.2 Gamma$	78	01111000
l Pa	24	18	reature Support (no_DPMS, no_Active On/very Low Power, RGB color display, 1 mning BLK	0A	00001010
200	25	19	Red/Green Low Bits (RxRy/GxGy)	28	00101000
Panel Color Coordinates	26	1A	Blue/White Low Bits (BxBy/WxWy)	65	01100101
inc	27	1B	Red X Rx = 0.590	97	10010111
rd	28	1C	Red Y Ry = 0.350	59	01011001
90	29	1D	Green X Gx = 0.330	54	01010100
, <u>,</u>	30	1E	Green Y $Gy = 0.555$	8E	10001110
olo	31	1F	Blue X Bx = 0.153	27	00100111
C	32	20	Blue Y By = 0.119	1E	000111110
nel	33	21	White X $Wx = 0.313$	50	01010000
Pa	34	22	W hite Y W y = 0.329	54	01010100
	35	23	Established timing 1 (00h if not used)	00	00000000
Establ ished Timin	36	24	Established timing 2 (00h if not used)	00	00000000
Est ish Tin	37	25			00000000
, ,,		26	Manufacturer's timings (00h if not used)	00	00000000
	38	27	Standard timing ID1 (01h if not used)	01	00000001
	40	28	Standard timing ID1 (01h if not used) Standard timing ID2 (01h if not used)	01	00000001
	41	29	Standard timing ID2 (01h if not used) Standard timing ID2 (01h if not used)	01	00000001
	42		Standard timing ID2 (01h it not used) Standard timing ID3 (01h if not used)	01	00000001
	43	2A 2B	Standard timing ID3 (01h if not used)	01	00000001
ng	43	2B 2C	Standard timing ID3 (01h if not used) Standard timing ID4 (01h if not used)	01	00000001
mi	44	2D	Standard timing ID4 (01h if not used) Standard timing ID4 (01h if not used)	01	00000001
Standard Timing ID	45	2D 2E	Standard timing ID4 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
ard	47	2E 2F	Standard timing ID5 (01h if not used) Standard timing ID5 (01h if not used)	01	00000001
nde	47	30	Standard timing ID5 (01h if not used)	01	00000001
Sta	48	31	Standard timing ID6 (01h if not used) Standard timing ID6 (01h if not used)	01	00000001
	50	32	Standard timing ID7 (01h if not used)	01	00000001
		33	Standard timing ID7 (01h if not used)	01	
	51 52	34	Standard timing ID7 (01h it not used) Standard timing ID8 (01h if not used)	01	00000001
		35			
	53	35	Standard timing ID8 (01h if not used)	01	00000001

Ver. 1.0 Sep. 10, 2009 25 / 27



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	Pixel Clock/10,000 (LSB) 75.1 MHz @ 60Hz	56	01010110
	55	37	Pixel Clock/10,000 (MSB)	1 D	00011101
	56	38	Horizontal Active (lower 8 bits) 1366 Pixels	56	01010110
ļ.	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 198 Pixels	C6	11000110
ļ.	58	3A	Horizontal Active / Horizontal Blanking(Thp-HA) (upper 4:4bits)	50	01010000
	59	3B	Vertical Avtive 768 Lines	00	00000000
# .	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ for DE only panels) 32 Lines	20	00100000
)to	61	3D	Vertical Active: Vertical Blanking (Tvp-HA) (upper 4:4bits)	30	00110000
: rri	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30	00110000
Timing Descriptor #1	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20	00100000
S D	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 3 Lines: 5 Lines	35	00110101
ing	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00	00000000
<u>"</u>	66	42	Horizontal Image Size (mm) 345 mm	59	01011001
	67	43	Vertical Image Size (mm) 194 mm	C2	11000010
	68	44	Horizontal Image Size / Vertical Image Size	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note : LSB is set to '1' if panel is DE-timing only. H/V can be ignored.	19	00011001
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	4B	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
2	77	4D	Descriptor Defined by manufacturer	00	00000000
# .	78	4E	Descriptor Defined by manufacturer	00	00000000
pto	79	4F	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	80	50	Descriptor Defined by manufacturer	00	00000000
es	81	51	Descriptor Defined by manufacturer	00	00000000
g L	82	52	Descriptor Defined by manufacturer	00	00000000
rin .	83	53	Descriptor Defined by manufacturer	00	00000000
	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90	5A	Flag	00	00000000
	91	5B	Flag	00	00000000
	92	5C	Flag	00	00000000
	93	5D	Data Type Tag (ASCII String)	FE	11111110
	94	5E	Flag	00	00000000
#3	95	5F	ASCII String L	4C	01001100
 4	96	60	ASCII String G	47	01000111
pto	97	61	ASCII String	20	00100000
cri	98	62	ASCII String D	44	01000100
)es	99	63	ASCII String i	69	01101001
g L	100	64	ASCII String s	73	01110011
iin	101	65	ASCII String p	70	01110000
Timing Descriptor #3	102	66	ASCII String 1	6C	01101100
	103	67	ASCII String a	61	01100001
	104	68	ASCII String y	79	01111001
	105	69	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h	0A	00001010
	106	6A	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h	20	00100000
		6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h	20	00100000



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	108	6C	Flag	00	00000000
	109	6D	Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Monitor Name, stored as ASCII)	FC	11111100
	112	70	Flag	00	00000000
#4	113	71	Monitor Name, stored as ASCII L	4C	01001100
or	114	72	Monitor Name, stored as ASCII P	50	01010000
Timing Descriptor #4	115	73	Monitor Name, stored as ASCII 1	31	00110001
scr	116	74	Monitor Name, stored as ASCII 5	35	00110101
De,	117	75	Monitor Name, stored as ASCII 6	36	00110110
50	118	76	Monitor Name, stored as ASCII W	57	01010111
nir	119	77	Monitor Name, stored as ASCII H	48	01001000
Tü	120	78	Monitor Name, stored as ASCII 3	33	00110011
	121	79	Monitor Name, stored as ASCII -	2D	00101101
	122	7A	Monitor Name, stored as ASCII T	54	01010100
	123	7B	Monitor Name, stored as ASCII L	4C	01001100
	124	7C	Monitor Name, stored as ASCII A	41	01000001
	125	7D	Monitor Name, stored as ASCII 3	33	00110011
Chec	126	7 E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
Ch	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	17	00010111

Ver. 1.0 Sep. 10, 2009 27 / 27