(   ) **Preliminary Specification**
(V) **Final Specification**

Module	24.0" Color TFT-LCD
Model Name	M240HTN01.0

Customer	Date

Approved by

Approved by	Date
Howard Lee	2012/01/09

Prepared by

Bernice Huang   2012/01/09

Note: This Specification is subject to change without notice.

Desktop Display Business Group /
AU Optronics Corporation

Content

Records of Revision

Version and Date	Page	Old Description	New Description	Remark
0.1 2011/11/21	All	First Edition for Customer	N/A	
0.2 2011/11/21	20		7.2 Add L/B CNT Mating Type	
1.0 2012/01/04	21		Final version 6.6 T5 min changes to 40 msec	

1   Handling Precautions

1) Since front polarizer is easily damaged, pay attention not to scratch it.

2) Be sure to turn off power supply when inserting or disconnecting from input connector.

3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.

4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.

6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.

7) Do not open or modify the Module Assembly.

8) Do not press the reflector sheet at the back of the module to any directions.

9) In case if a Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.

10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.

11) After installation of the TFT Module into an enclosure, do not twist nor bend the TFT Module even momentary. At designing the enclosure, it should be taken into consideration that no bending/twisting forces are applied to the TFT Module from outside. Otherwise the TFT Module may be damaged.

12) Small amount of materials having no flammability grade is used in the LCD module. The LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.

13) Avoid touching COF position while doing mechanical design.

14) While storing modules as spares for a long time, the following precautions are necessary:

   ➢ Store modules in a dark place. Do not expose them to sunlight or fluorescent light.

   ➢ Keep the temperature between 5°C and 35°C at normal humidity.

2   General Description

This specification applies to the 24 inch-FHD color a-Si TFT-LCD Module M240HTN01.0. The display supports the FHD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 6-bit + Hi-FRC data). The light source of this TFT-LCD module is W-LED. All input signals are 2-channel LVDS interface and this module doesn't contain a driver for backlight.

2.1   Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

| Items	Unit	Specification
Screen Diagonal	[mm]	609.7 (24.0")
Active Area	[mm]	531.36 (H) x 298.89 (V)
Pixels H x V		1920(x3) x 1080
Pixel Pitch	[um]	276.75 (per one triad) ×276.75
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		TN Mode, Normally White
White Luminance (Center)	[cd/m2]	250 cd/m2 (Typ.)
Contrast Ratio		1000 (Typ.)
Optical Response Time	[msec]	5ms (Typ., on/off)
Nominal Input Voltage VDD	[Volt]	+ 5.0 V
Power Consumption (VDD line + LED line)	[Watt]	17.55 watt (Typ. without inverter, all black pattern @ 60Hz)
Weight	[g]	2,065 gram (Typ.)
Physical Size	[mm]	556.0 (W) x 323.2 (H) x 11.5(D) (Typ.)
Electrical Interface		Dual channel LVDS
Support Color		16.7M colors (RGB 6-bit + Hi_FRC)
Surface Treatment		Anti-Glare, 3H
Temperature Range Operating Storage (Shipping)	[°C] [°C]	0 to +50 -20 to +60
RoHS Compliance		RoHS Compliance
TCO Compliance		TCO 5.1 Compliance

2.2 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C:

| Item	Unit	Conditions	Min.	Typ.	Max.	Note
Viewing Angle	[degree]	Horizontal (Right)	75	85	-	2
		CR = 10 (Left)	75	85	-	
		Vertical (Up)	70	80	-	
		CR = 10 (Down)	70	80	-	
Contrast Ratio		Normal Direction	600	1000	-	3
Response Time	[msec]	Rising Time (TrR)	-	3.5	7.4	4
		Falling Time (TrF)	-	1.5	2.6	
		Rising + Falling	-	5	10	
Color / Chromaticity Coordinates (CIE)		Red x	0.614	0.644	0.674	5
		Red y	0.302	0.332	0.362	
		Green x	0.307	0.337	0.367	
		Green y	0.590	0.62	0.650	
		Blue x	0.122	0.152	0.182	
		Blue y	0.022	0.052	0.082	
Color Coordinates (CIE) White		White x	0.283	0.313	0.343	
		White y	0.299	0.329	0.359	
Central Luminance	[cd/m2]		200	250	-	6
Luminance Uniformity	[%]		75	80	-	7
Crosstalk (in 60Hz)	[%]				1.5	8
Flicker	dB				-20	9

Note 1: **Measurement Method**

The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35°C). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.
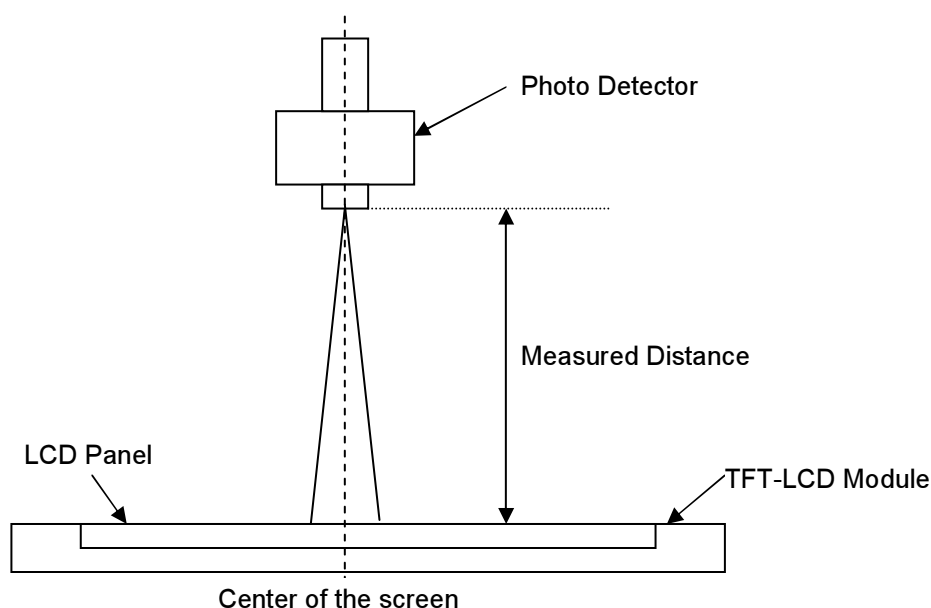
*Note 2:* **Definition of Viewing Angle** measured by ELDIM (EZContrast 88)

Viewing angle is the measurement of contrast ratio ≥ 10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (Φ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.
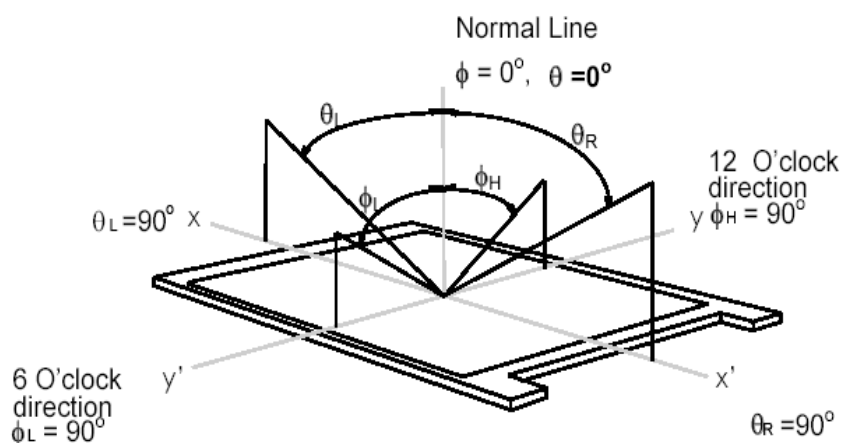
Note 3: **Contrast Ratio** measured by TOPCON SR-3

Note 4: **Response Time** measured by Westar TRD-100A

The output signals of photo detector are measured when the input signals are changed from "Full Black" to "Full White" (rising time, TrR), and from "Full White" to "Full Black" (falling time, TfF), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) of amplitudes.
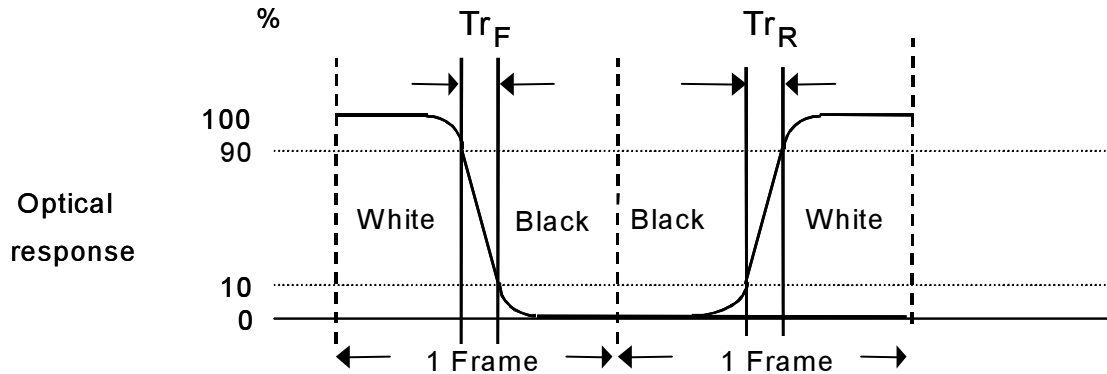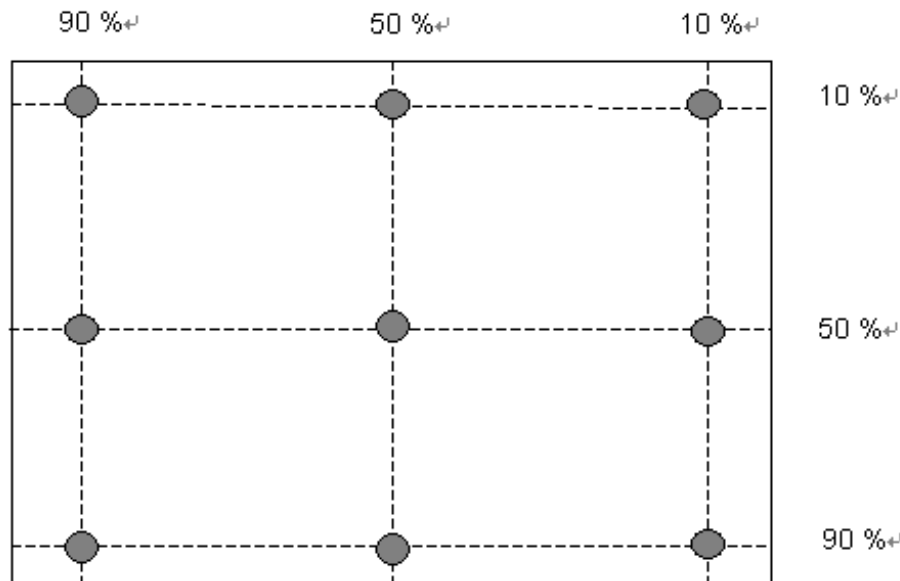

TrR + TfF = 5 msec (typ.).

Note 5: **Color Chromaticity and Coordinates (CIE)** measured by TOPCON SR-3

Note 6: **Central Luminance** measured by TOPCON SR-3

Note 7: **Luminance Uniformity** of these 9 points defined as below and measured by TOPCON SR-3

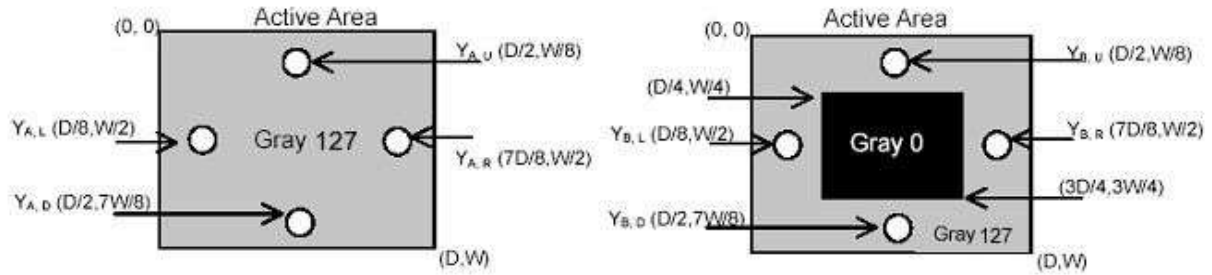$$Uniformity = \frac{Minimum\ Luminance\ in\ 9\ points\ (1\text{-}9)}{Maximum\ Luminance\ in\ 9\ Points\ (1\text{-}9)}$$

Note 8: **Crosstalk** defined as below and measured by TOPCON SR-3

CT = | YB – YA | / YA × 100 (%)
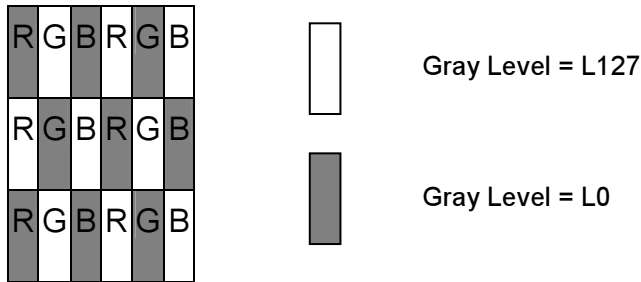
Where

YA = Luminance of measured location without gray level 0 pattern (cd/m2)

YB = Luminance of measured location with gray level 0 pattern (cd/m2)

Note 9: **Test Pattern Sub-checker Pattern** measured by TOPCON SR-3

Gray Level = L127

Gray Level = L0

Method: Record dBV & DC value with TRD-100
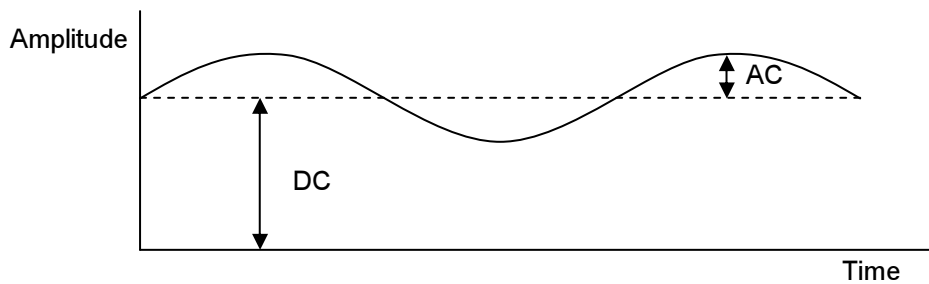
$$\text{Flicker (dB)} = 20\log\frac{\text{AC Level(at 30 Hz)}}{\text{DC Level}}$$

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3　Functional Block Diagram

The following diagram shows the functional block of the 24.0 inch Color TFT-LCD Module:

I/F PCB Interface:
FI-XPB30SRLA-HF11
01-187121-30091-3(A)
MSCK2407P30

Mating Type:
FI-X30HL(Locked Type)

4 Absolute Maximum Ratings

Absolute maximum ratings of the module are listed as follows:

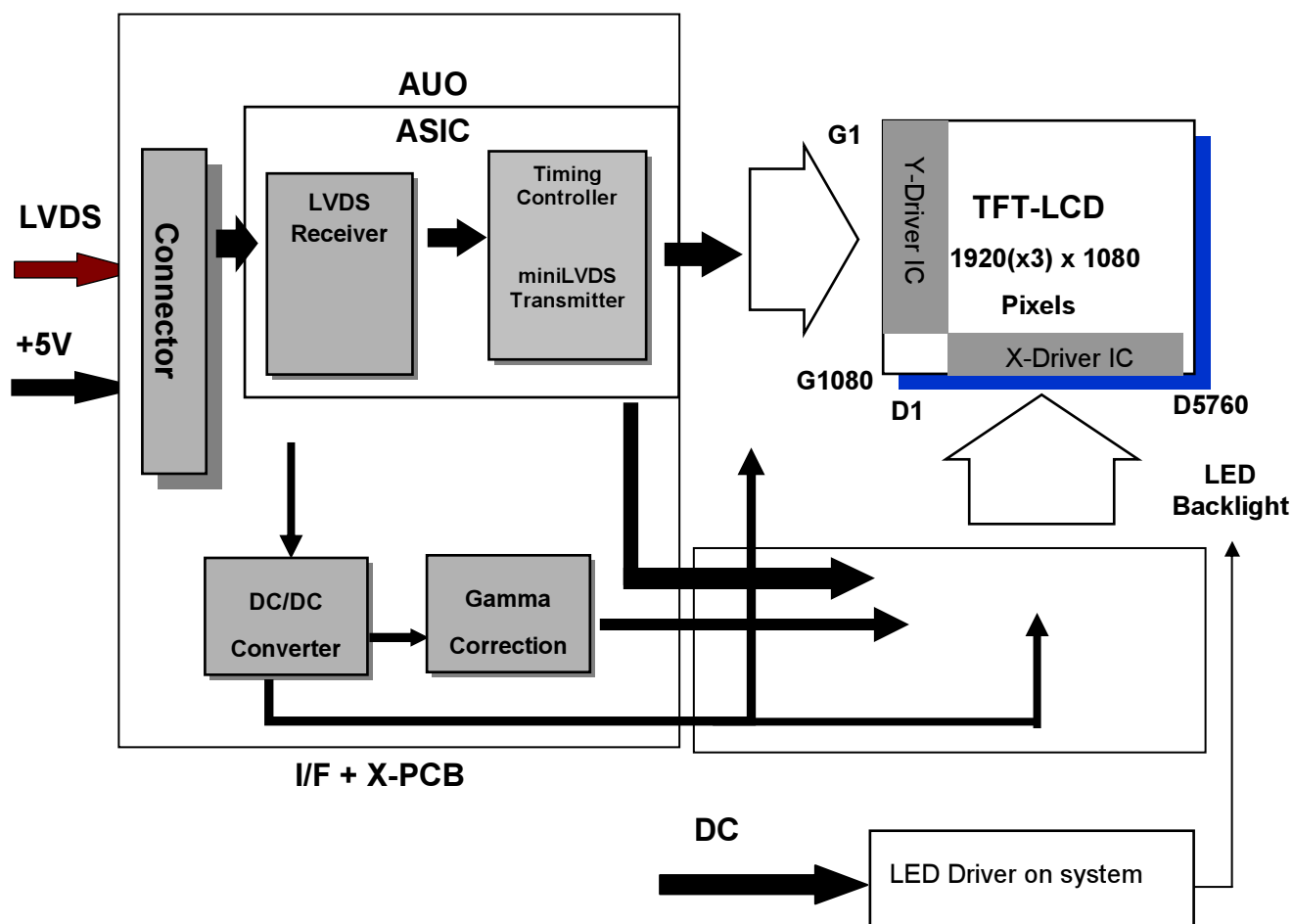## 4.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Conditions
Logic/LCD Drive	VDD	0	6.0	[Volt]	**Note 1, 2**

4.2 Backlight Unit

Item	Symbol	Min	Max	Unit	Conditions
LED Current	ILED	-	126	[mA]	**Note 1, 2**

4.3 Absolute Ratings of Environment

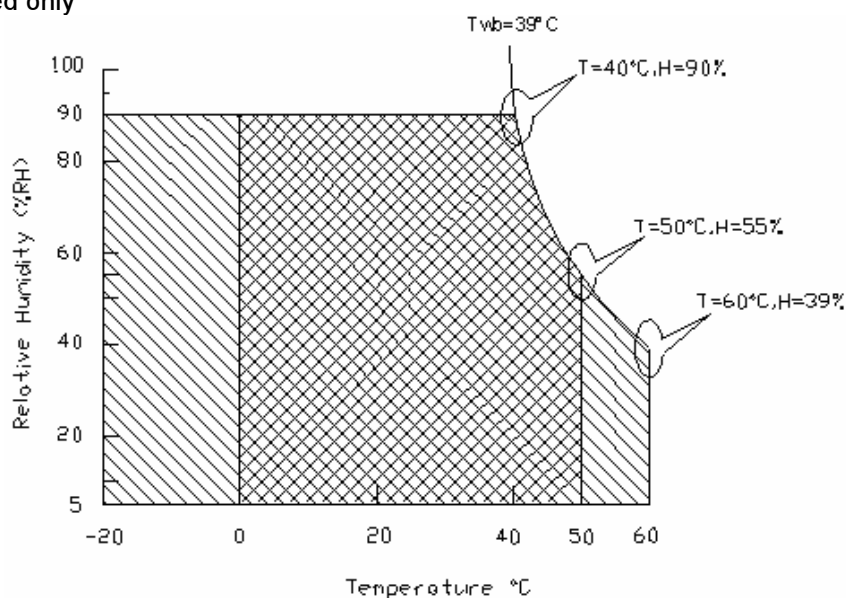| Item	Symbol	Min.	Max.	Unit	Conditions
Operating Temperature	TOP	0	+50	[°C]	**Note 3**
Glass Surface Temperature (Operation)	TGS	0	+65	[°C]	**Note3 Note4**
Operation Humidity	HOP	5	90	[%RH]	**Note 3**
Storage Temperature	TST	-20	+60	[°C]	
Storage Humidity	HST	5	90	[%RH]	

Note 1: With in Ta (25 °C)
Note 2: Permanent damage to the device may occur if exceeding maximum values
Note 3: Temperature and relative humidity range are shown as the below figure.
    1. 90% RH Max (Ta $\leqq$ 39°C)
    2. Max wet-bulb temperature at 39°C or less. (Ta $\leqq$ 39°C)
    3. No condensation
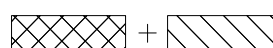*Note 4:* Function Judged only
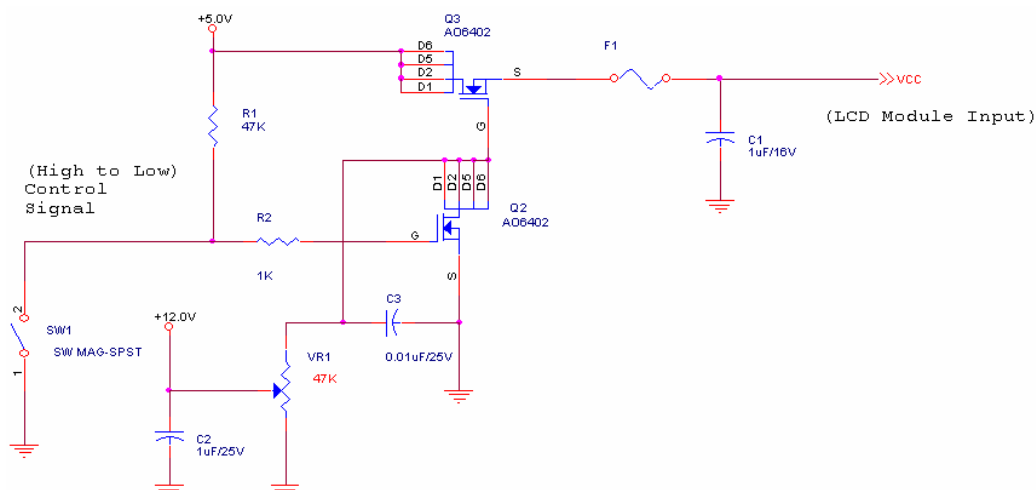
5 Electrical Characteristics

5.1 TFT LCD Module

5.1.1 Power Specification

Input power specifications are listed as follows:

Symbol	Description	Min	Typ.	Max	Unit	Conditions
VDD	Logic/LCD Drive Voltage	4.5	5.0	5.5	[Volt]	+/-10%
IDD1	Input Current	-	0.65	0.78	[A]	VDD= 5.0V, All Black Pattern at 60 Hz
		-	0.8	0.96	[A]	VDD= 5.0V, All Black Pattern at 75 Hz
PDD1	VDD Power	-	3.25	3.9	[Watt]	VDD= 5.0V, All Black Pattern at 60 Hz
		-	4	4.8	[Watt]	VDD= 5.0V, All Black Pattern at 75 Hz
IRush	Inrush Current	-	-	3	[A]	*Note 1*
VDDrp	Allowable Logic/LCD Drive Ripple Voltage	-	-	500	[mV] p-p	VDD= 5.0V, All Black Pattern at 75 Hz

Note 1: **Measurement Conditions:**

The duration of rising time of power input is 470 us.
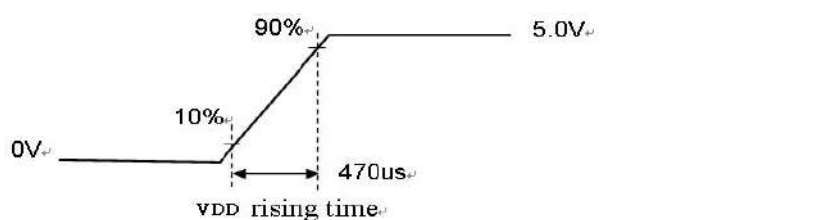
5.1.2 Signal Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off. Please refer to specifications of SN75LVDS82DGG (Texas Instruments) in detail.

1) Characteristics of each signal are listed as follows:

Symbol	Description	Min	Typ	Max	Units	Conditions
V_{TH}	Differential Input High Threshold	-	+50	+100	[mV]	V_{CM} = 1.2V **Note 1**
V_{TL}	Differential Input Low Threshold	-100	-50	-	[mV]	V_{CM} = 1.2V **Note 1**
$\|V_{ID}\|$	Input Differential Voltage	100	-	600	[mV]	**Note 1**
V_{CM}	Differential Input Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH}-V_{TL} = 200MV (max) **Note 1**

Note 1: LVDS Signal Waveform
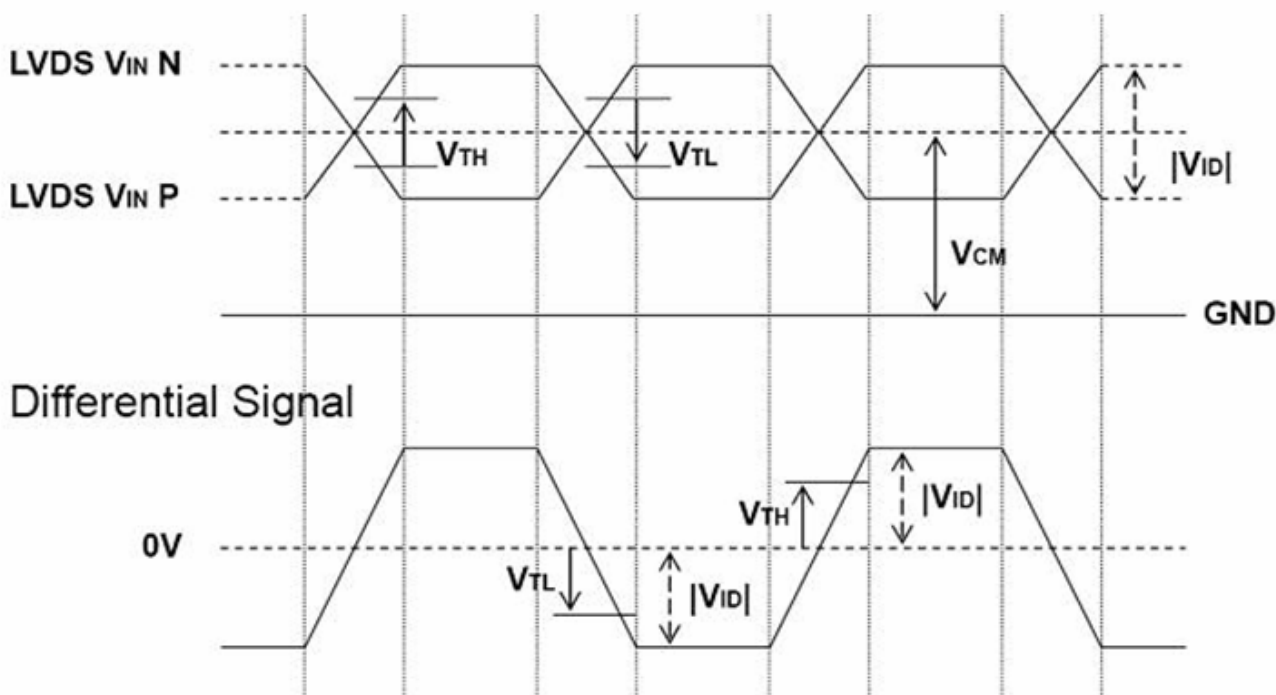
2) AC Characteristics

Symbol	Description	Min	Max	Units	Conditions
F_{DEV}	Maximum deviation of input clock frequency during SSC	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during SSC	-	300	KHz	
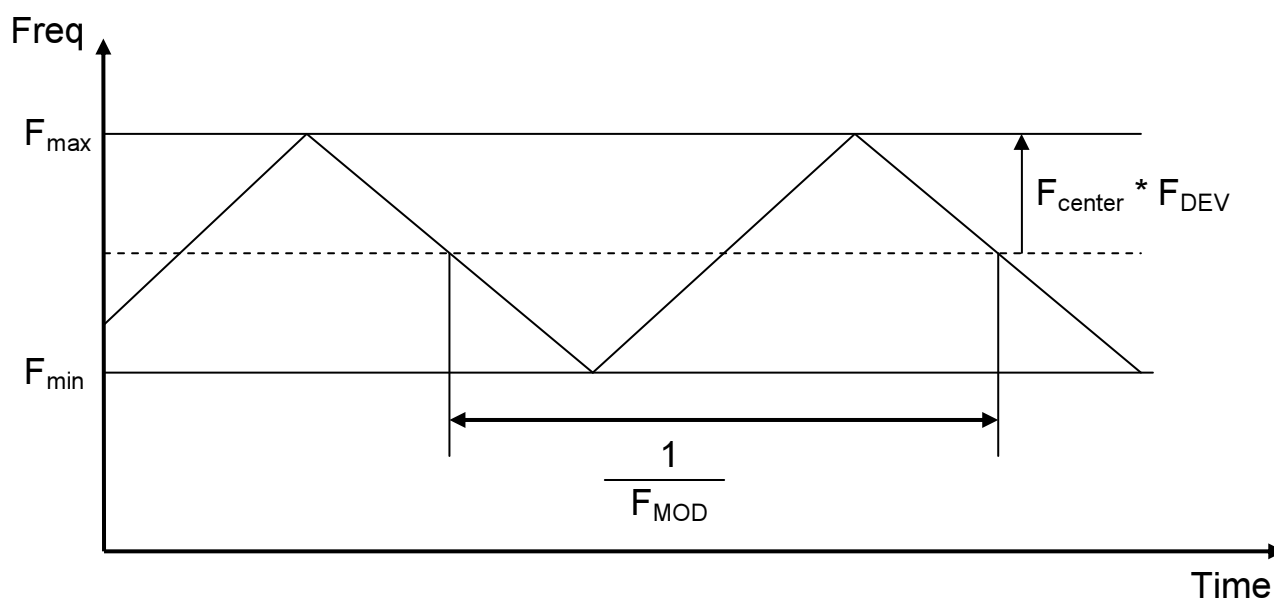
< Spread Spectrum>

5.2 Backlight Unit

Parameter guideline for LED driving is under stable conditions at 25 °C (Room Temperature):

Symbol	Description	Min.	Typ.	Max.	Unit	Note
IRLED	LED Operation Current	-	120	126	[mA] *Note 1*	Operating in fixed driving current
VLB	Light Bar Operation Voltage (for reference)	-	29.7	32.4	[Volt] *Note 2*	
PBLU	BLU Power Consumption (for reference)	-	14.3	16.3	[Watt]	
LTLED	LED Life Time (Typical)	30,000	-	-	[Hour] *Note 3*	

Note 1: The specified current is 100% duty of LED chip input current.

Note 2: The value showed is one light bar's operation voltage.

Note 3: Definition of life time: Brightness becomes to 50% of its original value. The minimum life time of LED unit is on the condition of IR_{LED} = 120mA and 25±2°C (Room Temperature).
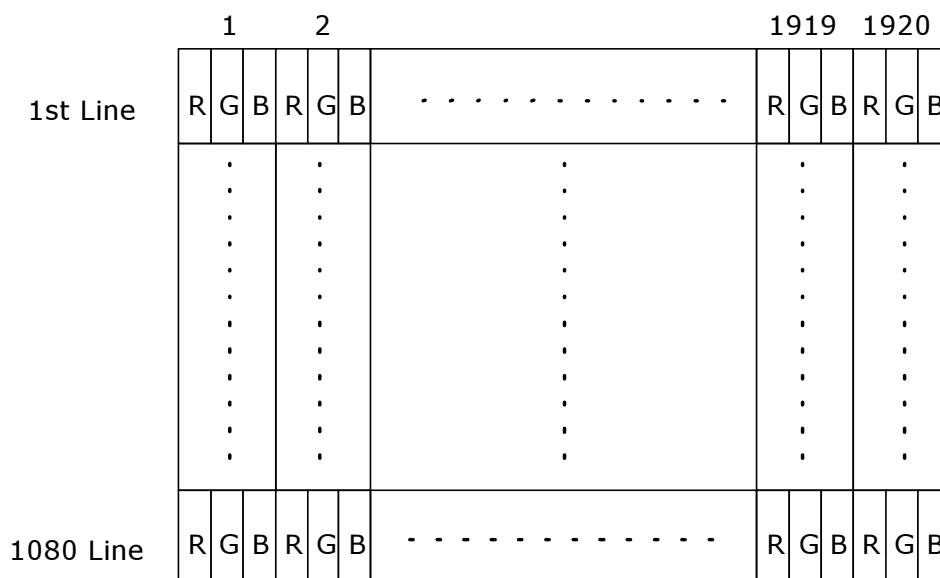
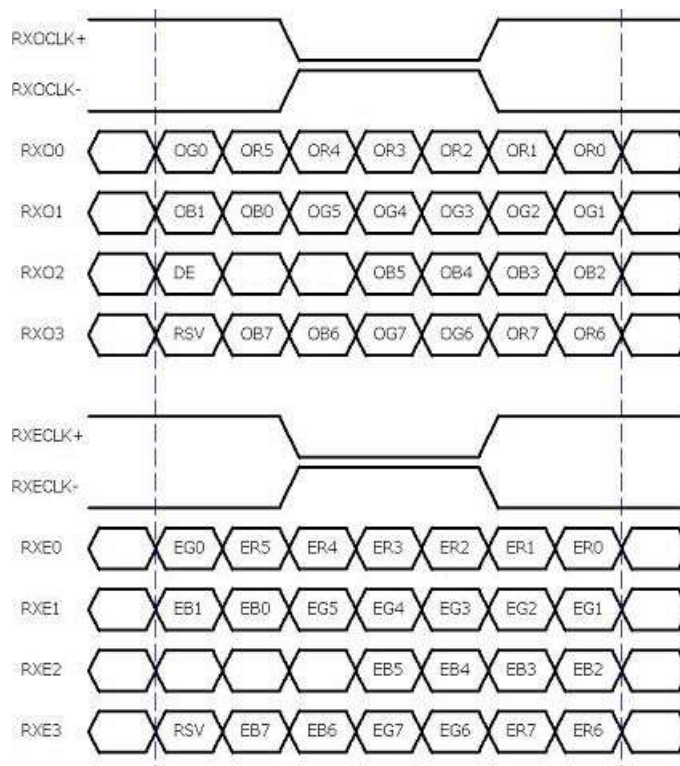**Note 4:** Each module consists of 36 pcs LED (4 strings x 9 pcs / string)

6 Signal Characteristics

6.1 Pixel Format Definition

Following figure shows the relationship between the input signals and LCD pixel format.

6.2 Input Data Format Definition

Note 1: R/G/B data 7:MSB, R/G/B data 0:LSB   O = "Odd Pixel Data"   E = "Even Pixel Data"

6.3 Signal Description

The module uses one LVDS receiver SN75LVDS82 (Texas Instruments). LVDS is a differential signal technology and high speed data transfer interface for LCD device. LVDS transmitter shall be SN75LVDS83 (negative edge sampling). The first LVDS port (RxOxxx) transmits odd pixels while the second LVDS port (RxExxx) transmits even pixels.

PIN #	Signal Name	Description
1	RxOIN0-	Negative LVDS differential data input (Odd data)
2	RxOIN0+	Positive LVDS differential data input (Odd data)
3	RxOIN1-	Negative LVDS differential data input (Odd data)
4	RxOIN1+	Positive LVDS differential data input (Odd data)
5	RxOIN2-	Negative LVDS differential data input (Odd data, DSPTMG)
6	RxOIN2+	Positive LVDS differential data input (Odd data, DSPTMG)
7	GND	Power Ground
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)
10	RxOIN3-	Negative LVDS differential data input (Odd data)
11	RxOIN3+	Positive LVDS differential data input (Odd data)
12	RxEIN0-	Negative LVDS differential data input (Even data)
13	RxEIN0+	Positive LVDS differential data input (Even data)
14	GND	Power Ground
15	RxEIN1-	Negative LVDS differential data input (Even data)
16	RxEIN1+	Positive LVDS differential data input (Even data)
17	GND	Power Ground
18	RxEIN2-	Negative LVDS differential data input (Even data)
19	RxEIN2+	Positive LVDS differential data input (Even data)
20	RxECLK-	Negative LVDS differential clock input (Even clock)
21	RxECLK+	Positive LVDS differential clock input (Even clock)
22	RxEIN3-	Negative LVDS differential data input (Even data)
23	RxEIN3+	Positive LVDS differential data input (Even data)
24	GND	Power Ground
25	NC	No connection (for AUO test only. Do not connect)
26	NC	No connection (for AUO test only. Do not connect)
27	NC	No connection (for AUO test only. Do not connect)
28	VDD	Power +5V
29	VDD	Power +5V
30	VDD	Power +5V

Note 1: LVDS connector Pin 1 starts from left side
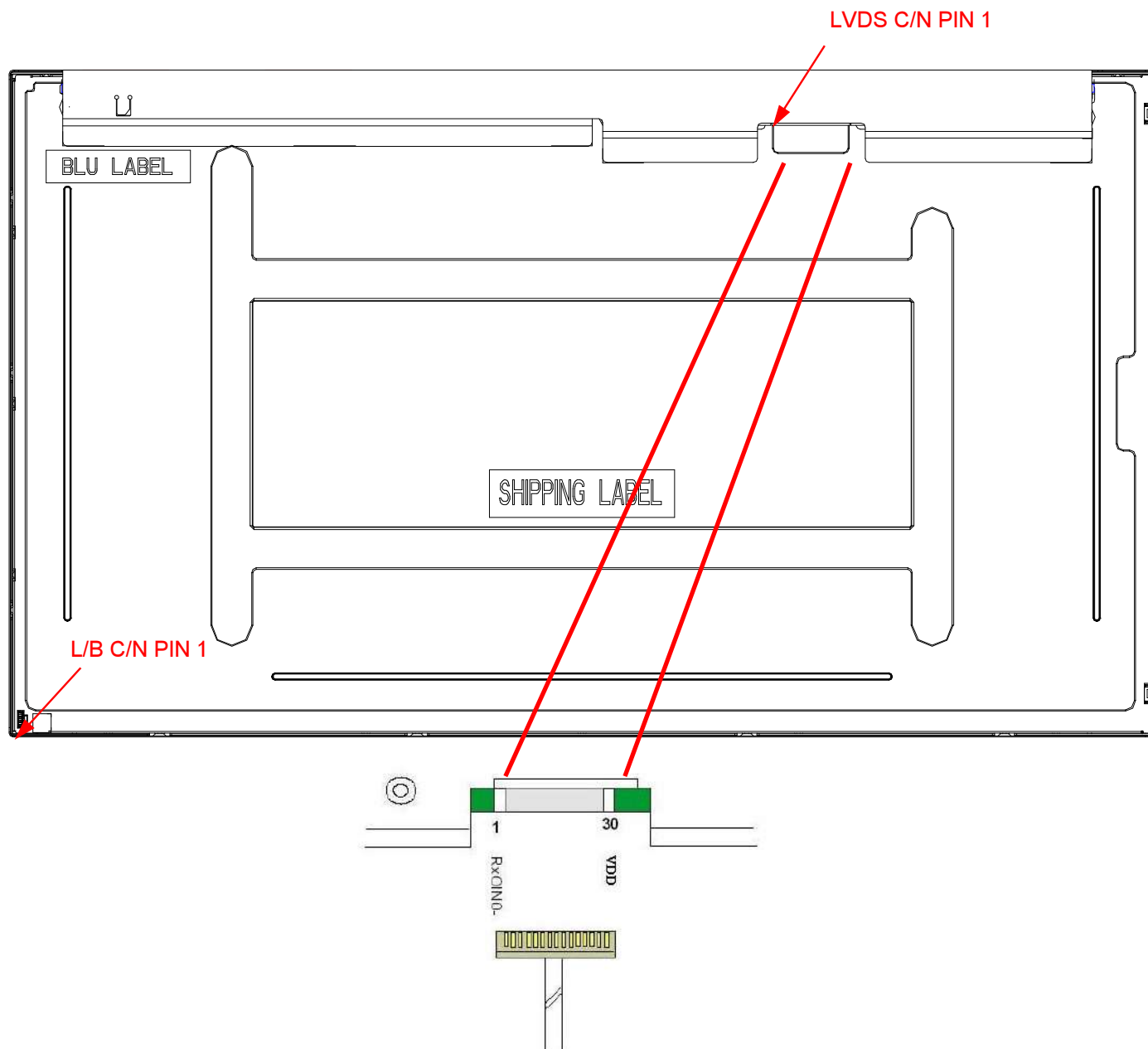
Note 2: Input signals of odd and even clock shall be the same timing.

6.4 Timing Characteristics

Basically, interface timing described here is not actual input timing of LCD module but close to output timing of SN75LVDS82DGG (Texas Instruments) or equivalent.

| Item		Symbol	Min	Typ	Max	Unit
V-section	Period	Tv	1088	1120	2047	[Th]
	Display Area	Tdisp(v)	1080	1080	1080	[Th]
	Blanking	Tbp(v)+Tfp(v)+PWvs	8	40	967	[Th]
H-section	Period	Th	1034	1060	2047	[Tclk]
	Display Area	Tdisp(h)	960	960	960	[Tclk]
	Blanking	Tbp(h)+Tfp(h)+PWhs	74	100	1087	[Tclk]
Data CLK	Period	Tclk	25	13.3	11.1	ns
	Frequency	Freq	40	75	94	[MHz]
Frame Rate		F	50	60	75	[Hz]

Note 1: DE mode only

6.5 Timing Diagram
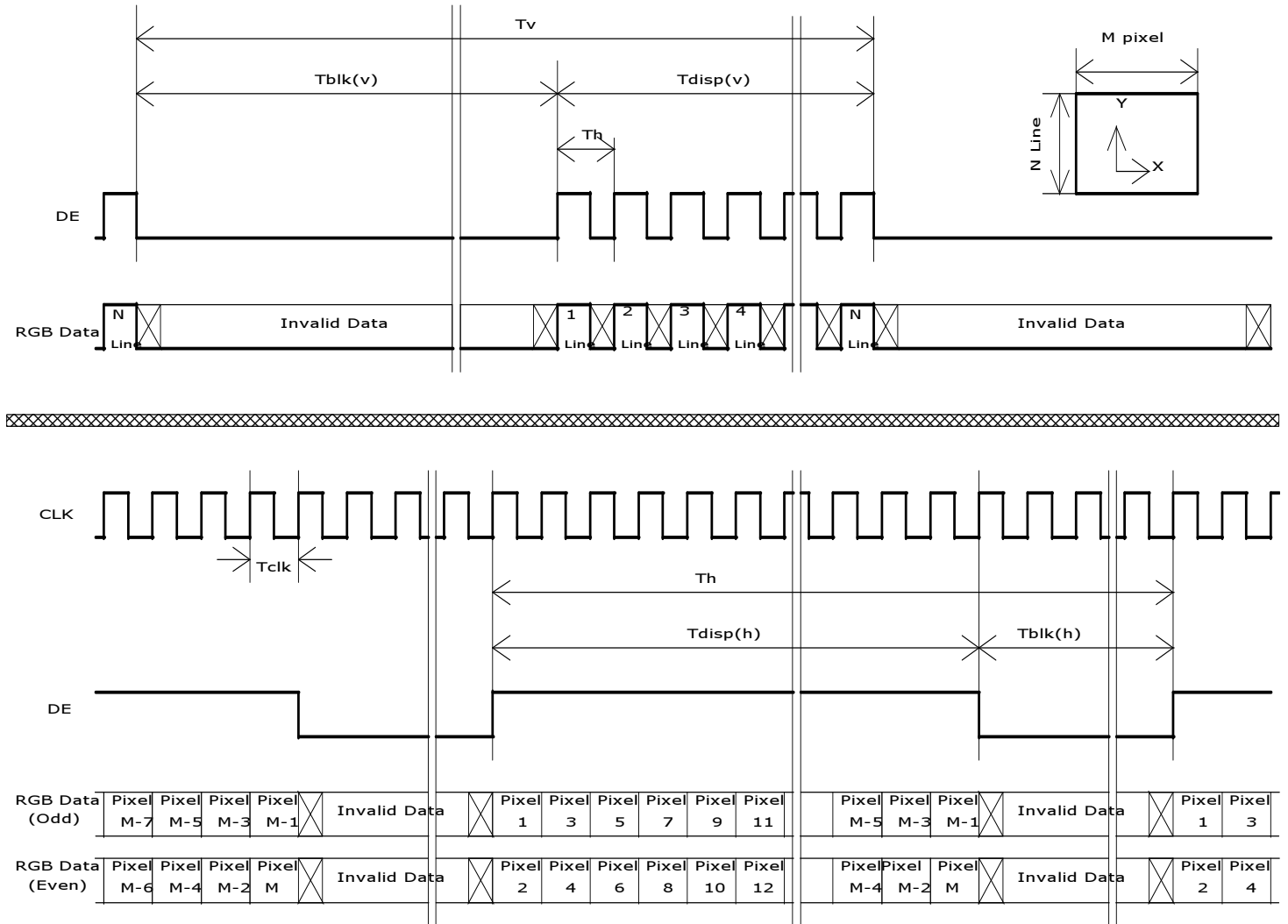
6.6 Power ON/OFF Sequence

VDD power and backlight power on/off sequence are specified as follows. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.
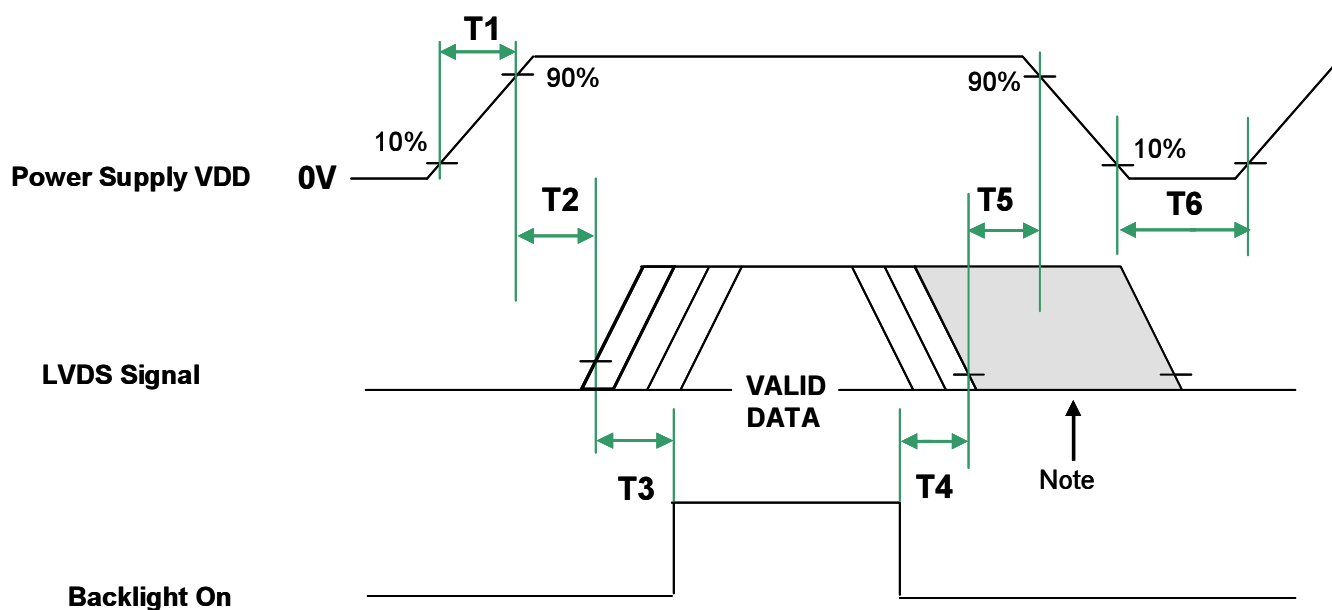
Note : Insert a white pattern after valid data and last until VDD falls to 10%

Parameter	Value		Unit
	Min.	Max.	
T1	0.5	10	[msec]
T2	0	50	[msec]
T3	500	-	[msec]
T4	200	-	[msec]
T5	40	-	[msec]
T6	1000	-	[msec]

7 Connector and Pin Assignment

Physical interface is described as follows for the connector on module. These connectors are capable of accommodating the following signals and will be following components.

7.1 TFT LCD Module

Connector Name / Designation	Interface Connector / Interface Card
Manufacturer	JAE P-TWO STM
Type Part Number	FI-XPB30SRLA-HF11 01-187121-30091-3(A) MSCK2407P30
Mating Housing Part Number	FI-X30HL (Locked Type)

7.1.1 LVDS Connector Pin Assignment

| Pin#	Signal Name	Pin#	Signal Name
1	RxOIN0-	2	RxOIN0+
3	RxOIN1-	4	RxOIN1+
5	RxOIN2-	6	RxOIN2+
7	GND	8	RxOCLKIN-
9	RxOCLKIN+	10	RxOIN3-
11	RxOIN3+	12	RxEIN0-
13	RxEIN0+	14	GND
15	RxEIN1-	16	RxEIN1+
17	GND	18	RxEIN2-
19	RxEIN2+	20	RxECLKIN-
21	RxECLKIN+	22	RxEIN3-
23	RxEIN3+	24	GND
25	NC (for AUO test only. Do not connect)	26	NC (for AUO test only. Do not connect)
27	NC (for AUO test only. Do not connect)	28	VDD
29	VDD	30	VDD

7.2 Backlight Unit

This connector is mounted on LED light bar.

Connector Name / Designation	Light Bar Connector
Manufacturer	ENTERY INDUSTRIAL CO., LTD.
Type Part Number	3707K-S06N-07 (Locking Type, Wire Harness)
Mating Type Part Number (Housing)	(1)H112K-P06N-03B(Black, Length: 7.3mm) (2)H112K-P06N-01B(White Length: 7.3mm) (3)H112K-D06N-04B(White; Length: 6.8mm) (4)H112K-D06N-05B (Black; Length: 6.8mm) or equivalent

7.2.1 Light Bar Connector Pin Assignment

Pin#	Signal Name
1	IRLED (current out)
2	IRLED (current out)
3	VLED (voltage in)
4	VLED (voltage in)
5	IRLED (current out)
6	IRLED (current out)

7.2.2 Light Bar Connector Dimension
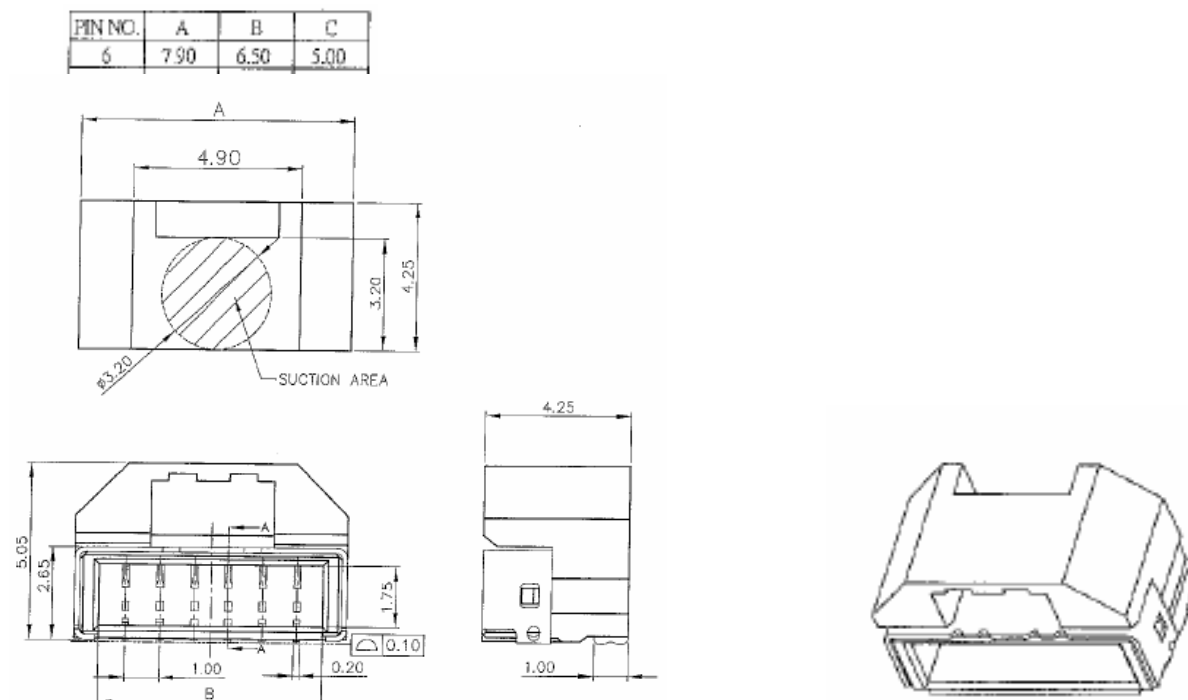
8 Reliability Test

Environment test conditions are listed as following table.

Items	Required Condition	Note
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300 hours	
High Temperature Operation (HTO)	Ta= 50°C, 50%RH, 300 hours	
Low Temperature Operation (LTO)	Ta= 0°C, 300 hours	
High Temperature Storage (HTS)	Ta= 60°C, 300 hours	
Low Temperature Storage (LTS)	Ta= -20°C, 300 hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Duration: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Drop Test	Height: 46 cm, package test	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	*Note 1*
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 8KV, 150pF(330Ω) 1sec, 15 points, 25 times/ point Air Discharge: ± 15KV, 150pF(330Ω) 1sec 15 points, 25 times/ point	*Note 2*
Altitude Test	Operation: 10,000 ft Non-Operation: 30,000 ft	

Note 1: The TFT-LCD module will not sustain damage after being subjected to 100 cycles of rapid temperature change. A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again. Power is not applied during the test. After temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.
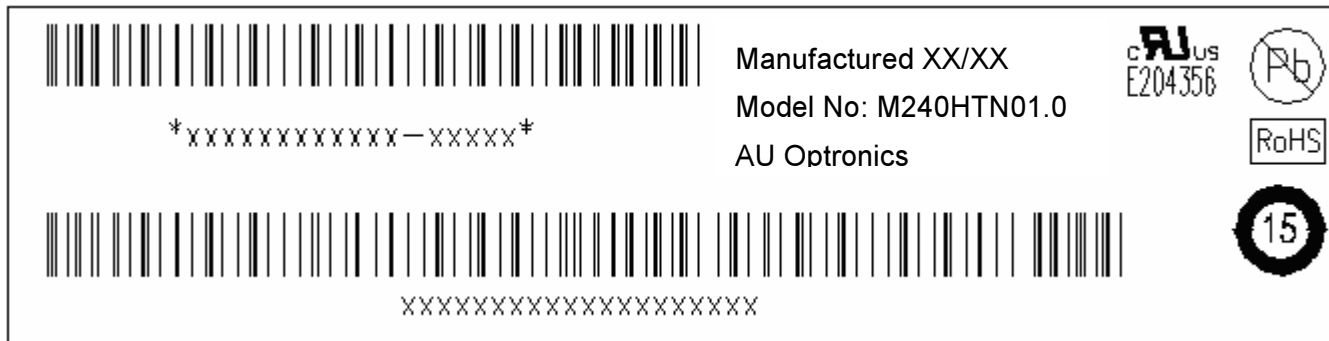
Note 2: EN61000-4-2, ESD class B: Certain performance degradation allowed:

> ➢ No data lost
> ➢ Self-recoverable
> ➢ No hardware failures

9 Shipping Label

The label on the panel is shown as below:

Manufactured XX/XX

Model No: M240HTN01.0

AU Optronics

xxxxxxxxxxxx—xxxxx

xxxxxxxxxxxxxxxxxxxxx

cRUus
E204356

(Pb)

RoHS

15

Note 1: For Pb Free products, AUO will add (Pb) for identification.

Note 2: For RoHS compatible products, AUO will add RoHS for identification.

Note 3: For China RoHS compatible products, AUO will add 15 for identification.

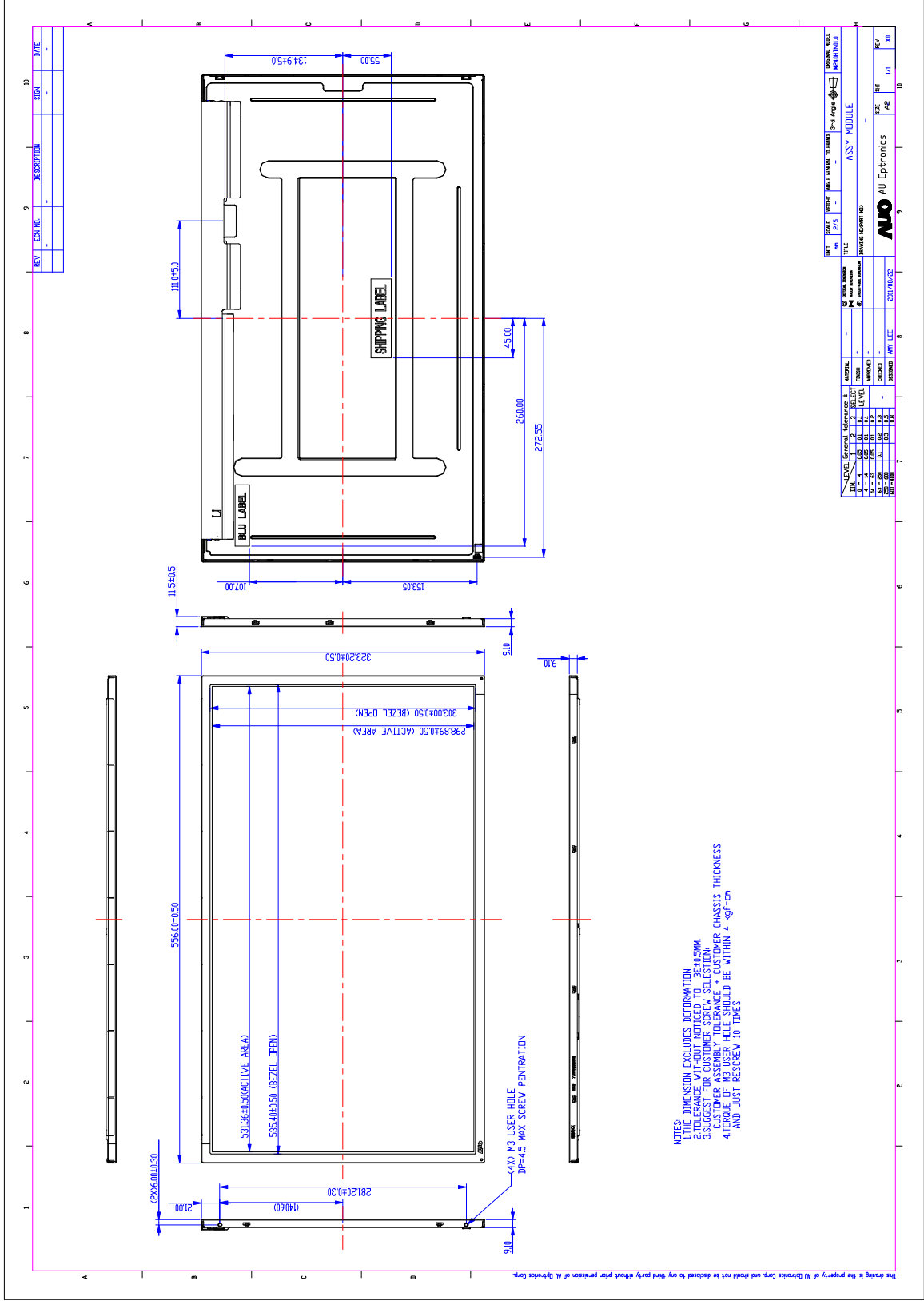***Note 4:*** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.
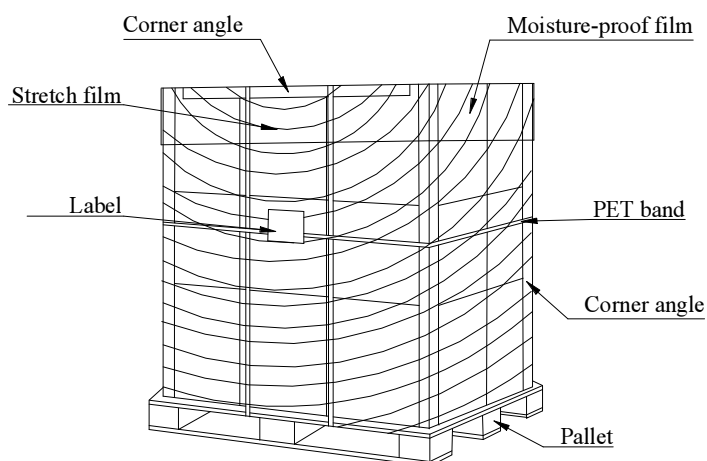
Product Specification

AU OPTRONICS CORPORATION

M240HTN01.0
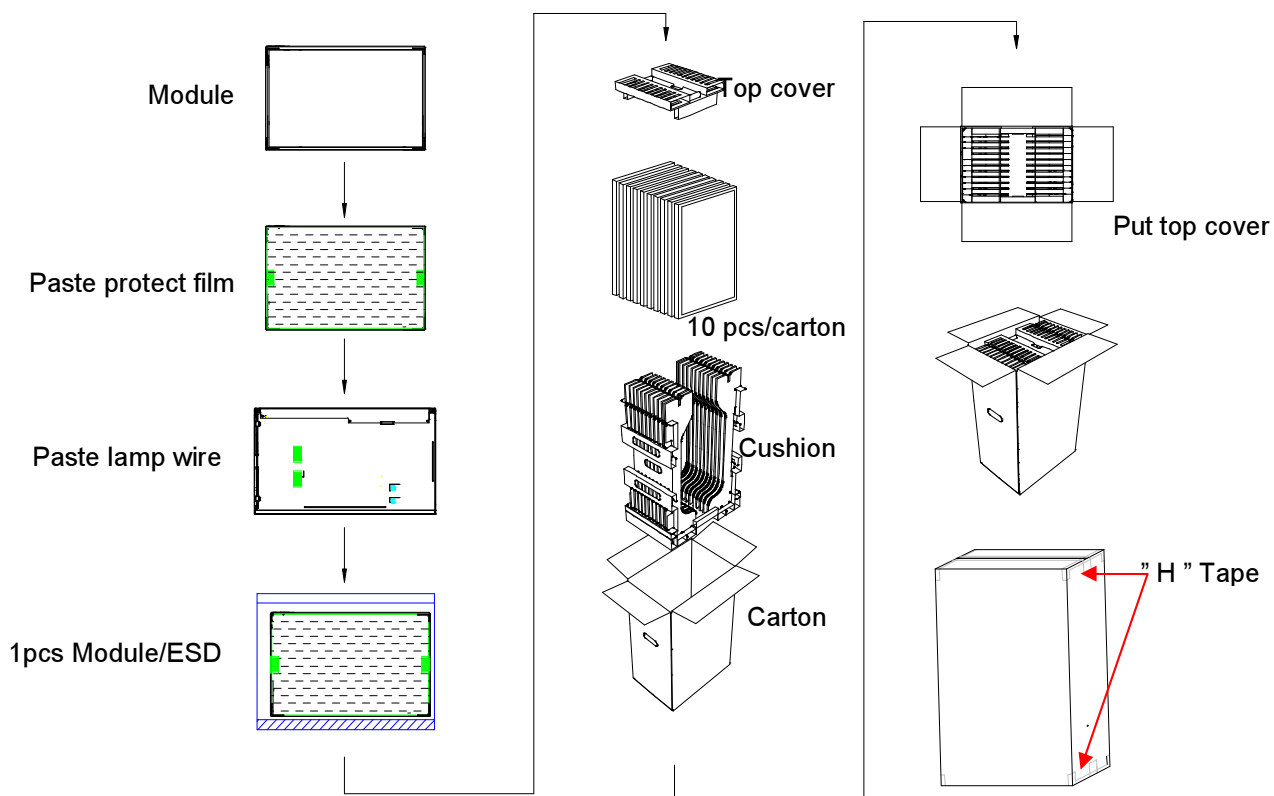
10 Mechanical Characteristics

Product Specification

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document version 1.0

11 Packing Specification

11.1 Packing Flow

11.2 Pallet and Shipment Information

	Item	Specification			Remark
		Q'ty	Dimension	Weight(kg)	
1	Panel	1	556(H)mm x 323.2(V)mm x 9.08(D)mm	2.065	*Note 1*
2	Cushion	1	--	1.7	
3	Box	1	406(L)mm x 281(W)mm x 651(H)mm	1.2	without Panel & cushion *Note 1*
4	Packing Box	10 pcs/Box	406(L)mm x 281(W)mm x 651(H)mm	23.55	with panel & cushion *Note 1*
5	Pallet	1	1150(L)mm x 840(W)mm x 138(H)mm	12	*Note 1*
6	Pallet after Packing	16 boxes/pallet	1140(L)mm x 830(W)mm x 1440(H)mm	388.8	*Note 1*

Note 1: Estimated value which is subject to change based on real measured data.