



) Preliminary Specification

Please return 1 copy for your confirmation with

your signature and comments.

) Final Specification

SPECIFICATION FOR APPROVAL

Title			10).1"	WSVGA TFT	LCD	
Customer]		SUPPLIER	LG Disp	olay Co., Ltd.
MODEL	LF	2101WS1-TLB1	1	*MODEL		LP101V	VS1
					Suffix	TLB1	
					hen you obtain star ease use the above		
APPROVED B	Υ	SIGNATURE			APPROVED	вү	SIGNATURE
,					G. J. Kwon/ Ma	anager	
					REVIEWED	ВҮ	
					C. J. Jun / Ma	nager	
/					PREPARED	ВҮ	

Ver. 0.0 July. 23, 2008 1 / 27

P. A. Choi / Engineer

Products Engineering Dept. LG Display Co., Ltd



Contents

No	ITEM	Page
	COVER	1
	CONTENTS	2
	RECORD OF REVISIONS	3
1	GENERAL DESCRIPTION	4
2	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	
3-1	ELECTRICAL CHARACTREISTICS	6
3-2	INTERFACE CONNECTION	7
3-3	LVDS SIGNAL TIMING SPECIFICATIONS	8
3-4	SIGNAL TIMING SPECIFICATIONS	10
3-5	SIGNAL TIMING WAVEFORMS	10
3-6	COLOR INPUT DATA REFERNECE	11
3-7	POWER SEQUENCE	12
4	OPTICAL SFECIFICATIONS	13
5	MECHANICAL CHARACTERISTICS	16
6	RELIABLITY	20
7	INTERNATIONAL STANDARDS	
7-1	SAFETY	21
7-2	EMC	21
8	PACKING	
8-1	DESIGNATION OF LOT MARK	22
8-2	PACKING FORM	22
9	PRECAUTIONS	23
А	APPENDIX. Enhanced Extended Display Identification Data	25



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	July. 11. 2008	All	First Draft (Preliminary Specification)	-
		[

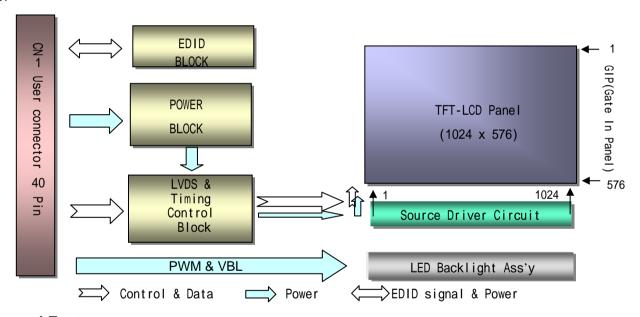


1. General Description

The LP101WS1 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 10.1inches diagonally measured active display area with WSVGA resolution(1024 horizontal by 576 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262.144 colors.

The LP101WS1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP101WS1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP101WS1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	10.1 inches diagonal
Outline Dimension	235(H) × 143(V) × .5.2(D,Max.) [mm]
Pixel Pitch	0.2175mm × 0.2175 mm
Pixel Format	1024 horiz. By 576 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.5 point)
Power Consumption	180
Weight	190g (Max.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Anti-glare treatment of the front polarizer
RoHS Comply	Yes



2. Absolute Maximum Ratings

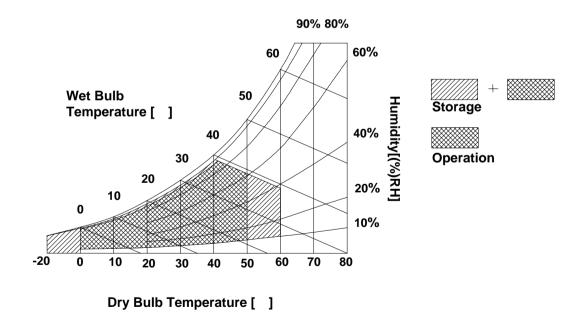
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Min Max		Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 0.0 July. 23, 2008 5 / 27



3. Electrical Specifications

3-1. Electrical Characteristics

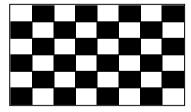
The LP101WS1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Cumbal			Unit	Notes		
Parameter	Symbol	Min	Min Typ		Offic	Notes	
MODULE :							
Power Supply Input Voltage	VCC				V_{DC}		
Power Supply Input Current	I _{cc}		B		mA	1	
Power Consumption	Pc				Watt	1	
Differential Impedance	Zm	90	100	110	Ohm	2	
LED Backlight (With LED Driver):							
LED Driver (@5V)	P _{DRIVER}				Watt		
Operating Voltage	V_{LED}		•		V		
Operating Current per string	I _{LED}		18V		mA	3	
Power Consumption	P _{BL}				Watt	4	
Life Time					Hrs	5	

Note)

1. The specified current and power consumption are under the Vcc = 3.3V, 25 , fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics. I_{LED} is the current of each LED's string, LED backlight has 6 strings on it.
- The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
- 5. The life time is determined as the time at which brightness of LED is 50% compare to that of initial value at the typical LED current.



3-2. Interface Connection

This LCD employs one interface connection, a 40 pin connector is used for the module electronics interface. The electronics interface connector is a model 20455-040E-0* manufactured by I-PEX.

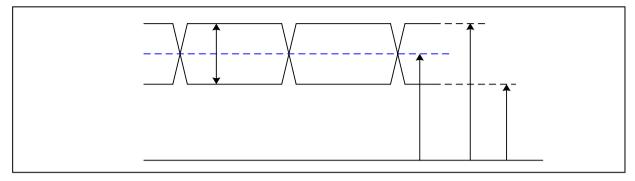
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	CT1/NC	Connector Test/No Connection(Reserved)	110100
12	VDD	+3.3V Power Supply	
3	VDD	+3.3V Power Supply	
4		+3.3V FOWER Supply	4 lataria a abina
1	V _{EDID} NC		1, Interface chips 1.1 LCD: SiW, 1port including
5	I -	No Connection	LVDS Receiver
6	CLK _{EDID}	EDID Clock Input	1.2 System :
7	DATA _{EDID}	EDID Data Input	* Pin to Pin compatible with LVDS
8	RxIN0-	LVDS differential data input	2. Connector
9	RxIN0+	LVDS differential data input	2.1 LCD :I-PEX 20455-040E-0*
10	GND	Ground	(Locking type)
11	RxIN1-	LVDS differential data input	or equivalent
12	RxIN1+	LVDS differential data input	2.2 Mating : TBD
13	GND	Ground	2.3 Connector pin arrangement
14	RxIN2-	LVDS differential data input	
15	RxIN2+	LVDS differential data input	40 П ПП П
16	GND	Ground	[11 11
17	RxCLKIN-	LVDS differential clock input	
18	RxCLKIN+	LVDS differential clock input	[LCD Module Rear View]
19	GND	Ground	[LOD Module Real view]
20	NC NC	No Connection	
21	NC	No Connection	
22	GND	Ground	
23	NC	No Connection	
24	NC	No Connection	
25	GND	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	GND	Ground	
29	NC	No Connection	
30	NC	No Connection	
31	VLED_GND	LED Ground	
1			
32	VLED_GND	LED Ground	
33	VLED_GND	LED Ground	
34	C12/NC	Connector Test/No Connection(Reserved)	
35	S_PWMIN	System PWM signal input	
36	BL_ON or NC	LED Enable or No Connection[Note 1]	
37	NC	No Connection	
38	VLED	+6V~+18V LED Power Supply	[Note 1]
39	VLED	+6V~+18V LED Power Supply	Let this pin NC if this pin is not used.
40	VLED	+6V~+18V LED Power Supply	(On: 2.0V ,Off:0~0.4V)



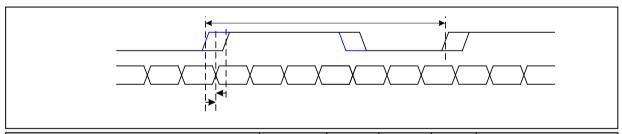
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



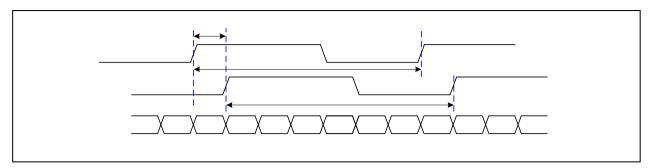
Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	V _{ID}	100	600	mV	-
LVDS Common mode Voltage	V _{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

3-3-2. AC Specification

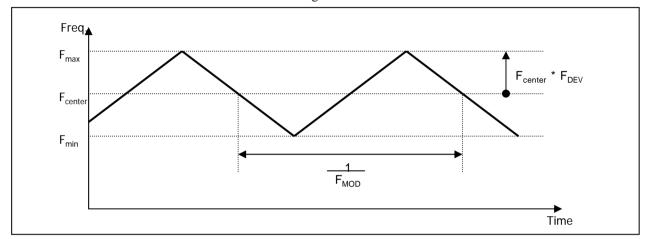


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{SKEW}	- 400	+ 400	ps	Eclk 65MHz
LVDS Clock to Data Skew Margin	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	ı	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	VDS +





< Clock skew margin between channel >

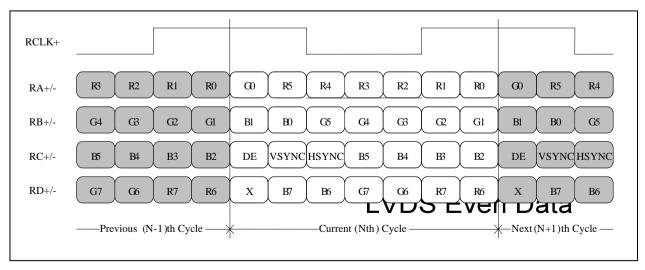


< Spread Spectrum >

3-3-3. Data Format

- LVDS 1 Port

LVDS Odd Clock



< LVDS Data Format >



3-4. Signal Timing Specifications

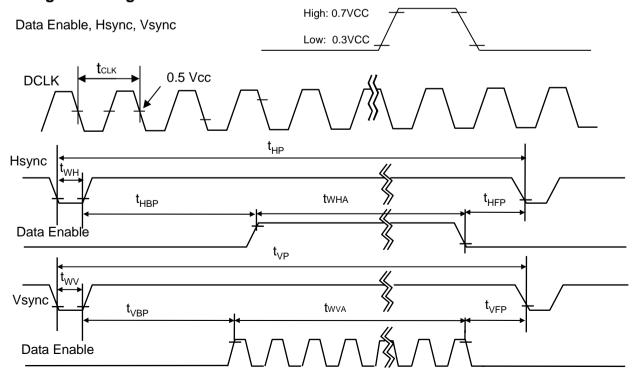
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 5. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}				MHz	
	Period	Thp					
Hsync	Width	t _{WH}				tCLK	
	Width-Active	t _{WHA}					
	Period	t _{VP}					
Vsync	Width	t _{wv}		TBD		tHP	
	Width-Active	t _{wva}					
	Horizontal back porch	t _{HBP}				+C1 V	
Data	Horizontal front porch	t _{HFP}				tCLK	
Enable	Vertical back porch	t _{VBP}				+I ID	
	Vertical front porch	t _{VFP}				tHP	









3-6. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
		MSE					LSB						LSB						LSB
	I	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	В 0
	Black	0	0			0	0	0	0		0	0	0	0	0	0	0	0	0
	Red	1 	. 1 	1	1	1	1	0 	0		0	0	0	0				0	0
	Green	0					0	1 	1				1	0				0	0
Basic	Blue	0	0	0		0	0	0	0	0	0	0	0	1					
Color	Cyan	0	0	0		0	0	1	1	. 1	1		1	1	1	.1	. 1	1	1
	Magenta	1	1	1	. 1	1		0	0	0	0	0	0	1	1	1	. 1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		ļ			 												 		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



3-7. Power Sequence

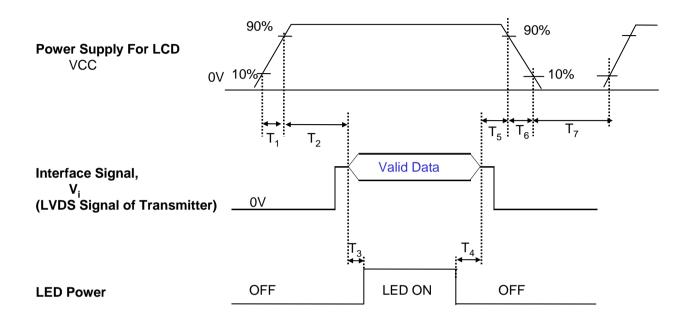


Table 7. POWER SEQUENCE TABLE

Parameter		Value	Units	
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. LED power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to Θ .

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

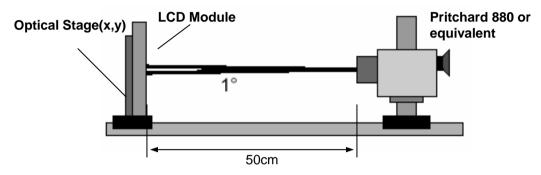


Table 8. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, $f_V=60Hz$, $f_{CLK}=69.3MHz$, $I_{RL}=17$ mA

		•	Values	3-0.01,11-00	T IZ, ICLK	09.3MHZ, I _{BL} = 17 HIA	
Parameter	Symbol	Min				Notes	
Contrast Ratio	CR		Тур			1	
Surface Luminance, white	L _{WH}		200	-	cd/m ²	2	
Luminance Variation	δ _{WHITE}	-	1.4	1.6]	3	
Response Time	Tr _R + Tr _D	-	16	25	ms	4	
Color Coordinates]		
RED	RX						
	RY						
GREEN	GX		TBD				
	GY						
BLUE	ВХ						
	BY						
WHITE	WX	0.283	0.313	0.343			
	WY	0.299	0.329	0.359			
Viewing Angle]	5	
x axis, right(Φ=0°)	Θr	30	-		degree		
x axis, left (⊕=180°)	ΘΙ	30	.	. .	degree		
y axis, up (Φ=90°)	Θu	10	-	-	degree		
y axis, down (Φ=270°)	Θd	20		.	degree		
Gray Scale			2.2			6	



Note)

1. Contrast Ratio(CR) is defined mathematically as

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, ... L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13})}{\text{Minimum}(L_1, L_2, \dots L_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

*
$$f_{V} = 60$$
Hz

Gray Level	Luminance [%] (Typ)
LO	
L7	
L15	
L23	₹ \$0
L31	48.
L39	
L47	
L55	
L63	



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

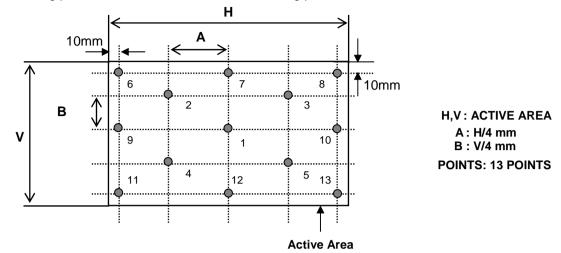


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

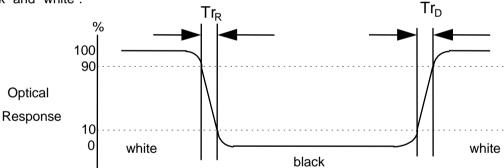
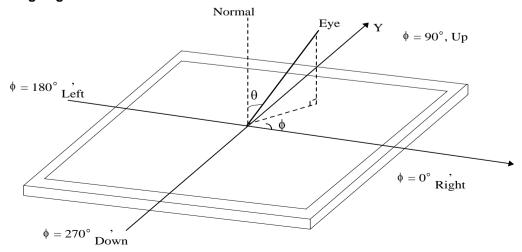


FIG. 4 Viewing angle





5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP101WS1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

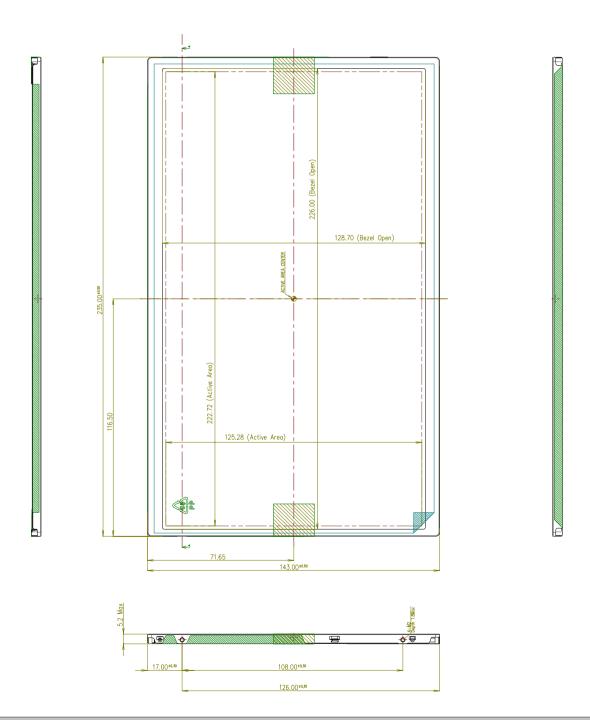
	Horizontal	235.0 ± 0.5 mm				
Outline Dimension	Vertical	143.0 ± 0.5 mm				
	Thickness	5.2mm (max)				
Bezel Area	Horizontal	226.00 mm				
Dezei Area	Vertical	128.70 mm				
Active Display Area	Horizontal	222.72 mm				
Active Display Area	Vertical	125.28 mm				
Weight	190g (Max.)					
Surface Treatment	Anti-glare treatment of the front polarizer					



<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm

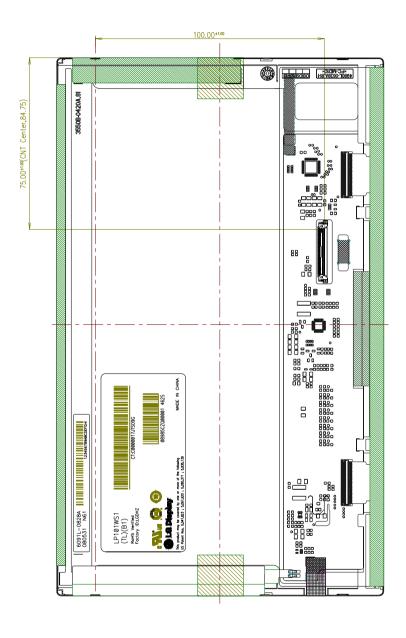






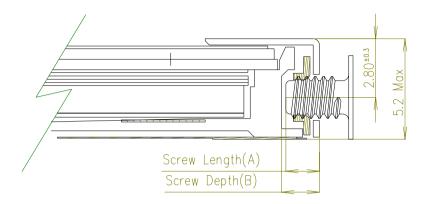
<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



Section A-A

- * Mounting Screw Length (A) = 1.5(Min) /1.8(Max)
- * Mounting Screw Hole Depth (B) = 1.8(Min)
- * Mounting hole location: 2.8(typ.)
- * Torque : 2.0 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.



6. Reliability

Environment test condition

No.	Test Item	Conditions				
1	High temperature storage test	Ta= 60°C, 240h				
2	Low temperature storage test	Ta= -20°C, 240h				
3	High temperature operation test	Ta= 50°C, 50%RH, 240h				
4	Low temperature operation test	Ta= 0°C, 240h				
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis				
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)				
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr				

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH) D: YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 40 pcs

b) Box Size: 395mm × 390mm × 309mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 - Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)
	0	00	Header	00
	1	01	Header	FF
	2	02	Header	FF
Header	3	03	Header	FF
Ea	4	04	Header	FF
H	5	05	Header	FF
	6	06	Header	FF
	7	07	Header	00
	8	08	EISA manufacture code (3 Character ID) LPL	32
	9	09	EISA manufacture code (Compressed ASC)	0C
•	10	0A	Panel Supplier Reserved - Product Code 0000h	00
Vendor/Product EDID Version	11	0В	(Hex. LSB first)	00
rod rsić	12	0C	LCD Module Serial No - Preferred but Optional ("0" If not used)	00
endor / Produs EDID Version	13	0 D	LCD Module Serial No - Preferred but Optional ("0" If not used)	00
) <u>*</u> (2	14	0E	LCD Module Serial No - Preferred but Optional ("0" If not used)	00
	15	0F	LCD Module Serial No - Preferred but Optional ("0" If not used)	00
Ze	16	10	Week of Manufacture 0 weeks	00
	17	11	Year of Manufacture 2008years	12
	18	12	EDID structure version # = 1	01
	19	13	EDID revision # = 3	03
2	20	14	Video input Definition = Digital signal	80
Display Parameters	21	15	Max H image size (Rounded cm) = 30 cm	1E
ids.	22	16	Max V image size (Rounded cm) = 19 cm	13
D M	23	17	Display gamma = (gamma*100)-100 = Earn yle:(2.2*100)-100=120 = 2.2 Gamma	78
_	24	18	Feature Support (no_DPMS, no_A ff \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	0A
<u>§</u>	25	19	Red/Green Low Bits (RxRy/GxGy)	8A
<u>na</u>	26	1A	Blue/White Low Bits (BxBy/WxWy)	35
zije	27	1B	$Red X \qquad Rx = 0.572$	92
, <u>0</u> 0	28	1C	Red Y Ry =0.344	58
Ç	29	1D	Green X Gx = 0.338	56
No.	30	1E	Green Y Gy =0.545	8B
Ö	31	1F	Blue X Bx = 0.156	28
nel nel	32	20	Blue Y By = 0.12	1E
Panel Color Coordinates	33 34	21	White X Wx = 0.313	50
		22	White Y Wy =0.329	54
l rec	35	23	Established timing 1 (00h if not used)	00
ing.	36	24	Established timing 2 (00h if not used)	00
Established Timings				0.0
E 1	37	25	Manufacturer's timings (00h if not used)	00
	38	26	Standard timing ID1 (01h if not used)	01
	39	27	Standard timing ID1 (01h if not used)	01
	40	28	Standard timing ID2 (01h if not used)	01
	41	29	Standard timing ID2 (01h if not used)	01
	42	2A	Standard timing ID3 (01h if not used)	01
Su	43	2B	Standard timing ID3 (01h if not used)	01
ni.	44	2C	Standard timing ID4 (01h if not used)	01
	45	2D	Standard timing ID4 (01h if not used)	01
nd nd	46	2E	Standard timing ID5 (01h if not used)	01
nde	47	2F	Standard timing ID5 (01h if not used) Standard timing ID6 (01h if not used)	01
Standard Timing ID	48	30	Standard timing ID6 (01h if not used) Standard timing ID6 (01h if not used)	01
S	50	32	Standard timing ID (01n it not used) Standard timing ID (01h if not used)	01
	51	33	Standard timing ID7 (01h if not used)	01
	52	34	Standard timing ID8 (01h if not used)	01
	53	35	Standard timing ID8 (01h if not used)	01
			6 - 1 (s	71



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte	Byte	Field Name and Comments	Value
	(Dec) 54	(Hex) 36	Pixel Clock/10,000 (LSB) 69.3 MHz @ 59.96Hz	(Hex) 12
	55	37	Pixel Clock/10,000 (MSB)	1B
	56	38	Horizontal Active (lower 8 bits) 1280 Pixels	00
	57	39	Horizontal Blanking(Thp-HA) (lower 8 bits) 126 Pixels	7E
	58	3A	Horizontal Blanking(Thp-HA) (upper 4:4bits)	50
	59	3B	Vertical Avtive 800 Lines	20
#	60	3C	Vertical Blanking (Tvp-HA) (DE Blanking typ.for DE only panels) 22 Lines	16
<u>ç</u>	61	3D	Vertical Active : Vertical Blanking (Tvp-HA) (upper 4:4bits)	30
m.	62	3E	Horizontal Sync. Offset (Thfp) 48 Pixels	30
Timing Descriptor#1	63	3F	Horizontal Sync Pulse Width (HSPW) 32 Pixels	20
Z.	64	40	Vertical Sync Offset(Tvfp): Sync Width (VSPW) 3 Lines: 6 Lines	36
ing	65	41	Horizontal Vertical Sync Offset/Width (upper 2bits)	00
Ţ,	66	42	Horizontal Image Size (mm) 304 mm	30
	67	43	Vertical Image Size (mm) 190 mm	BE
	68	44	Horizontal Image Size / Vertical Image Size	10
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate (Vsync_NEG, Hsync_NEG), DE only note: LSB is	19
			set to '1' if panel is DE-timing only. H/V can be ignored.	
	72	48	Flag	00
	73 74	49 4A	Flag	00
	75	4A 4B	Flag Data Type Tag (Descriptor Defined by manufacturer)	00
	76	4C	Flag	00
23	77	4D	Descriptor Defined by manufacture	00
L #	78	4E	Descriptor Defined by manufacture	00
oto	79	4F	Descriptor Defined by manufacturer	00
Tining Descriptor #2	80	50	Descriptor Defined by manufacturer Descriptor Defined by manufacturer	00
, S	81	51	Descriptor Defined by manufacturer	00
g I	82	52	Descriptor Defined by manufacturer	00
ain,	83	53	Descriptor Defined by manufacturer	00
<u>s</u>	84	54	Descriptor Defined by manufacturer	00
	85	55	Descriptor Defined by manufacturer	00
	86	56	Descriptor Defined by manufacturer	00
	87	57	Descriptor Defined by manufacturer	00
	88	58	Descriptor Defined by manufacturer	00
	89	59	Descriptor Defined by manufacturer	00
	90	5A	Flag	00
	91	5B	Flag	00
	92	5C	Flag	00
	93	5D	Data Type Tag (ASCII String)	FE
	94		Flag	00
#2	95	5F	ASCII String L	4C
	96	60	ASCII String G	47
ig.	97	61	ASCII String P	50
esc	98	62	ASCII String h	68
Į Ž	99	63	A SCII String i	69
ing	100	64	ASCII String 1	6C
Timing Descriptor #3	101	65	ASCII String i	69 70
1	102	66	A SCII String p	73
	103 104	67 68	ASCII String s ASCII String L	4C
	104	69	ASCII String C	43
	105	6A	ASCII String D	44
	107	6B	Manufacturer P/N(If<13 char> 0Ah, then terminate with ASC code 0Ah,set remaining char = 20h)	0A
	107	UB	- Code O/II, set religining endi – 2011)	VA



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)
	108	6C	Flag	00
	109	6D	Flag	00
	110	6E	Flag	00
	111	6F	Data Type Tag (ASCII String)	FE
	112	70	Flag	00
#	113	71	ASCII String L	4C
Timing Descriptor #4	114	72	ASCII String P	50
ipt	115	73	ASCII String 1	31
scr	116	74	ASCII String 4	34
De	117	75	ASCII String 1	31
00	118	76	ASCII String W	57
nin	119	77	ASCII String X	58
Tü	120	78	ASCII String 5	35
	121	79	ASCII String -	2D
	122	7A	ASCII String T	54
	123	7B	ASCII String L	4C
	124	7C	ASCII String A	41
	125	7D	ASCII String 1	31
ksum	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00
Checksum	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	63