# SPECIFICATION FOR APPROVAL

(	٧	)	<b>Preliminary Specification</b>
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	(	,	) <b>F</b>	inal	Sp	ecif	ica	tio	n
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Title	1	14.1" WXGA TFT LCD			
Customer		SUPPLIER	LG.Philips LCD Co., Ltd.		
MODEL		*MODEL	LP141WX3		
		Suffix	TLA1		

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE					
/						
/						
Diagram of a sure for each						
Please return 1 copy for your confirmation with your signature and comments.						

APPROVED BY	SIGNATURE					
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## **RECORD OF REVISIONS**

Revision No	Revision Date	Page	Description	EDID ver
0.0	Dec. 06, 2006	-	Pre CAS	V0.0

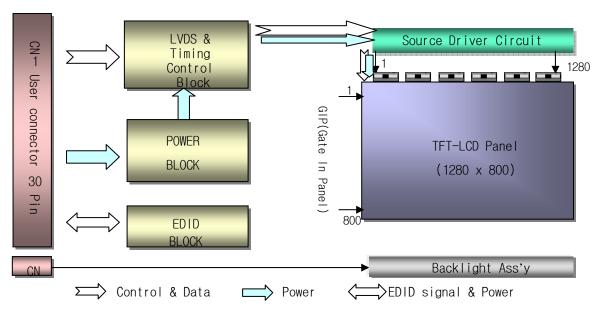


### 1. General Description

The LP141WX3 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 14.1 inches diagonally measured active display area with WXGA resolution(800 vertical by 1280 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP141WX3 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP141WX3 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP141WX3 characteristics provide an excellent flat display for office automation products such as Notebook PC.



## **General Features**

Active Screen Size	14.1 inches diagonal
Outline Dimension	319.5 (H, typ) × 205.5(V, typ) × 5.3(D, max) [mm]
Pixel Pitch	0.2373 mm × 0.2373 mm
Pixel Format	1280 horiz. By 800 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m <sup>2</sup> (Typ.5 point)
Power Consumption	Total 5.33 Watt(Typ.) @ LCM circuit 1.33Watt(Typ.), B/L input 4.0Watt(Typ.)
Weight	400 g (Max.), 390g(Typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Anti-glare treatment of the front polarizer
RoHS Comply	Yes

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## 2. Absolute Maximum Ratings

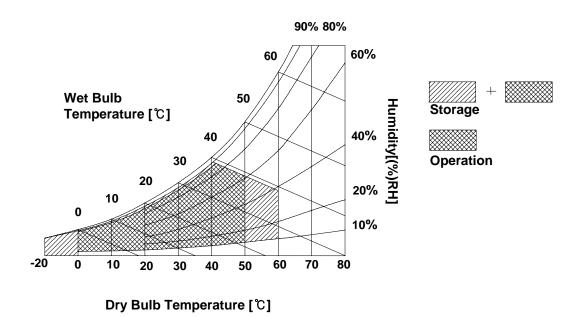
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Office		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

The LP141WX3 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Parameter Symbol Unit Notes Min Тур Max MODULE: VCC  $V_{DC}$ Power Supply Input Voltage 3.3 3.0 3.6 Power Supply Input Current 400 Ma  $I_{CC}$ 460 Power Consumption Watt Рс 1.33 1.6 Differential Impedance Ohm Zm 100 110 2 90 LAMP : Operating Voltage 640(6.5mA) 660(6.0mA) 880(2.0mA)  $\rm V_{\rm RMS}$  $V_{\mathsf{BL}}$ **Operating Current** 6.0 6.5 2.0 I<sub>BL.</sub> mA<sub>RMS</sub> 4.00 **Power Consumption** 4.28  $P_{BL}$ Operating Frequency 60 80  $f_{BL}$ 45 kHz Discharge Stabilization Time 3 Min Ts Hrs Life Time 15,000 Established Starting Voltage at 25℃ Vs 1180  $V_{\mathsf{RMS}}$ at 0 ℃ 1415  $V_{RMS}$ 

Table 2. ELECTRICAL CHARACTERISTICS

#### Note)

- 1. The specified current and power consumption are under the Vcc = 3.3V,  $25\,^{\circ}C$ , fv = 60Hz condition whereas full black pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The typical operating current is for the typical surface luminance  $(L_{WH})$  in optical characteristics.
- 4. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 5. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 6. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.
  Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 7. It is defined the brightness of the lamp after being lighted for 5 minutes as 100%.

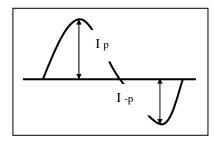
  T<sub>s</sub> is the time required for the brightness of the center of the lamp to be not less than 95%.
- 8. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

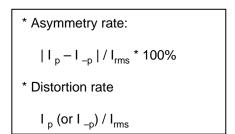
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#### Note)

- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
   It shall help increase the lamp lifetime and reduce leakage current.
  - a. The asymmetry rate of the inverter waveform should be less than 10%.
  - b. The distortion rate of the waveform should be within  $\sqrt{2 \pm 10\%}$ .
    - \* Inverter output waveform had better be more similar to ideal sine wave.





Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

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#### 3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model GT101-30S-HR11 manufactured by LSC.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	
5	NC	Reserved for supplier test point	
6	CIK EEDID	DDC Clock	1, Interface chips
7	DATA EEDID	DDC Data	1.1 LCD : THINE,
8	R <sub>IN</sub> 0-	Negative LVDS differential data input	KE5M5U2518CFP (LCD Controller)
9	R <sub>IN</sub> 0+	Positive LVDS differential data input	including LVDS Receiver (LVDSRX_SPI_UMOD)
10	GND	Ground	1.2 System : HP Tianshan or equivalent
11	R <sub>IN</sub> 1-	Negative LVDS differential data input	* Pin to Pin compatible with LVDS
12	R <sub>IN</sub> 1+	Positive LVDS differential data input	2. Connector
13	GND	Ground	2.1 LCD : GT101-30S-HR11, LGC or
14	R <sub>IN</sub> 2-	Negative LVDS differential data input	its compatibles
15	R <sub>IN</sub> 2+	Positive LVDS differential data input	2.2 Mating : FI-X30M or equivalent. 2.3 Connector pin arrangement
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	30 1 ПППП
18	CLKIN+	Positive LVDS differential clock input	<u>                                   </u>
19	GND	Ground	
20	NC	No Connect	[LCD Module Rear View]
21	NC	No Connect	[EGB Module Real view]
22	GND	Ground	
23	NC	No Connect	
24	NC	No Connect	
25	GND	Ground	
26	NC	No Connect	
27	NC	No Connect	
28	GND	Ground	
29	NC	No Connect	
30	NC	No Connect	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored Pink and the low voltage side terminal is White.

Condition: VCC =3.3V



## **Product Specification**

## 3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

**Table 6. TIMING TABLE** 

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f <sub>CLK</sub>	66.4	71.0	75.4	MHz	
Hsync	Period	Thp	1368	1440	1480		
	Width	t <sub>wH</sub>	16	32	48	tCLK	
	Width-Active	t <sub>WHA</sub>	1280	1280	1280		
Vsync	Period	t <sub>VP</sub>	809	823	848		
	Width	t <sub>wv</sub>	2	6	6	tHP	
	Width-Active	t <sub>wva</sub>	800	800	800		
Data	Horizontal back porch	t <sub>HBP</sub>	58	80	98	+CL IV	
Enable	Horizontal front porch	t <sub>HFP</sub>	14	48	62	tCLK	
	Vertical back porch	t <sub>VBP</sub>	5	15	40	+UD	
	Vertical front porch	t <sub>VFP</sub>	2	2	2	tHP	

## 3-4. Signal Timing Waveforms

High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc **DCLK**  $t_{HP}$ Hsync **t**WHA  $t_{HFP}$  $t_{HBP}$ Data Enable  $t_{VP}$ Vsync  $t_{VFP}$ twva  $t_{VBP}$ Data Enable

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## 3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

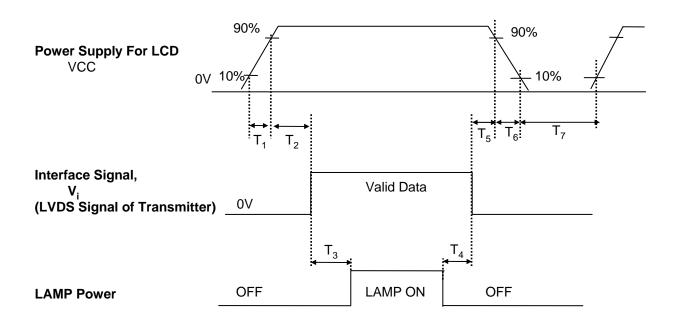
Table 7. COLOR DATA REFERENCE

									Inp	ut Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	50101	MSE	3					MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0	0		0	0	0	0	0		0	0
	Red	1	1	1	. 1	. 1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	. 1			1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																	 		
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																	 		••••
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	 0	0	0	 0	0	0	0	·····  1
BLUE			• • • • •														 		••••
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	 0	0	1	 1		 1	 1	
	BLUE (63)	0	0	0		0		0	0	0	0	 0	0		 1	1		1	<u>.</u> 1
	(,							L											

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## 3-6. Power Sequence



**Table 8. POWER SEQUENCE TABLE** 

Parameter		Value		Units
	Min.	Тур.	Max.	
T <sub>1</sub>	0.5	-	10	(ms)
T <sub>2</sub>	0	-	50	(ms)
T <sub>3</sub>	200	-	-	(ms)
T <sub>4</sub>	200	-	-	(ms)
T <sub>5</sub>	0	-	50	(ms)
T <sub>6</sub>	0	-	10	(ms)
T <sub>7</sub>	200	-	-	(ms)

#### Note)

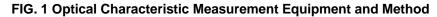
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.

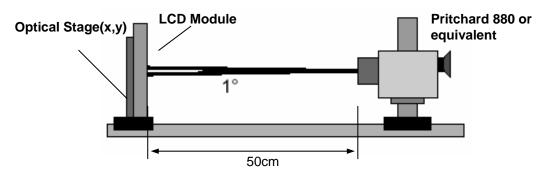


## 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\Theta$  equal to  $0^{\circ}$ .

FIG. 1 presents additional information concerning the measurement equipment and method.





**Table 9. OPTICAL CHARACTERISTICS** 

Ta=25°C, VCC=3.3V,  $f_{V}=60Hz$ ,  $f_{CLK}=71.0MHz$ ,  $I_{BL}=6.0mA$ 

Danier de la constant	Ol		Values			N-4
Parameter	Symbol	Min	Тур	MAx	Units	Notes
Contrast Ratio	CR	300	400	-		1
Surface Luminance, white	L <sub>WH</sub>	170	200		cd/m <sup>2</sup>	2
Luminance Variation	$\delta_{\text{WHITE}}$	-	TBD	TBD	]]	3
Response Time						4
Rise Time	Tr <sub>R</sub>	-	5.5	9	ms	
Delay Time	Tr <sub>D</sub>	-	10.5	16	ms	
Color Coordinates						
RED	RX	TBD	TBD	TBD		
	RY	TBD	TBD	TBD	[	
GREEN	GX	TBD	TBD	TBD		
	GY	TBD	TBD	TBD		
BLUE	ВХ	TBD	TBD	TBD		
	BY	TBD	TBD	TBD		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle					]	5
x axis, right(Φ=0°)	Θr	40	-	-	degree	
x axis, left (Φ=180°)	Θl	40	-	-	degree	
y axis, up (Φ=90°)	Θu	15	-	-	degree	
y axis, down (Φ=270°)	Θd	35	-	-	degree	
Gray Scale						6

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#### Note)

1. Contrast Ratio(CR) is defined mathematically as

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = Average(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation ( $\delta_{WHITE}$ ) is determined by measuring L<sub>N</sub> at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{ WHITE}} = \frac{\text{Maximum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \ \dots \ \mathsf{L}_{13})}{\text{Minimum}(\mathsf{L}_{1}, \mathsf{L}_{2}, \ \dots \ \mathsf{L}_{13})}$$

- 4. Response time is the time required for the display to transition from white to black (rise time, Tr<sub>R</sub>) and from black to white(Decay Time, Tr<sub>D</sub>). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

\* 
$$f_{V} = 60Hz$$

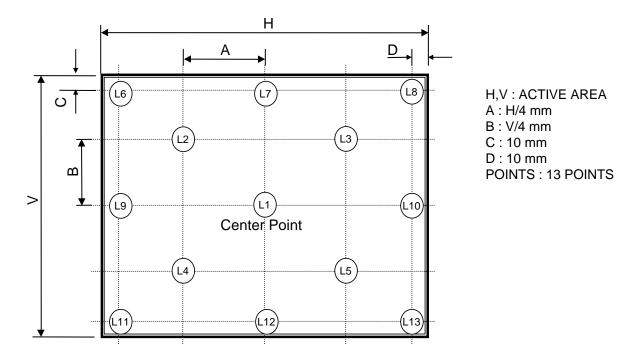
Gray Level	Luminance [%] (Typ)
LO	TBD
	TBD
L15	TBD
	TBD
L31	TBD
	TBD
	TBD
L55	TBD
L63	TBD

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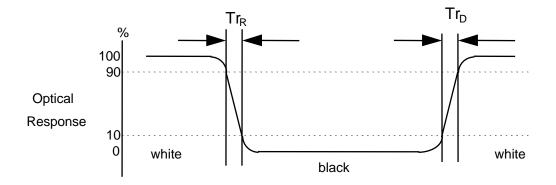
#### FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>



#### FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

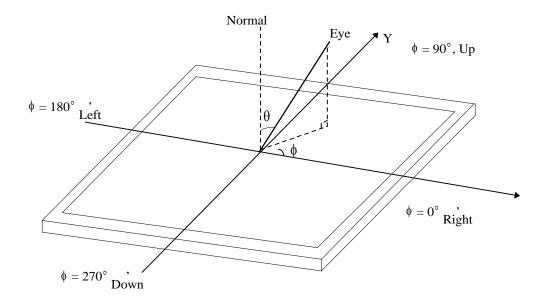


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# FIG. 4 Viewing angle

## <Dimension of viewing angle range>



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## 5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP141WX3. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	319.5 ± 0.5mm				
Outline Dimension	Vertical	205.5 ± 0.5mm				
	Depth	5.3mm (max)				
Bezel Area	Horizontal	307.96 ± 0.5mm				
bezei Alea	Vertical	194.60 ± 0.5mm				
Active Display Area	Horizontal	303.74 mm				
Active Display Area	Vertical	189.84 mm				
Weight	390g (Typ.) 400g (Max.)					
Surface Treatment	Anti-glare treatment of the front	polarizer				

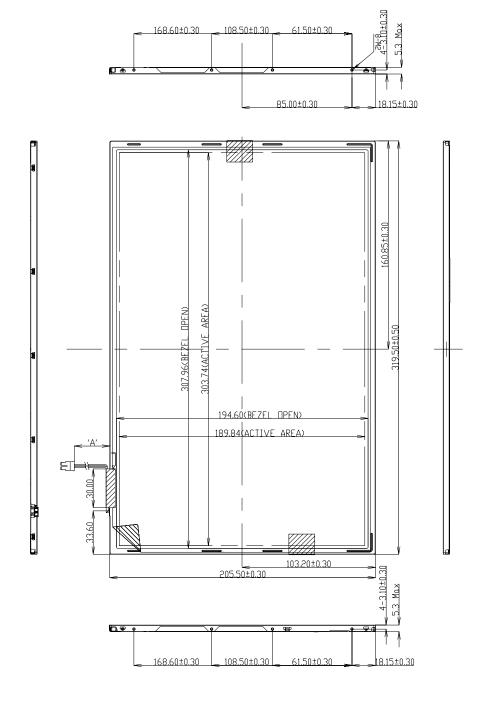
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<FRONT VIEW>

Note) Unit:[mm], General tolerance:  $\pm$  0.5mm





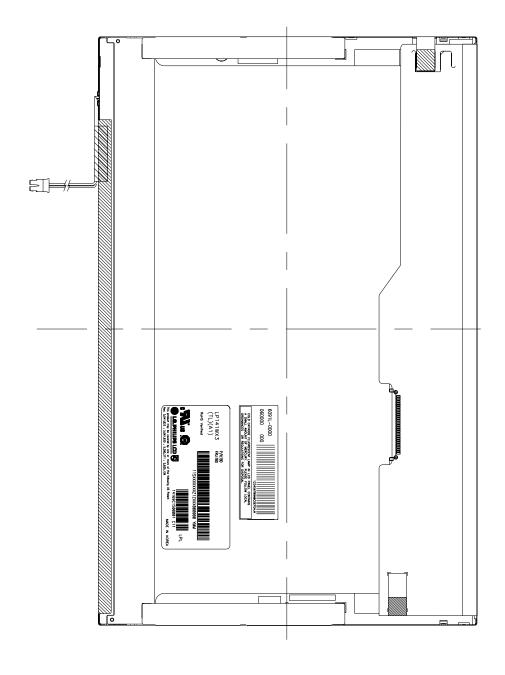
Lamp Wire Length : 61mm  $\pm$  5mm



<REAR VIEW>

Note) Unit:[mm], General tolerance:  $\pm$  0.5mm

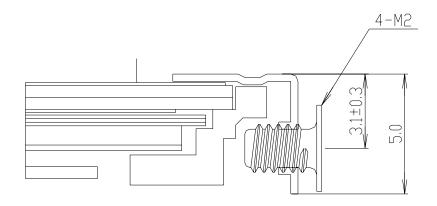






## [ DETAIL DESCRIPTION OF SIDE MOUNTING SCREW ]

# **TBD**



SECTION A-A SCALE 5/1

\*SCREW(8ea) TORQUE : 2kgf.cm max \*Mounting SCREW Depth : 2.5mm max

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## 6. Reliability

#### **Environment test condition**

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

## { Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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#### 7. International Standards

#### 7-1. Safety

a) UL 60950, Third Edition, Underwriters Laboratories, Inc., Dated Dec. 11, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

b) CAN/CSA C22.2, No. 60950, Third Edition, Canadian Standards Association, Dec. 1, 2000.

Standard for Safety of Information Technology Equipment, Including Electrical Business Equipment.

c) EN 60950 : 2000, Third Edition

IEC 60950: 1999, Third Edition

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



## 8. Packing

## 8-1. Designation of Lot Mark

a) Lot Mark

		А	В	С	D	Е	F	G	Н	I	J	К	L	М
--	--	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH  $F \sim M$ : SERIAL NO.

#### Note

#### 1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

#### b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

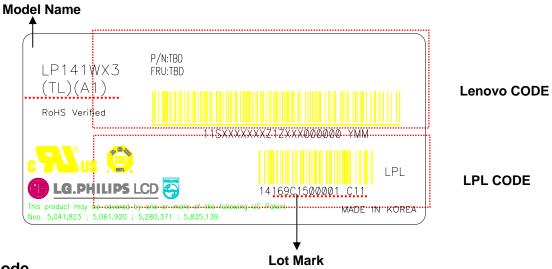
## 8-2. Packing Form

a) Package quantity in one box: 20 pcs

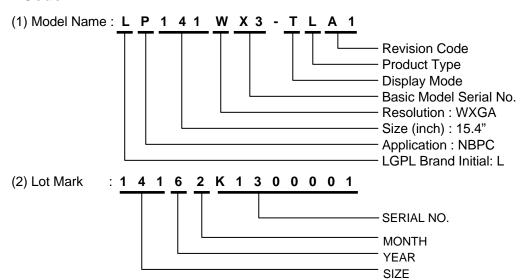
b) Box Size: 430 X 334 X 287



## 8-3. Label Description



#### **LPL Code**



#### **Lenovo Code**

1)P/N : TBD

2)FRU: TBD

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#### 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

#### 9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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#### 9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

#### 9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
  - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



Byte#	Byte#	Field Name and Com	ments	Va	lue	Value	
(decimal)	(HEX)	Field Name and Com	TETIO	(H	EX)	(binary)	
0	00	Header		0	0	0000 0000	
1	01	Header		F	F	1111 1111	
2	02	Header		F	F	1111 1111	
3	03	Header		F	F	1111 1111	Header
<u>4</u> 5	04 05	Header		F	F	1111 1111	
6	06	Header Header		F	F F	1111 1111	
7	07	Header		0	0	0000 0000	
8	08	EISA manufacturer code = LPL		3	2	0011 0010	
9	09	Compressed ASCII		0	C	0000 1100	
10	0A	Product code =		0	0	0000 0000	
11	0B	(Hex, LSB first)	New Generation	0	0	0000 0000	
12	0C	LCD Module Serial No. = 0 (If not used)		0	0	0000 0000	Vander/
13		LCD Module Serial No. = 0 (If not used)		0		0000 0000	Vender/
	OD OF				0		Product ID
14	0E	LCD Module Serial No. = 0 (If not used)		0	0	0000 0000	
15	0F	LCD Module Serial No. = 0 (If not used)		0	0	0000 0000	
16	10	Week of manufacture		0	0	0000 0000	
17	11	Year of manufacture = 2007		1	1	0001 0001	
18	12	EDID Structure version # = 1		0	1	0000 0001	EDID Version/
19	13	EDID Revision # = 1		0	1	0000 0001	Revision
20	14	Video input definition = Digital I/p,non TMDS	CRGB	8	0	1000 0000	<b>5</b>
21	15	Max H image size(cm) = 30.374cm(30)		1	E	0001 1110	Display
22	16	Max V image size(cm) = 18.984cm(19)		7	3	0001 0011	Parameter
23 24	17 18	Display gamma = 2.20 Feature support(DPMS) = Active off, RGB Co	Nor	0	8 A	0111 1000	
25	19	Red/Green low Bits	DIOI	0	0	0000 1010	
26	19 1A	Blue/White Low Bits		0	0	0000 0000	
27	1B	Red X Rx = TBD		0	0	0000 0000	
28	1C	Red Y Ry = TBD		0	0	0000 0000	
29	1D	Green X Gx = TBD		0	0	0000 0000	Color
30	1E	Green Y Gy = TBD		0	0	0000 0000	Characteristic
31	1F	Blue X Bx = TBD		0	0	0000 0000	
32	20	Blue Y By = TBD		0	0	0000 0000	
33	21	White X		5	0	0101 0000	
34	22	White Y Wy = 0.329		5	4	0101 0100	
35	23	Established Timing I		0	0	0000 0000	Established
36	24	Established Timing II		0	0	0000 0000	Timings
37	25	Manufacturer's Timings		0	0	0000 0000	
38	26	Standard Timing Identification 1 was not use	d	0	1	0000 0001	
39	27	Standard Timing Identification 1 was not use		0	1	0000 0001	
40	28	Standard Timing Identification 2 was not use		0	1	0000 0001	
41	29	Standard Timing Identification 2 was not use	d	0	1	0000 0001	
42	2A	Standard Timing Identification 3 was not use	d	0	1	0000 0001	
43	2B	Standard Timing Identification 3 was not use	d	0	1	0000 0001	
44	2C	Standard Timing Identification 4 was not use	d	0	1	0000 0001	Standard
45	2D	Standard Timing Identification 4 was not use	d	0	1	0000 0001	Timing ID
46	2E	Standard Timing Identification 5 was not use		0	1	0000 0001	J
47	2F	Standard Timing Identification 5 was not use		0	1	0000 0001	
48	30	Standard Timing Identification 6 was not use		0	1	0000 0001	
49	31	Standard Timing Identification 6 was not use		0	1	0000 0001	
50	32	Standard Timing Identification 7 was not use		0	1	0000 0001	
51	33	Standard Timing Identification 7 was not use		0	1	0000 0001	
		-		+		0000 0001	
52	34	Standard Timing Identification 8 was not use		0	1		
53	35	Standard Timing Identification 8 was not use	а	0	1	0000 0001	



	Byte#	Byte#	Field Name and Comments	Va	lue	Value	
55   37   (Storoet LSB first)   1   R   0000 0000	(decimal)	(HEX)	-	(HE	ΞX)		
56   38	54	36	1280 X 800 @ 60Hz mode: pixel clock = 68.9Mz = 71.00WHz	В	С	1011 1100	
57   39	55	37	(Stored LSB first)	1	В	0001 1011	
58   38   Vertical Native = 800 lines   Service   Service   Vertical Native = 800 lines   Service   Service   Vertical Ranking = 16 lines   Service   Ser	56	38	Horizontal Active = 1280 pixels	0	0	0000 0000	
59	57	39	Horizontal Blanking = 128 pixels => 160 pixels	Α	0	1010 0000	
60   3C   Vertical Blanking = 16 lines ⇒ 23 lines   5 tandard   1   7   0001 0111     61   3C   Vertical Active : Vertical Branking = 800 : 16 ⇒ 20   2 tandard   3   0   0011 0000     62   3E   Horizontal Sync Diffset = 21 pixels ⇒ 48 pixels   2   0   0010 0000     63   3F   Horizontal Sync Diffset = 4 lines   24 pixels ⇒ 28 pixels   2   0   0010 0000     64   40   Vertical Sync Diffset = 4 lines   3   0   0   0   0000 0000     65   41   Horizontal Vertical Sync Offset   4   line   5   3   0   0   0   0     66   42   Horizontal Image Size = 803.74min(394)   0   3   0   0   0   0     67   43   Vertical Image Size = 803.74min(394)   0   0   0   0   0     68   44   Horizontal & Vertical Image Size   1   0   0   0   0   0     69   45   Horizontal Border = 0   0   0   0   0   0   0     70   46   Vertical Border = 0   0   0   0   0   0   0     71   47   Non-indraced, Normal display, no stereo, Dipital separate sync, HV pol negatives   1   8   0   0   0   0     73   49   Detailed Timing Descriptor #2   0   0   0   0   0   0     74   4A   Detailed Timing Descriptor #2   0   0   0   0   0   0     75   48   Detailed Timing Descriptor #2   0   0   0   0   0   0     76   4C   D   0   0   0   0   0   0     77   4D   0   0   0   0   0   0     78   4F   D   0   0   0   0   0   0     80   50   D   0   0   0   0   0     90   5A   Detailed Timing Descriptor #3   0   0   0   0   0     90   5A   Detailed Timing Descriptor #3   0   0   0   0   0     90   5A   Detailed Timing Descriptor #3   0   0   0   0     90   5A   Detailed Timing Descriptor #3   0   0   0   0     90   65   D   0   0   0   0   0     90   65   D   0   0   0   0   0   0     90   65   D   0   0   0   0   0	58	3A	Horizontal Active: Horizontal Blanking = 1280: 150	5	0	0101 0000	
Standard   1	59	3B	Vertical Avtive = 800 lines	2	0	0010 0000	
Fig.	60	3C		1	7	0001 0111	
62   3E	61	3D	Vertical Active: Vertical Blanking = 800: 16 => 800; Standard	3	0	0011 0000	Timing
63	62	3E	Horizontal Sync. Offset = 21 pixels => 48 pixels (version3.5)	3	0		-
64		3F		2	0		
65	_						
66							
67							
68							
Horizontal Border = 0							
70							
71	-						
72         48         Detailed Timing Descriptor #2         0         0         0         0000 0000           73         49         0         0         0         0000 0000           75         4B         0         0         0         0000 0000           76         4C         0         0         0         0000 0000           77         4D         0         0         0000 0000         0000 0000           78         4E         0         0         0         0000 0000         0000 0000           80         50         0         0         0000 0000         0000 0000         0000 0000         0000 0000         0000 0000         0000 0000         #2         0         0         0000 0000         #2         #2         0         0         0000 0000         #2         #2         4         0         0         0000 0000         #2         #2         4         0         0         0000 0000         #2         #2         0         0         0         0000 0000         #2         4         0         0         0         0         0         0         0         0         0         0         0         0         0							
73				+	-		
74			Detailed Timing Descriptor #2	-			
75				_			
76				-			
77							
78					-		
Timing   Respond					-		Detailed
80   50							
81				-			_
83       53         84       55         85       55         86       56         87       57         88       58         89       59         90       5A         Detailed Timing Descriptor #3       0         91       5B         92       5C         93       5D         94       5E         95       5F         1       4         1       6         96       60         98       62         99       63         100       6         101       65         101       6         102       66         103       67         104       6         105       69         106       60         107       60         108       60         109       60         100       60         100       60         100       60         100       60         100       60         100       60 <td< td=""><td></td><td></td><td></td><td>0</td><td>0</td><td></td><td></td></td<>				0	0		
84       55       0       0       0 000 0000         85       55       0       0 0 0000 0000         86       56       0       0 0 0000 0000         87       57       0 0 0 0000 0000         88       58       0 0 0 0000 0000         90       5A       Detailed Timing Descriptor #3       0 0 0000 0000         91       5B       0 0 0 0000 0000         92       5C       0 0 0000 0000         93       5D       F E 1111 1110         94       5E       0 0 0000 0000         95       5F       L       4 C 0100 1100         96       60       G       4 7 0100 0111         97       61       P       5 0 0101 0000         98       62       h       6 9 0110 1000         99       63       i       6 9 0110 1000         100       64       I       6 9 0110 1001         101       65       i       6 9 0110 1001         102       66       p       7 3 0111 0011         104       68       L       4 C 0100 1100         104       68       L       4 C 0100 1100         104       69       C 0100	82	52		0	0	0000 0000	
S5	83	53		0	0	0000 0000	
86       56         87       57         57       0         88       58         59       0         90       5A         Detailed Timing Descriptor #3       0         91       5B         92       5C         93       5D         94       5E         95       F         10       0         96       60         97       61         100       6         8       62         10       6         100       64         100       64         101       65         102       66         103       67         104       68         105       69         106       6A         106       6A         106       6A         106       6A         107       1000011000000000000000000000000000000	84	55		0	0	0000 0000	
87         57         0         0         0         0000 0000         0000 0000         0000 0000         0         0000 0000         0         0000 0000         0         0         0         0000 0000         <				_			
88         58         0         0         0         00000000         0         00000000         0         00000000         0         0         00000000         0 <t< td=""><td></td><td></td><td></td><td></td><td>-</td><td></td><td></td></t<>					-		
S9   59   Detailed Timing Descriptor #3   0   0   0000 0000     90					-		
90         5A         Detailed Timing Descriptor #3         0         0         0000 0000           91         5B         0         0         0000 0000           92         5C         0         0         0000 0000           93         5D         F         E         1111 1110           94         5E         0         0         0000 0000           95         5F         L         4         C         0100 1110           96         60         G         4         7         0100 0111         Detailed           97         61         P         5         0         0101 0000         Timing           98         62         h         6         8         0110 1000         #3           100         64         I         6         9         0110 1001         #3           101         65         I         I         6         9         0111 0001         #3           102         66         P         7         0         0111 0001         10         10         10         10         10         10         10         10         10         10         10         10         10				_	-		
91         5B         0         0         0000 0000         0000 0000         0         0000 0000         0         0         0000 0000         0         0         0         0000 0000         0         0         0         0         0000 0000         11         0         0         1         0         0         1         0         0         1         0         0         1         1         0         0         0         1         1         0         0			Datailed Timing Descriptor #0		-		
92         5C         0         0         0000 0000           93         5D         F         E         1111 1110           94         5E         0         0         0000 0000           95         5F         L         4         C         0100 110           96         60         G         4         7         0100 0111           97         61         P         5         0         0101 0000           98         62         h         6         8         0110 1000           99         63         i         6         9         0110 1001         #3           100         64         I         6         9         0110 1001         #3           102         66         p         7         0         0111 0001         #3           103         67         s         7         3         0111 0011         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         0000         00000         0000         0000         0			Detailed Hilling Descriptor #3				
93   5D     F   E   1111 1110							
94 5E 0 0 0 0000 0000 95 5F L L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0 0111 0000 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 0 4 4 0 0100 0100							
95 5F L 4 C 0100 1100 96 60 G 4 7 0100 0111 97 61 P 5 0 0101 0000 98 62 h 6 6 8 0110 1000 99 63 i 6 9 0110 1001 100 64 I 6 C 0110 1100 101 65 i 6 9 0110 1001 102 66 p 7 0 0111 0001 103 67 s 7 3 0111 0011 104 68 L 4 C 0100 1100 105 69 C 4 3 0100 0011 106 6A D 0 4 4 0 0100 0100				···		1111 1110	
96         60         G         4         7         0100 0111         Detailed Timing           97         61         P         5         0         0101 0000         Timing           98         62         h         6         8         0110 1000         0110 1000         0110 1001         0110 1001         0110 1001         0110 1001         0110 1001         0110 1001         0110 1001         0111 1000			L				
97       61       P       5       0       0101 0000       Timing Description         98       62       h       6       8       0110 1000       H       Description       #3         100       64       I       6       9       0110 1001       #3         101       65       i       6       9       0110 1001       100 <td></td> <td></td> <td>G</td> <td>4</td> <td>-</td> <td></td> <td>Detailed</td>			G	4	-		Detailed
99     63     i     6     9     0110 1001     #3       100     64     I     6     C     0110 1100       101     65     i     6     9     0110 1001       102     66     p     7     0     0111 0000       103     67     s     7     3     0111 0011       104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100		61		5	0		Timing
100     64     I     6     C     0110 1100       101     65     i     6     9     0110 1001       102     66     p     7     0     0111 0000       103     67     s     7     3     0111 0011       104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100					8		Description
101     65     i     6     9     0110 1001       102     66     p     7     0     0111 0000       103     67     s     7     3     0111 0011       104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100							#3
102     66     p     7     0     0111 0000       103     67     s     7     3     0111 0011       104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100							
103     67     s     7     3     0111 0011       104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100							
104     68     L     4     C     0100 1100       105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100							
105     69     C     4     3     0100 0011       106     6A     D     4     4     0100 0100							
106 6A D D 4 4 0100 0100				· · · · · ·			
	107	6B	LF			0000 1010	



			٠, /	_	) / I	
Byte#	Byte#	Field Name and Comments	_	lue	Value	
(decimal)	(HEX)	Field Name and Confronts	(H	EX)	(binary)	
108	6C	Detailed Timing Descriptor #4	0	0	0000 0000	
109	6D		0	0	0000 0000	
110	6E		0	0	0000 0000	
111	6F		F	Ε	1111 1110	
112	70		0	0	0000 0000	
113	71	L	4	С	0100 1100	
114	72	Р	5	0	0101 0000	Detailed
115	73	1	3	1	0011 0001	Timing
116	74	4	3	4	0011 0100	Description
117	75	1	3	1	0011 0001	#4
118	76	W	5	7	0101 0111	
119	77	X	5	8	0101 1000	
120	78	3	3	3	0011 0011	
121	79	_	2	D	0010 1101	
122	7A	T	5	4	0101 0100	
123	7B	L	4	С	0100 1100	
124	7C	А	4	1	0100 0001	
125	7D	1	3	1	0011 0001	
126	7E	Extension flag = 00	0	0	0000 0000	Extension Flag
127	7F	Checksum	6	С	0110 1100	Checksum

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