

Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(◆) Final Specification

Title	13.3" WXGA TFT LCD
-------	--------------------

Customer	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LP133WX1
Suffix	TLN2

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	SIGNATURE
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Products Engineering Dept.
LG. Philips LCD Co., Ltd

Product Specification

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RECORD OF REVISIONS

[illegible]

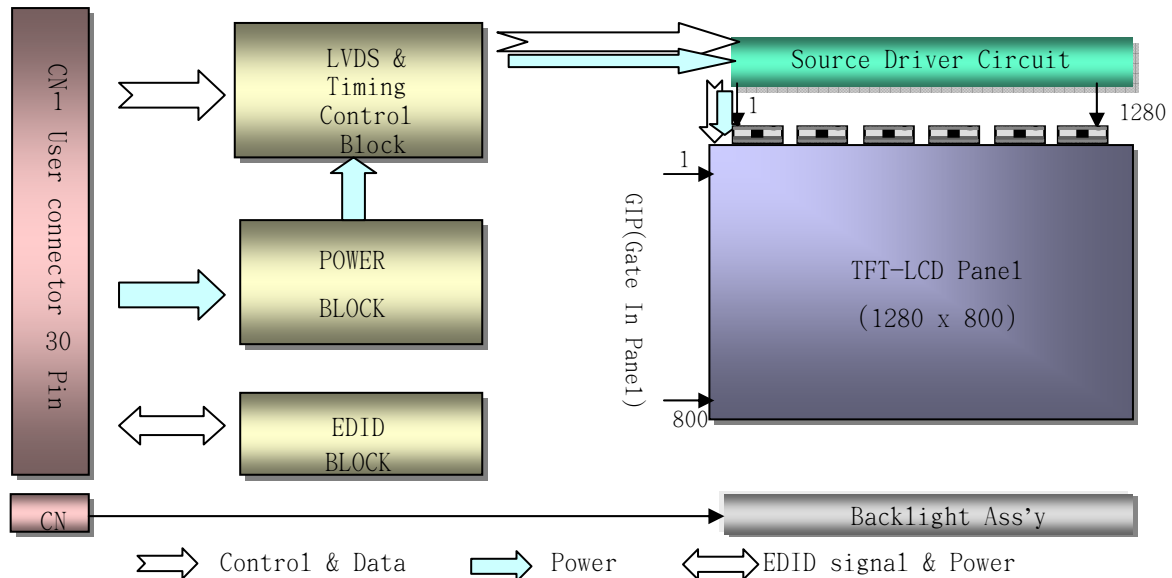
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1. General Description

The LP133WX1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 13.3 inches diagonally measured active display area with WXGA resolution(800 vertical by 1280 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP133WX1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP133WX1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP133WX1 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	13.3 inches diagonal
Outline Dimension	299.0(H)[typ.] × 195.0(V)[typ.] × 5.5(D) mm [Max.]
Pixel Pitch	0.2235 mm × 0.2235 mm
Pixel Format	1280 horiz. By 800 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	220 cd/m ² (Typ.5 point)
Power Consumption	Total 4.28 Watt(Typ.) @ LCM circuit 0.48 Watt(Typ.), B/L input 3.8Watt(Typ.)
Weight	340g [Typ.] , 350g [Max.]
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Glare, hard coating 3H
RoHS Comply	Yes

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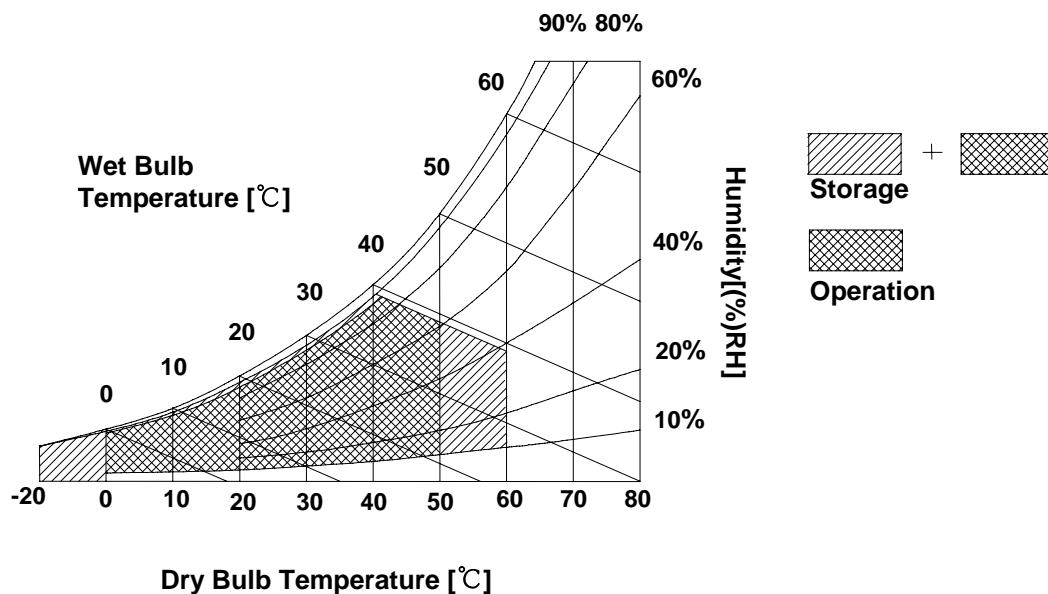
2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C
Operating Temperature	TOP	0	50	°C	1
Storage Temperature	HST	-20	60	°C	1
Operating Ambient Humidity	HOP	10	90	%RH	1
Storage Humidity	HST	10	90	%RH	1

Note : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

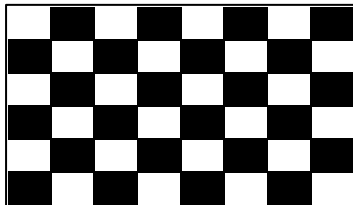
The LP133WX1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
MODULE :						
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V _{DC}	
Power Supply Input Current	I _{CC}	Mosaic	146	168	mA	1
Power Consumption	P _c	-	0.48	0.55	Watt	1
Differential Impedance	Z _m	90	100	110	Ohm	2
LAMP :						
Operating Voltage	V _{BL}	605	640	855	V _{RMS}	
Operating Current	I _{BL}	2.0	6.0	7.0	mA _{RMS}	3
Power Consumption	P _{BL}	-	3.8	4.2		
Operating Frequency	f _{BL}	45	60	80	kHz	
Discharge Stabilization Time	T _s	-	-	3	Min	4
Life Time		15,000	-	-	Hrs	5
Established Starting Voltage at 25°C at 0 °C	Vs			1140	V _{RMS}	
				1370	V _{RMS}	

Note)

1. The specified current and power consumption are under the Vcc = 3.3V , 25°C , fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



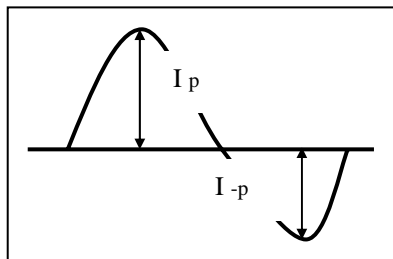
2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
3. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
4. Define the brightness of the lamp after being lighted for 5 minutes as 100%, T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
5. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.

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Note)

6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.
Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
7. It is defined the brightness of the lamp after being lighted for 5 minutes as 100%.
 T_s is the time required for the brightness of the center of the lamp to be not less than 95%.
8. The lamp power consumption shown above does not include loss of external inverter.
The applied lamp current is a typical one.
9. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

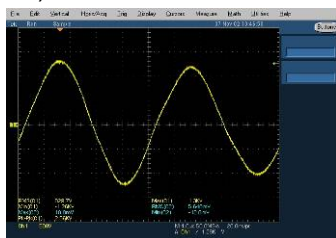
$$|I_p - I_{-p}| / I_{rms} * 100\%$$

* Distortion rate

$$I_p \text{ (or } I_{-p}) / I_{rms}$$

10. Inverter open voltage must be more than lamp voltage for more than 1 second for start-up.
Otherwise, the lamps may not be turned on.
※ Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

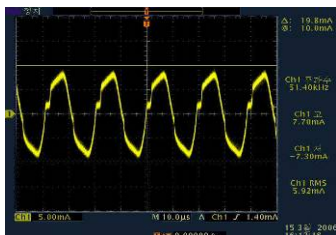
Ex of current wave)



Normal current wave - Standard



Abnormal current wave - Bad



Abnormal current wave - Bad



Abnormal current wave - Bad


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3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model FI-XB30SRL-HF11 manufactured by JAE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	<p>1. Interface chips 1.1 LCD : SW, SW0615_M (LCD Controller) including LVDS Receiver 1.2 System : it must include international standard LVDS Transmitter. * Pin to Pin compatible with LVDS</p> <p>2. Connector 2.1 LCD : FI-XB30SRL-HF11, JAE. its compatibles 2.2 Mating : FI-X30C2L or equivalent. 2.3 Connector pin arrangement</p>  <p>[LCD Module Rear View]</p>
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	
5	NC	Reserved for supplier test point	
6	Clk EEDID	DDC Clock	
7	DATA EEDID	DDC Data	
8	R _{IN} 0-	Negative LVDS differential data input	
9	R _{IN} 0+	Positive LVDS differential data input	
10	GND	Ground	
11	R _{IN} 1-	Negative LVDS differential data input	
12	R _{IN} 1+	Positive LVDS differential data input	
13	GND	Ground	
14	R _{IN} 2-	Negative LVDS differential data input	
15	R _{IN} 2+	Positive LVDS differential data input	
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	
18	CLKIN+	Positive LVDS differential clock input	
19	GND	Ground	
20	NC	No Connect	
21	NC	No Connect	
22	GND	Ground	
23	NC	No Connect	
24	NC	No Connect	
25	GND	Ground	
26	NC	No Connect	
27	NC	No Connect	
28	GND	Ground	
29	NC	No Connect	
30	NC	No Connect	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible.

The mating connector part number is AMP1674817-2 or equivalent.

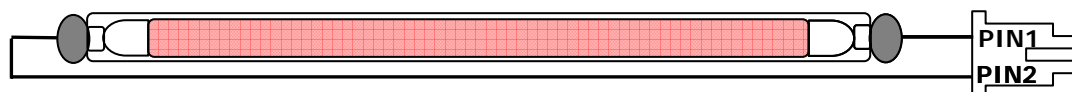


Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

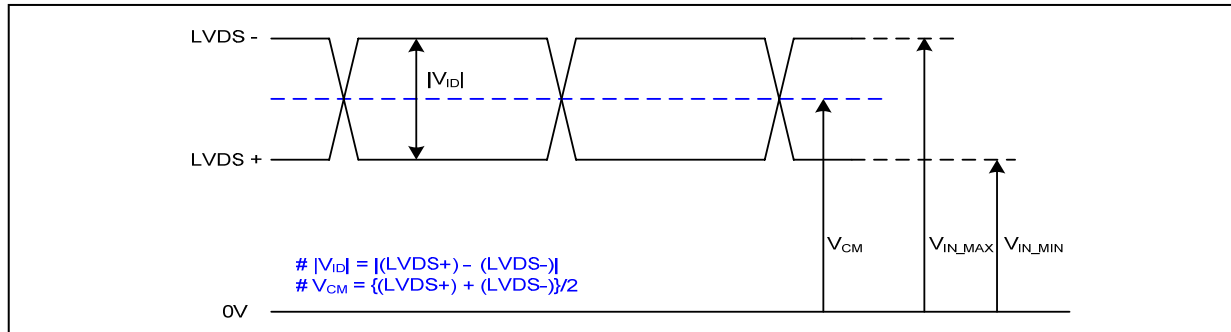
Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes : 1. The high voltage side terminal is colored Pink and the low voltage side terminal is Yellow.

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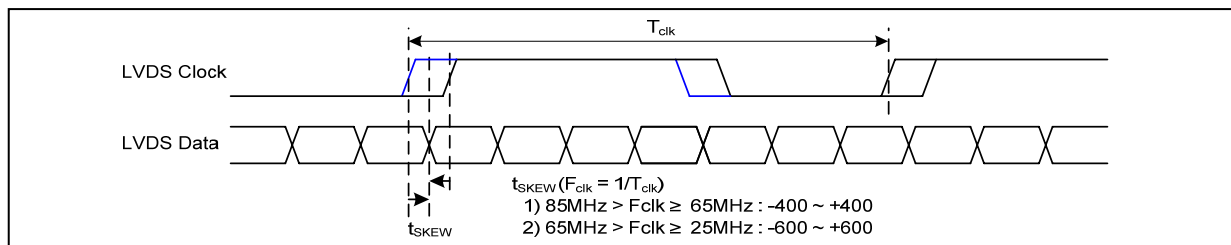
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



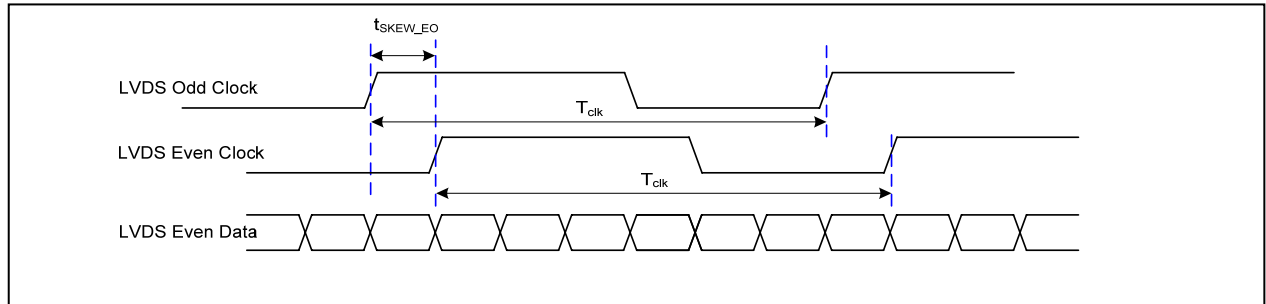
Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	V_{ID}	100	600	mV	-
LVDS Common mode Voltage	V_{CM}	0.6	1.8	V	-
LVDS Input Voltage Range	V_{IN}	0.3	2.1	V	-

3-3-2. AC Specification

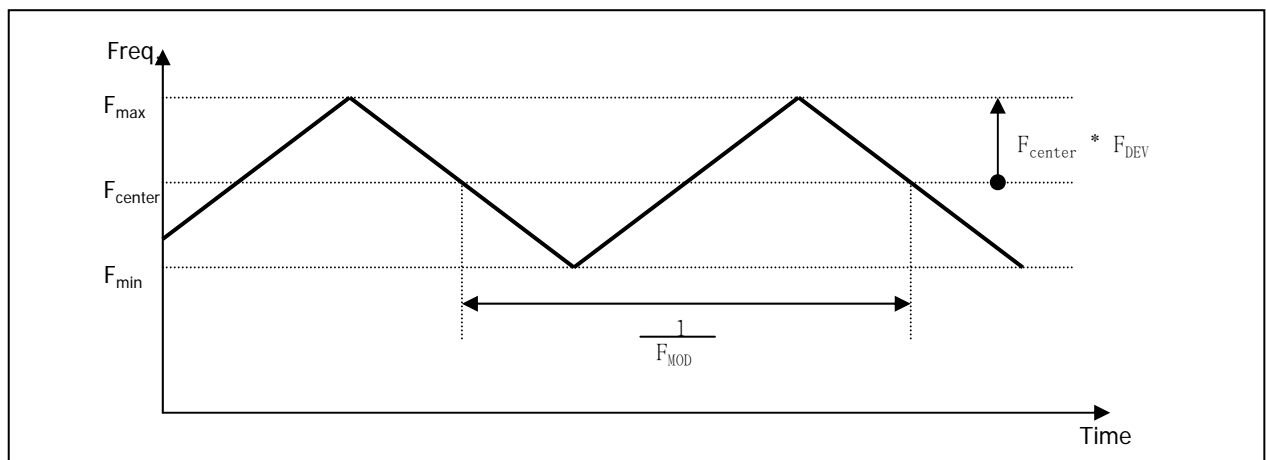


Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skew Margin	t_{SKEW}	- 400	+ 400	ps	$85MHz > F_{clk} \geq 65MHz$
	t_{SKEW}	- 600	+ 600	ps	$65MHz > F_{clk} \geq 25MHz$
LVDS Clock to Clock Skew Margin (Even to Odd)	t_{SKEW_EO}	- 1/7	+ 1/7	T_{clk}	-
Maximum deviation of input clock frequency during SSC	F_{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F_{MOD}	-	200	KHz	-

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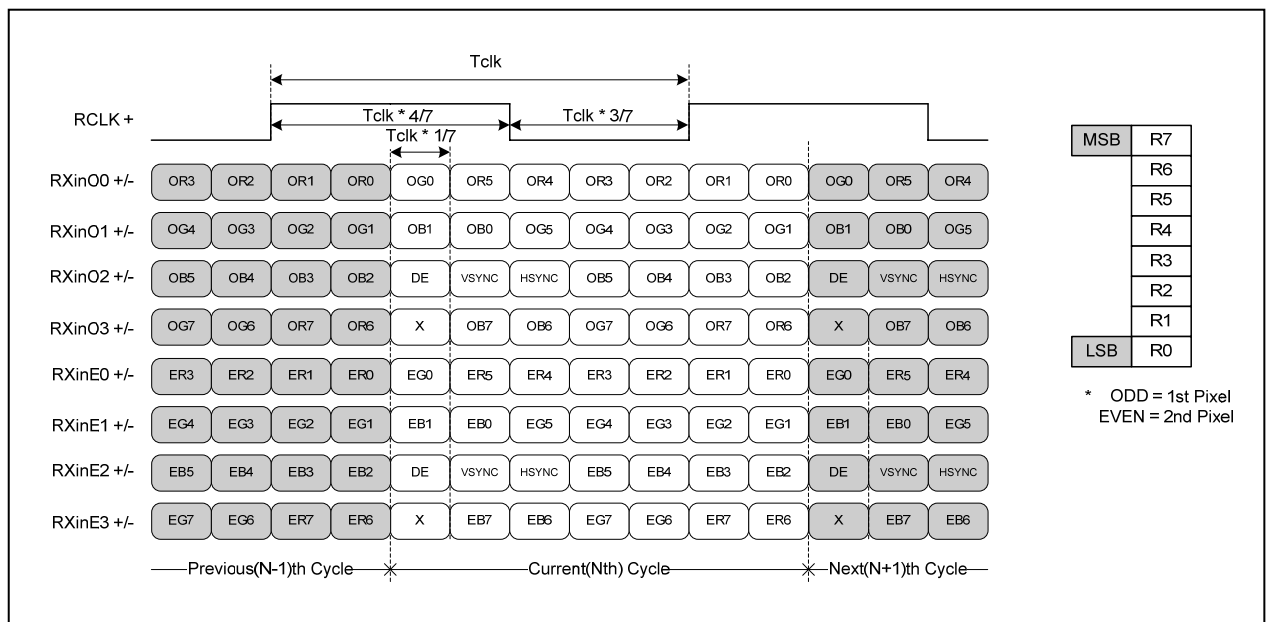
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

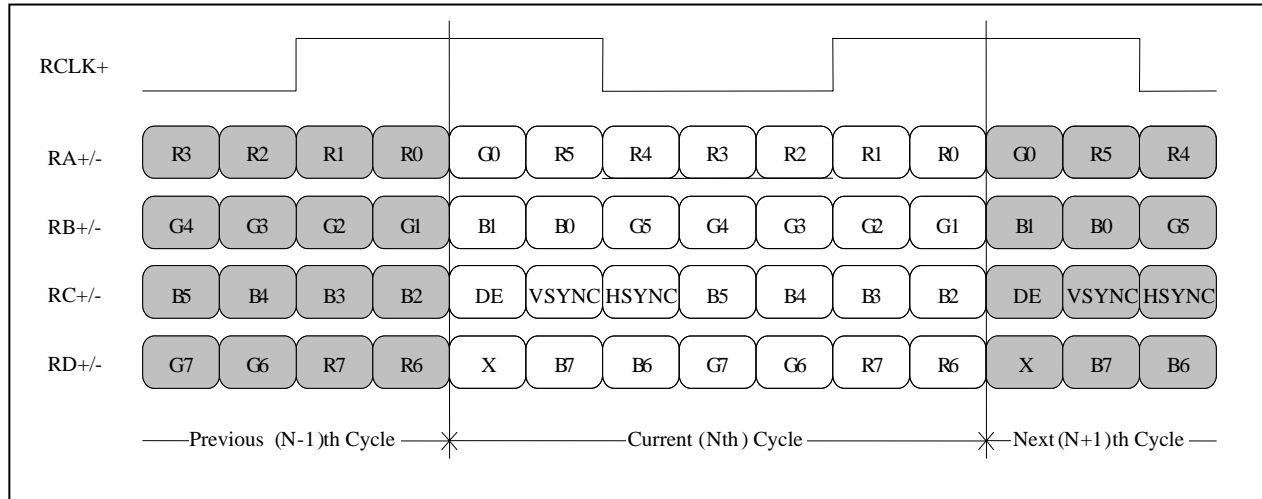
1) LVDS 2 Port



< LVDS Data Format >

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2) LVDS 1 Port



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3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

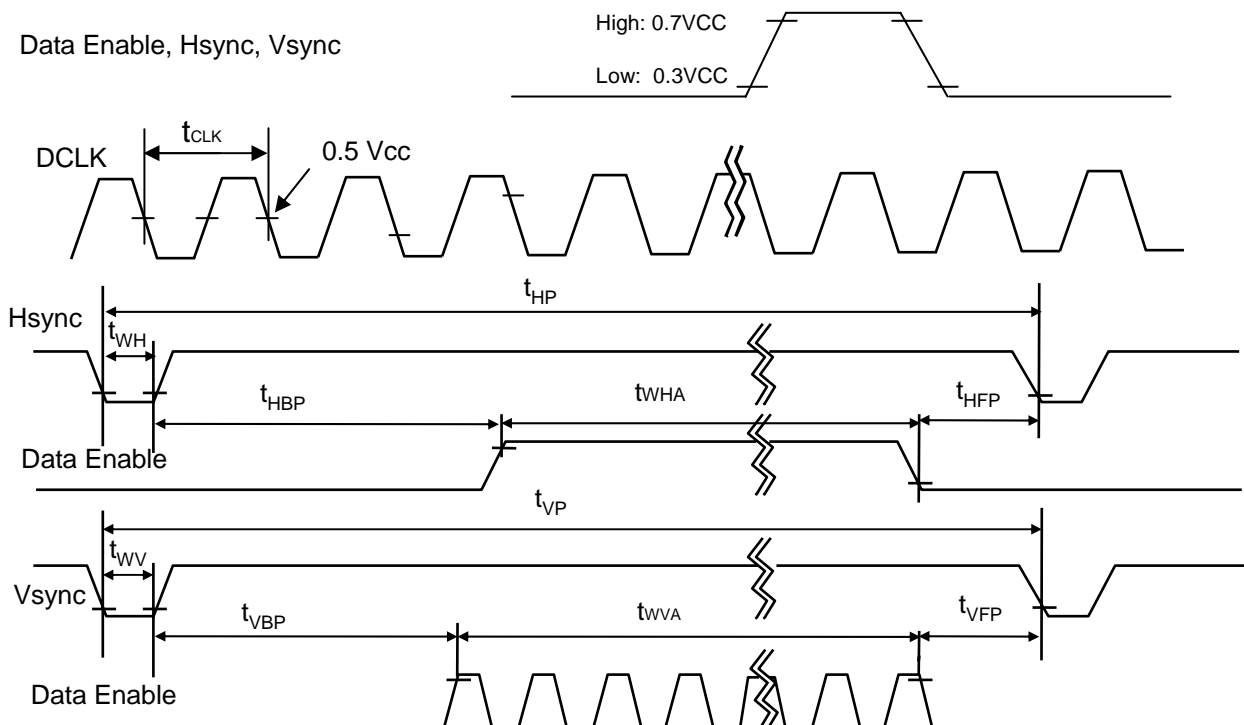
Table 6. TIMING TABLE

ITEM	Symbol		Min	Typ	Max	Unit	Note
DCLK	Frequency	fCLK	-	69.3	-	MHz	
Hsync	Period	Thp	1366	1416	1488	tCLK	
	Width	tWH	16	32	48		
	Active	tWHA	1280	1280	1280		
Vsync	Period	tVP	811	816	847	tHP	
	Width	tWV	3	6	9		
	Active	tWVA	800	800	800		
Data Enable	Horizontal back porch	tHBP	54	80	98	tCLK	
	Horizontal front porch	tHFP	16	24	62		
	Vertical back porch	tVBP	5	6	35	tHP	
	Vertical front porch	tVFP	4	4	4		

3-5. Signal Timing Waveforms

Condition : VCC = 3.3V

Data Enable, Hsync, Vsync



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3-6. Color Input Data Reference

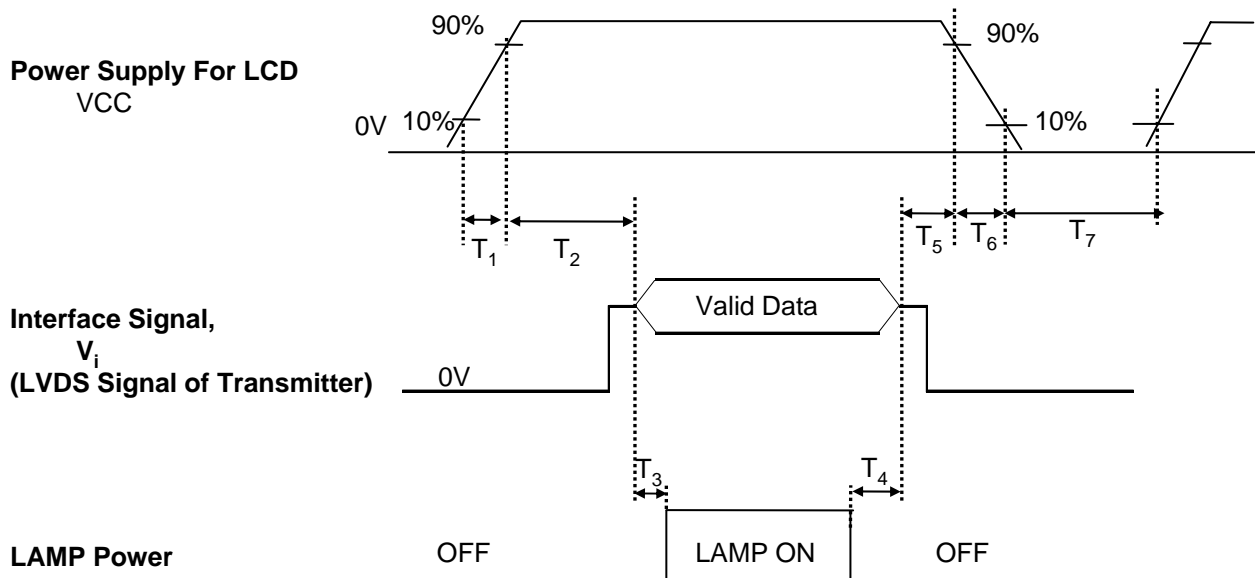
The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color ; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																	
		RED						GREEN						BLUE					
		MSB			LSB			MSB			LSB			MSB			LSB		
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
					
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
					
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BLUE	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
					
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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3-7. Power Sequence


Table 8. POWER SEQUENCE TABLE

Parameter	Value			Units
	Min.	Typ.	Max.	
T ₁	0	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	200	-	-	(ms)

Note)

1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
2. Please avoid floating state of interface signal at invalid period.
3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
4. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

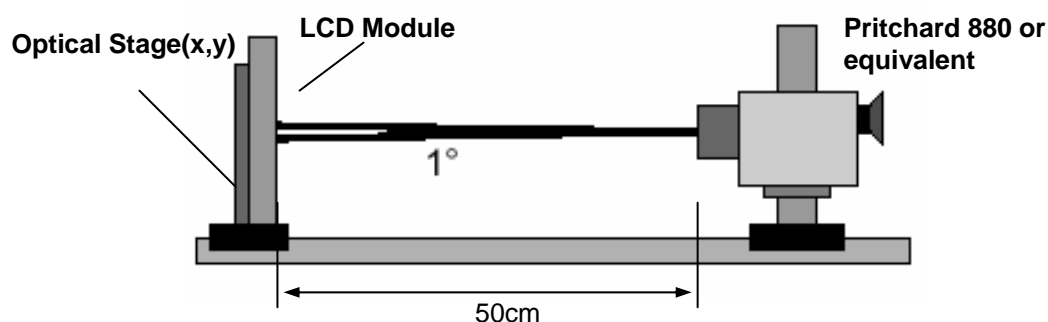


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, fCLK= 69.3MHz, FBL= 60KHz, IBL= 6.0mA

Parameter	Symbol	Values			Units	Notes
		Min	Typ	MAx		
Contrast Ratio	CR	500	-	-		1
Surface Luminance, white	L _{WH}	200	220	-	cd/m ²	2
Luminance Variation	δ_{WHITE}		-	1.7		3
Response Time						4
Rise Time+Decay Time	Tr _R +Tr _D	-	25	35	ms	
Color Coordinates						
RED	RX	0.560	0.590	0.620		
	RY	0.313	0.343	0.373		
GREEN	GX	0.298	0.328	0.358		
	GY	0.510	0.540	0.570		
BLUE	BX	0.131	0.161	0.191		
	BY	0.118	0.148	0.178		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right($\Phi=0^\circ$)	Θ_r	40	45	-	degree	
x axis, left ($\Phi=180^\circ$)	Θ_l	40	45	-	degree	
y axis, up ($\Phi=90^\circ$)	Θ_u	15	20	-	degree	
y axis, down ($\Phi=270^\circ$)	Θ_d	30	35	-	degree	
Gray Scale			-			6

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Note)

1. Contrast Ratio(CR) is defined mathematically as

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH} = \text{Average}(L_1, L_2, \dots L_5)$$

3. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula.
 For more information see FIG 2.

$$\delta_{WHITE} = \frac{\text{Maximum}(L_1, L_2, \dots L_{13})}{\text{Minimum}(L_1, L_2, \dots L_{13})}$$

4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.

6. Gray scale specification

* $f_V = 60\text{Hz}$

Gray Level	Luminance [%] (Typ)
L0	0.24
L7	1.54
L15	5.39
L23	12.07
L31	23.0
L39	37.8
L47	54.7
L55	74.9
L63	100

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FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

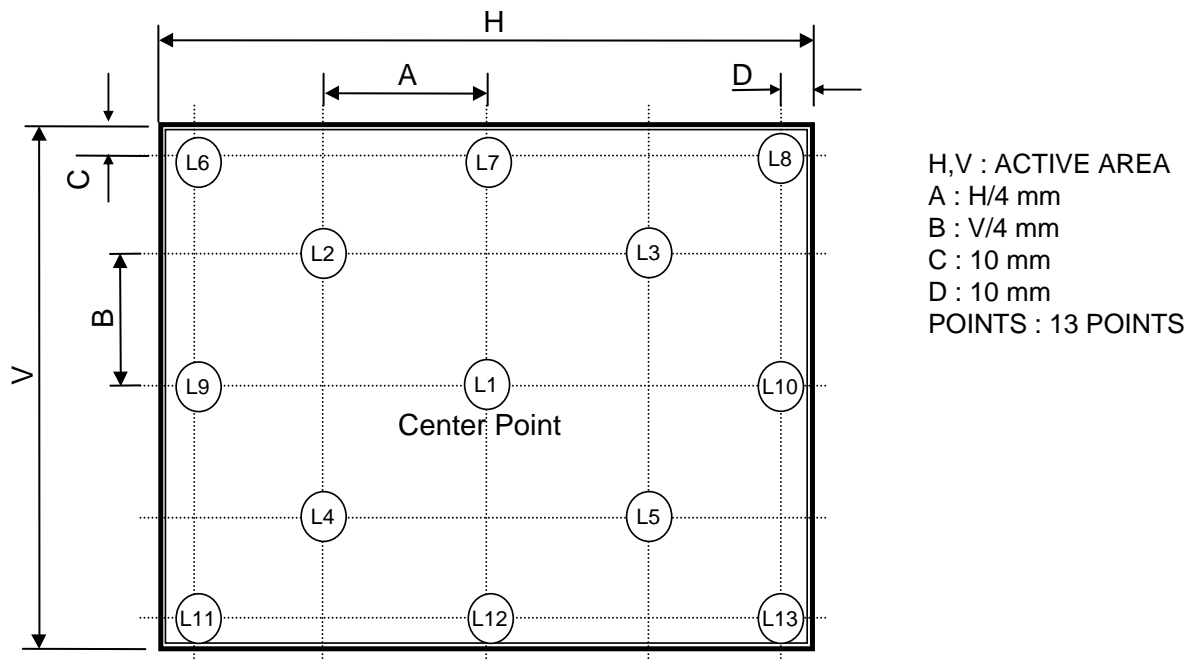
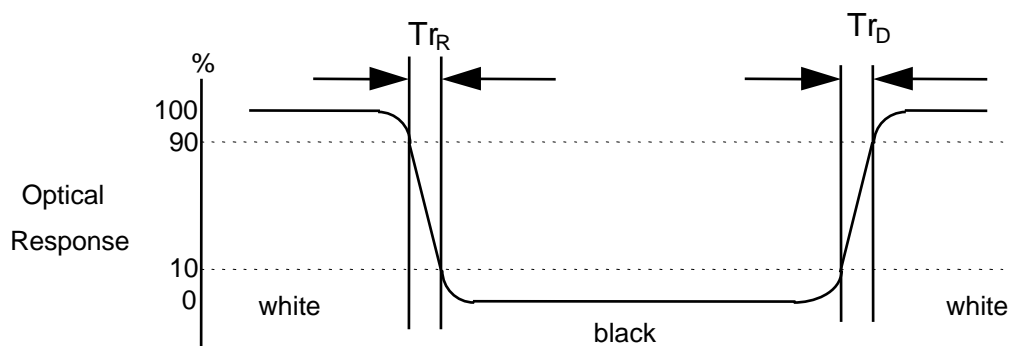


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



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5. Mechanical Characteristics

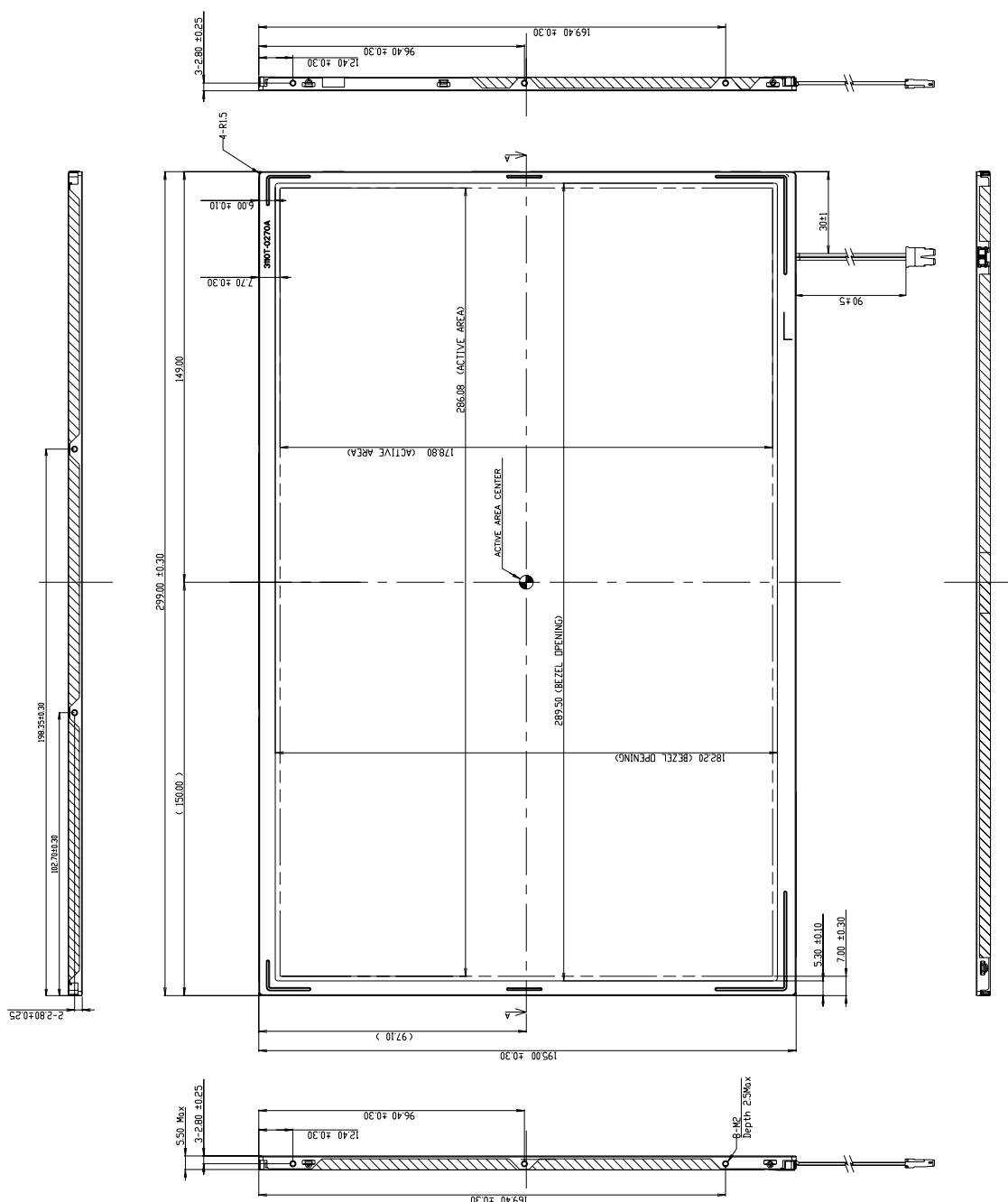
The contents provide general mechanical characteristics for the model LP133WX1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	$299.0 \pm 0.3\text{mm}$
	Vertical	$195.0 \pm 0.3\text{mm}$
	Depth	5.2mm(Typ.),5.5mm(Max.)
Bezel Area	Horizontal	$289.5 \pm 0.3\text{mm}$
	Vertical	$182.2 \pm 0.3\text{mm}$
Active Display Area	Horizontal	286.08 mm
	Vertical	178.8 mm
Weight	350g(Max.)	
Surface Treatment	Glare, hard coating 3H	

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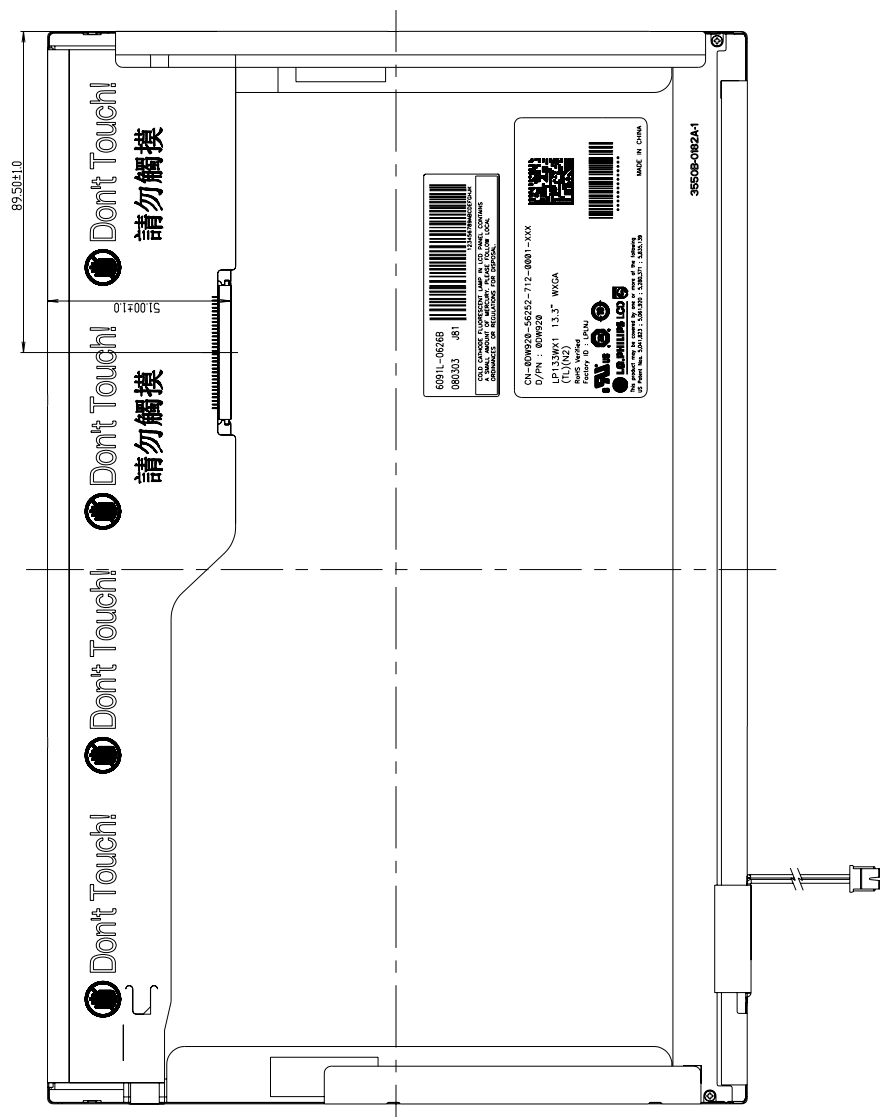
<FRONT VIEW>

Note) Unit:[mm], General tolerance: $\pm 0.5\text{mm}$



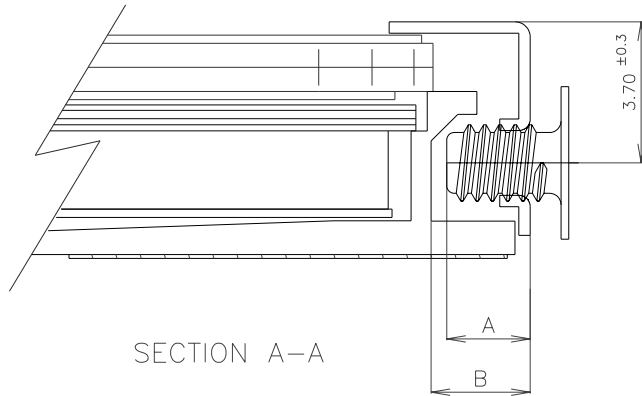
Product Specification

<REAR VIEW>



Product Specification

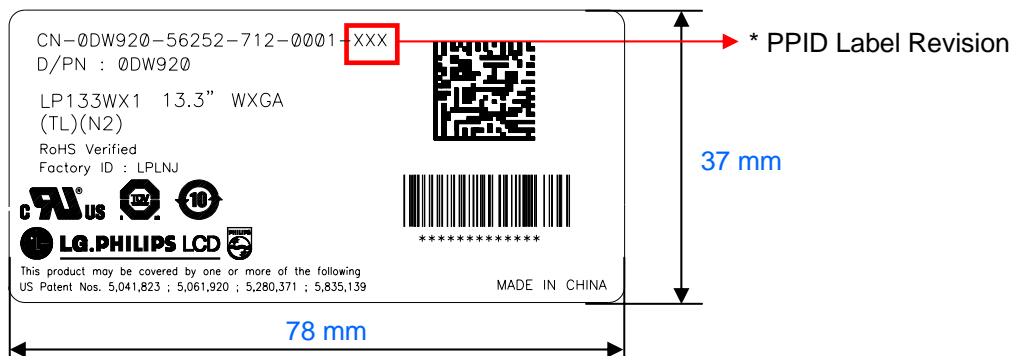
[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



- * Mounting Screw Length (A)
= 2.0(Min) / 2.5(Max)
- * Mounting Screw Hole Depth (B)
= 2.5(Min)
- * Mounting hole location : 3.7(typ.)
- * Torque : 2.5 kgf.cm(Max)
(Measurement gauge : torque meter)

Notes : 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.

[DETAIL INFORMATION OF PPID LABEL AND REVISION CODE]

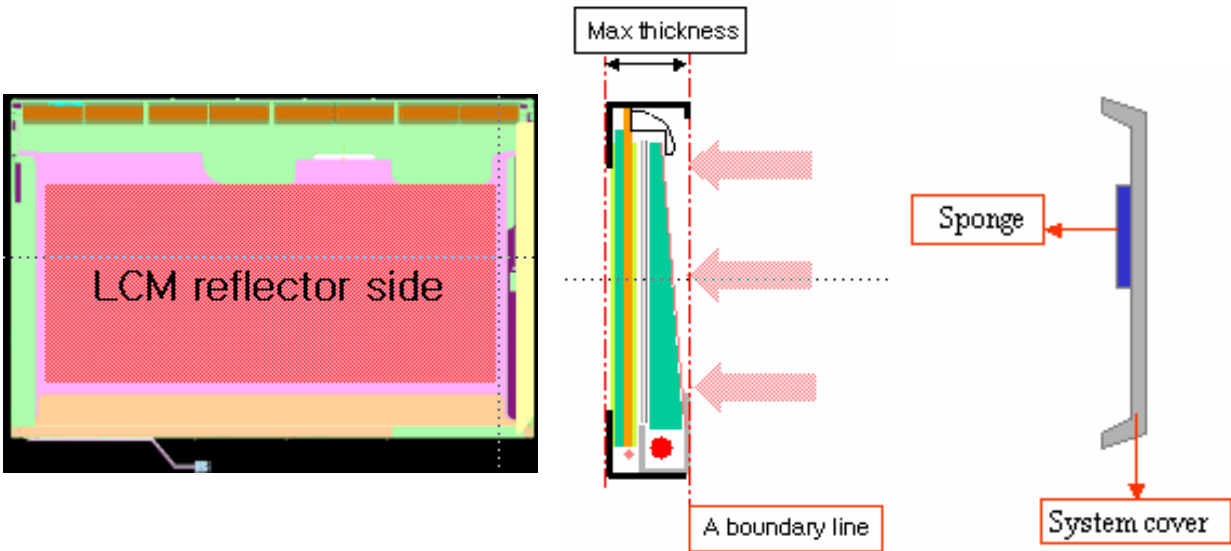
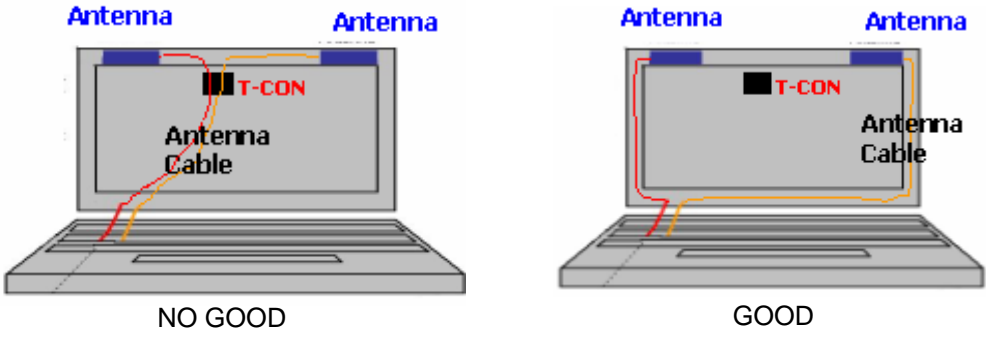


* PPID Label Revision :

It is subject to change with Dell event. Please refer to the below table for detail.

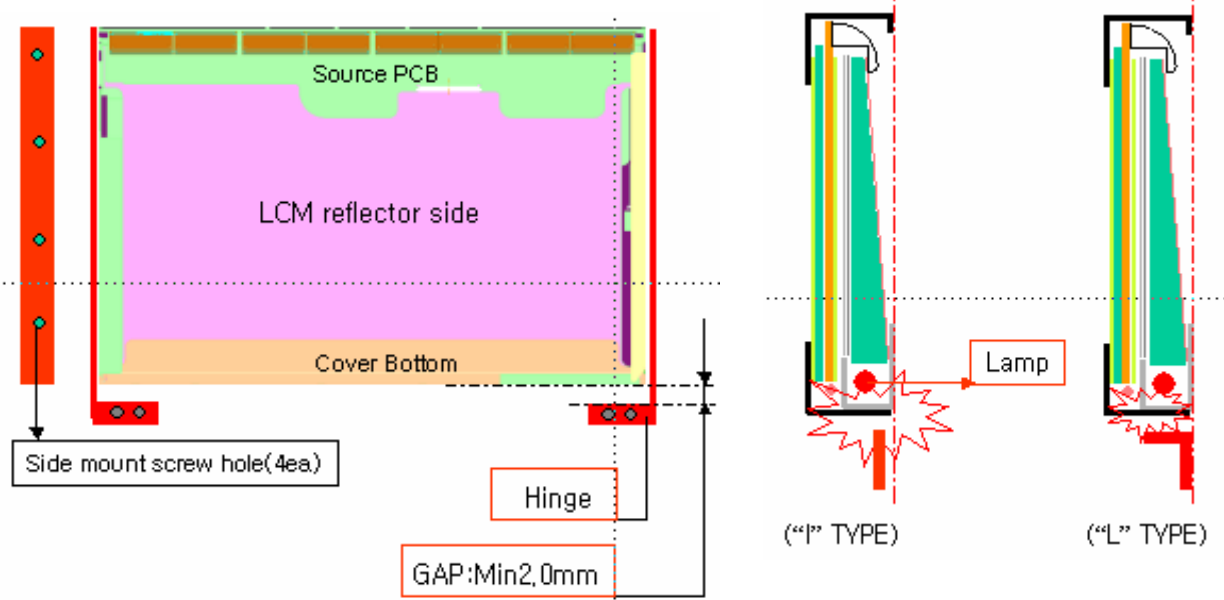
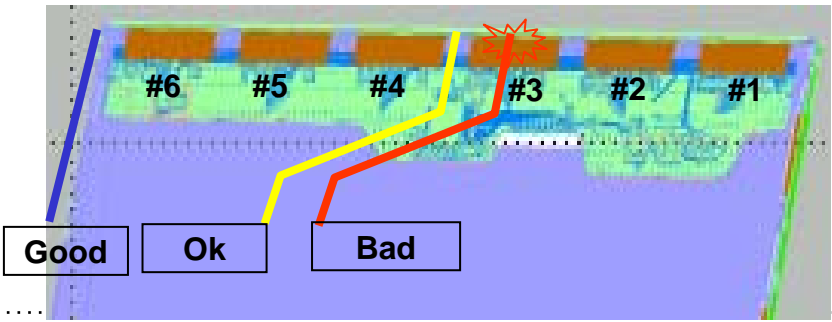
Classification	No Change	1st Revision	2nd Revision	...	9th Revision	...
SST(WS)	X00	X01	X02	...	A09	...
PT(ES)	X10	X11	X12	...	A19	...
ST(CS)	X20	X21	X22	...	A29	...
XB(MP)	A00	A01	A02	...	A09	...

LPL Proposal for system cover design.(Appendix)


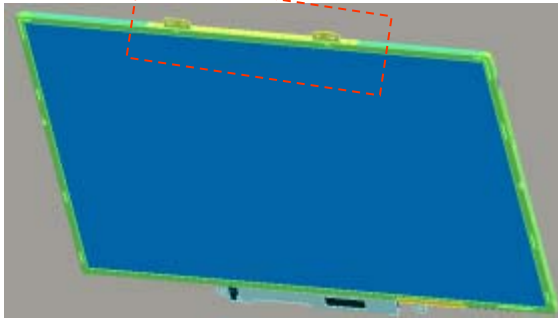
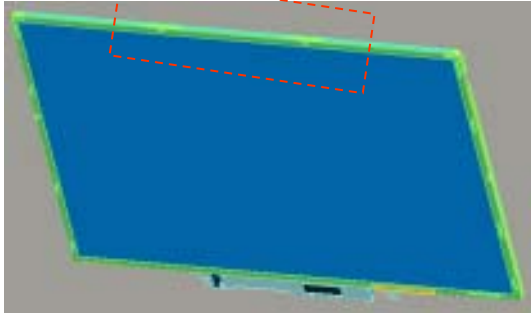
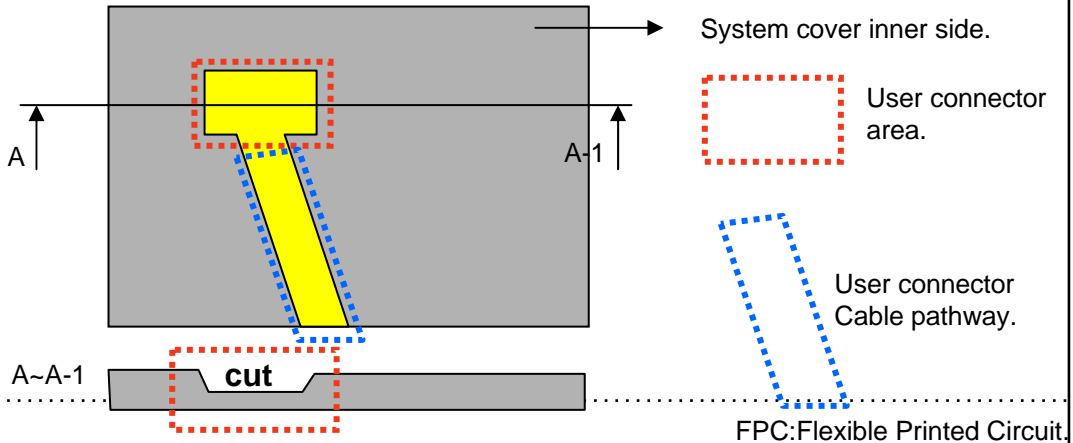
1	Gap check for securing the enough gap between LCM and System cover.	
		
Define	<p>1.Rear side of LCM is sensitive against external stress,and previous check about interference is highly needed.</p> <p>2.In case there is something from system cover comes into the boundary above,mechanical interference may cause the FOS defects. (Eg:Ripple,White spot..)</p>	
2	Check if antenna cable is sufficiently apart from T-CON of LCD Module.	
Define		
	1.If system antenna is overlapped with T-CON,it might be cause the noise.	

Product Specification

LPL Proposal for system cover design.

3	Gap check for securing the enough gap between LCM and System hinge.	
		
Define	<p>1. At least 2.0mm of gap needs to be secured to prevent the shock related defects.</p> <p>2. "L" type of hinge is recommended than "I" type under shock test.</p>	
4	Checking the path of the System wire.	
		
Define	<p>1. COF area needs to be handled with care.</p> <p>2. GOOD → Wire path design to system side.</p> <p>OK → Wire path is located between COFs.</p> <p>BAD → Wire path overlapped with COF area.</p>	

LPL Proposal for system cover design.

5	Using a bracket on the top of LCM is not recommended.	
<div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;">  <p>bracket</p>  <p>With bracket</p> </div> <div style="text-align: center;">  <p>Without bracket</p> </div> </div>		
Define	<p>1.Condition without bracket is good for mechanical noise,and can minimize the light leakage from deformation of bracket.</p> <p>2.The results shows that there is no difference between the condition with or without bracket.</p>	
6	Securing additional gap on CNT area..	
		
Define	<p>1.CNT area is specially sensitive against external stress,and additional gap by cutting on system cover will be helpful on removing the Ripple.</p> <p>2.Using a thinner CNT will be better. (eg: FPC type)</p>	

Product Specification

6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(l.e. run 180G 6ms for all six faces)
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

7. International Standards

7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition, European Committee for Electrotechnical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)
E : MONTH

D : YEAR
F ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box : 30 pcs

b) Box Size : 475.0 mm × 348.0 mm × 274.0 mm

9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Product Specification
APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)	
0	00	Header	0 0	0000 0000	Header
1	01	Header	F F	1111 1111	
2	02	Header	F F	1111 1111	
3	03	Header	F F	1111 1111	
4	04	Header	F F	1111 1111	
5	05	Header	F F	1111 1111	
6	06	Header	F F	1111 1111	
7	07	Header	0 0	0000 0000	Vender/ Product ID
8	08	ESA manufacturer code (3 Character ID) = LPL	3 2	0011 0010	
9	09	ESA manufacture code (Compressed ASCII)	0 C	0000 1100	
10	0A	PanelSupplierReserved - Productcode	0 0	0000 0000	
11	0B	PanelSupplierReserved - Productcode	0 0	0000 0000	
12	0C	LCD Module SerialNo. = 0 (If not used)	0 0	0000 0000	
13	0D	LCD Module SerialNo. = 0 (If not used)	0 0	0000 0000	
14	0E	LCD Module SerialNo. = 0 (If not used)	0 0	0000 0000	
15	0F	LCD Module SerialNo. = 0 (If not used)	0 0	0000 0000	
16	10	Week of Manufacture = 00	0 0	0000 0000	EDID Version/ Revision
17	11	Year of Manufacture = 2008	1 2	0001 0010	
18	12	EDID Structure version # = 1	0 1	0000 0001	
19	13	EDID Revision # = 3	0 3	0000 0011	Display Parameter
20	14	Video Input Definition = Digital signal, 6 bit DeHony	9 0	1001 0000	
21	15	Max H image size (cm) = 28.61cm (29)	1 D	0001 1101	
22	16	Max V image size (cm) = 17.88cm (18)	1 2	0001 0010	
23	17	Display gamma = 2.2	7 8	0111 1000	
24	18	Feature support (DPM S) = Active off, RGB Color	0 A	0000 1010	Color Characteristic
25	19	Red/Green Low Bits	3 1	0011 0001	
26	1A	Blue/White Low Bits	4 5	0100 0101	
27	1B	Red X = 0.590	9 7	1001 0111	
28	1C	Red Y = 0.343	5 7	0101 0111	
29	1D	Green X = 0.328	5 4	0101 0100	
30	1E	Green Y = 0.540	8 A	1000 1010	
31	1F	Blue X = 0.161	2 9	0010 1001	
32	20	Blue Y = 0.148	2 6	0010 0110	
33	21	White X = 0.313	5 0	0101 0000	Established Timings
34	22	White Y = 0.329	5 4	0101 0100	
35	23	Established timings 1 (00h if not used)	0 0	0000 0000	
36	24	Established timings 2 (00h if not used)	0 0	0000 0000	Standard Timing ID
37	25	Manufacturer's timings (00h if not used)	0 0	0000 0000	
38	26	Standard Timing Identification 1 was not used	0 1	0000 0001	
39	27	Standard Timing Identification 1 was not used	0 1	0000 0001	
40	28	Standard Timing Identification 2 was not used	0 1	0000 0001	
41	29	Standard Timing Identification 2 was not used	0 1	0000 0001	
42	2A	Standard Timing Identification 3 was not used	0 1	0000 0001	
43	2B	Standard Timing Identification 3 was not used	0 1	0000 0001	
44	2C	Standard Timing Identification 4 was not used	0 1	0000 0001	
45	2D	Standard Timing Identification 4 was not used	0 1	0000 0001	
46	2E	Standard Timing Identification 5 was not used	0 1	0000 0001	
47	2F	Standard Timing Identification 5 was not used	0 1	0000 0001	
48	30	Standard Timing Identification 6 was not used	0 1	0000 0001	
49	31	Standard Timing Identification 6 was not used	0 1	0000 0001	
50	32	Standard Timing Identification 7 was not used	0 1	0000 0001	
51	33	Standard Timing Identification 7 was not used	0 1	0000 0001	
52	34	Standard Timing Identification 8 was not used	0 1	0000 0001	
53	35	Standard Timing Identification 8 was not used	0 1	0000 0001	

Product Specification
APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)	
54	36	PixelClock/10,000 (LSB)	1 2	0001 0010	Detailed Timing Description #1
55	37	PixelClock/10,000 (MSB) / 1280 x 800 @ 60Hz pixelclock = 69.3MHz	1 B	0001 1011	
56	38	HorizontalActive = 1280 pixels	0 0	0000 0000	
57	39	HorizontalBlanking = 136 pixels	8 8	1000 1000	
58	3A	HorizontalActive : HorizontalBlanking = 1280 : 136	5 0	0101 0000	
59	3B	VerticalActive = 800 lines	2 0	0010 0000	
60	3C	VerticalBlanking = 17 lines	1 1	0001 0001	
61	3D	VerticalActive : VerticalBlanking = 800 : 16	3 0	0011 0000	
62	3E	HorizontalSync Offset = 24 pixels	1 8	0001 1000	
63	3F	HorizontalSync Pulse Width = 32 pixels	2 0	0010 0000	
64	40	VerticalSync Offset = 4 lines : Sync Width = 6 lines	4 6	0100 0110	
65	41	HorizontalVertical Sync Offset/Width upper 2bits = 0	0 0	0000 0000	
66	42	Horizontal Image Size = 286mm	1 E	0001 1110	
67	43	Vertical Image Size = 179mm	B 3	1011 0011	
68	44	Horizontal & Vertical Image Size	1 0	0001 0000	Detailed Timing Description #2
69	45	HorizontalBorder = 0	0 0	0000 0000	
70	46	VerticalBorder = 0	0 0	0000 0000	
71	47	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol negatives	1 B	0001 1011	
72	48	PixelClock/10,000 (LSB)	1 2	0001 0010	
73	49	PixelClock/10,000 (MSB) / 1280 x 800 @ 60Hz pixelclock = 69.3MHz	1 B	0001 1011	
74	4A	HorizontalActive = 1280 pixels	0 0	0000 0000	
75	4B	HorizontalBlanking = 136 pixels	8 8	1000 1000	
76	4C	HorizontalActive : HorizontalBlanking = 1280 : 136	5 0	0101 0000	
77	4D	VerticalActive = 800 lines	2 0	0010 0000	
78	4E	VerticalBlanking = 17 lines	1 1	0001 0001	
79	4F	VerticalActive : VerticalBlanking = 800 : 16	3 0	0011 0000	
80	50	HorizontalSync Offset = 24 pixels	1 8	0001 1000	
81	51	HorizontalSync Pulse Width = 32 pixels	2 0	0010 0000	
82	52	VerticalSync Offset = 4 lines : Sync Width = 6 lines	4 6	0100 0110	Detailed Timing Description #3
83	53	HorizontalVertical Sync Offset/Width upper 2bits = 0	0 0	0000 0000	
84	54	Horizontal Image Size = 286mm	1 E	0001 1110	
85	55	Vertical Image Size = 179mm	B 3	1011 0011	
86	56	Horizontal & Vertical Image Size	1 0	0001 0000	
87	57	HorizontalBorder = 0	0 0	0000 0000	
88	58	VerticalBorder = 0	0 0	0000 0000	
89	59	Non-interlaced, Normal display, no stereo, Digital separate sync, H/V pol negatives	1 B	0001 1011	
90	5A	Flag	0 0	0000 0000	
91	5B	Flag	0 0	0000 0000	
92	5C	Flag	0 0	0000 0000	
93	5D	Dummy Descriptor	F E	1111 1110	
94	5E	Flag	0 0	0000 0000	
95	5F	DeLP/N 1st Character = D	4 4	0100 0100	
96	60	DeLP/N 2nd Character = W	5 7	0101 0111	
97	61	DeLP/N 3rd Character = 9	3 9	0011 1001	
98	62	DeLP/N 4th Character = 2	3 2	0011 0010	
99	63	DeLP/N 5th Character = 0	3 0	0011 0000	
100	64	LCD Supplier EED Revision # = A00	8 0	1000 0000	
101	65	Manufacturer P/N = 1	3 1	0011 0001	
102	66	Manufacturer P/N = 3	3 3	0011 0011	
103	67	Manufacturer P/N = 3	3 3	0011 0011	
104	68	Manufacturer P/N = W	5 7	0101 0111	
105	69	Manufacturer P/N = X	5 8	0101 1000	
106	6A	Manufacturer P/N = 1	3 1	0011 0001	
107	6B	Manufacturer P/N (If < 13 char, then terminate with ASCII code 0Ah, set remain in ch	0 A	0000 1010	

Product Specification
APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte# (decimal)	Byte# (HEX)	Field Name and Comments	Value (HEX)	Value (binary)	
108	6C	Flag	0 0	0000 0000	Detailed Timing Description #4
109	6D	Flag	0 0	0000 0000	
110	6E	Flag	0 0	0000 0000	
111	6F	Data Type Tag : ASCII String	0 0	0000 0000	
112	70	Flag	0 0	0000 0000	
113	71	SM BUS Value = 10 nits	1 F	0001 1111	
114	72	SM BUS Value = 17 nits	3 6	0011 0110	
115	73	SM BUS Value = 24 nits	4 2	0100 0010	
116	74	SM BUS Value = 30 nits	4 C	0100 1100	
117	75	SM BUS Value = 60 nits	6 B	0110 1011	
118	76	SM BUS Value = 100 nits	8 A	1000 1010	
119	77	SM BUS Value = 160 nits	B 2	1011 0010	
120	78	SM BUS Value = Max (Typically = FFh, 220nits)	F F	1111 1111	
121	79	Number of LVDS receiver chips = 1 or 2	0 1	0000 0001	
122	7A	BST Enable: Yes = 01' No = 00'	0 1	0000 0001	
123	7B	(If <13 char, then terminate with ASCII code 0Ah, set remaining char=20h)	0 A	0000 1010	
124	7C	(If <13 char, then terminate with ASCII code 0Ah)	2 0	0010 0000	
125	7D	(If <13 char, then terminate with ASCII code 0Ah)	2 0	0010 0000	
126	7E	Extension flag = 00	0 0	0000 0000	Extension Flag
127	7F	Checksum	6 2	0110 0010	Checksum