

General Motors 8.0" WVGA GM part number 23498633 INX part name DJ080EA-01G part number GD0800EA00340

Product Specification

GM 8.0" WVGA

Product code: [DJ080EA-01G]

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Signature:	Signature: :_	

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1 REVISIONS

Number	Module	Item	Date
A		Draft Specification	JUNE 5th 2014
A1		3.1/4/8.1.3/9.3.2/11/16	Jan 6 th 2015
		`	
	,		

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2 FEATURES

The active matrix color TFT LCD module has an 8.0 inch diagonal active area containing 800xRGB x 480 pixels and has an 8-bits (x3) colors capability. The TFT is in aSi (Amorphous Silicon) technology. Display drivers are placed on the panel. The module further includes LED backlight and electronics and LED driver on a PCBA.

Typical Applications are automotive driver information, infotainment, navigation, and telematics systems.

The product is designed for the requirement of the green product, and the specification complies with INX's "Green Product Chemical Substance Specification Standard Hand Book".

3 REFERENCE DOCUMENTS

- GIS-398 Directed Buy Display Requirements Specification [Revision 1.2 / June 29, 2011] *
- Component Technical Specification (CTS) Directed Buy Color TFT Displays [Revision 0.6draft / June 28, 2011] *
- SPI doc. No: 20110323001AA
- Application notes:
 - o INX Application Note Mounting INX TFT Module
 - \circ Thread forming screws Mounting INX TFT Module
 - o A brief note on thermal management for LCD module
 - 0
 - Note: In the event there is a conflict between the GM document (GIS) and this INX Specification, the GM document (GIS) will take precedence, except for the below listed approved deviations, for which this INX Specification will take precedence.

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3.1 Approved deviations:

Original:			Approved Deviation:
GM Spec.	Section		CMI Spec. Section: / Or CMI Deviation Commment:
			Section 5.2 Module Drawing
CTS	2.4	\rightarrow	Section 5.3 Exploded Drawing
CTS	2.5.2	\rightarrow	Section 6.1 I/O Pinning
CTS	2.8	\rightarrow	Section 5.4 Main ID Label Drawing
			Deviatoin Comment: Some test set-ups need to be redefined to equivalent lab testing
			since CMI can not test on vehicle assembled level. Some tests are intended for
GIS-398	-050	\rightarrow	system supplier rather than for the display supplier.
			Deviatoin Comment:
			4.2" Size: Thickness 12 [mm] okay when excluding 'mounting features'
			8.0" Size: Height 120.7 [mm] (iso 120.0 [mm])
			8.0" Size: Thickness 12 [mm] (iso 13.5 [mm]), excl. 'mounting features'
			4.2" Contrast Ratio: 1:1000 MIN / 1:1800 TYP (iso 1:1100 MIN / 1:2000 TYP)
			4.2" Reflectivity: SCI 5.5% / SCE 1.9% TYP (iso 7%)
			8.0" Reflectivity: SCI 2.6% / SCE 0.6% TYP (iso 1%)
GIS-398	-070	\rightarrow	[due to change from AGAR to AGLR]
GIS-398	-080	\rightarrow	Section 11. Optical Characteristics (Luminance & Contrast)
GIS-398	-090	\rightarrow	Section 11. Optical Characteristics (Dimming Ratio)
GIS-398	-100	\rightarrow	"environmental temperature" is assumed to be 'panel surface' for the Display
GIS-398	-110	\rightarrow	Section \11. Optical Characteristics (Response Time)
GIS-398	-130	\rightarrow	Section 11. Optical Characteristics (Luminance Homogeneity)
GIS-398	-140	\rightarrow	Section 15.1.15 Functional Optical check
			Deviatoin Comment: CR>10:1 is difficult at 45klx, due to display is transmissive, not
			transflective. CR depends very much on front surface reflection and brightness out of
GIS-398	-150	\rightarrow	the display and viewing angle. Must be verified on the samples inside the application
GIS-398	-160	\rightarrow	Section 11. Optical Characteristics (Surface Reflectance)
GIS-398	-180	\rightarrow	Section 11. Optical Characteristics (Colour-point)
GIS-398	-200	\rightarrow	Deviatoin Comment: CMI will use H3 "According to JIS K5600"
GIS-398	-210	\rightarrow	Deviatoin Comment: "Not Applicable" as Tier1 will add the Application Protection Film
GIS-398	-230	\rightarrow	Section 11. Optical Characteristics (Polariser absorbtion angle)
			Deviatoin Comment: From -20degC to 75degC with reduced brightness and contrast
GIS-398	-250	\rightarrow	and response time according to the panel temperature (typical TFT behavior)
GIS-398	-270	\rightarrow	Section 15. Quality Requirements

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4 GENERAL SPECIFICATIONS

Key Parameters	Description	Unit
Display size (diagonal)	8.0	inch
Aspect ratio	15:9	-
Number of dots (H x V)	800 x RGB x 480	dot
Color arrangement	RGB Stripe	-
LCD type	Transmissive, aSi	-
Image mode	Normally Black	-
Brightness	min. 450 / typ. 650	Cd/m ²
Operating Temperature	-40 to +85	°C
Functional Temperature	-40 to +90	°C
Storage Temperature	-40 to +95	°C
Electronics and interface		
Interface method	Parallel RGB	-
	(HSY+VSY + DE mode)	
Color depth	RGB 8bits=16M	-
Mechanical dimensions		
Module outline dimension	191.8 x 120.7 x 12.0	mm
(H x V x T)	(excluding mounting features)	
Active area (H x V)	174.0 x 104.4	mm
Dot pitch (H x V)	0.2175 x 0.2175	mm
Subpixel pitch (H x V)	0.0725 x 0.2175	mm
Weight	370 +/- 10	g
Basic display features		
Surface treatment	AG380LR	-
Visibility with polarized	Visible	-
sunglasses		
Power consumption(typ.)	5.5	W

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4.1 Basic features

- 3x8bits (or 3x6 option) RGB with DE, Hsync, Vsync and clock input
- COG including timing controller
- Internal NTC Temperature sensor
- Left / right and top / bottom scanning
- Integrated LED backlight driving with high dimming ratio

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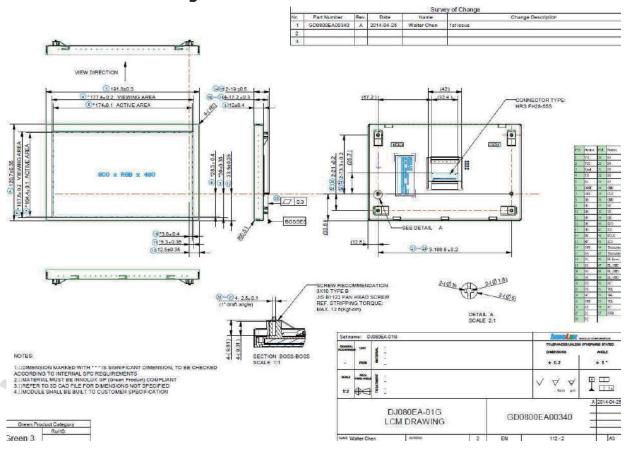
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5 MECHNICAL DESCRIPTION

5.1 Module key components

- TFT panel
- · PCBA including LED driving circuit
- · LED backlight, including light guide and optical foils
- Metal frame(s)

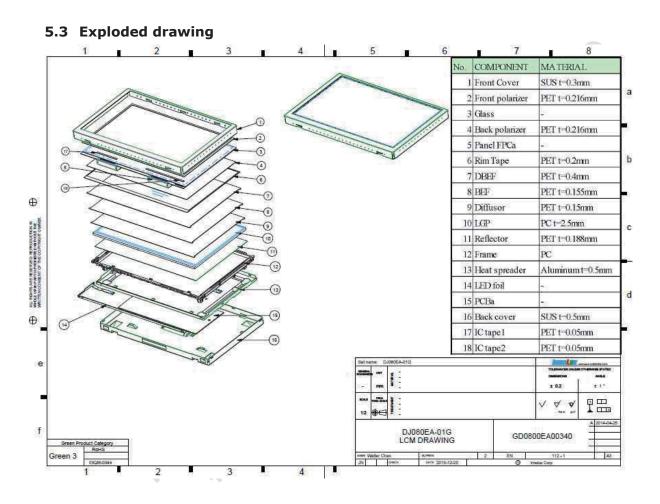
5.2 Module drawing



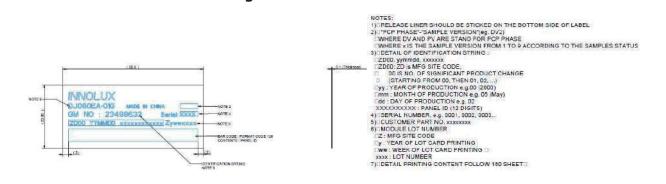
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5.4 Main ID label drawing



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6 INPUT / OUTPUT PINNING AND CONNECTOR DESCRIBTION

6.1 I/O Pinning

Recommend Connector type:

Input connector 55 Pole

FH28-55S-0.5SH(05)

PIN No.	Symbol	I/O	Function
1	VCC	Power	External main and I/O power supply ; Power3V3
2	VCC	Power	External main and I/O power supply : Power3V3
3	RESET	Input	Reset panel Driver IC and LED Driver IC
			RESET = 0:reset
			RESET = 1:normal operation
4	ТВ	Input	Vertical shift direction (gate output) selection.
			TB = 0: Bottom->Top
			TB = 1: Top ->Bottom (default: Customer to Pull high, internal IC Pull high*)
5	RL	Input	Horizontal shift direction (source output) selection.
			RL = 1: Left -> Right(default: Customer to Pull high, internal IC Pull high*)
			RL = 0: Right -> Left
6	DINT	Input	Input data format selection.
			DINT = 0:6-bits mode
			DINT = 1:8-bits mode(default: Customer to Pull high, internal IC Pull high*)
7	GND	Power	Ground
8	В0	Input	Blue Data (for 6bits mode, customer pull low, IC internally are pulled high*)
9	B1	Input	Blue Data (for 6bits mode, customer pull low, IC internally are pulled low*)
10	B2	Input	Blue Data (6bits B0 / 8bits B2)
11	В3	Input	Blue Data (6bits B1 / 8bits B3)
12	B4	Input	Blue Data (6bits B2 / 8bits B4)
13	B5	Input	Blue Data (6bits B3 / 8bits B5)
14	В6	Input	Blue Data (6bits B4 / 8bit B6)
15	В7	Input	Blue Data (6bit B5 / 8bit B7)
16	GND	Power	Ground
17	G0	Input	Green Data (for 6bits mode, customer pull low, IC internally are pulled
			high*)
18	G1	Input	Green Data(for 6bits mode, customer pull low, IC internally are pulled low*)
19	G2	Input	Green Data (6bit G0 / 8bit G2)
20	G3	Input	Green Data (6bit G1 / 8bit G3)
21	G4	Input	Green Data (6bits G2 / 8bits G4)

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22	G5	Input	Green Data (6bits G3 / 8bits G5)
23	G6	Input	Green Data (6bits G4 / 8bits G6)
24	G7	Input	Green Data (6bits G5 / 8bits G7)
25	GND	Power	Ground
26	R0	Input	Red Data(for 6bits mode, customer pull low, IC internally are pulled high*)
27	R1	Input	Red Data(for 6bits mode, customer pull low, IC internally are pulled low*)
28	R2	Input	Red Data (6bits R0 / 8bits R2)
29	R3	Input	Red Data (6bits R1 / 8bits R3)
30	R4	Input	Red Data (6bits R2 / 8bits R4)
31	R5	Input	Red Data (6bits R3 / 8bits R5)
32	R6	Input	Red Data (6bits R4 / 8bits R6)
33	R7	Input	Red Data (6bits R5 / 8bits R7)
34	GND	Power	Ground
35	CLK	Input	Pixel clock
36	GND	Power	Ground
37	HS	Input	Horizontal sync
38	VS	Input	Vertical sync
39	DE	Input	Data Enable
40	SDO	Output	SPI interface
41	SDI	Input	SPI interface, if no use, pin should pull low
42	SCLK	Input	SPI interface, if no use, pin should pull low
43	SCS	Input	SPI interface, if no use, pin should pull high
44	Thermistor	Output	NTC connection thermistor 1 : at module PCBa
45	Thermistor	Output	NTC connection thermistor 2 : at module backlight LED FPCa.
46	BL Error	Not Used	Pull high / No function
47	BLGND	Power	Backlight's Ground
48	BLGND	Power	Backlight's Ground
49	BLGND	Power	Backlight's Ground
50	NC		
51	VBL	Power	Backlight voltage
52	VBL	Power	Backlight voltage
53	VBL	Power	Backlight voltage
54	NC		
55	PWM	Input	Digital PWM Dimming Input

* Note: Source driver internal pull high / pull low data

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Pull low/ high resistor	R_{l}	125	250	375	kΩ	For I/O circuit

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7 ABSOLUTE MAXIMUM RATINGS

There is a difference between the maximum value of a parameter's specification and its absolute maximum value. The maximum value indicates that the performance will be reduced when you go beyond this value, but this is reversible. Where the absolute maximum value as indicated in this section is a value beyond which permanent damage to the product or its function may be expected.

Item	Symbol	Min	Тур	Max	Unit	Remark
Input supply voltage	VCC	-0.3		3.6	V	4
Backlight supply voltage	VBL	-0.3	12	, 25	V	
Logic input voltage	Vin	-0.3		3.6	٧	
Logic output voltage	Vout	-0.3		3.6	V	
Relative humidity	RH	-	-	90	%	@ 60 ℃, Note 1
Operation temperature	OTR	-40	`	85	${\mathbb C}$	Note 2
Functional temperature	FTR	-40		90	${\mathcal C}$	Note 2
Storage temperature	STR	-40		95	${\mathcal C}$	

Note 1: No condensation allowed under any condition

Note 2: Panel surface temperature should not exceed 90°C. For temperature ranges +85°C to +90°C, any picture quality, electrical characteristic, optical characteristic, etc, are not guaranteed, however, the display module will operate and will be readable/legible. The display module should not be operated continuously under the extreme temperatures for more than 24 hours

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8 ELECTRICAL CHARACTERISTICS

8.1 Electrical description and block diagram

8.1.1 Description display electronics

The display module comes with a 8 bits (6-bits) RGB digital interface. The display's data and synchronization signals (DE, CLK,HS, VS,...), which generates all necessary control signals for the source and gate drivers. DC-DC converter on the module produces the voltages required for driving of panel. Please refer to the block diagram in section 8.1.3.

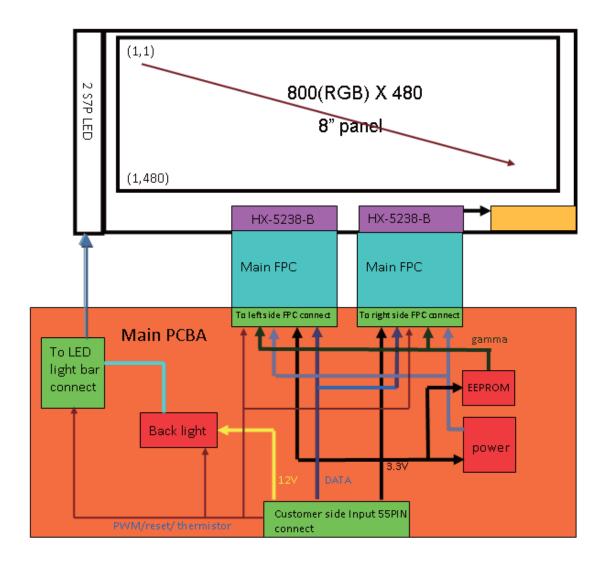
8.1.2 Description backlight electronics

LED driver electronic includes LED driver, dimming control and LED driver is a boost converter used to step-Up the input voltage of +12V for driving the LED backlight. PWM dimming control pin is provided to control the brightness of LED backlight.

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8.1.3 Block diagram

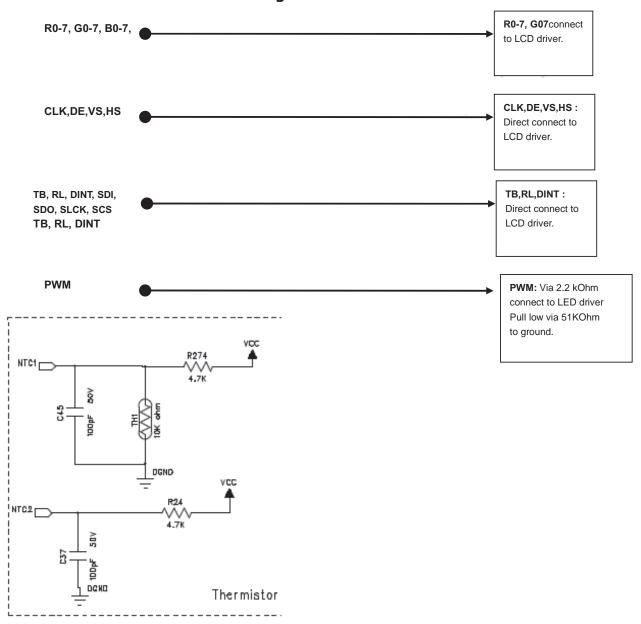


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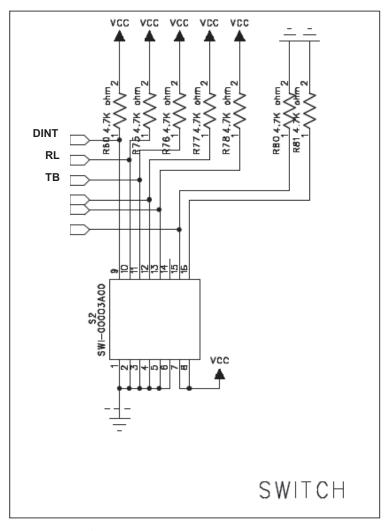
8.1.4 Electrical Block diagram



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** Application circuit example of DINT \ RL \ TB **

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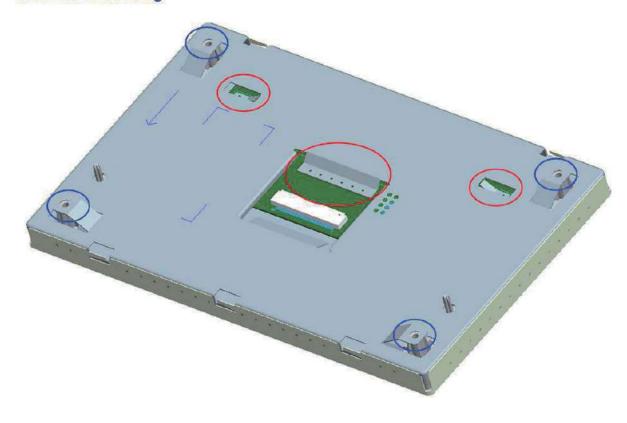


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8.1.5 Grounding Block diagram

Internal Grounding

External Grounding



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8.2 Recommended operating conditions

8.2.1 Recommended voltage supply range

Typical Voltage rating

Parameter	Symbol	Min.	Тур.	Max	Unit
Input supply voltage	VCC	3	3.3	3.6	Volt
Allowed ripple voltage		-100		100	mV
Input supply current *1	I_{VCC}	100	120	150	mA
Inrush current supply voltage *2	I_{VCC}	-	-	8	Α
Parasitic Current (VCC) *5	I_{VCC}	-	-	5	mA
Backlight supply voltage	VBL	8	12	18	Volt
Backlight supply current *3	I_{VBL}	300	400	500	mA
Inrush current backlight *4	I_{VBL}	-	-	28	Α
Parasitic Current (VBL) *5	I_{VBL}	-	-	50	uA
Logic low level input voltage	VIL	0	-	0.3VCC	Volt
Logic high level input voltage	VIH	0.7VCC	-	VCC	Volt
Logic Low level output voltage	VOL	GND	-	GND+0.4	Volt
Logic high level output voltage	VOH	VCC-0.4	-	-	Volt

^{*1:} Based on PV sample with white pattern and VCC is 3.3V.

*5: Based on PV sample measured I_{VCC} (VCC=3.3V) and I_{VBL} (VBL=12V) when RESET pin is pulled low.

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^{*2:} Based on PV sample measured with current probe after VCC below 0.3V. The duration of peak current is below 40 micron sec.

^{*3:} Based on PV sample with white pattern and VBL is 12V.

^{*4:} Based on PV sample measured with current probe after VBL below 0.3V. The duration of peak current is below 40 micron sec.



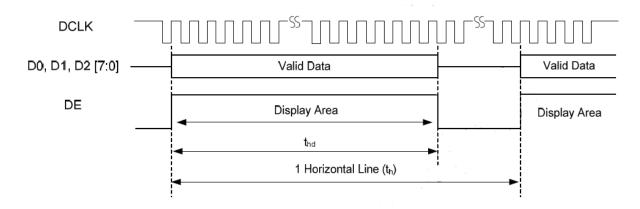
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8.2.2 Capacitive input characteristics on power supply pins

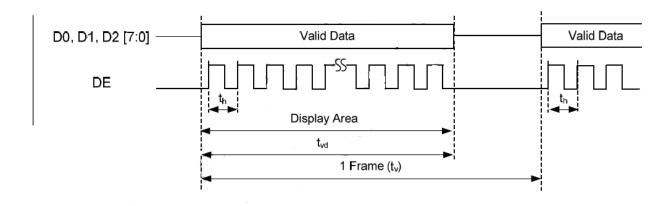
VBL 12V Input capacitance is 24.2uF $\pm 10~\%$ VCC 3V3 Input capacitance is 49.6uF $\pm 10~\%$

8.2.3 Input characteristics for TTL mode signals Only DE mode

Horizontal



Vertical



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8.3 Backlight control

8.3.1 PWM Dimming input.

The brightness can be controlled using an external PWM input. The device accepts PWM frequency up to 20 kHz and a minimum pulse width down to 1 μ sec. A 5000:1 dimming ratio is achieved at a frequency of 200 Hz. A logic high signal enables all two current sources and a logic low disables them.

8.3.2 PWM Dimming Function of LED Driver

Item	Symbol	Min	Тур	Max	Unit
PWM Freq	PWM Frequency	200			Hz
PWM Voltage Level	PWM H	3.0		5.5	V
F vvivi voitage Level	PWM L	0	-	0.8	V

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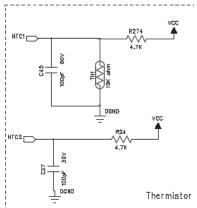


General Motors 8.0" WVGA GM part number 23498633 INX part name DJ080EA-01G part number GD0800EA00340

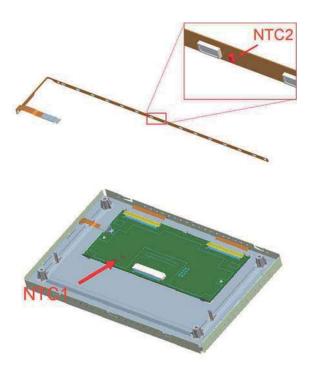
8.4 Backlight NTC resistor

Type: NCP15XH103F03RC / Murata (Refer to data sheet.) The nominal value of the NTC is around 10kohm at 25 degrees.

Refer to data sheet.



Note: NTC1 and NTC2 with same design

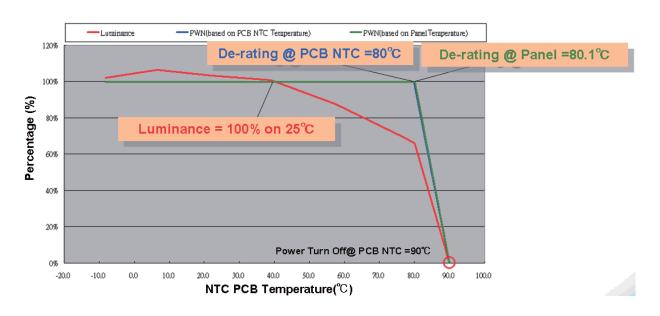


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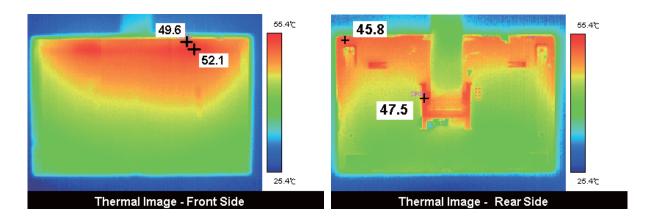


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8.5 Recommend de-rating curve



8.6 Thermal Image @25°C



<Since different material has different emissivity, different spectrum is used for different material>

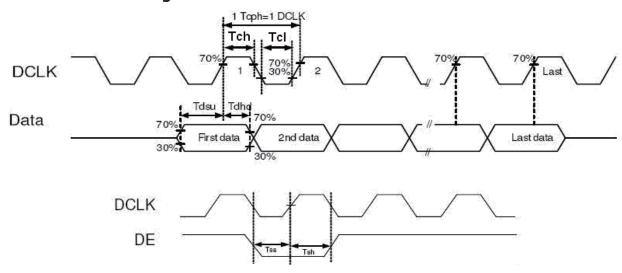
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9 TIMING

9.1 Video timing



Item	Symbol		Spec.		Unit
item	Symbol	Min.	Тур.	Max.	Offic
DCLK period	T_{cph}	12	-	-	ns
DCLK high duty ratio	T _{ch}	20%*Tcph		80%*Tcph	ns
DCLK low duty ratio	T _{cl}	20%*Tcph		80%*Tcph	ns
Data setup time	T _{dsu}	5	-	-	ns
Data hold time	T_{dhd}	5	-	-	ns
DE setup time	T _{ss}	5	-	-	ns
DE hold time	T _{sh}	5	-	-	ns

Table: Input data parameters

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9.2 Input timing

Himax5238B

Parameter	Cymala o l	800)xRGBx	480	Unit
Parameter	Symbol	Min.	Тур.	Max.	UIIII
DCLK Frequency	FDCLK	-	26.36	1	MHZ
Horizontal valid data	thd		800		DCLK
1 Horizontal Line	th	820	832	1483	DCLK
Vertical valid data	tvd		480		Н
1 Vertical field	tv	490	528	576	Н
Frame rate	FR	50	60	65	HZ

Optional input timing

GIS-317

Parameter	Symbo I		xRGBx [3:0]=0 Typ.		Unit
DCLK Frequency	F _{DCLK}	26.62	33.26	34.60	MHz
Horizontal valid data	t _{hd}		800	000	
Hsync Pulse Width	t _{hpw}	5	-	t _h - 5	DCLK
Hsync back porch	t _{hbp}	20	-	222	DCLK
Hsync front porch	t _{hfp}	2	-	-	DCLK
1 Horizontal Line	t _h	1024	1056	1088	DCLK
Vertical valid data	t _{vd}		480		Н
Vsync Pulse Width	t _{vpw}	2	-	t _v - 2	Н
Vsync back porch	t _{vbp}	3	5 (only	/)	Н
Vsync front porch	t _{vfp}	2	-	-	Н
1 Vertical field	t _v	520	525	530	Н
Frame rate	FR	50	60	60	Hz
Note (quatemer's ange)		(GIS-317	7	
Note (customer's spec.)		(V1.6	_24sep	2010)	

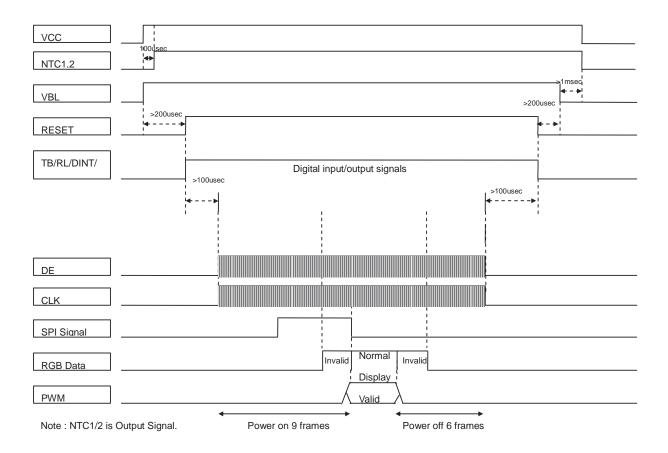
(VCC1=2.7~3.6V, VCC2=VCC1, VSS1=VSS2=VSSA=0V, T_{OP} =95 $^{\circ}$ C ~-40 $^{\circ}$ C)

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9.3 Power ON / OFF sequence 9.3.1 Power ON / OFF

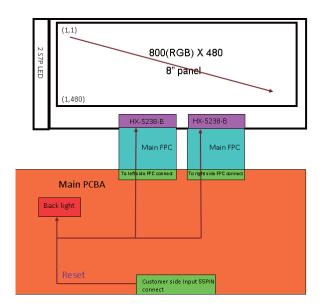


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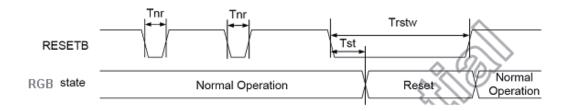
General Motors 8.0" WVGA GM part number 23498633 INX part name DJ080EA-01G part number GD0800EA00340

9.3.2 Reset timing



Function	Description		Reset	Remark
Function	Description	High	Low	Remark
	Panel normal operation when Reset High Panel turn off (black) when Reset Low	Normal	Reset & Standby*	
	Backlight normal operation when Reset High Backlight turn off when Reset Low	Enable	Disable	

Note: There could be some flicker depending on reset release compared to CLK, DE, VS, HS,...., etc timing if normal power on/off is not followed (See 9.3.1)



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Typical reset timing

Parameter	Symbol	Min	Тур.	Max	Unit
RESET low pulse width	T rstw	10	-	-	usec.
Negative noise pulse width	T nr		-	4	usec.
Reset start time	T st	4	-		usec.

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10. COLOR COMBINATION TABLE

O I I I I I I I I I I I I I I I I I I I	Black Blue Green Cyan Red Magent a Yellow White Black darker	Grey Scale GS0 GS1 GS2 ↓ ↓ GS25 3	R 0 0 0 0 0 1 1 1 1 0 1 0 0 1 0 0 0 0 0	R 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	R 2 0 0 0 0 1 1 1 1 0 0 0 0 0 0 0 0 0 0 0	R 3 0 0 1 1 1 1 0 0 0	R 4 0 0 0 0 1 1 1 1 0	R 5 0 0 0 0 1 1 1 1 1 0 0	R 6 0 0 0 1 1 1	R 7 0 0 0 0 1 1 1 1 1 1 1	G 0 0 1 1 0 0	G 1 0 0 1 1 0	G 2 0 0 1 1 1 0 0	G 3 0 0 1 1 0	G 4 0 0 1 1 0	G 5 0 0 1 1 0	G 6 0 1 1 0	G 7 0 0 1 1	B 0 0 1 0 1 0	B 1 0 1 0 1	B 2 0 1 0 1 0 1	B 3 0 1 0 1	B 4 0 1 0 1 0 1	B 5 0 1 0 1 0 1	B 6 0 1 0 1 0	B 7 0 1 0 1 0 1
C	Black Blue Green Cyan Red Magent a Yellow White Black fl darker fl brighter	GS0 GS1 GS2 ↓ ↓ ↓ GS25	0 0 0 0 1 1 1 1 0 1	0 0 0 0 1 1 1 1 0 0	0 0 0 1 1 1 1 0 0	0 0 0 0 1 1 1 1 0	0 0 0 0 1 1 1 1	0 0 0 0 1 1 1	0 0 0 0 1 1 1	0 0 0 0 1 1	0 0 1 1 0	0 0 1 1 0	0 0 1 1 0	0 0 1 1 0	0 0 1 1 0	0 0 1 1 0	0 0 1 1	0 0 1	0 1 0 1	0 1 0 1	0 1 0 1 0	0 1 0 1 0	0 1 0 1	0 1 0 1	0 1 0 1	0 1 0 1
O I I I I I I I I I I I I I I I I I I I	Blue Green Cyan Red Magent a Yellow White Black ↑ darker ↑ brighter		0 0 0 1 1 1 1 0 1 0	0 0 0 1 1 1 1 0 0	0 0 0 1 1 1 1 0 0	0 0 0 1 1 1 1 0	0 0 0 1 1 1 1 0	0 0 0 1 1 1	0 0 0 1 1 1	0 0 0 1 1	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1 0	0 1 1	0 1 1	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0	1 0 1 0
L () () () () () () () () () () () () ()	Green Cyan Red Magent a Yellow White Black ↑ darker ↑ brighter		0 0 1 1 1 1 0 1 0	0 0 1 1 1 1 0 0	0 0 1 1 1 1 0 0	0 0 1 1 1 1 0 0	0 0 1 1 1 1 0	0 0 1 1 1 1 1 1	0 0 1 1 1 1 1 1	0 0 1 1 1 1	1 1 0 0	1 1 0	1 1 0	1 1 0	1 1 0	1 1 0	1	1	0 1 0	0 1 0	0 1 0	0 1 0	0 1 0	0 1 0	0 1 0	0 1 0
O U I I I I I I I I I I I I I I I I I I	Cyan Red Magent a Yellow White Black ↑ darker ↑ brighter	GS0 GS1 GS2 ↓ ↓ GS25	0 1 1 1 1 0 1 0	0 1 1 1 1 0 0	0 1 1 1 1 0 0	0 1 1 1 1 0 0	0 1 1 1 1 1 0	0 1 1 1 1 1	0 1 1 1 1	0 1 1 1	1 0 0	1 0	0	0	0	0	1	1	0	1	0	0	0	0	0	1 0
U I I R S S S S S S S S S S S S S S S S S	Red Magent a Yellow White Black darker brighter	- - - - - - - - - - - - - - - - - - -	1 1 1 1 0 1 0	1 1 1 0 0 1	1 1 1 1 0 0	1 1 1 1 0 0	1 1 1 1 0	1 1 1 1	1 1 1 1	1 1 1	0	0	0	0	0	0			0	0	0	0	0	0	0	0
R S N R I D G	Magent a Yellow White Black ↑ darker ↑ brighter	- - - GS0 GS1 GS2 ↓ ↓	1 1 0 1 0	1 1 0 0 1	1 1 1 0 0	1 1 1 0	1 1 1 0	1 1 1	1 1 1	1	0						0	0								_
R I E 1	a Yellow White Black ↑ darker ↑ brighter	- GS0 GS1 GS2 ↓ ↓	1 1 0 1 0	1 1 0 0 1	1 1 0 0	1 1 0	1 1 0	1	1	1		0	0	0	0	•				1	1	1	1	1	1	1
R I E 1 D 0	White Black darker	- GS0 GS1 GS2 ↓ ↓	1 0 1 0	1 0 0 1	1 0 0	1 0 0	1	1	1		1				U	0	0	0	1							
R I E 1 D 0	Black darker brighter	GS0 GS1 GS2 ↓ ↓ GS25	0 1 0	0 0 1	0 0	0	0			1		1	1	1	1	1	1	1 \	0	0	0	0	0	0	0	0
E 1	ndarker ndarker ndarker the darker	GS1 GS2 ↓ ↓ GS25	1 0 ↓	0 1	0	0		0			1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
D (darker ↑ ↓ brighter	GS2 ↓ ↓ GS25	0	1	0		0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	↑ ↓ brighter	↓ ↓ GS25	\downarrow	\	_	Λ		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↓ brighter	↓ GS25	_	÷		U	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	brighter	GS25	\downarrow		\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
				\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
			1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
1	↓	GS25 4	0	1	1	1	1	1	1	1	Ô	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS25 5	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G I	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
E	darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow		\downarrow	\downarrow	\downarrow		\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow		\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow	\downarrow
	brighter	GS25 3	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
1	↓	GS25 4	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
(Green	GS25 5	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
ВІ	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
l	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	1	↓	<u>↓</u>	<u>↓</u>	<u>↓</u>	<u>↓</u>	<u>↓</u>	<u>↓</u>	<u>↓</u>	\downarrow	J	-	-	-	<u>↓</u>	<u>↓</u>	<u>↓</u>	<u>↓</u>	↓	<u> </u>	-	<u>↓</u>	<u>↓</u>		_	→
	₩ 1	↓		\	↓	↓	<u></u>		-		↓				↓		-	↓	↓			<u> </u>		-		↓
	brighter	GS25 3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
	↓	GS25 4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS25 5	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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11. OPTICAL CHARACTERISTICS

Conditions unless specified otherwise:

- $T_{AMB} = 25^{\circ}C \pm 3^{\circ}C$
- Supply voltage module = 12V (Backlight voltage)
- Elapsed time from switch ON module (including backlight) is greater than 30 minutes
- RGB test patterns only
- Brightness = 100% unless specified.
- Measurements are conducted perpendicular

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Luminance 650 version	L	a = 0° B = 0°	450	650	-		
		a = +30° ß = 0°	315	455	-		
		a = -30° B = 0°	315	455	-	cd/	
		a = 0° B = +30°	315	455	ı	cd/ m²	
		a = 0° ß = -30°	315	455	-	1112	
		a = +45° β = 0°	225	325	-		
		a = -45° B = 0°	225	325	-		
Luminance homogeneity	1.15	100% ×	٥٢			0/	
(Note 1)	Lhom	L_{min}/L_{max}	85		-	%	
Dimming ratio	Dim			1.5000			
(Note 2)	Dim	-		1:5000	_	-	
	Rfsurf			2.6	-	%	
Surface reflectance	(SCI)	-	-	2.0	_	70	
(anti-glare)	Rfsurf			0.6		%	
	(SCE)	-	-	0.6	_	%	
Contrast ratio	CR	+25°C	1:1100	1:2000	-	-	
(Note 3)		a = +30° β = 0°	1:275	-	-		
		a = -30° B = 0°	1:275	-	-		
		a = 0° B = +30°	1:275	-	-		
		a = 0° ß = -30°	1:275	-	-		
		a = +45° β = 0°	1:165	-	-		

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Item			Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
				a = -45° β = 0°	1:165	-	-		
				a = +80° ß = 0°	> 1:10	-	-		
				a = -80° B = 0°	> 1:10	-	-		
				a = 0° B = +80°	> 1:10	-	-	-	
				a = 0° ß = -80°	> 1:10	-	-	,	4
		white	X _W	-	0.250	0.290	0,330		
	Write		Уw	-	0.280	0.320	0.360		
	red		X _R	-	0.585	0.615	0.645		
Colour-point greer		reu	УR	-	0.318	0.348	0.378		Note 4
		groon	X _G	-	0.29	0.320	0.35	-	Note 4
		green	Уg	-	0.585	0.615	0.645		
	-		X _B	-	0.12	0.150	0.18		
		blue	У В	-	0.065	0.095	0.125		
NTSC ratio			NTSC	-		65		-	-
	Black	c to white	Tr	At 25°C	-	16	-		Note 5
	Whit	e to black	Tf	At 25°C	-	6	-		Note 5
Response	Black	c to white	Tr	At -20°C	-	210	-		
time	time White to black		Tf	AL -20°C	-	105	-	ms	
Black to white		Tr	At 200C	-	550	-			
	White to black		Tf	At -30°C		280	-		
Polariser abs	Polariser absorption angle		Р	-	-	0	-	(deg)	

Note 1: Definition of homogeneity according to the VESA standard (version 2.0, dated 1st June 2001 – page 118, section 306-1). Luminance is measured at the 9 specified points on the screen in full-white display pattern.

Note 2: Dimming Ratio measurement:

Maximum brightness (LED maximum PWM driving) has to be measured first, then minimum brightness. Waiting time for dimming ratio measurement is 30 minutes for maximum brightness and then another 30 minutes for minimum brightness.

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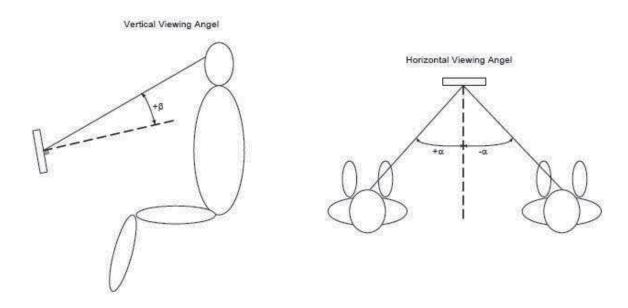


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Note 3: Contrast Ratio measurement:

The contrast ratio (CR) is the ratio between the transmission (τ) in a full white area (all RGB signal bits =1) and the transmission (τ_d) in a dark area (all RGB signal bits =0):

 $CR = \tau / \tau_d$



Note 4: Color-point:

Mentioned color points are target values. Actual color points depend on color filter availability and will be updated / confirmed after sample measurement.

Note 5: Switching time:

Switching times at higher than ambient temperatures ($> 25^{\circ}$ C) are equal or better (faster) than the specified values for 25° C in above table due to the physics of Liquid Crystal material. Due to this they are normally not specified nor measured.

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12. COMMUNICATION INTERFACE SPECIFICATION

12.1 SPI interface spec. (Preliminary concept).

Use of the SPI commands to alter the FoS (Front of Screen) performance should be agreed with General Motors' Display Engineering.

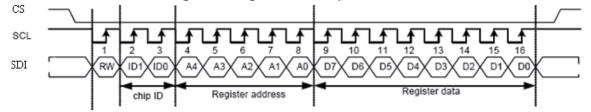
12.2 Physical: Single address.

SPI Signal Timing Chart

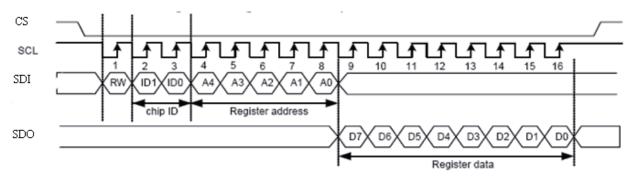
READ:

The read/write control bit RW would be equal to "L", chip ID bits ID[1:0], serial address bits A[4:0] and serial data bits D[7:0] are read at the rising edge of the serial clock SCL, via the serial input pin SDI when writing.

The serial interface signal timing chart and specification are shown as below.



Write:

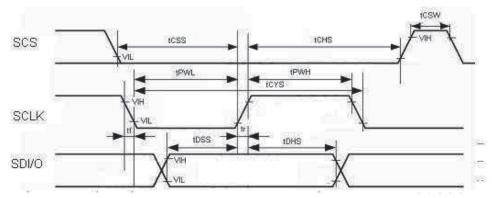


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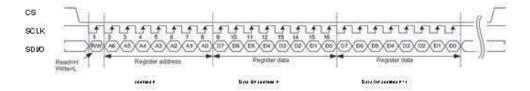
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12.3 Detailed Timing



Remarks: SPI registers will be reset to the default values when "Reset" is active (low)

12.4 Burst mode



12.5 SPI registers

SPI register details are published in a separate document (Doc #: 20110323001AA), which can be obtained on request through GM.

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13. LUMINANCE OVER LIFE TIME

Condition	MTBF	Unit	Remark
Ambient temperature 25°C in continuous operation	15k	hour	Note 1
at typical luminance	IJK	Hour	Note 1

Note 1: Luminance over life time is defined as the time during which the luminance of the module will retain 70% of the original value at ambient temperature of 25°C.

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14. ENVIRONEMTAL / RELIABILITY TESTS

RELIABILITY

No	Test Item	Condition	Reference
1	High Temperature Operation (1)*	Ta=+85°C, 240hrs	IEC60068-2-2-Bb
2	High Temperature Storage	Ta=+95°C, 504hrs	IEC60068-2-2-Bb
	(non-operation)		
3	Low Temperature Operation	Ta=-40°C, 240hrs	IEC60068-2-1-Ab
4	Low Temperature Storage	Ta=-40°C, 504hrs	IEC60068-2-2-Ab
	(Non-operation)		
5	High Temperature & High	Ta=+60°C, 90% RH, 504hrs	IEC60068-2-78 Cab
	Humidity Operation		
6	Thermal Shock (non-operation)	-40°C, 30 min ←→85°C, 30 min; 168 cycles	IEC60068-2-14-Na
7	UV exposure resistance	765 W/m^2 / 168 hrs;	IEC60068-2-5-Sa
		Power off.	
8	Surface Discharge	C=150pF, R=330Ω;	IEC61000-4-2
	(non-operation)	Air Discharge: ±15KV	
		Contact Discharge: ±8KV	
9	Vibration (non-operation)	(Non-operation)	IEC60068-2-27-Ea
		3 directions: X, Y, Z axes;	IEC60068-2-6Fc
		Sweeps: 10 (1 oct/min);	
		Frequency: 10 →150 →10 Hz;	
		10-58 Hz: constant amplitude 0.75mm	
		peak;	
		58-150 Hz: constant acceleration 10g	
		peak	
10	Shock (non-operation)	3 directions: X, Y, Z axes	IEC60068-2-27-Ea
		Repeats: 6Peak acc.: 100G	
		Pulse duration: 6ms	

^{(1)* 85} degrees for HOT testing is referring to the TFT surface temperature, not ambient temperature

(2) Ta: Ambient Temperature

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14.1 EMC level requirements:

CISPR25 Level 5 is target to be reached

14.2 ESD

TEST	CONDITIONS	METHOD	REMARK
Human Body mode	10 KV (330Ω,150pF)	IEC 61000-4-2	Not operated

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15. QUALITY REQUIREMENTS

15.1. Shipping Cosmetic Specification

15.1.1. Inspection Conditions

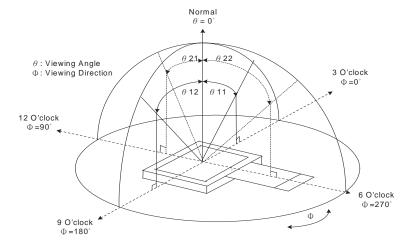
Viewing time: < 10 seconds Viewing distance: 35cm

Ambient illumination: Standard 150~600Lux(light-on), 700~1500 Lux (light- off)

Ambient temperature: 20...25°C

Light source condition: Based on the specification

Inspection view angle: U/D/L/R: $10^{\circ}(\theta 21/\theta 22/\theta 11/\theta 12)$

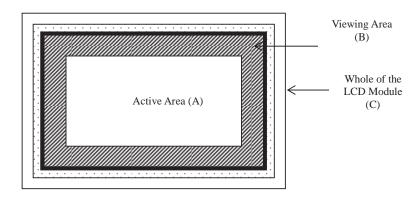


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15.1.2. Inspection Zone



A zone: The inside of the Active Area (as defined on the Product Drawing)

B zone: The inside of the Viewing Area (as defined on the Product Drawing) except

the A Zone

C zone: Whole of the LCD Module except the zone A and B. (Including FPC& Metal

Frame)

15.1.3. Functional Defects

Incomplete segment zones	not allowed
Segment shorts (dark or bright	not allowed
segments/dots)	
Wrong position of the good	not allowed
viewing area	
Irregular contrast	not allowed

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15.1.4 Cosmetic Inspection

For all below Inspection Items the 1st criteria is visibility through a 5%ND Filter:

Not Visible: Ignore

Visible: Apply below table

	VISIDIC:	Apply below table		I		
lı	nspection item	Inspection	Acceptable Qty	Applied	Inspection	Note
		standards		Zone	Mode	
	Line defect	Not visible	None	А	Light-On	1
	Mura (Uniformity)	Not visible	None	А	Light-On	2
	Cross talk	Not visible	None	А	Light-On	
	Bright dot (Sub-pixel)	1dot	0	А	Light-On	3
	Dark dot	1dot	3	А	Light-On	4
	Dark /Bright spots /foreign matter	Average Diameter (mm) $D \le 0.15$ $0.15 < D \le 0.4$ $0.4 < D$	Acceptable Qty Ignored 2 0	А	Light-On	a D=(a+b)/2
1. 2. 3.	Jointed dot Bright & Bright Dark & Dark Bright & Dark	2 jointed dot ≤ 2 jointed dot ≤ 2 jointed dot ≤	0 0 0	А	Light-On	5
1. 2. 3.	Dot Distance (Between) Bright & Bright Dark & Dark Bright & Dark	≤10 mm ≤10 mm ≤10 mm	0 0	А	Light-On	

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Inspection item	Inspection	Acceptable Qty	Applied	Inspection	Note
	standards		Zone	Mode	
	Width (mm) Length (mm)	Acceptable Qty			
	W ≤ 0.03	Ignored	А		
Lints	0.03 < W ≤ 0.08 & L ≤ 2.0	Ignored	А	Light-On Light-Off	
	2.0 < L ≤ 3.0	2	А		W
	3.0 < L	0	А		**
	0.08 < W	0	A,B		
	Width (mm) Length (mm)	Acceptable Qty			
Scratches	$W \le 0.08 \qquad L \le 10.0$ $W \le 0.08 \qquad L > 10.0$ $W > 0.08 \qquad L \le 10.0$	3 0 0	A,B	Light-On Light-Off	
Dents, Bubbles	Average Diameter (mm) $D \le 0.15$ $0.15 < D \le 0.5$ $D > 0.5$	Acceptable Qty Ignored 3 0	A,B	Light-On Light-Off	a D=(a+b)/2



Note 1: Line Defect



Note3: Bright Dot Defect



Note2: Mura



Note 4: Dark Dot Defect



Note2: Uniformity



Note 5: Joined dot defect

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15.1.5. Functional Optical check

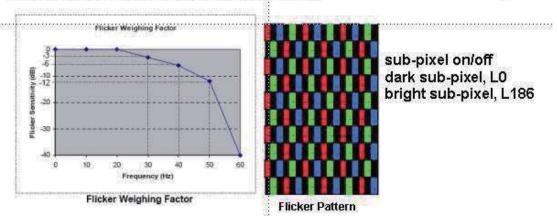
Inspection	Inspection	Acceptable Oty	Applie	dInspection	Note
item	standards		Zone	Mode	
Flicker	N/A	≤ -20dB	А	Light-On	Note1
Gamma	Smooth	1.9 – 2.5			Note 2
	Gray-to-Gray		Α	Light-On	
	appearance				

Note1:

The flicker level is defined using Fast Fourier Transformation (FTT) as follows: $Flic \ker = 20 \log_{10} \left(2 \frac{f_{FFTC}(n)}{f_{FFTC}(0)} \right) + FS(Hz)$ (dB) **CMI spec. proposal<=-20db**

where fFFTC(n) is the nth FFT coefficient, and fFFTC(0) is the 0th FFT coefficient which is DC component. FS(Hz) is the flicker sensitivity as a function of frequency.

The flicker level shall be measured with the test pattern below using photodiode with PMT in a range from 5Hz to 80Hz. The data acquisition window shall be at least 1 second and the sampling rate shall be at least 10,000 Hz. The peak flicker level shall be reported based on the calculation using above formula in which FS(Hz) is determined by the flicker weighing factor in below graphic using linear interpolation between the fisted values.



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Note2:

Gamma (y) are defined by

$$L = aV\gamma + L_{b}...(1)$$

L, L,: gray-scale and black luminance; V:gray-signal signal Level

$$\log(1) \longrightarrow \log(\mathbf{L} - \mathbf{L_b}) = \log \mathbf{a} + \gamma \log \mathbf{V}...(2)$$

Linear Fitting to obtain

$$\gamma = \{\log(L - L_b) - \log a\}/\log V$$

Gamma curve is measured using 8 equally input levels, Black(0), 32,64,96,128, 160, 192, 224, White(255).

	Minimum	Typical	Maximum
Gamma value	1.9	2.2	2.5

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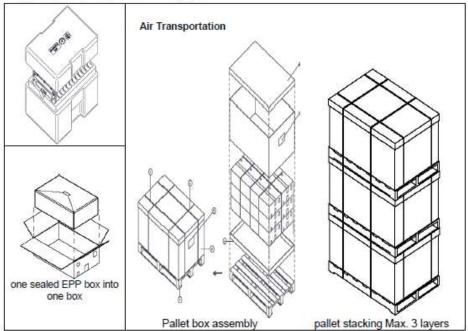


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16. PACKING DEFINITION

DJ080PA-01A /DJ080PA-01B /DJ080EA-01G Module delivery packing method -8B02M000GN00N

- (1) First Level: Inner box level with standard packing method EPP box in box. INX packing method on this level for this product is 378 x 284 x 210mm.
 - Put the sealed module into EPP bottom (8701B00017000) one by one.
 - One full EPP bottom contains of <u>11</u> pieces modules.
 - Put one EPP top (8A04B00027000) onto EPP bottom, seal the EPP box with 2 rolls of tape.
 - The seal the EPP box with desiccant bags (8A02B0000B000) will be put into an anti-static bag (8401B0005G000). The ESD bag is sealed with tapes.
 - Put one sealed EPP boxed into one box (8201B000K2000).
 - . The packing quantity per box is 11 pieces. weight (kg): 5.27 / carton
- (2) Second Level: Pallet box level. INX packing method (8B02M000E900N) on this level for this product is 1200 x 800 x 850mm.
 - One pallet box is placed on a poly wood pallet wrapped (8A04B00001000) by strips with heatmelt joint.
 - One pallet box contains 3 parts-bottom (8201B000K5000), sleeve (8201B000K3000) and cover (8201B000K6000).
 - · Altogether 24 pieces boxes on one pallet (3 layers and 8 pieces boxes in each layer).
 - If not enough full boxes to deliver, fill the pallet box with empty box. Paste "Empty" label for indication
 - The full packing quantity per outer box is 11 x 24 = 264 pieces. weight (kg): 144.48 / pallet
- (3) The pallet packing stack is max. 3 layers.



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17. HANDLING AND SAFETY REQUIREMENTS

Warning

The display glass may break when it is dropped or bumped on hard surface. Handle with care. Should the display break, do not touch the liquid crystal material. In case of contamination with liquid crystal material, wash immediately with water and soap.

The display module contains parts that operate at high voltage. Under no circumstances should the front or back cover or the inverter shielding be removed during operation. Do not touch circuit at the backside of the module.

Caution

The display should not be exposed to harmful gasses, such as acid and alkali gasses, which corrode electronic components.

Disassembling the display module can cause permanent damage and invalidates the warrant agreements.

Observe general precautions that are common to handling delicate electronic components. The glass can break and polarizer can easily be damaged. Moreover the display is sensitive to static electricity and other rough environmental conditions.

Caution

Allow enough space at the back of the module for sufficient airflow to disperse heat generated by the backlighting system.

During handling, when the module was dropped to the floor, please do not use the module anymore.

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18. DEFINITIONS

Data sheet							
status							
Objective	This data sheet contains target or goal specifications for						
Specification	product development.						
Preliminary	This data sheet contains preliminary data; supplementary						
Specification	data may be published later.						
Product	This data sheet contains final product specification.						
Specification							

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operating of the device at these or any other conditions above those given in the Characteristics sections of the specification is not implied. Expose to limiting values for extended periods may affect device reliability.

19. LIFE SUPPORT APPLICATIONS

These products are not designed for use in life saving appliances, devices or systems where malfunctioning of these products can reasonably be expected to result in personal injury. INX customers using or selling these products for use in such applications do so at their own risk and agree full non liability of INX for any damages resulting from such improper use or sale.

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