

FOR APPROVAL

() Preliminary Specificatio	n
() Final Specification	

TITLE	24	4.0" WUXGA TF	T LCD
BUYER		SUPPLIER	LG Display Co., Ltd.
MODEL		MODEL	LM240WU8
		SUFFIX	SLF1

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE
Please return 1 copy for your c with your signature and comm	

APPROVED BY	SIGNATURE DATE
SangHoon Lee / G.Manager	
REVIEWED BY	
JaeHoon Song / Manager [C]	
Suyoung An / Manager [M]	
Sangho Han / Manager [O]	
JongChun Lim / Manager [P]	
PREPARED BY	
KyungWoo Lee / Engineer	

Ver. 0.0 Nov., 13, 2019 1 / 31



Contents

No.	Item	Page
	Cover	1
	Contents	2
	Record of Revisions	3
1	General Description	4
2	Absolute Maximum Ratings	5
3	Electrical Specifications	6
3-1	Electrical Characteristics	6
3-2	Interface Connections	9
3-3	Signal Timing Specifications	14
3-4	Signal Timing Waveforms	15
3-5	Color Data Reference	16
3-6	Power Sequence	17
3-7	Power Dip Condition	18
4	Optical Specifications	19
5	Mechanical Characteristics	23
6	Reliability	26
7	International Standards	27
7-1	Safety	27
7-2	Environment	27
8	Packing	28
8-1	Designation of Lot Mark	28
8-2	Packing Form	29
9	Precautions	30
9-1	Mounting Precautions	30
9-2	Operating Precautions	30
9-3	Electrostatic Discharge Control	31
9-4	Precautions For Strong Light Exposure	31
9-5	Storage	31
9-6	Handling Precautions For Protection Film	31



Record of Revisions

Revision No	Revision Date	Page	Before	After	Application Date
0.0	Nov. 13, 2019	-	-	First Draft(Preliminary)	



1. General Description

LM240WU8-SLF1 is a color active matrix liquid crystal display with a Light Emitting Diode(LED) backlight assembly without LED driver. The matrix employs a-Si thin film transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 24inch diagonally measured active display area with WUXGA resolution(1920 horizontal by 1200 vertical pixel array). Each pixel is divided into red, green and blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16.78 Million colors with A-FRC (Advanced Frame Rate Control). It has been designed to apply 8-bit 2port LVDS interface. It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.

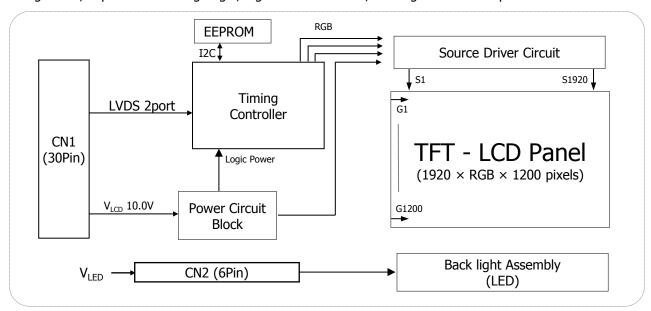


FIG.1 Block Diagram

General Features

Active Screen Size	24.1inches(61.13cm)(Aspect ratio 16:10)
Outline Dimension	546.4(H) x 352.0(V) x 11.7(D) mm(Typ.)
Pixel Pitch	0.270(H) x 0.270(V) mm
Pixel Format	1920(H) x 1200(V) Pixels. RGB stripes arrangement.
Color Depth	16.78 Million colors, 8 Bit(6Bit + A-FRC)
Luminance, White	300cd/m²(Center 1Point, Typ.)
Viewing Angle(CR>10)	R/L 178° (Typ.), U/D 178° (Typ.)
Power Consumption	Total 13.35Watt (Typ.)(2.15Watt@ Mosaic_V _{LCD} , 11.2Watt@ Is=95mA
Weight	2,650 g (Typ.)
Display Operating Mode	Transmissive mode, Normally black
Panel type	Forward type
Surface Treatment	Anti-Glare treatment of the front polarizer(Haze25%, 3H)



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 2-1. Absolute Maximum Ratings

Parameter	Cumbal	Val	ues	Linita	Notos
Parameter	Symbol	Min	Max	Units	Notes
Power Supply Input Voltage	V_{LCD}	-0.3	+11.0	V_{DC}	At 25 ℃
Operating Temperature	T _{OP}	0	50	°C	
Storage Temperature	T _{ST}	-20	60	C	1 2 2
Operating Ambient Humidity	H _{OP}	10	90	%RH	1,2,3
Storage Humidity	H _{ST}	10	90	%RH	
LCM Surface Temperature(Operation)	T _{surface}	0	65	°C	1,4

Notes:

- 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.
- 2) Maximum storage humidity is up to 40°C, 70% RH only for 4 corner light leakage mura.
- 3) Storage condition is guaranteed under packing condition.
- 4) LCM surface temperature should be measured under the condition of V_{LCD} = Typ, f_V = 60Hz, T_a = 25 °C, no humidity and typical LED string current.
- * f_V = Frame frequency * T_a = Ambient temperature

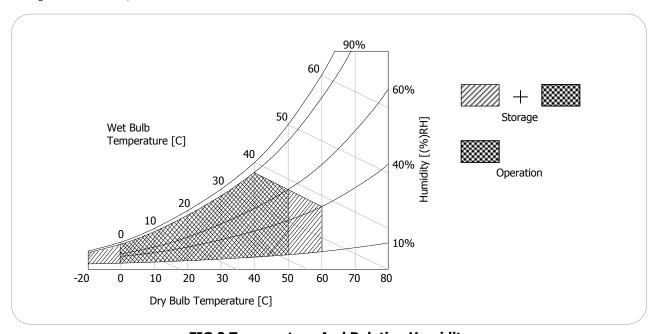


FIG.2 Temperature And Relative Humidity



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 3-1. Electrical Characteristics

Dougnostou	Cymahal	Values			Unit	Notos	
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
Module:							
Power Supply Input voltage	VLCD	9.5	10.0	10.5	Vdc	4	
Permissive Power Input Ripple	VRIPPLE	-		400	mVp-p	1	
Dower Cumply Input Current	ILCD Typ.	-	215	270	mA		
Power Supply Input Current	ILCD Max.	-	220	275	mA	2	
Daway Cangumation	PLCD Typ.	-	2.15	2.70	Watt	(Non-fix)	
Power Consumption	PLCD Max.		2.20	2.75	Watt		
Rush Current	Irush	-	-	4.0	Α	3	

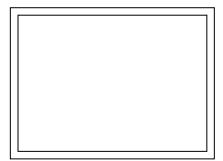
Notes:

- 1) Permissive power ripple should be measured under the condition of $V_{LCD} = Typ$, $25\pm2^{\circ}C$, $f_{V} = Max$. Refer to page 7 for the pattern and more information.
- 2) The specified current and power consumption can be measured under the V_{LCD} = Typ, $25\pm2^{\circ}C$, f_{V} = 60Hz and the pattern should be changed according to the typical or maximum power condition. The max. current can be measured only with the maximum power pattern. See the page 7 for details.
- 3) Maximum condition of inrush current:
- The duration of rush current is about 5ms and rising time of power input is 500us $\pm 20\%$.(Min). 4) V_{LCD} level must be measured between two points on PCB of LCM V_{LCD} (test point) ~ LCM Ground. (Test condition: Maximum power pattern, 25°C, $f_V = 60$ Hz)

^{*} f_V = Frame frequency



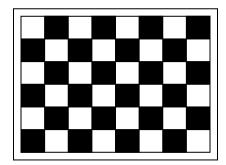
• Permissive Power Input Ripple($V_{LCD} = Typ, 25 \,^{\circ}\text{C}$, $f_{V}(frame frequency) = Max condition)$



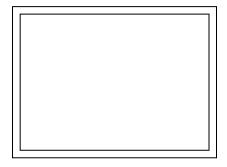
White Pattern

For the exact ripple measurement, the condition of Max 20MHz is recommended in the bandwidth configuration of oscilloscope.

• **Power Consumption**($V_{LCD} = Typ$, 25°C, $f_V(frame\ frequency) = 60Hz\ condition)$



Typical Power Pattern



Maximum Power Pattern

FIG.3-1 Mosaic Pattern & White Pattern For Power Consumption Measurement



Table 3-2. LED Bar Electrical Characteristics

Darameter	Cymbol		Values	Unit	Notes	
Parameter	Symbol	Min	Тур	Max	Ullit	Notes
LED String Current	Is	-	95	100	mA	1,2
LED String Voltage	Vs	27.5	29.5	31.5	V	1,3
Power Consumption	PBar	-	11.2	12	Watt	2,5
LED Life Time	LED_LT	30,000	-	-	Hrs	4

Note: The LED consists of 40LED packages, 4 strings(parallel) x 10 packages(serial) x 1bar

Notes:

1) The specified values are for single LED bar.

2) The specified current is defined as the input current for single LED string with 100% duty cycle.

3) The specified voltage is the input LED string voltage at typical current 100% duty cycle.

4) The LED life time is defined as the when brightness of LED itself reach to the 50% of initial value under the conditions at T_a = 25±2℃ and typical LED string current.
 5) The power consumption shown above does not include the loss of external LED driver.

5) The power consumption shown above does not include the loss of external LED driver. The typical power consumption is calculated as Pbar = Vs(Typ.) x Is(Typ.) x No. of strings. The maximum power consumption is calculated as PBar = Vs(Max.) x Is(Typ.) x No. of strings.

Ver. 0.0 Nov., 13, 2019 8 / 31



3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(Receptacle): IS100-L300-C23(Manufactured by UJU)
- Mating Connector(Plug): FI-X30C2L(Manufactured by JAE) or equivalent

<u>Table 3-3. Module Connector(CN1) Pin Configuration</u>

No	Symbol	Description	No	Symbol	Symbol
1	RXO0-	Minus signal of odd channel 0(LVDS)	16	RXE1+	Plus signal of even channel 1(LVDS)
2	RXO0+	Plus signal of odd channel 0(LVDS)	17	GND	Ground
3	RXO1-	Minus signal of odd channel 1(LVDS)	18	RXE2-	Minus signal of even channel 2(LVDS)
4	RXO1+	Plus signal of odd channel 1(LVDS)	19	RXE2+	Plus signal of even channel 2(LVDS)
5	RXO2-	Minus signal of odd channel 2(LVDS)	20	RXEC-	Minus signal of even clock channel(LVDS)
6	RXO2+	Plus signal of odd channel 2(LVDS)	21	RXEC+	Plus signal of even clock channel(LVDS)
7	GND	Ground	22	RXE3-	Minus signal of even channel 3(LVDS)
8	RXOC-	Minus signal of odd clock channel(LVDS)	23	RXE3+	Plus signal of even channel 3(LVDS)
9	RXOC+	Plus signal of odd clock channel(LVDS)	24	GND	Ground
10	RXO3-	Minus signal of odd channel 3(LVDS)	25	NC	No Connection(I2C serial interface for LCM)
11	RXO3+	Plus signal of odd channel 3(LVDS)	26	NC	No Connection(I2C serial interface for LCM)
12	RXE0-	Minus signal of even channel 0(LVDS)	27	ITLC	Interlace image sticking reduction mode selection
13	RXE0+	Plus signal of even channel 0(LVDS)	28	V_{LCD}	Power Supply +10.0V
14	GND	Ground	29	V _{LCD}	Power Supply +10.0V
15	RXE1-	Minus signal of even channel 1(LVDS)	30	V_{LCD}	Power Supply +10.0V

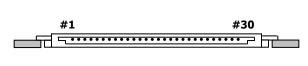
Notes:

- 1) All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2) All V_{ICD}(power input) pins should be connected together.
- 3) All input level of LVDS signals are based on the EIA 644 standard.
- 4) ITLC is used for image sticking reduction in interlace mode.
 (L: Normal mode, H: Interlace image sticking reduction mode)

This pin should be connected to GND in normal mode.

(Low level Input Voltage : GND \sim 0.4V, High level Input Voltage : 1.6 \sim 3.6V)





IS100-L300-C23 Rear view of LCM



Required signal assignment for flat link(TI:SN75LVDS83) transmitter

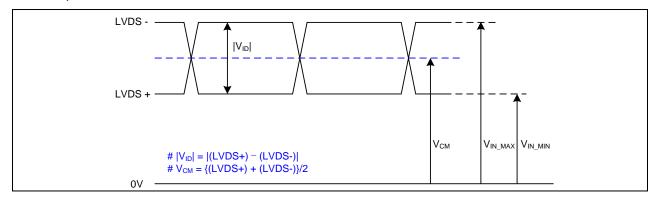
No	Pin Name	Required Signal	No	Pin Name	Required Signal
1	VCC	Power supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input(R7)	30	D26	TTL Input(DE)
3	D6	TTL Input(R5)	31	Tx CLKIN	TTL Level clock Input
4	D7	TTL Input(G0)	32	PWR DWN	Power down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input(G1)	34	PLL VCC	Power supply for PLL
7	D9	TTL Input(G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input(G6)	36	LVDS GND	Ground pin for LVDS
9	VCC	Power supply for TTL Input	37	Tx OUT3+	Positive LVDS differential data output 3
10	D11	TTL Input(G7)	38	Tx OUT3 -	Negative LVDS differential data output 3
11	D12	TTL Input(G3)	39	Tx CLKOUT+	Positive LVDS differential clock output
12	D13	TTL Input(G4)	40	Tx CLKOUT -	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	Tx OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input(G5)	42	Tx OUT2 -	Negative LVDS differential data output 2
15	D15	TTL Input(B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input(B6)	44	LVDS VCC	Power supply for LVDS
17	VCC	Power supply for TTL Input	45	Tx OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input(B7)	46	Tx OUT1 -	Negative LVDS differential data output 1
19	D18	TTL Input(B1)	47	Tx OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input(B2)	48	Tx OUT0 -	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input(B3)	50	D27	TTL Input(R6)
23	D21	TTL Input(B4)	51	D0	TTL Input(R0)
24	D22	TTL Input(B5)	52	D1	TTL Input(R1)
25	D23	TTL Input(RSVD)	53	GND	Ground pin for TTL
26	VCC	Power supply for TTL Input	54	D2	TTL Input(R2)
27	D24	TTL Input(HSYNC)	55	D3	TTL Input(R3)
28	D25	TTL Input(VSYNC)	56	D4	TTL Input(R4)
	1	ı			I.

1) Refer to LVDS transmitter data sheet for detail description. 2) 7 means MSB and 0 means LSB at R,G,B pixel data.



3-2-2. LVDS Signal Specifications

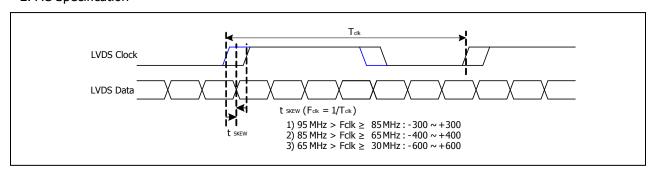
1. DC Specification



Parameter	Symbol	Min	Max	Unit	Notes
LVDS Differential voltage	$ V_{ID} $	150	600	mV	
LVDS Common mode voltage	V_{CM}	1.0	1.5	٧	
LVDS Input voltage range	V_{IN}	0.7	1.8	٧	
Change in common mode voltage	ΔVCM	-	250	mV	

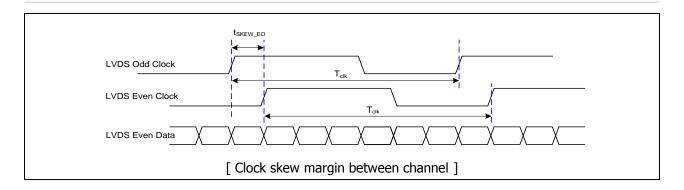
Notes: Does not have any Noise & Peaking in LVDS Signal.

2. AC Specification

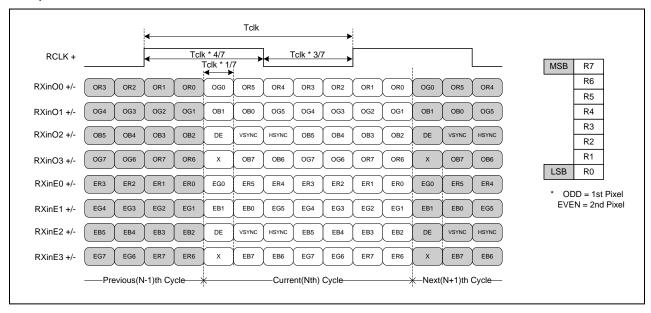


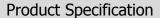
Parameter	Symbol	Min	Max	Unit	Notes
	t _{SKEW}	- 300	+ 300	ps	95MHz > Fclk ≥ 85MHz
LVDS Clock to data skew margin	t _{SKEW}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t _{SKEW}	- 600	+ 600	ps	65MHz > Fclk ≥ 30MHz
LVDS Clock to clock skew margin(Even to odd)	t _{SKEW_EO}	- 1/7	+ 1/7	T _{clk}	-





3. Data Format 1) LVDS 2 Port







3-2-3. Backlight Connector Pin Configuration

The LED interface connector is a SM06B-SHJH(HF)_Manufactured by JST or equivalent.

The mating connector is a SHJP-06V-S(HF), SHJP-06V-A-K(HF) or equivalent.

The pin configuration for the connector is shown in the table below.

Table 3-4. LED Connector Pin Configuration

Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	FB2	Channel2 Current Feedback	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	FB3	Channel3 Current Feedback	
6	FB4	Channel4 Current Feedback	

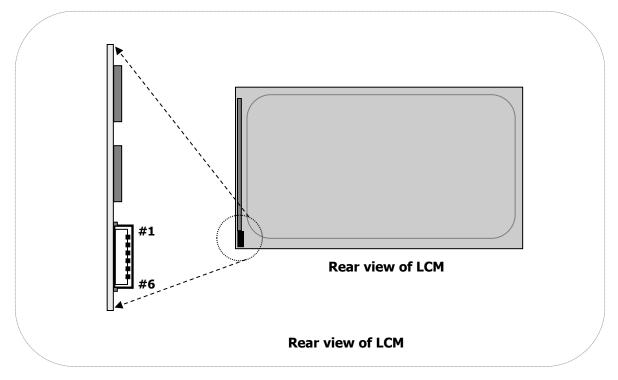


FIG.3-2 Backlight Connector View

Ver. 0.0 Nov., 13, 2019 13 / 31



3-3. Signal Timing Specifications

This is the signal timing requirement from the signal transmitter. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Table 3-5. Timing Table

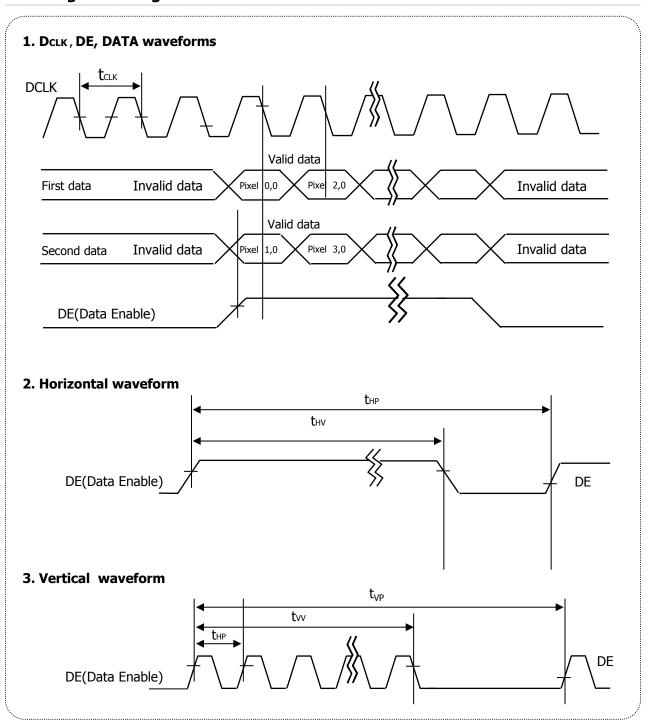
Item	Symbol	Symbol	Min	Тур	Max	Unit	Notes
DCLK	Period	tCLK	12.19	12.98	16.06	ns	Pixel frequency
DCLK	Frequency	fCLK	62.24	77	82	MHz	(Typ. 154MHz)
	Period	tHP	1013	1040	1048	tCLK	
	Horizontal Valid	tHV	960	960	960	tCLK	124
Hsync	Horizontal Blank	tHB	53	80	88	tCLK	1,3,4
	Width-Active	tWV	6	6	6	tCLK	
	Period	tVP	1229	1235	1390	tHP	
	Vertical Valid	tVV	1200	1200	1200	tHP	2.4
Vsync	Vertical Blank	tVB	29	35	190	tHP	2,4
	Frequency	fV	49.5	60	61	Hz	

Notes

- 1) The value of Hsync Period, Hsync Width and Hsync valid should be even number times of tCLK. If the value is odd number times of tCLK, it can make asynchronous signal timing and cause abnormal display.
- 2) The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3) The value of Hsync Period, Hsync Width, and Horizontal Back Porch should be divided by 4 without a remainder.
- 4) The polarity of Hsync, Vsync is not restricted.



3-4. Signal Timing Waveforms





3-5. Color Data Reference

The brightness of each primary color(Red,Green,Blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color.

The table below provides a reference for color versus data input.

Table 3-6. Color Data Reference

											I	npu	t Cc	olor	Dat	а									
	Color				RE	Đ							GRE	EEN							BL	UE			
		MS							SB									MS							.SB
	T		R6																						
	Black	0	0	0	0	0	0	0		0	0	0	0	0	0	0		0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1		0	0	0	0	0	0	0	0		0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3-6. Power Sequence

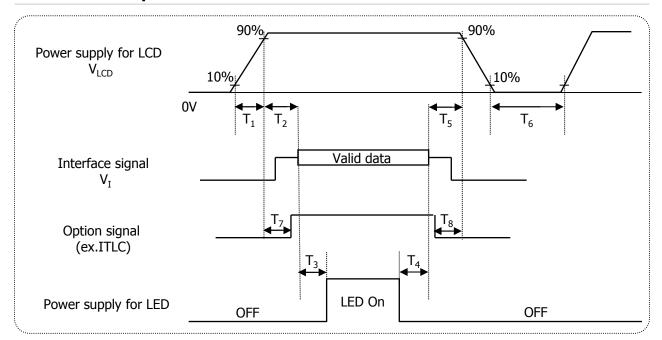


Table 3-7. Power Sequence

Davamatav		Values		Haita
Parameter	Min.	Тур.	Max.	Units
T ₁	0.5	-	10	ms
T ₂	0.01	-	50	ms
T ₃	500	-	-	ms
T ₄	200	-	-	ms
T ₅	0.01	-	50	ms
T ₆	1000	-	-	ms
T ₇	0.5	-	T2	ms
T ₈	0	-	-	ms

Notes:

- 1) Power sequence should be kept all the time including below cases for normal operation.
 - AC/DC Power On/Off
 - Mode change (resolution, frequency, timing, sleep mode, color depth change, etc.) The violation of power sequence can cause a significant trouble in display and reliability.

- Please avoid floating state of interface signal during signal invalid period.
 When the interface signal is invalid, be sure to pull down the V_{LCD}.(0V)
 Please turn off the power supply for LED when the level of V_{LCD} changes to prevent noise issue.
 When measuring valid data starting point, it can be measured that LVDS signal starts swing.



3-7. Power Dip Condition

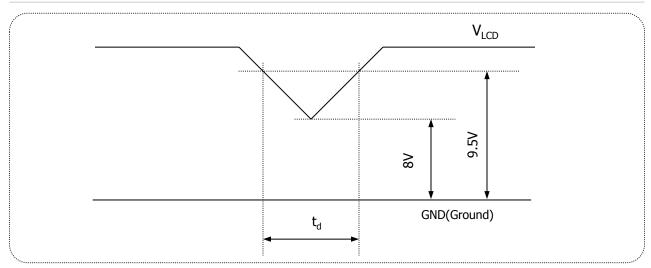


FIG.3-3 Power Dip Condition

For proper operation, stable power supply of V_{LCD} is necessary and power dip is allowed only in below condition. Except this condition, power on/off should follow power sequence specification exactly.

1) Dip Condition
$$8V \le V_{LCD} < 9.5V$$
 , $t_d \le 20 ms$



4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25±2°. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° and aperture 1 degree. FIG.4-1 presents additional information concerning the measurement equipment and method.

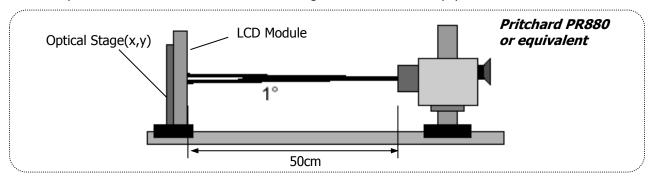


FIG.4-1 Optical Characteristic Measurement Equipment And Method

Table 4-1. Optical Characteristics

(T_a =25 °C, V_{LCD} =Typ, f_V =60 Hz, DCLK=154MHz, I_S =95mA)

Davam	otor	Cumbal		Values		Unito	Notos
Param	leter	Symbol	Min.	Тур.	Max.	Units	Notes
Contrast Ratio		CR	700	1000	-		1
Surface Luminance, white		L _{WH}	240	300	-	cd/m ²	2
Luminance Variation		δ _{WHITE}	75	-	-	%	3
Response Time	onse Time Gray to Gray		-	14	28	ms	4
	D - 1	Rx		0.672			
	Red	Ry	Typ -0.03	0.323	Typ +0.03		
	Croon	Gx		0.305			
Color Coordinates [CIE 1931]	Green	Gy		0.628			
(By PR650)	DI.	Bx		0.149			
	Blue	Ву		0.063			
	\\\/\	Wx		0.313			
	White	Wy		0.329			
Color Temperature		-	_	6500	-	K	
Viewing Angle	Horizontal	θ_{H}	170	178	-	Dogwoo	F
(CR>10, General)	Vertical	$\theta_{\sf V}$	170	178	-	Degree 5	5
Gray Scale		-		2.2			6



Notes:

Contrast Ratio(CR) is defined mathematically as: (By PR880)
 It is measured at center point(1)

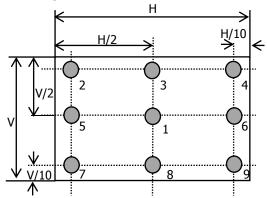
- 2) **Surface Luminance(LwH)** is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.4-1. *(By PR880)*
- 3) The Variation in Surface Luminance , δ_{WHITE} is defined as: (By PR880)

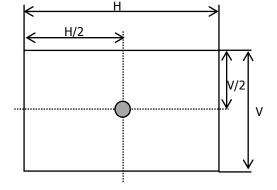
$$\delta_{\text{WHITE}} = \frac{\text{Minimum(LP1,LP2,, LP9)}}{\text{Maximum(LP1,LP2,, LP9)}} \times 100(\%)$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations. For more information see FIG.4-2.

<Measuring Point For Luminance Variation>

<Measuring Point For Surface Luminance>





@ H,V: Active Area

FIG.4-2 Measure Point for Luminance



Notes:

- 4) The Gray To Gray Response Time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ". (By RD805)
 - Gray step: 5 Step
 - $T_{\text{GTG_AVR}}$ is the total average time at rising time and falling time for "Gray To Gray ". For the GTG measurement, the sampling rate of oscilloscope is 500k/s.

Table 4-2. GTG Gray

Crov to C	Gray to Gray		Rising Time									
Gray to G			G191	G127	G63	G0						
	G255											
	G191											
Falling Time	G127											
-	G63											
	G0											

Response Time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

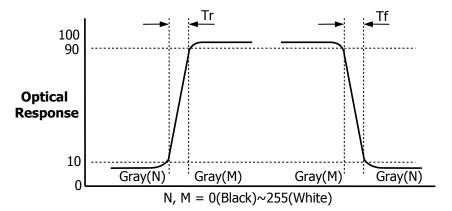


FIG.4-3 Response Time



Notes:

5) **Viewing Angle** is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.4-4. **(By PR880)**

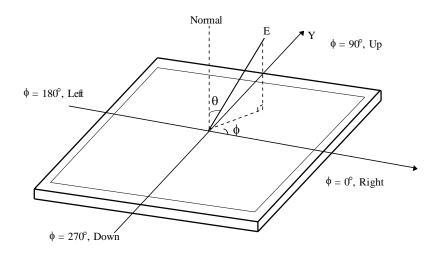


FIG.4-4 Viewing Angle

6) **Gamma Value** is approximately 2.2. For more information see below table.

Table 4-3. Gray Scale Specification

Gray Level	Relative Luminance [%](Typ)
0	0.10
15	0.30
31	1.08
47	2.50
63	4.72
79	7.70
95	11.49
111	16.20
127	21.66
143	28.20
159	35.45
175	43.80
191	53.00
207	63.30
223	74.48
239	86.80
255	100

Ver. 0.0 Nov., 13, 2019 22 / 31



5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

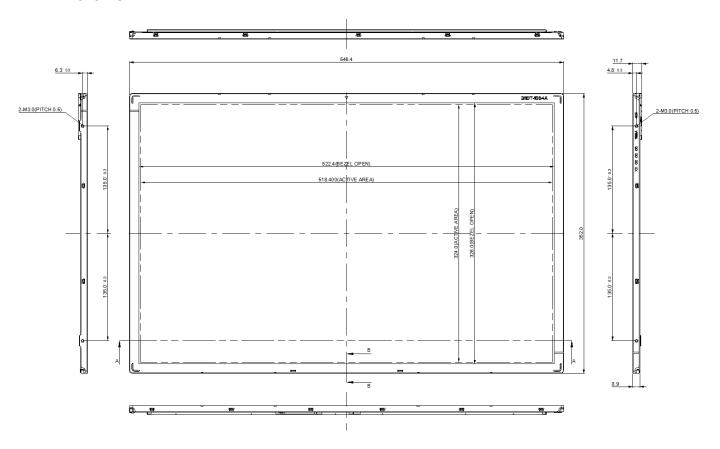
	Horizontal	546.40mm					
Outline Dimension	Vertical	352.00mm					
	Depth	11.70mm					
Doral Aves	Horizontal	522.40mm					
Bezel Area	Vertical	328.00mm					
Active Dienlay Area	Horizontal	518.40mm					
Active Display Area	Vertical	324.00mm					
Weight	Typ: 2,650 g , Max: 2,780 g						
Surface Treatment	Anti-Glare treatment of the front p	Anti-Glare treatment of the front polarizer(Haze25%, 3H)					

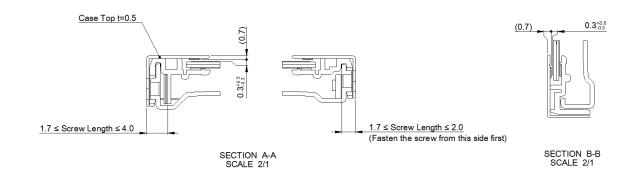
Note: Please refer to a mechanical drawing in terms of tolerance at the next page.

<sup>Outline dimensions (horizontal, vertical and outside depth) are measured by using vernier calipers.
The inside depth dimensions are measured by using height gauge, when LCM is put face down onto a</sup> flat surface.



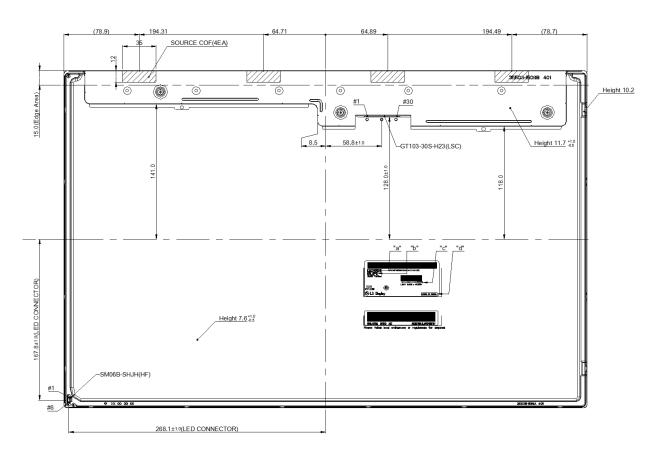
<Front View>







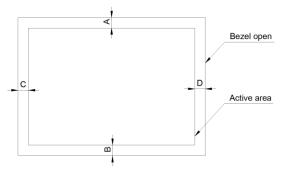
<Rear View>



- 1. Unspecified tolerances are to be ± 0.5
- 2. Tilt and partial disposition tolerance of display area are as following.

 (1) Y-direction : I A-B I ≤ 1.4

 (2) X-direction : I C-D I ≤ 1.4



- 3. Torque of User Hole(Mount): 3.0 ~4.0kgf.cm 4. I/F Connector Specification: GT103-30S-H23(LSC) 5. LED Connector Specification: JST, SM06B-SHJH(HF) 6. The COF area is weak & sensive, so please don't press the COF area LGD Highly recommendation:

System chassis or frame should be designed to keep the IPS Panel flat as it is vulnerable to panel light-leakage caused by deformation.



6. Reliability

Environment test condition

No	Test Item	Condition	Notes				
1	High temperature storage test	T _a = 60℃, 240h	1				
2	Low temperature storage test	T _a = -20℃, 240h	1				
3	High temperature operation test	T _a = 50℃, 50%RH, 240h	1				
4	Low temperature operation test	T _a = 0℃, 240h	1				
5	Humidity condition operation	T _a = 40℃, 90%RH	1				
6	Vibration test (non-operating)	Waveform: Random Vibration level: 1.0Grms Bandwidth: 10-300Hz Duration: X,Y,Z, 10min One time each direction					
7	Shock test (non-operating)	Shock level : 100G Waveform : Half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction					
8	Altitude Operating Storage / Shipment	0 - 10,000 feet (3,048m) 0 - 40,000 feet (12,192m)					
9	Maximum storage humidity for 4 corner light leakage Mura	Max 70%RH, $T_a = 40^{\circ}C$					

Note 1) Result Evaluation Criteria:

TFT-LCD panels test should take place after cooling enough at room temperature.

In the standard condition, there should be no particular problems that may affect the display function.

^{*} T_a = Ambient Temperature



7. International Standards

7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Canadian Standards Association.

 Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1, European Committee for Electro-technical Standardization(CENELEC). Information Technology Equipment Safety Part 1: General Requirements.
- d) IEC 60950-1, The International Electro-technical Commission(IEC).
 Information Technology Equipment Safety Part 1: General Requirements.

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A B C D E F G H I J K L M

A,B,C: Size(Inch)

E: Month F ~ M: Serial No.

Notes:

1) Year

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2) Month

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

D: Year

b) Location of Lot Mark

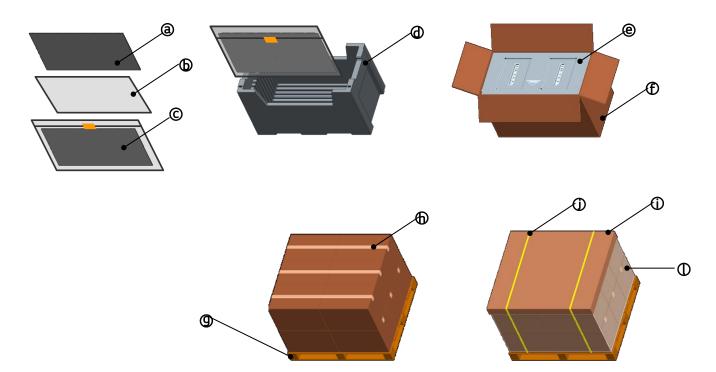
Serial No. is printed on the label. The label is attached to the backside of the LCD module.

This is subject to change without prior notice.



8-2. Packing Form

a) Package quantity in one box: 10eaPackage quantity in one Pallet: 120eab) Box Size: 635mm X 370mm X 430mmC) Pallet Ass'y Size: 1140mmX1300mmX983mn



No.	Description	Material				
a	LCM	-				
•	Protect film	OPP				
©	AL-Bag	AL				
0	Packing,Bottom	EPS				
e	Packing,Top	EPS				
(f)	Вох	Paper(SW)				
9	Pallet	Plywood				
6	Tape	OPP				
Û	Angle Cover	Paper(SW)				
①	BAND	PP				
•	Wrap	-				



9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- 1) You must mount a module using holes arranged in rear side.
- 2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- 3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- 4) You should adopt radiation structure to satisfy the temperature specification.
- 5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- 6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.

 (Some cosmetics are detrimental to the polarizer.)
- 7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- 8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- 9) Do not open the case because inside circuits do not have sufficient strength.
- 10) System frame should not have an interference with panel which can cause LC Leakage/Panel Crack due to the contraction of system frame at low temperature condition or panel damage by any other circumstances.

9-2. Operating Precautions

- 1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- 2) Brightness depends on the temperature.(In higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- 3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- 4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- 5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- 6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- 7) A screw which is fastened up the steels should be a machine screw.(if not, it causes metallic foreign material and deal LCM a fatal blow)
- 8) Please do not set LCD on its edge.
- 9) When LCMs are used for public display, defects such as Yogore & image sticking can not be guaranteed.
- 10) LCMs cannot support "Interlaced Scan Method"
- 11) When this forward model is used as a reverse-type model (PCB on bottom side) or a Portrait-type mode at storage and operation, LGD can not guarantee any defects of LCM.
- 12) Please conduct image sticking test after 2-hour aging with Rolling Pattern at normal temperature.(25~40°C)



9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions For Strong Light and Hazardous Materials Exposure

Strong light exposure causes degradation of polarizer and color filter.

The LCM should be avoided direct contact with hazardous materials such as sulfur, acetic acid, chlorine, etc. These materials may cause chemical reaction such as sulfurization, corrosion, discoloration, etc.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- 1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
- 2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions For Protection Film

- The protection film is attached to the bezel with a small masking tape. When the protection film
 is peeled off, static electricity is generated between the film and polarizer. This should be peeled
 off slowly and carefully by people who are electrically grounded and with well ion-blown
 equipment or in such a condition, etc.
- 2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- 3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.