

LQ104V1DW02

TFT-LCD Module

Spec. Issue Date: August 26, 2005

No: LD-17817A

SPEC No. LD-17817A PREPARED BY: DATE **SHARP** FILE No. ISSUE: AUG. 26. 2005 APPROVED BY: DATE PAGE : 21 pages MOBILE LIQUID CRYSTAL DISPLAY GROUP APPLICABLE GROUP SHARP CORPORATION MOBILE LIQUID CRYSTAL DISPLAY SPECIFICATION **GROUP** REVISION: AUG. 26. 2005 DEVICE SPECIFICATION FOR TFT-LCD Module MODEL No. LQ104V1DW02 These parts have corresponded with the RoHS directive. ☐ CUSTOMER'S APPROVAL DATE PRESENTED Tohaka BY T. NAKA

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SHARP CORPORATION

RECORDS OF REVISION

LQ104V1DW02

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1. Application

This specification applies to color TFT-LCD module, LQ104V1DW02

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a $640\times3\times480$ dots panel with 262,144 colors by supplying 18 bit data signal (6bit/color), four timing signals, +3.3V/+5V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a low-reflection, a wide-view angle, a higher-contrast and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use.

Backlight-driving DC/AC inverter is not built in this module.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H)×158.4(V)	mm
Pixel format	640(H)×480(V)	pixel
	(1 pixel = R + G + B dots)	
Pixel pitch	0.330(H)×0.330(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally black	
Unit outline dimensions *1	246.5(W)×179.4(H)×15.5max(D)	mm
Mass	620(max)	g
Surface treatment	Anti-glare and hard-coating 3H	

^{*1:} excluding backlight cables.

Outline dimensions is shown in Fig.1

Assembling form is shown in Fig.4

4. Input Terminals

4-1. TFT-LCD panel driving

Cl	N]	1		Į	Js	ec	l c	o	nr	ie	cto	01	:	D	F	9	N	1	4	-3	11)_	1	V(.	32)
		1																	T					3	3 1	
		2																						30		
		C	N	1	pi	n a	ırra	ın	gei	me	nt	fr	or	n	m	0	du	le	SI	ıri	àc	e				

CN1 pin arrangement from module surface (Transparent view)

(Hirose Electric Co., Ltd.)

Corresponding connector:

DF9-31S-1V(32) (Hirose Electric Co., Ltd.)

DF9A-31S-1V(22) (Hirose Electric Co., Ltd.)

DF9B-31S-1V(32) (Hirose Electric Co., Ltd.)

DF9C-31S-1V(22) (Hirose Electric Co., Ltd.)

(※) Please do not use it besides corresponding connector

Pin No.	Symbol	Function	Remark
1	GND		
2	CK	Clock signal for sampling each data signal	
3	Hsync	Horizontal synchronous signal	[Note1]
4	Vsync	Vertical synchronous signal	[Note1]
5	GND		
6	R0	R E D data signal(LSB)	
7	R1	RED data signal	
8	R2	RED data signal	
9	R3	RED data signal	
10	R4	RED data signal	
11	R5	R E D data signal(MSB)	
12	GND		
13	G0	GREEN data signal(LSB)	
14	G1	GREEN data signal	
15	G2	GREEN data signal	
16	G3	GREEN data signal	
17	G4	GREEN data signal	
18	G5	GREEN data signal(MSB)	
19	GND		
20	В0	BLUE data signal(LSB)	
21	B1	BLUE data signal	
22	B2	BLUE data signal	
23	В3	BLUE data signal	
24	B4	BLUE data signal	
25	B5	BLUE data signal(MSB)	
26	GND		
27	ENAB	Signal to settle the horizontal display position	[Note2]
28	Vcc	+3.3/5.0V power supply	
29	Vcc	+3.3/5.0V power supply	
30	R/L	Horizontal display mode select signal	[Note3]
31	U/D	Vertical display mode select signal	[Note4]

* The shielding case is connected with GND.

[Note1] 480 line, 400 line or 350 line mode is selected by the polarity combination of the both synchronous signas.

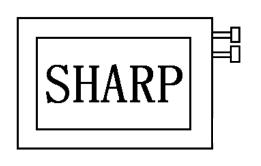
Mode	480 lines	400 lines	350 lines
Hsync	Negative	Negative	Positive
Vsync	Negative	Positive	Negative

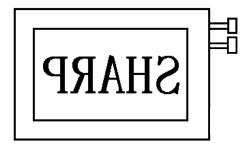
[Note2] The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.

In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2.

Don't keep ENAB "High" during operation.

[Note3] [Note4]

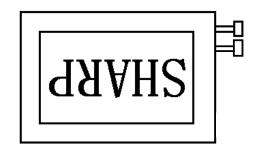




R/L=H i g h, U/D=L o w

 $R/L = L \circ w$, $U/D = L \circ w$





R/L=H i g h, U/D=H i g h

R/L=Low, U/D=High

4-2. Backlight driving

Used connector

 $: BHR-02(8.0)VS-1N \qquad (JST)$

Corresponding connector

(JST)

SM02(8.0)B-BHS-1N (JST)

: SM02(8.0)B-BHS-1

CN2, CN3

Pin no.	Symbol	Func	Cable color	
1	V_{HIGH}	Power supply for lamp	(High voltage side)	Pink
2	V_{LOW}	Power supply for lamp	(Low voltage side)	White

[Note] The input voltage waveform to the terminal No.1 of CN2 and CN3 should be in phase. There may be discharge between the terminals if it is input in opposite phase.

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	$V_{\rm I}$	Ta=25°C	$-0.3 \sim \text{Vcc} + 0.3$	V	[Note1]
+5V supply voltage	Vcc	Ta=25°C	$0 \sim + 6$	V	
Storage temperature	Tstg	_	$-20 \sim +70$	$^{\circ}$	[Note2,3]
Operating temperature (Panel surface)	Тора	_	$0 \sim +70$	$^{\circ}\!\mathbb{C}$	

[Note1] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB, R/L, U/L

[Note2] Humidity: 95%RH Max. at $Ta \le 40^{\circ}$ C.

Maximum wet-bulb temperature at 39°C or less at Ta>40°C.

No condensation.

[Note3] Maximum value : Panel surface temperature

6. Electrical Characteristics

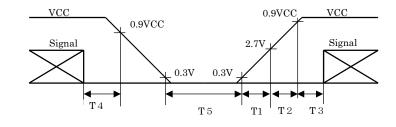
6-1.TFT-LCDpaneldriving

 $Ta=25^{\circ}C$

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Supply voltage	Vcc	+3.0	+3.3 +5.0	+5.5	V	[Note1]
Supply	Current dissipation	Icc	_	185	270	m A	Vcc=3.3V [Note2]
		Icc	_	145	230	m A	Vcc=5.0V [Note2]
Permi	issive input ripple voltage	V _{RF}	_	_	100	mVp-p	
Input	voltage (Low)	V_{IL}	_	_	0.3Vcc	V	
Input	voltage (High)	V _{IH}	0.7Vcc	_	_	V	[Note3]
Inp	ut current 1	I _{OL1}	-10.0	_	10.0	μΑ	V _I =0V [Note 4]
		I_{OH1}	-10.0	_	10.0	μΑ	V _I =Vcc [Note 4]
Inp	ut current 2	I _{OL2}	-800	_	_	μΑ	V _I =0V [Note 5]
		I _{OH2}	-10.0	_	10.0	μΑ	V _I =Vcc [Note 5]
Inp	ut current 3	I _{OL3}	-10.0	_	10.0	μΑ	V _I =0V [Note 6]
		I _{OH3}	_	_	800	μΑ	V _I =Vcc [Note 6]

[Note 1] Vcc-turn-on conditions

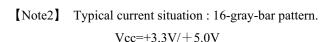
$$\begin{array}{l} 0 < T \ 1 \leqq 1 \ 5 \ m \ s \\ 0 < T \ 2 \leqq 1 \ 0 \ m \ s \\ 0 < T \ 3 \leqq 1 \ 0 \ 0 \ m \ s \\ 0 < T \ 4 \leqq 1 \ s \\ T \ 5 > 2 \ 0 \ 0 \ m \ s \end{array}$$



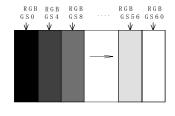
VCC

Vcc-dip conditions

- 1) 2. $5 V \le V c c$ t $d \le 1 0 m s$
- 2) V c c < 2. 5 V Vcc-dip condition should also follow The Vcc-turn-on conditions



- [Note3] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB, R/L,U/D
- [Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB
- [Note5] R/L[Note6] U/D



Τd

2.5V

6-2. Backlight driving

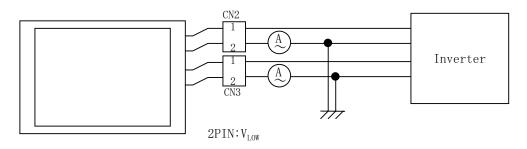
The backlight system is an edge-lighting type with double CCFT (Cold Cathode Fluorescent Tube). The characteristics of lamp are shown in the following table. (It is usually required to measure under the following condition.

condition : I_L =6.0mA,Ta=25°C \pm 2°C, F_L =60kHz.)

Ta=25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Re	mark
Lamp current	IL	3.5	6.0	7.0	mArms	[Note1]	
Lamp voltage	$V_{\scriptscriptstyle m L}$	420	470	520	Vrms		
Lamp power consumption	PL	_	2.8	_	W	[Note2]	
Lamp frequency	FL	35	60	70	KHz	[Note3]	
Kick-off voltage	Vs	_	_	740	Vrms	Ta=25°C	[Note8]
[Note4]				(1080)	Vrms		[Note9]
		_	_	790	Vrms	Ta =0°C	[Note8]
		_	_	(1150)	Vrms		[Note9]
Lamp life time	LL	50000	_	_	hour	[Note5]	I_L =6. OmA
		30000	_	_	hour	[Note5]	I_L =7. OmA

[Note1] Lamp current is measured with current meter for high frequency as shown below.



- [Note2] At the condition of $I_I = 6.0 \text{mArms}$
- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency ,and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference. Please check it in a actual finished product.
- [Note4] The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on.

Please make it the circuit where the voltage does not fall till both lamps turn on.

Please connect GND of a metal case of a module and inverter circuit, the method to impress of the Voltage is Slide Up.

It is also affected by the run of connecting leads between the inverter and the backlight; therefore, when you use this for mass production, please check this by the state incorporated into the device you use and design with an allowance.

Please use the inverter of sine wave the positive wave and the negative wave of which are symmetric without occurrence of spike waves. The input voltage waveform to the terminal No.1 of CN2 and CN3 should be in phase. There may be discharge between the terminals if it is input in opposite phase.

If one of the lamps is destroyed or become unable to light for its life, voltage discharged between the terminals No.1 of CN2 and CN3 may occur, so please apply fail-safe design to an inverter circuit.

[Note5] a) These are reference values as the lamps are consumable goods. These values are not guaranteed. Above value is applicable when lamp (the long side of LCD module) is placed horizontally. (Landscape position)

The life time is defined as the value when either of the item below is applicable when lighting continuously with $Ta=25^{\circ}C$ and $I_{L}=6.0$ mArms.

- (1) When the illuminance becomes 50% of the initial value.
- (2) When the lighting start voltage at the lowest temperature operation becomes 810Vrms (inverter output voltage).

b) Avoid continuous operation under low temperature as the lamp used for this module steeply decreases brightness when used under low temperature for a long time.
(Continuous operation for one month under low temperature may cause decrease of the brightness to 50% of the initial value.) It is recommended to change the lamp regularly if you have to use.

Lamp lifetime may vary if lamp is in portrait position due to the change of mercury density inside the lamp.

[Note6] The characteristics of the inverter power supply affects largely to the lighting performance and the life of the backlight. Please check to avoid lighting failure of the backlight like flicker and non-lighting by the mismatch between the backlight and the inverter power supply when you prepare the inverter power supply. It is recommended to check by the conditions as close to the actual device as possible. Please use the inverter power supply with safety circuits like over-voltage/ overcurrent detector circuits and discharge waveform detector circuits. Please use the detector circuit which can be controlled for lamp by lamp. If one side becomes open, overcurrent may flow to one lamp on the other side.

Recommended inverter is "CXA-P1212B-WJL(TDK corporation)". (This inverter is PWM dimmer system (I_L =6.5mA(Max)), so it becomes L_L =40000(min) regardless of the dimmer.)

- [Note7] Please design considering the variation of the impedance of the two lamps and the variation of the ballast capacitor capacity of the inverter.
- [Note8] The voltage at the output of inverter circuitry
- [Note9] The voltage at the output of transformer built in inverter circuitry
- Note) Insulate the high voltage area in order to prevent direct contacts to the area. As countermeasures for excessive heat or exothermic fire, use protection elements such as fuses to cut the circuit.

 Use burn-resistant (or noncombustible) material for board or resin.

7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2- $1 \sim 3$.

7-1. Timing characteristics

Parar	neter	Symbol	Mode	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc	all	_	25.18	28.33	MHz	
	High time	Tch	"	5	_	_	ns	
	Low time	Tcl	"	10	_	_	ns	
Data	Setup time	Tds	IJ	5	_	_	ns	
	Hold time	Tdh	"	10	_	_	ns	
Horizontal	Cycle	TH	"	30.00	31.78	_	μ s	
sync. signal			"	750	800	900	clock	
	Pulse width	ТНр	"	2	96	200	clock	
Vertical	Cycle	TV	480	515	525	560	line	
sync. signal			400	446	449	480	line	
			350	447	449	510	line	
	Pulsewidth	TVp	all	1	_	34	line	
Horizontal dis	Horizontal display period		11	640	640	640	clock	
Hsync-Clock		ТНс	"	10	_	Tc-10	ns	
phase difference								
Hsync-Vsync		TVh	"	0	_	ТН-ТНр	clock	
phase differer	nce							

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding to the rising edge of ENAB signal is displayed at the left end of the active area.

Parar	symbol	Min.	Тур.	Max.	Unit	Remark	
Enable signal	Setup time	Tes	5	_	Tc-10	ns	
	Pulse width	Тер	2	640	640	clock	
Hsync-Enable signal		ТНе	44	_	TH-664	clock	
phase differen	ice						

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2- ①~③. Be careful that the module does <u>not</u> work when ENAB is fixed "High". When the phase difference is below 104 clock, keep the "High level of ENAB is signal longer Than 104-The. If it will not be keeped, the display starts from the data of C104(clock).

7-3. Vertical display position

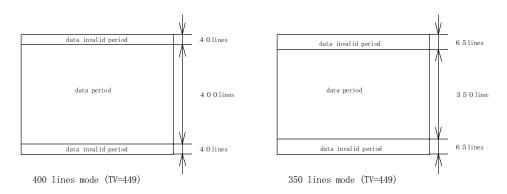
The vertical display position is automatically centered in the active area at each mode of VGA ,480-,400-,and 350-line mode . Each mode is selected depending on the polarity of the synchronous signals described in 4-1(Note1).

In each mode, the data of TVn is displayed at the top line of the active area. And the display position will be centered on the screen like the following figure when the period of vertical synchronous signal, TV, is typical value.

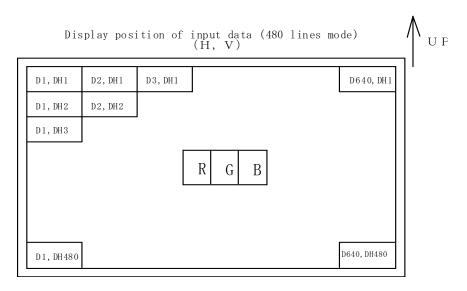
In 400-,and 350-line mode,the data in the vertical data invalid period is also displayed, So ,inputting all data "0" is recommended during vertical data invalid period.

ENAB signal has no relation to the vertical display position.

Mode	V-data start(TVs)	V-data	V-display start(TVn)	V-display period	Unit	Remark
		period(TVd)				
480	34	480	34	480	line	
400	34	400	443-TV	480	line	
350	61	350	445-TV	480	line	



7-4. Input Data Signals and Display Position on the screen



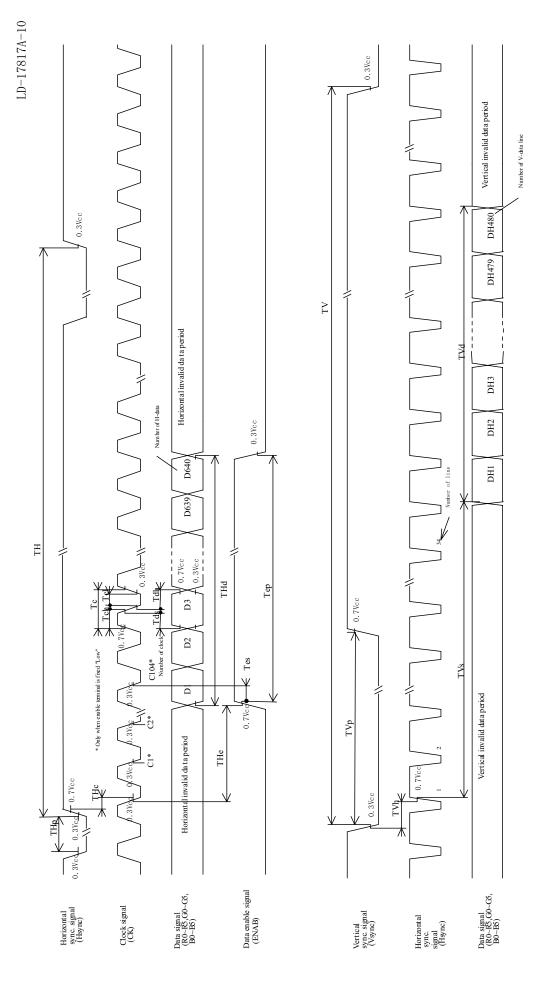


Fig2-1 Input signal waveforms (480 line mode)

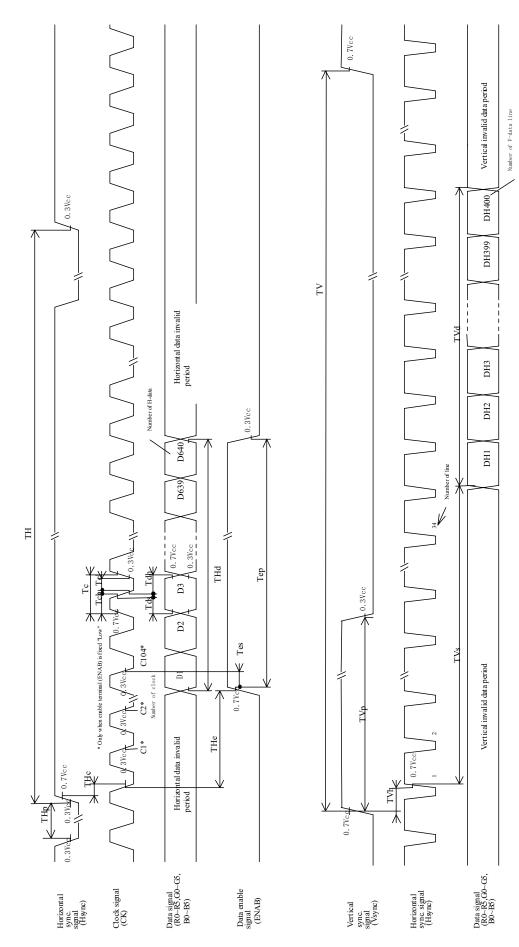


Fig.2-2 Input signal waveforms (400 line mode)

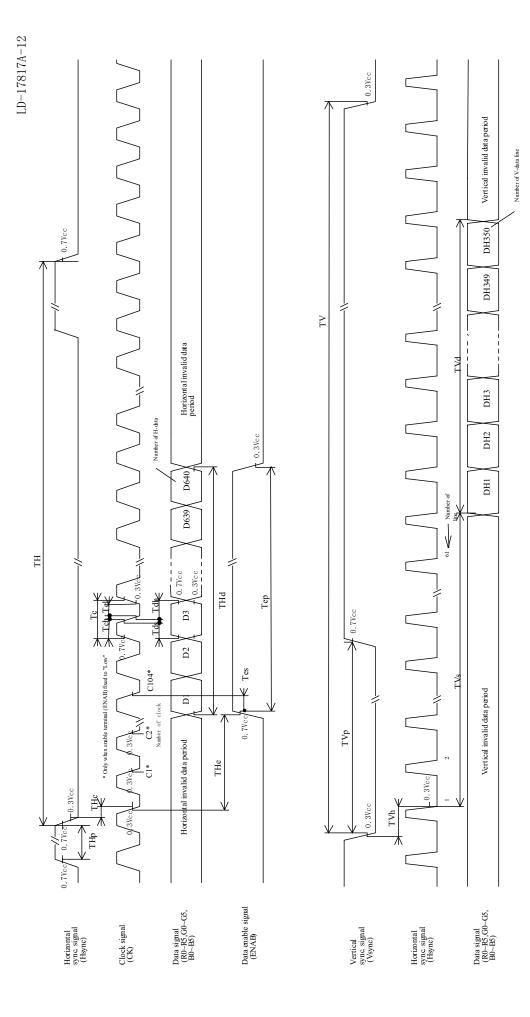


Fig.2-3 Input signal waveforms (350 line mode)

8. Input Signals, Basic Display Colors and Gray Scale of Each Color

0. mp	ut Signais, E	Jusic Di	spiay	COIO	is and	Gray	y Scar	C 01 1	zacii (C0101										
	Colors & Data signal						al													
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	B1	B2	В3	B4	B5
		Scale																		
Basic Color Basic Color	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green		0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan		0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
(ঐন্বয়ু Scale of Red Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	\downarrow	\downarrow					\					↓							
of R _₩	Û	V	→				V					+								
ed ed	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green Gray Scale of Green	仓	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
y Sc	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
മിക ര ale o	仓	\downarrow	\downarrow					↓					↓							
f Gr	Û	\downarrow	\downarrow					↓					↓							
reem	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
্রিন্ধ্যু Scale of Blue Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	仓	→	V				↓					↓								
	Û	→	↓				↓					↓								
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 :Low level voltage, 1 : High level voltage

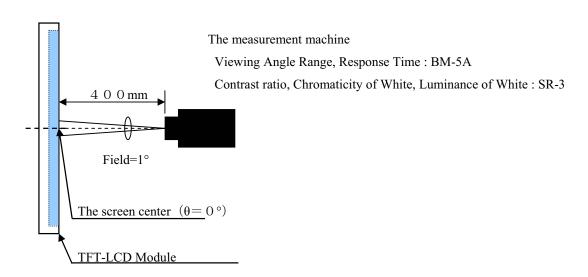
Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

9. Optical Characteristics

 $Ta=25^{\circ}C$, Vcc=+5V

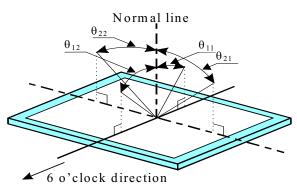
	Parameter	Symbol	Condition	Min	Тур	Max	Unit	Remark
Viewing	ng Horizontal		CR>10	70	80	_	Deg.	[Note1,4]
Angle Range	Vertical	θ11,θ12		70	80	-	Deg.	
Contrast ratio)	CR	θ= O °	250	450	-	_	[Note2,4]
Response	Rise(White→Black)	τr		_	12	_	ms	[Note3,4]
Time	Decay(Black→White)	τd		_	13	_	ms	
Chr	omaticity of White	X		0.263	0.313	0.363	-	[Note4]
		у		0.279	0.329	0.379	-	I _L =6.0mArms
Lumi	inance of white	ΥL		300	380	_	cd/m ²	f=60kHz

%The measurement shall be executed 50 minutes after lighting at rating. (condition:I_L=6.0mArms) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig below.



Optical characteristics measurement method.

[Note1] Definitions of viewing angle range:

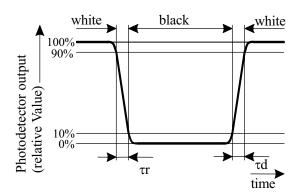


[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

After power supply injection, measure it 50 minutes later.[An initial characteristic]

10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it. Blow away dust on the polarizer with antistatic N_2 blow. It is undesirable to wipe off because a polarizer is sensitive. It is recommended to peel off softly using the adhesive tape when soil or finger oil is stuck to the polarizer. When unavoidable, wipe off carefully with a cloth for wiping lenses.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling electric components.
- h) Protection film is attached to the module surface to prevent it from being scratched.Peel the film off slowly, just before the use, with strict attention to electrostatic charges.Blow off 'dust' on the polarizer by using an ionized nitrogen.
- i) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- j)Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- k) Metal cases of the module should be connected to GND. If it is not connected, items below may possibly occur.
 - a) Destabilization to EMI and external noises
 - b) Display noise that comes from the backlight
 - c) Destabilization of inverter circuit output
 - d) Partial heating
- l) There are high voltage portions on the backlight and very dangerous. Careless touch may lead to electrical shock. When exchange lamps or service, turn off the power without tail.
- m) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
- n)Cold cathode fluorescent lamp in LCD panel contains a small amount of mercury, please follow local ordinances or regulations for disposal.
- o) Please do not pull Backlight cable forcibly

- p) Liquid crystal contained inside the module coagulates under the rated storage temperature, and causes breakage of the panel. And if the temperature is beyond the rated storage temperature, liquid crystal becomes isotropic liquid and may not return to the original state. Preserve at around ambient temperature as long as possible.
- q) Because of the effect by the leakage current to the adjacent conductor caused by lamp lead run, discharge starting voltage may be required more than specified.
- r) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- s) The volume of the module is adjusted to the optimum when shipped, so do not modify the adjusted value. If the adjusted value is modified, it may not satisfy this spec.
- t) Notice: Never dismantle the module, because it will cause failure.
- u) Be careful when using it for long time with fixed pattern display as it may cause afterimage.
- v) Please use with off state of the TFT LCD panel drive power supply and signals, when the backlight is not lit for long time like power save mode.

Abide by the precautions which are normally applied to electronic parts.

12. Reliability test items

No.	Test item	Conditions	Remark
1	High temperature storage test	Ta=70°C 240h	
2	Low temperature storage test	Ta= -20°C 240h	
3	High temperature	Ta=40°C ; 95%RH 240h	
	& high humidity operation test	(No condensation)	
4	High temperature operation test	Ta=70°C(Panel surface) 240h	
5	Low temperature operation test	Ta= 0°C 240h	
6	ESD test	Contact charge Once for each input terminal (150pF 330 Ω)	
		$\pm 200\mathrm{V}$	
7	Shock test	Max. gravity: 490m/s ²	
	(non- operating)	Pulse width: 11ms, half sine wave	
		Direction: $\pm X, \pm Y, \pm Z$ once for each direction.	
8	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.075mm	
	(non- operating)	: $57\sim500$ Hz/Gravity: 9.8 m/s ²	
		Sweep time: 11 minutes	
		Test period: 3 hours	
		(1 hour for each direction of X,Y,Z)	

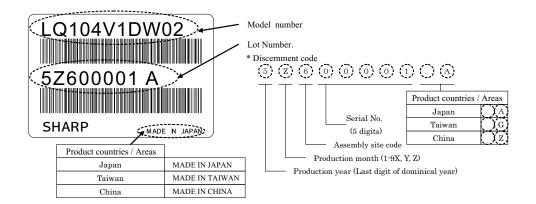
[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

13. Others

13-1 Label: Module

indication position is shown in Fig.1



14. Packing Form

Packing form is shown in Fig3.

a) Piling number of cartons: MAX. 6

b) Package quantity in one carton: 10 pcs

c) Carton size : 467(W)×305(D)×233(H)mm

d) Total mass of 1 carton filled with full modules: 8kg

e) Safekeeping condition of carton

①Temperature $0\sim40^{\circ}$ C

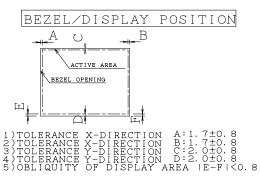
②Humidity Less than 60%RH

③Environment There don't be gas letting you cause corrosion

4 Period Around 3 months5 Environment to open More than 60%RH

Please carry out static electricity measures.

If any problem occurs in relation to the description of this specification , it shall be resolved through discussion with spirit of cooperation.



note1)General tolerance is ±0.5
note2)Take care of set design to hide
the scratches and bubbles appeared on
the polarizer or other frame area.
which is located outside of active area.
note3)Wrinkle/wound/lifting/bubble of the
AL sheet is not counted as a defect.
note4)Warp and floating for printed wiring
and chassis are excluded from the
thickness of the unit.
note5)Recommended tighten torque for mounting
0.294±0.02N·m(3.0±0.2ksf·cm)

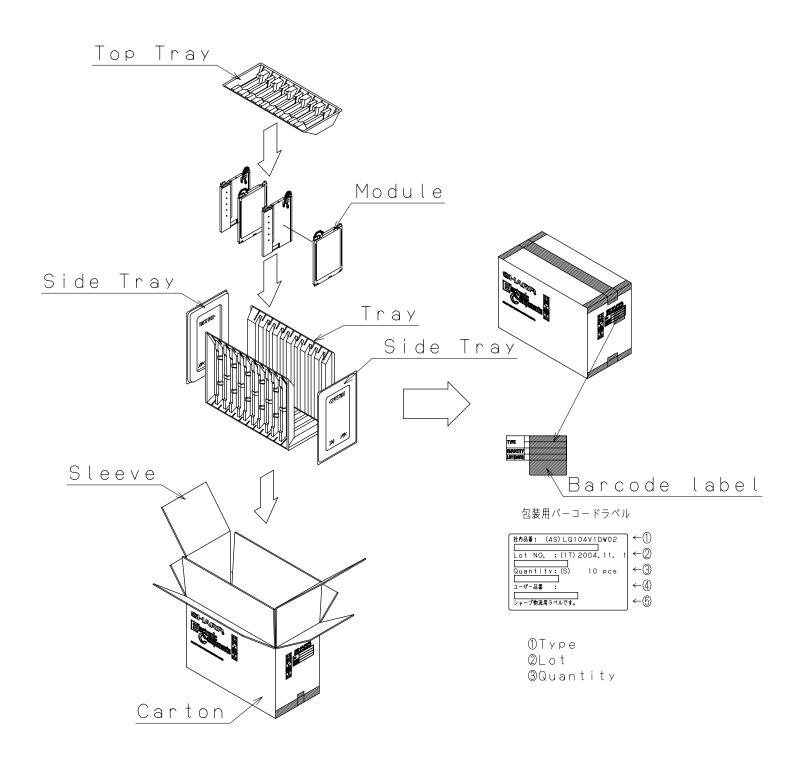


Fig. 3 Packing Form

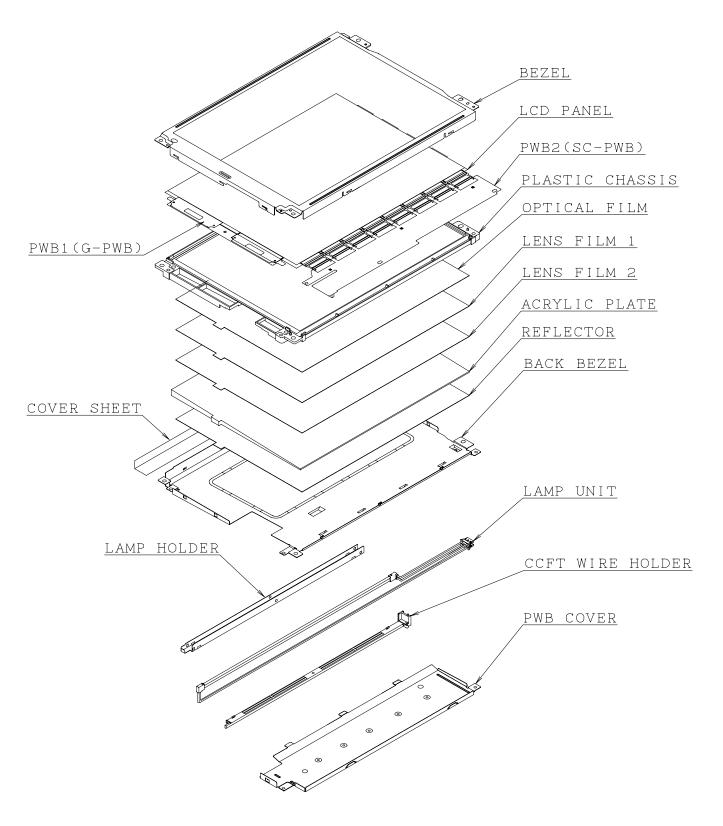


Fig4. Module Structure



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