

SPECIFICATION FOR APPROVAL

() Preliminary Specification	n
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Title

(♦) Final Specification

Customer	ASUS	SUPPLIER	LG Display Co., Ltd.				
MODEL		*MODEL	LP156WF4				

*When you obtain standard approval, please use the above model name without suffix

SPB1

15.6" Full HD TFT LCD

Suffix

APPROVED BY	SIGNATURE
1	

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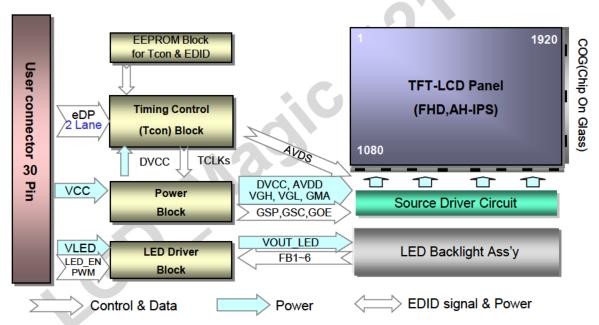
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
1.0	Jul. 27. 2012	-	Final Specification	1.0
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1. General Description

The LP156WF4 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. This TFT-LCD has 15.6 inches diagonally measured active display area with FHD resolution (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP156WF4 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP156WF4 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the subpixels, the LP156WF4 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.6 inches diagonal
Outline Dimension	359.5(H, typ.) × 224.1(V, typ.) × 3.4(D,max) [mm] (with Bracket & PCB Board)
Pixel Pitch	0.17925 mm x 0.17925 mm
Pixel Format	1920 horiz. By 1080 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	300 cd/m² (Typ.5 point)
Power Consumption	Total 7.28 W (Typ.) Logic : 1.68 W (Typ.@ Mosaic), B/L : 5.6 W (Typ.@VLED12V)
Weight	330g (Max.) / 320g (Typ.)
Display Operating Mode	Normally Black
Surface Treatment	Anti glare treatment of the front Polarizer
RoHS Compliance	Yes
BFR/PVC/As Free	Yes for all



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

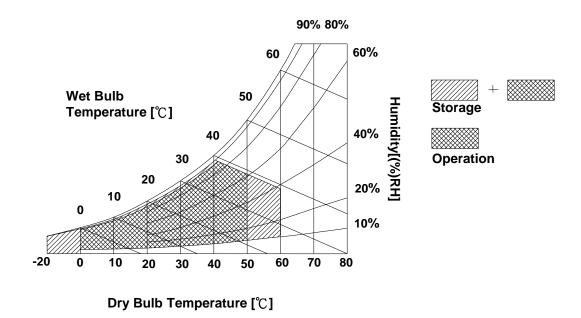
Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Office		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Hst	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.

Note: 2. Storage Condition is guaranteed under packing condition.





3. Electrical Specifications

3-1. Electrical Characteristics

The LP156WF4 requires two power inputs. The first logic is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second backlight is the input about LED BL with LED Driver.

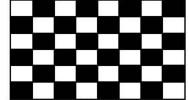
Table 2. ELECTRICAL CHARACTERISTICS

Parameter		0		Values		11	Natas
		Symbol	Min	Тур	Max	Unit	Notes
LOGIC:							
Power Supply Input Voltage		Vcc	3.0	3.3	3.6	V	1
Power Supply Input Current	Mosaic	Icc	_	510	585	mA	2
Power Consumption		Pcc	_	1.68	1.93	W	2
Power Supply Inrush Current		Icc_p	-	-	1500	mA	3
Differential Impedance		Zm	90	100	110	Ω	4
BACKLIGHT : (with LED Drive	er)						
LED Power Input Voltage		VLED	7.0	12.0	21.0	V	5
LED Power Input Current		ILED	_	470	520	mA	6
LED Power Consumption		PLED	_	5.6	6.2	W	6
LED Power Inrush Current		ILED_P	_	-	1500	mA	7
PWM Duty Ratio			5	-	100	%	8
PWM Jitter		-	0	-	0.2	%	9
PWM Impedance		Zpwm	20	40	60	kΩ	
PWM Frequency		Fpwm	200	-	1000	Hz	10
PWM High Level Voltage		V _{PWM_H}	3.0	-	5.3	V	
PWM Low Level Voltage		V _{PWM_L}	0	-	0.3	V	
LED_EN Impedance		Zрwм	20	40	60	kΩ	
LED_EN High Voltage		VLED_EN_H	3.0	-	5.3	V	
LED_EN Low Voltage		VLED_EN_L	0	-	0.3	V	
Life Time			12,000	-	-	Hrs	11

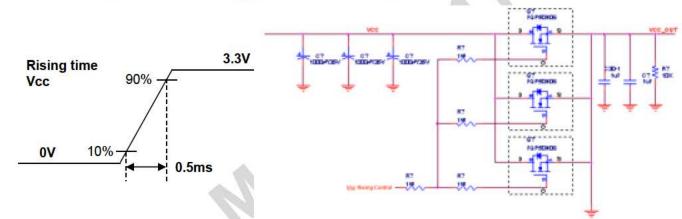


Note)

- The measuring position is the connector of LCM and the test conditions are under 25℃, fv = 60Hz, Black pattern.
- 2. The specified lcc current and power consumption are under the Vcc = 3.3V , 25°C , fv = 60Hz condition and Mosaic pattern.

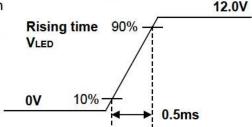


- 2. This Spec. is the max load condition for the cable impedance designing.
- The below figures are the measuring Vcc condition and the Vcc control block LGD used. The Vcc condition is same as the minimum of T1 at Power on sequence.



- 4. This impedance value is needed for proper display and measured form eDP Tx to the mating connector.
- 5. The measuring position is the connector of LCM and the test conditions are under 25°C.
- 6. The current and power consumption with LED Driver are under the Vled = 12.0V , 25℃, Dimming of Max luminance and White pattern with the normal frame frequency operated(60Hz).
- The below figures are the measuring VIed condition and the VIed control block LGD used.

VLED control block is same with Vcc control block.



- The operation of LED Driver below minimum dimming ratio may cause flickering or reliability issue.
- 9. If Jitter of PWM is bigger than maximum, it may induce flickering.
- 10. This Spec. is not effective at 100% dimming ratio as an exception because it has DC level equivalent to 0Hz. In spite of acceptable range as defined, the PWM Frequency should be fixed and stable for more consistent brightness control at any specific level desired.
- 11. The life time is determined as the time at which brightness of LCD is 50% compare to that of minimum value specified in table 7. under general user condition.



3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector used for the module electronics interface and the other connector used for the integral backlight system.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

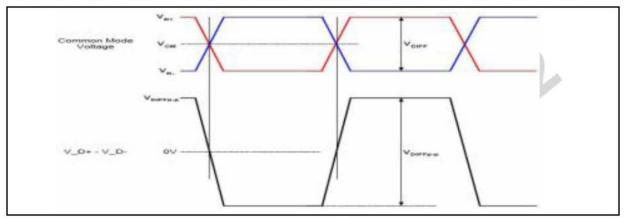
Pin	Symbol	Description	Notes
1	NC	NO Connect	[Interface Chip]
2	GND	High Speed (Main Link) Ground	1. LCD : Analogix, ANX9858 (LCD Controller
3	Lane1_N	Complement Signal-Lane 1	Including eDP Receiver.
4	Lane1_p	True Signal-Main Lane 1	System : TBD or equivalent * Pin to Pin compatible with eDP
5	GND	High Speed (Main Link) Ground	Fill to Fill compatible with ebr
6	Lane0_N	Complement Signal-Lane 0	[Connector] CABLINE-VS RECE ASS'Y, I-PEX
7	Lane0_p	True Signal-Main Lane 0	or its compatibles
8	GND	High Speed (Main Link) Ground	[Mating Connector]
9	AUX_P	True Signal-Auxiliary Channel	CABLINE-VS PLUG CABLE
10	AUX_N	Complement Signal-Auxiliary Channel	ASS'Y or equivalent.
11	GND	High Speed (Main Link) Ground	[Connector pin arrangement]
12	vcc	LCD Logic and driver power (3.3V Typ.)	30 1
13	vcc	LCD Logic and driver power (3.3V Typ.)	
14	NC	NO Connect	
15	GND	Ground	[LCD Module Rear View]
16	GND	Ground	
17	HPD	HPD signal pin	
18	GND	LED Backlight Ground	
19	GND	LED Backlight Ground	
20	GND	LED Backlight Ground	
21	GND	LED Backlight Ground	
22	LED_EN	LED Backlight On/Off	
23	PWM	System PWM Signal input for dimming	
24	NC	NO Connect	
25	NC	NO Connect	
26	VLED	LED Backlight Power (7.0V-21V)	
27	VLED	LED Backlight Power (7.0V-21V)	
28	VLED	LED Backlight Power (7.0V-21V)	
29	VLED	LED Backlight Power (7.0V-21V)	
30	NC	NO Connect	



3-3. eDP Signal Timing Specifications

3-3-1. DC Specification

The VESA Display Port related AC specification is compliant with the VESA Display Port Standard v1.1a.



Description	Symbol	Min	Max	Unit	Notes	
Differential mode to mode lawy to the se		120	-	\/	For high bit rate	
Differential peak-to-peak Input voltage	VDIFF p-p	40	-	mV	For reduced bit rate	
Rx DC common mode voltage	VcM	0	2.0	V		

3-3-2. AC Specification

The VESA Display Port related AC specification is compliant with the VESA Display Port Standard v1.1a.

Description	Symbol	Min	Тур	Max	Unit	Notes	
Unit Interval for high bit rate (2.7Gbps/lane)	UI_High_Rate		370	5	ps	Range is nominal ±350ppm. DisplayPort Link Rx does not require local crystal for link	
Unit Interval for high bit rate (1.62Gbps/lane)	UI_Low_Rate	9 <u>5</u> 8	617	7.	ps	clock generation	
Lane-to-Lane skew	V Rx-SKEW- INTER_PAIR	2 4 7		5200	ps	-	
Lana intra nair akaw	V Rx-SKEW- INTRA_PAIR	1		100	ps	For high bit rate	
Lane intra-pair skew			(#)	300	ps	For reduced bit rate	



3-4. Signal Timing Specifications

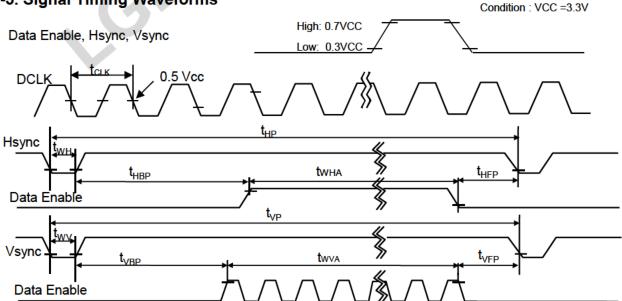
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of eDP Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f _{CLK}	-	69.00	-	MHz	eDP 2 Lane
	Period	t _{HP}	1040	1044	1048		
Hsync	Width	t _{WH}	16	18	20	tCLK	
	Width-Active	t _{WHA}	960	960	960	2	
	Period	t _{VP}	1098	1102	1106		
Vsync	Width	t _{wv}	5	5	5	tHP	
	Width-Active	t _{wva}	1080	1080	1080		
	Horizontal back porch	t _{HBP}	48	50	52	tCLK	
Data	Horizontal front porch	t _{HFP}	16	16	16	ICLK	
Enable	Vertical back porch	t _{VBP}	10	12	14	tHP	
	Vertical front porch	t _{VFP}	3	5	7	INP	

Appendix) all reliabilities are specified for timing specification based on refresh rate of 60Hz. However, LP156WF4 has a good actual performance even at lower refresh rate (e.g. 40Hz or 50Hz) for power saving mode, whereas LP156WF4 is secured only for function under lower refresh rate. 60Hz at Normal mode, 50Hz, 40Hz at Power save mode. Don't care Flicker level (power save mode).

3-5. Signal Timing Waveforms





3-6. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 5. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	ΞD					GRE	EEN					BL	UE		
	50.01	MSE	3				LSB	-						MSE					LSB
	1	R 5	R 4	R 3	R2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В 3	B 2	B 1	В0
	Black	0	0			0	0	0	0	0	0		0	0				0	0
	Red	1	1			1	1	0	0	0	0		0	0	0		0	0	0
	Green	0	0	0		0	0	1	1		1	. 1	1	0	0		0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	.1	1		1
Color	Cyan	0	0	0	0	0	0	1	1	1		. 1	1	1	1	1	. 1		1
	Magenta	1	1	1	1	1		0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED		ļ																	
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					 						 								
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		ļ			 			ļ			 						 		
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	 1	 1	1



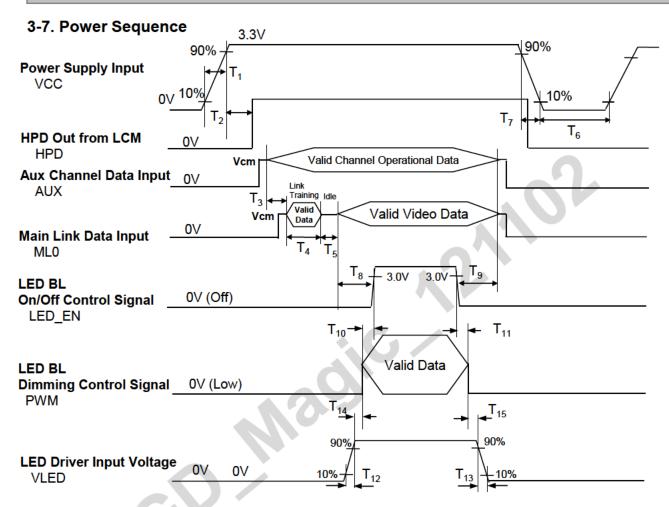


Table 6. POWER SEQUENCE TABLE

Logic		Value		Lluita	LED		Value		Llmita
Parameter	Min.	Тур.	Max.	Units	Parameter	Min.	Тур.	Max.	Units
T ₁	0.5	-	10	ms	T ₉	200	-	-	ms
T ₂	0	1	200	ms	T ₁₀	0	1	-	ms
T ₃	50	75	1	ms	T ₁₁	0	1	-	ms
T ₄	0	1	1	ms	T ₁₂	0.5	1	-	ms
T ₅	0	1	1	ms	T ₁₃	0	1	5000	ms
T ₆	500	-	-	ms	T ₁₄	10	-	-	ms
T ₇	3	-	10	ms	T ₁₅	10	-	-	ms
T ₈	200	-	-	ms					

Note)

- 1. Do not insert the mating cable when system turn on.
- 2. Valid Data have to meet "3-3. eDP Signal Timing Specifications"
- 3. eDP, LED_EN and PWM need to be on pull-down condition on invalid status.
- 4. LGD recommend the rising sequence of VLED after the Vcc and valid status of eDP turn on.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0°.

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

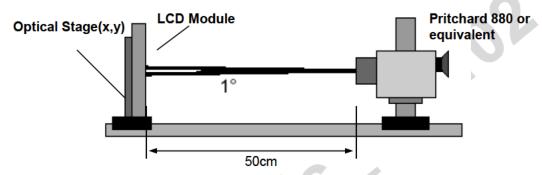


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, fv=60Hz, f_{CLK}= 69.0MHz

				,		-001 12, 1 _{CLK} - 03.01/11 12
Parameter	Symbol		Values		Units	Notes
Falailletei	Symbol	Min	Тур	Max	Office	Notes
Contrast Ratio	CR	400	700	-		1
Surface Luminance, white	L _{wh}	255	300	-	cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.4	1.6		3
Response Time	$Tr_R + Tr_D$	-	35	50	ms	4
Color Coordinates						
RED	RX	0.599	0.629	0.659		
	RY	0.320	0.350	0.380		
GREEN	GX	0.319	0.349	0.379		
	GY	0.584	0.614	0.644		
BLUE	BX	0.124	0.154	0.184		
	BY	0.082	0.112	0.142		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ=0°)	⊖r	80	85	-	degree	
x axis, left (Ф=180°)	Θl	80	85	-	degree	
y axis, up (Φ=90°)	Θu	80	85	-	degree	
y axis, down (Φ=270°)	Θd	80	85	-	degree	
Gray Scale						6



Note)

1. Contrast Ratio(CR) is defined mathematically as

2. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$LWH = Average(L1, L2, ... L5)$$

The variation in surface luminance, The panel total variation (δ WHITE) is determined by measuring LN
at each test position 1 through 13 and then defined as following numerical formula.
 For more information see FIG 2.

$$\delta$$
 WHITE = Maximum(L1,L2, ... L13) / Minimum(L1,L2, ... L13)

- 4. Response time is the time required for the display to transition from white to black (rise time, TrR) and from black to white(Decay Time, TrD). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

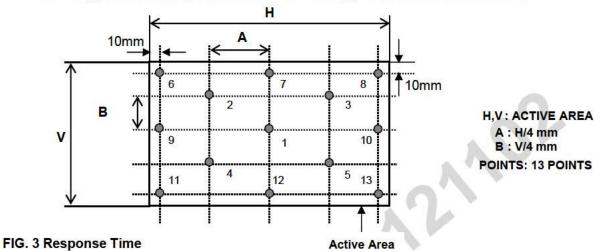
*
$$fV = 60Hz$$

Gray Level	Luminance [%] (Typ)
LO	0.13
L7	0.70
L15	4.53
L23	10.8
L31	20.3
L39	33.0
L47	49.0
L55	73.0
L63	100.0

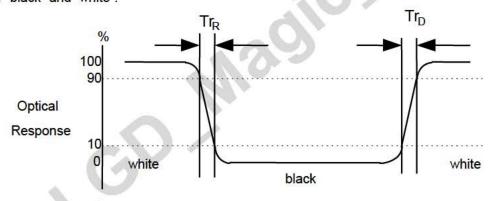


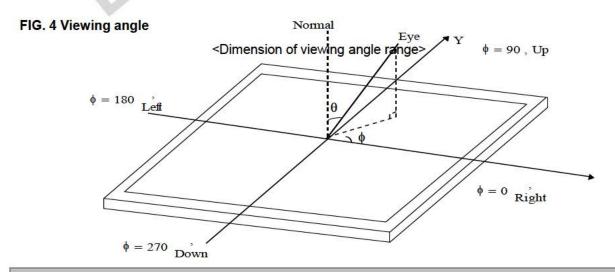
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".







5. Mechanical Characteristics

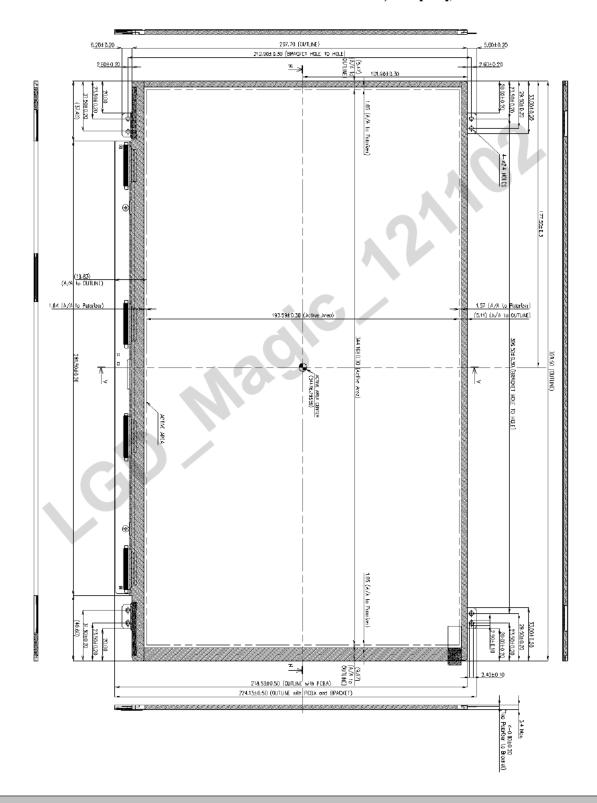
The contents provide general mechanical characteristics for the model LP156WF4. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	359.5 ± 0.5mm
Outline Dimension	Vertical	207.7 ± 0.5mm
	Thickness	3.4mm (max)
Bezel Area	Horizontal	347.55 ± 0.5mm
bezei Alea	Vertical	196.9 ± 0.5mm
Active Dieplay Area	Horizontal	344.16 ± 0.3 mm
Active Display Area	Vertical	193.59 ± 0.3 mm
Weight	330g (Max.) / 320g (Typ.)	
Surface Treatment	Anti-Glare treatment of the front p	oolarizer



<FRONT VIEW>

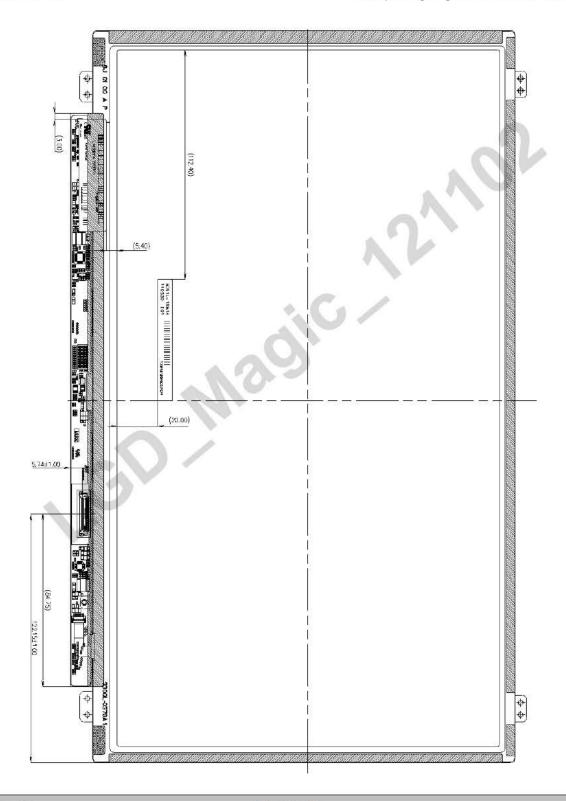
Note) Unit:[mm], General tolerance: ± 0.5mm





<REAR VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
 Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electro technical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electro technical Commission (IEC). Information Technology Equipment Safety Part 1: General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

	Α	В	С	D	E	F	G	Н	I	J	К	L	М
--	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C: SIZE(INCH) D: YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20 pcs

b) Box Size: 478 x 365 x 328



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to t h e module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) When handling the LCD module, it needs to handle with care not to give mechanical stress to the PCB and Mounting Hole area."

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm~200mV(Over~and~under~shoot~voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

Written by	dochoi@ledisolav.	- Andrews	EDID Data_ ver. 1.0		2112/7/11
	Byte	Byta (Max)	Field Name and Comments	Value	Value
	(Ibr)	(Mag	Paste	(Mes)	(Bir.)
	-	61	Heate Q		aummi
	2	82	Header	FF	13213311
Header	3	113	Heuds	FF	mm
2	4	04	Heder	FF	11311311
-	3	0.5	Bridge	FF	пппп
	6	06	Header Header		DESCRIPTION
	- 1	7 00	ID Munifortus None LOD	30	00210000
	9	10	II) Manufacture blens	E4	11200390
	10	0A	ID Product Code 0323h	23	00200811
3 -	11	6.8	(Hex LSB first)	03	nesorii
endor / Product EDID Version	12	90	ID Serial No - Optional ("Olls" Enot used, Humber Only and LSB First)	00	00830830
FE	13	62	ID Secial 140 - Optional (*00h* Erest need, 14mber Only and LSB First)	00	06890690
2 5	14	4E	ID Secial No Optional (*90b.* Error used, Humber Only and LSB First)	00	00800690
23	15	11	ID Senal No Optional ("10%:"Enct used, Number Only and LSB First)	00	освоскво
- E G	16	16	PA-ek of Meraficture - Optimal 00 weeks	00	00000000
2	17	11	Ven of Monthstone 2012 years	16	ocstoppe
	18	12	KDID crustus version # - 1	01	ocessess
	19	111	KD ID revision # = \$	04	оквисско
		_	Video input Definition - Input is a Dugital Video capual Interface , Colo Sit Depth : 6 Sits per Primary Color ,	-	
100	26	14	Digital Video Interface Standard Supported: DisplayFort is supported	95	TOSTOGRI
	31	15	Horizontal Screen Size (Rounded can) = 1.5 can	23	90590011
Display	22	16	Vartical Screen Size (Rounded can) = 19 can	13	ocalostr
3 1	23	17	Display Treader Characteristic (Guana)=(guana*100)-100 = Ebangle (2.2*100)-100+120	78	03313490
Display Parameters	24	10	Fourze Support [Display Power Mesagement(DDM): Standby Mode is not supported, Suspend Mode is not supported, Active Off = Wey Low Power is not supported Supported Color Blanding Founds: ROB 4.4.4 is VCrOb 4.4.4, Other Festion Support Flags: No _8.00, Preferred Timing Mode, No _Display is continuous frequency (Multi-mode Base RDID and Extension Block)]	0A	90003650
	25	18	Red/Ones. Low Bits (Bally/OcOy)	25	жомона
	26	14	Illus/Whits Low lite (Itcley/Vh(We)	B5	Tennont.
	27	18	Red X Re= 0.629	Al	Intensit
10000	28	10	Red Y Ry = 0.330	59	00.811681
5 5	29	110	Great: X Oc = 0.349	59	ojstjasj
8.8	30	1E	Green V Dy = 0.614	9D	18411181
~ 7	31	11	Elm X Brv0154	27	0000011
Panel Color Coordinates	372	20	10a-Y By=0.112	1C	00811190
11000000	n	71	White X Wh = 0.313	50	olejono
	-34	22	White Y 19ty = 0.329	54	02420200
24 7	35	23	Established timing 1 (Optional Other not used)	00	00810889
Brish and Trime	36	24	Ertsblished timing 2 (Optional_00h ifnot used)	00	- OCHIONIO
40 11	37	25	Mendicturer's titaings (Optional, 10h frot used)	00	-,00630683
	38	market de la companya del la companya de la company	Standard timing ID 1 (Optional_SIb finot wied)		oceanest
	39 40		Standard traing ID 1 (Optional, 9 In finet used) Standard traing ID 2 (Optional, 9 In finet used)		ocesses
1000	61		Standard timing 10:2 (Optional & In front used)	01	and the state of the later is the later in the later is the later in the later in the later is the later in t
9	42		Standard trining ID3 (Optional, 6th afree navel)	01	OCREORS]
90	43		Standard thaing ID3 (Optional_SIIs froctured)	01	.00010001
- 5	44		Standard timing ID4 (Optional_0 th shot used)	01	OCESCES]
2	43		Standard timing ID4 (Optional 9 th short used)	01	ocescexI
-	46		Standard training IDS (Optional SIIs first used)	01	ocescent
Standard Timing ID	47		Standard triaing ID5 (Optional #1h afrot used)	01	Mesonati
	48	30	Standard thing ID6 (Optional_Sile.front used) Dissolutioning ID6 (Optional_Sile.front used)	01	ocanosal
- 4	50	32	Standard traing ID7 (Optional Silt Startuses)	01	OCRDORAL
65			Properties of comments and of Adjusters of the State of S	98	_
- 65		-	Standard timber ID7 (Ontional SIIIs from used)	0.1	DESCRIPTION
8	51 52	33	Standard timing ID7 (Optional_Sth.finot used) Standard timing ID9 (Optional_Sth.finot used)	01	possons)



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
	54	36	Pixel Clock/10,000 (LSB) 138 MHz @ 60 Hz	E8	11101000
	55	37	Pixel Clock/10,000 (MSB)	35	00110101
	56	38	Horizontal Active (HA) (lower 8 bits) 1920 pixels	80	10000000
	57	39	Horizontal Blanking (HB) (lower 8 bits) 168 pixels	A8	10101000
	58	3A	Horizontal Active (HA) / Horizontal Blanking (HB) (upper 4:4bits)	70	01110000
#1	59	3B	Vertical Avtive (VA) 1080 line	38	00111000
Timing Descriptor #1	60	3C	Vertical Blanking (VB) (DE Blanking typ for DE only panels) 22 lines	16	00010110
ip	61	3D	Vertical Active (VA) / Vertical Blanking (VB) (upper 4:4bits)	40	01000000
scr	62	3E	Horizontal Front Porch in pixels (HF) (lower 8 bits) 32 pixels	20	00100000
Des	63	3F	Horizontal Sync Pulse Width in pixels (HS) (lower 8 bits) 36 pixels	24	00100100
0.0	64	40	Vertical Front Porch in lines (VF): Vertical Sync Pluse Width in lines (VS) (lower 4 bits) 5 lines: 5 lines	55	01010101
į	65	41	Horizontal Front Porch/ Sync Pulse Width/ Vertical Front Porch/ Sync Pulse Width (upper 2bits)	00	00000000
Ti.	66	42	Horizontal Vedio Image Size (mm) (lower 8 bits) 345 mm	59	01011001
- '	67	43	Vertical Vedio Image Size (mm) (lower 8 bits) 194 mm	C2	11000010
	68	44	Horizontal Image Size / Vertical Image Size (upper 4 bits)	10	00010000
	69	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	70	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	71	47	Non-Interlace, Normal display, no stereo, Digital Separate [Vsync_NEG, Hsync_NEG (outside of V-sync)]	18	00011000
	72	48	Flag	00	00000000
	73	49	Flag	00	00000000
	74	4A	Flag	00	00000000
	75	_	Data Type Tag (Descriptor Defined by manufacturer)	00	00000000
	76	4C	Flag	00	00000000
#2	77		Descriptor Defined by manufacturer	00	00000000
± ±	78	4E	Descriptor Defined by manufacturer	00	00000000
btc .	79	4 F	Descriptor Defined by manufacturer	00	00000000
cri	80	50	Descriptor Defined by manufacturer	00	00000000
<u>s</u>	81		Descriptor Defined by manufacturer	00	00000000
g L	82	52	Descriptor Defined by manufacturer	00	00000000
Timing Descriptor #2	83	53	Descriptor Defined by manufacturer	00	00000000
<u></u>	84	54	Descriptor Defined by manufacturer	00	00000000
	85	55	Descriptor Defined by manufacturer	00	00000000
	86	56	Descriptor Defined by manufacturer	00	00000000
	87	57	Descriptor Defined by manufacturer	00	00000000
	88	58	Descriptor Defined by manufacturer	00	00000000
	89	59	Descriptor Defined by manufacturer	00	00000000
	90		Flag	00	00000000
	91		Flag	00	00000000
	92		Flag	00	00000000
	93		Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	94	5E	Flag	00	00000000
#3	95	5F	Alphanumeric Data String (ASCII String) L	4C	01001100
	96				01000111
Timing Descriptor	97		Alphanumeric Data String (ASCII String) Alphanumeric Data String (ASCII String)	20	00100000
ini,	98	62	Alphanumeric Data String (ASCII String) D	44	0100000
es	99		Alphanumeric Data String (ASCII String) i	69	01101001
ď	100				01101001
ing			Alphanumeric Data String (ASCII String) Alphanumeric Data String (ASCII String)	73	01110011
<u>ii</u>	101	65	Alphanumeric Data String (ASCII String) Alphanumeric Data String (ASCII String)	70 6C	
7	102	66	Alphanumeric Data String (ASCII String)	6C	01101100
	103		Alphanumeric Data String (ASCII String)	61	01100001
	104	68	Alphanumeric Data String (ASCII String)	79	01111001
	105		Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	0A	00001010
	106	6A	Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000
	107	6B	Manufacturer P/N(If<13 char-> 0Ah, then terminate with ASC II code 0Ah, set remaining char = 20h)	20	00100000



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

	Byte (Dec)	Byte (Hex)	Field Name and Comments	Value (Hex)	Value (Bin)
Timing Descriptor #4	108		Flag	00	00000000
	109		Flag	00	00000000
	110	6E	Flag	00	00000000
	111	6F	Data Type Tag (Alphanumeric Data String (ASCII String))	FE	11111110
	112	70	Flag	00	00000000
	113	71	Alphanumeric Data String (ASCII String)	4C	01001100
	114	72	Alphanumeric Data String (ASCII String)	50	01010000
	115	73	Alphanumeric Data String (ASCII String)	31	00110001
	116	74	Alphanumeric Data String (ASCII String) 5	35	00110101
	117	75	Alphanumeric Data String (ASCII String) 6	36	00110110
	118	76	Alphanumeric Data String (ASCII String)	57	01010111
	119	77	Alphanumeric Data String (ASCII String)	46	01000110
	120	78	Alphanumeric Data String (ASCII String)	34	00110100
	121	79	Alphanumeric Data String (ASCII String)	2D	00101101
	122	7A	Alphanumeric Data String (ASCII String)	53	01010011
	123	7B	Alphanumeric Data String (ASCII String)	50	01010000
	124	7C	Alphanumeric Data String (ASCII String)	42	01000010
	125	7 D	Alphanumeric Data String (ASCII String)	31	00110001
Chec	126	7E	Extension flag (# of optional 128 panel ID extension block to follow, Typ = 0)	00	00000000
	127	7 F	Check Sum (The 1-byte sum of all 128 bytes in this panel ID block shall = 0)	69	01101001