(	( )	<b>Preliminary Specification</b>
(	<b>V</b>	Final Specification

Module	19"W Color TFT-LCD
Model Name	M190PW01 V8 (ES7.0 from V80L)

Customer	Date
Approved by	
Note: This Specification change without	

Approved by	Date			
<u>Flossie Chuang</u>	<u>Dec, 30, 2019</u>			
Prepared by	Date			
<u>TimJTLi</u>	<u>Dec, 30, 2019</u>			
AU Optronics corporation				

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# **Record of Revision**

Version	Date	Page	Old description	New Description	Remark	
1.0	2014/3/3	All	First version release	-		
1.1	2014/3/13	20	Symbol   Description   Min   Typ   Max   Unit   Remarks	3.4.5   Input Timing specification		
1.3	2014/4/20	13	TFT-LCD Connector Manufacturer Name: CHIEF	TFT-LCD Connector Manufacturer Name: STARCONN		
1.2	2014/4/28	31	7 Mechanical Characteristics – no user hold 2D drawing	7 Mechanical Characteristics – add user hold 2D drawing		
	2015/10/2	7	5	2.1 Display Characteristics - Power Consumption (LCD Module+ Backligh unit) = 10.4 (Typ.) Watt LCD module : PDD (Typ.)= 2.0 @ Black pattern,Fv=60Hz Backlight unit : P <sub>BLU</sub> (Typ.) = 8.4@Is=60mA TCO 6.0 Compliance	2.1 Display Characteristics - Power Consumption (LCD Module+ Backligh unit = 9.0 (Typ.) LCD module : PDD (Typ.)= 2.0 @ Black pattern,Fv=60Hz Backlight unit : P <sub>BLU</sub> (Typ.) =7.0@Is=50mA TCO 7.0 Compliance	
1.3			2015/10/2	7	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
		27	4.3.2 Recommended Operating  Condition    Symbol   Description   Min.   Typ.   Max.   Unit:   Remark:	## 4.3.2 Recommended Operating Condition    Symbol   Description   Min.   Typ.   Max.   Unit:   Remark:   100% day rato of LED chips   LED String Current:     50.5   55-   [mA]-   100% day rato of LED chips   100% day rato of Led to the second   100% day rato		
		28	Note 4-4: Definition of life time:  a. Brightness of LED becomes to 50% of its original value	Note 4-4: Definition of life time:  a. Brightness of LED becomes to 50% of its original value		



M190PW01 V8

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			b. Test condition: Is = 60mA and 25 $^{\circ}$ C (Room Temperature)	b. Test condition: Is = 50mA and 25 $^{\circ}$ C (Room Temperature)	
		All	Product specification release		ES7.0
2.0	2016/1/20	23	3.5 Power ON/OFF Sequence  Pewer Sequence Timings  Symbols   Value   Max.   Units   Remarks	3.5 Power ON/OFF Sequence    Power Sequence Timings	
2.1	2017/5/5	34	8.2 Pallet and shipment information  8.3 Pallet and shipment information  8.4 Pallet and shipment information  8.5 Pallet and shipment information  8.6 Pallet and shipment information  8.7 Pallet and shipment information  8.8 Pallet and shipment information  8.8 Pallet and shipment information  8.8 Pallet and shipment information  8.9 Pallet and shipment information  8.8 Pallet and shipment information  9. Pallet and shipment information  10. Pal		
		6	2.1 Display Characteristics  Power Consumption. (LCD Module + Backligh unit).  [Watt] 90 (Typ.) (LCD module : PDD (Typ.) = 2.0 @ Black pattern, Fv=80Hz  Backlight unit : Pnu (Typ.) = 7 Ogles=50mA.	2. I Display Characteristics    Power Consumpton- (LCD Module + Backligh unti)-   (Watt)- 7.74 (Typ.)- LCD module - POD (Typ.) = 2.0 @ Black pattern. Fv=60Hz - Backlight unti : P <sub>Back</sub> (Typ.) = 6.74@(ls=45mA-	∨80L
3.0	2019/12/30	28	## 4.3.2 Recommended Operating Condition    Symbol   Description   Min.   Typ.   Max.   Unit.   Remark.	4.3.2 Recommended Operating  Condition  Symbol Description Min. Typ. Max. Unit. Remark.  Is. LED String Current.	
		34	Second	No.	

#### 1 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
- 10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
- 11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
- 12) Please avoid touching COF position while you are doing mechanical design.
- 13) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- 14) Do not apply the same pattern for a long time, it will enhance relevant defect.



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#### **2 General Description**

This specification applies to the 19 inch wide Color a-Si TFT-LCD Module M190PW01 V8 The display supports the WXGA $^+$  - 1440(H) x 900(V) screen format and 16.7M colors (8 bits RGB data input). The input interface is Dual channel LVDS and this module doesn't contain an driver board for backlight.

#### 2.1 Display Characteristics

The following items are characteristics summary on the table under 25 °C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	481.42 (18.95")
Active Area	[mm]	408.24 (H) x 255.15 (V)
Pixels H x V	-	1440(x3) x 900
Pixel Pitch	[um]	283.50 (per one triad) ×283.50
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	TN Mode, Normally White
White Luminance ( Center )	[cd/m <sup>2</sup> ]	250 (Typ.)
Contrast Ratio	-	1000 (Typ.)
Response Time	[msec]	5 (Typ., on/off)
Power Consumption	[Watt]	7.74 (Typ.)
(LCD Module + Backligh unit)		LCD module : PDD (Typ.)= 2.0 @ Black pattern,Fv=60Hz
		Backlight unit : P <sub>BLU</sub> (Typ.) =5.74@Is=45mA
Weight	[Grams]	1520 (Typ)
Outline Dimension	[mm]	428.0(H) × 278.0(V) × 10.3(D) Typ.
Electrical Interface	-	Dual channel LVDS (8bits RGB data input)
Support Color	-	16.7M colors
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 7.0 Compliance



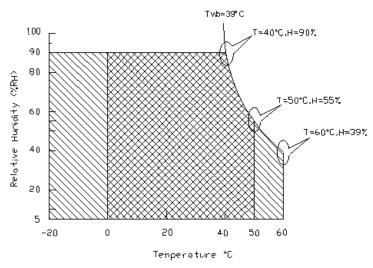
#### 2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Symbol Description		Max.	Unit	Remark
TOP	TOP Operating Temperature		+50	[°C]	Note 2-1
TGS	TGS Glass surface temperature (operation)		+65	[°C]	<b>Note 2-1</b> Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

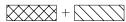
- 1. 90% RH Max ( Ta  $\leq$  39 $^{\circ}$ C)
- 2. Max wet-bulb temperature at 39°C or less. ( Ta  $\leq$  39°C)
- 3. No condensation



**Operating Range** 



Storage Range





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### 2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

#### **Test Condition:**

1. Equipment setup: Please refer to Note 2-2.

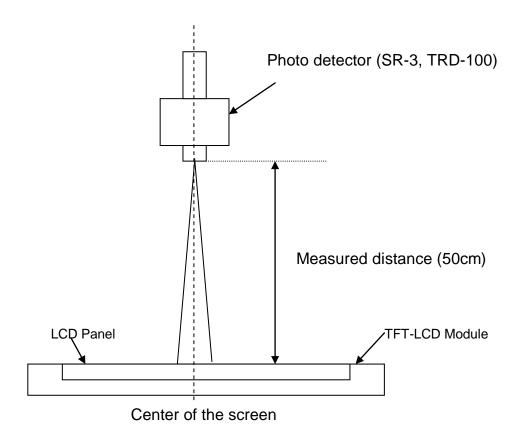
2. Panel Lighting time: 30 minutes

3. VDD=5.0V, Fv=60Hz,Is=50mA,Ta=25 $^{\circ}$ C

Symbo	Descriptio	Min.	Тур.	Max.	Unit	Remark	
L <sub>w</sub>	White Luminance (Center of screen)			250	-	[cd/m2]	<b>Note 2-2</b> By SR-3
L <sub>uni</sub>	Luminance Uniformit	y (9 points)	75	80	-	[%]	<b>Note 2-3</b> By SR-3
CR	Contrast Ratio (Cente	er of screen)	600	1000	-	-	Note 2-4  By SR-3
$\theta_{R}$	Horizontal Viewing	Right	75	85	-		
$\theta_{L}$	Angle (CR=10)	Left	75	85	-		
$\Phi_{H}$	Vertical Viewing Angle	Up	70	80	-		
$\Phi_{\scriptscriptstyle  m L}$	(CR=10)	Down	70	80	-	[degree]	Note 2-5
$\theta_{R}$	Horizontal Viewing	Right	75	88	-	[uogroo]	By SR-3
$\theta_{L}$	Angle (CR=5)  Vertical Viewing Angle (CR=5)	Left	75	88	-		
$\Phi_{H}$		Up	70	85	-		
$\Phi_{\scriptscriptstyle m L}$		Down	70	85	-		
$T_R$		Rising Time	-	3.5	5.5		Note 2-6
$T_F$	Response Time	Falling Time	-	1.5	2.5	[msec]	Ву
-		Rising + Falling	-	5	8		TRD-100
R <sub>x</sub>		Red x	0.609	0.639	0.669		
$R_y$		Red y	0.309	0.339	0.369		
G <sub>x</sub>		Green x	0.294	0.324	0.354		
Gy	Color Coordinates	Green y	0.592	0.622	0.652		
B <sub>x</sub>	(CIE 1931)	Blue x	0.122	0.152	0.182	-	By SR-3
Ву		Blue y	0.023	0.053	0.083		
W <sub>x</sub>		White x	0.283	0.313	0.343		
W <sub>y</sub>		White y	0.299	0.329	0.359		
СТ	Crosstalk	-	-	1.5	[%]	<b>Note 2-7</b> By SR-3	
F <sub>dB</sub>	Flicker (Center of	screen)	-	-	-20	[dB]	<b>Note 2-8</b> By SR-3



#### Note 2-2: Equipment setup:

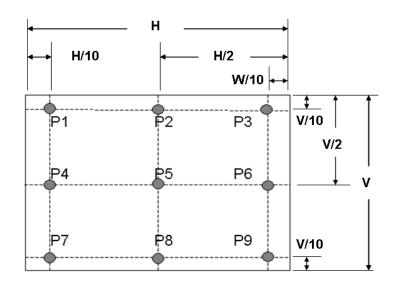


Note 2-3: Luminance Uniformity Measurement

#### **Definition:**

 $Luminance\ Uniformit\ y = \frac{Minimum\ Luminance\ of\ 9\ Points\ (P1 \sim P9)}{Maximum\ Luminance\ of\ 9\ Points\ (P1 \sim P9)}$ 

a. Test pattern: White Pattern





#### Note 2-4: Contrast Ratio Measurement

#### **Definition:**

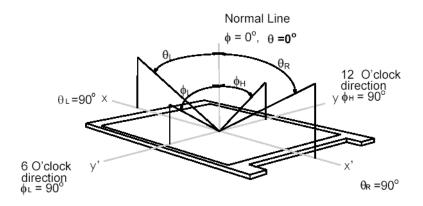
Contrast Ratio =  $\frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$ 

a. Measured position: Center of screen (P5) & perpendicular to the screen  $(\theta=\Phi=0^{\circ})$ 

#### **Note 2-5:** Viewing angle measurement

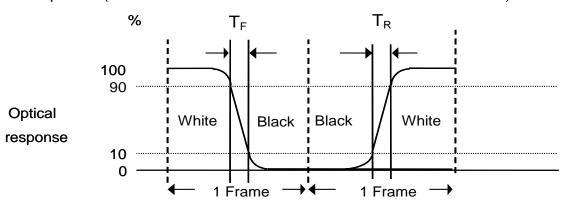
**Definition:** The angle at which the contrast ratio is greater than 10 & 5.

a. Horizontal view angle: Divide to left & right ( $\theta$  Vertical view angle: Divide to up & down ( $\Phi_H$  &



#### Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Black" to "White" (rising time,  $T_R$ ), and from "White" to "Black" (falling time,  $T_F$ ), respectively. The response time is interval between the 10% and 90% of optical response. (*Black & White color definition: Please refer section 3.4.3*)





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#### Note 2-7: Crosstalk measurement

#### **Definition:**

 $CT = Max. (CT_H, CT_V);$ 

#### Where

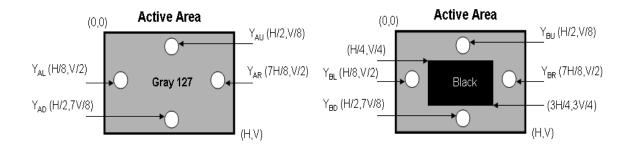
a. Maximum Horizontal Crosstalk:

$$CT_H = Max. (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

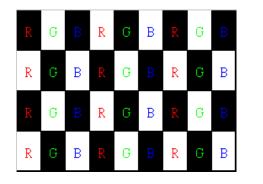
$$CT_V = Max. (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

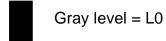
b.  $Y_{AU}$ ,  $Y_{AD}$ ,  $Y_{AL}$ ,  $Y_{AR}$  = Luminance of measured location without Black pattern  $Y_{BU}$ ,  $Y_{BD}$ ,  $Y_{BL}$ ,  $Y_{BR}$  = Luminance of measured location with Black pattern



#### Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.





Gray level = L127

R: Red, G: Green, B:Blue

b. Measured position: Center of screen (P5) & perpendicular to the screen ( $\theta$ = $\Phi$ =0°)



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#### 2.4 Mechanical Characteristics

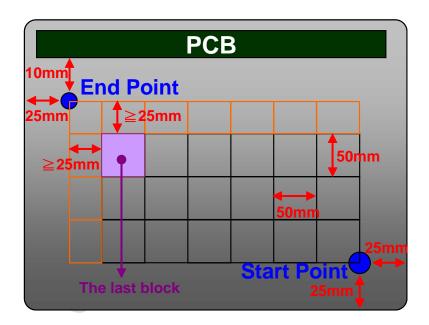
Symbol	Description	Min.	Max.	Unit	Remark
$P_{bc}$	Backside Compression	2.5	-	[Kgf]	Note 2-9

#### Note 2-9: Test Method:

The point is at a distance from right-downside 25mm x 25mm defined as the Start Point of Measure Points, and the point is at a distance 25mm from left-side & around 10mm from PCB defined as the End Point.

Align 50mm x 50mm block from Start Point on the Bezel Back, and the corners of each block are Measure Points.

If the distance from the last block to each side of the End Point  $\geq$  25mm, add other blocks to make sure that most area of Bezel Back can be measured.



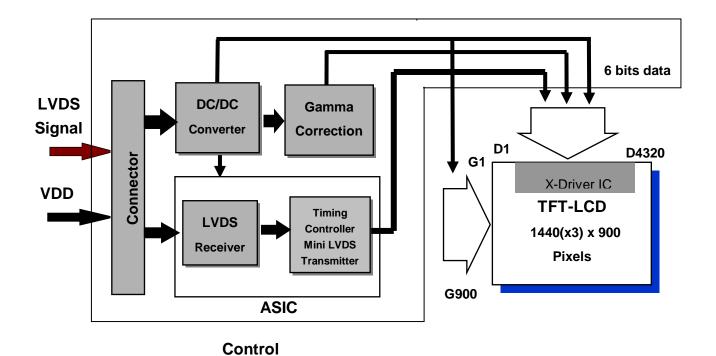


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#### 3 TFT-LCD Module

#### 3.1 Block Diagram

The following shows the block diagram of the 19 inch Color TFT-LCD Module.





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#### 3.2 Interface Connection

### 3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	STM	STARCONN	
TI 1-LOD Connector	Part Number	MSCKT2407P30HB	093G30-02001A-M4	
Mating Connector	Manufacturer	JAE		
Mating Connector	Part Number	FI-X30HL (Locked Type)		

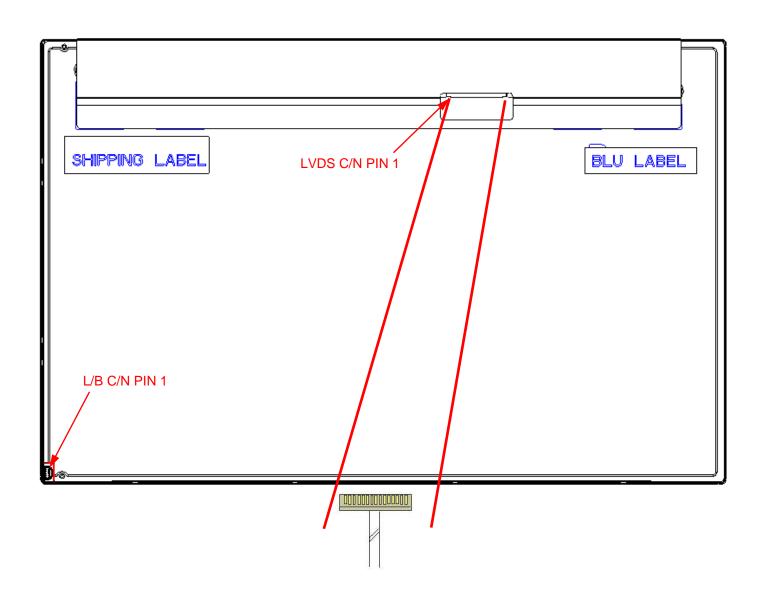
### 3.2.2 Connector Pin Assignment

PIN#	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	GND	Ground	
25	NC	No connection (for AUO test only. Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	



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27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	





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#### 3.3 Electrical Characteristics

#### 3.3.1 Absolute Maximum Rating

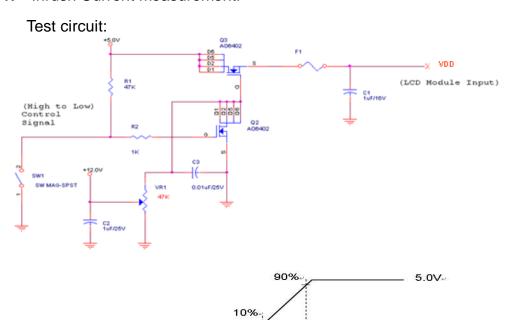
Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25°ℂ

#### 3.3.2 Recommended Operating Condition

Symbol	Description	Min	Тур	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
10.0	Power supply	-	0.4	0.5	[A]	VDD= 5.0V, Black Pattern, Fv=60Hz
IDD	Input Current (RMS)		0.5	0.6	[A]	VDD= 5.0V, Black Pattern, Fv=75Hz
PDD	VDD Power	-	2.0	2.5	[Watt]	VDD= 5.0V, Black Pattern, Fv=60Hz
PDD	Consumption		2.5	3.0	[Watt]	VDD= 5.0V, Black Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-		500	[mV]	VDD= 5.0V, Black Pattern, Fv=75Hz

#### Note 3-1: Inrush Current measurement:



document version 3.0

vdd rising time-

¥ 470us⊬

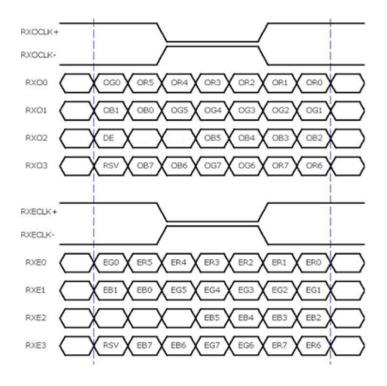
The duration of VDD rising time: 470us.

#### 3.4 Signal Characteristics

#### 3.4.1 LCD Pixel Format

		1			2			1	43	9	14	140	0
1st Line	R	G	В	R	G	В		R	G	В	R	G	В
		-			-		-		•		·	-	
		-			-							-	
		-			-							-	
		:			:				:			:	
		:			:		:		:			:	
		:			:		· ·		:			:	
900 Line	R	G	В	R	G	В		R	G	В	R	G	В

#### 3.4.2 LVDS Data Format



<b>8 Bit</b> Color Bit Order									
MSB	<b>SB</b> R7 G7 B7								
	R6	G6	B6						
	R5	G5	B5						
	R4	G4	B4						
	R3	G3	В3						
	R2	G2	B2						
	R1	G1	B1						
LSB	R0	G0	B0						

#### Note 3-2:

- a. O = "Odd Pixel Data" E = "Even Pixel Data"
- b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2<sup>nd</sup> data is 2 (Even Pixel Data) and the last data is 1440 (Even Pixel Data).

### 3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

												Col	or Inp	out D	ata											
Color	Gray Level	RED data (MSB:R7, LSB:R0)						GREEN data (MSB:G7, LSB:G0)					BLUE data (MSB:B7, LSB:B0)					Remark								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	ВЗ	В2	B1	BO	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
	9	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	ம	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Ш	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	



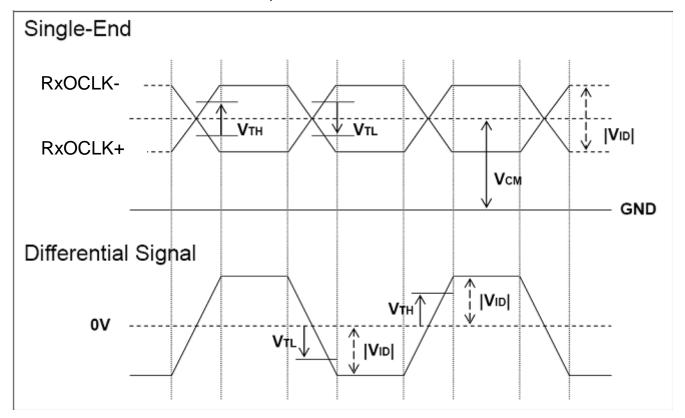
### 3.4.4 LVDS Specification

#### a. DC Characteristics:

Symbol	Description	Min	Тур	Max	Units	Condition
$V_{TH}$	LVDS Differential	_	-	+100	[mV]	V <sub>CM</sub> = 1.2V
VIH	Input High Threshold			1100	[1117]	V CM — 1.2 V
$V_{TL}$	LVDS Differential	-100	_	_	[mV]	$V_{CM} = 1.2V$
V TL	Input Low Threshold	-100	_	-	[IIIV]	V <sub>CM</sub> = 1.2 V
V <sub>ID</sub>	LVDS Differential	100	_	600	[mV]	
VID	Input Voltage	100	-	000	[IIIV]	
\/	LVDS Common Mode	.1.0	.1.2	.15	Γ\ /1	\/ \/ - 200m\/
$V_{CM}$	Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH}$ - $V_{TL}$ = 200mV

### **LVDS Signal Waveform:**

Use RxOCLK- & RxOCLK+ as example.

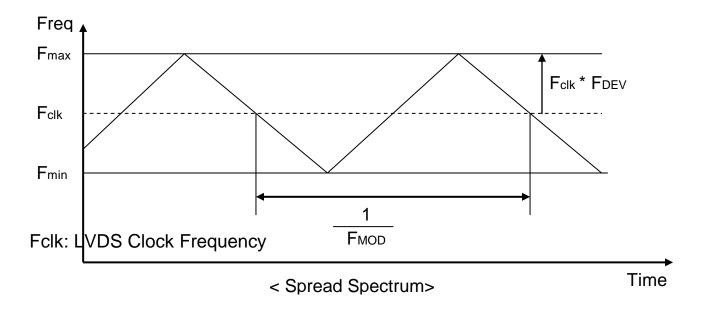


#### b. AC Characteristics:



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Symbol	Description	Min	Max	Unit	Remark
F <sub>DEV</sub>	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F <sub>MOD</sub>	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	





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#### 3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Descript	ion	Min.	Тур.	Max.	Unit	Remark
Tv		Period	914	916	1801	Th	
Tdisp (v)	Vertical Section	Active	900	900	900	Th	
Tblk (v)		Blanking	14	16	901	Th	
Fv		Frequency	49	60	76	Hz	
Th		Period	760	964	1498	Tclk	
Tdisp (h)	Horizontal Section	Active	720	720	720	Tclk	
Tblk (h)	Tionzomai Coonon	Blanking	40	244	778	Tclk	
Fh		Frequency	44.9	55.0	88.2	KHz	Note 3-3
Tclk	LVDS Clock	Period	15.0	18.9	29.3	ns	1/Fclk
Fclk		Frequency	34.1	53.0	67.1	MHz	Note 3-4

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

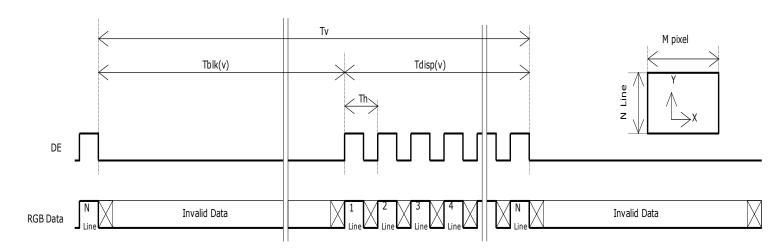
Fh (Min.) = Fclk (Min.) / Th (Min.); Fh (Typ.) = Fclk (Typ.) / Th (Typ.); Fh (Max.) = Folk (Max.) / Th (Min.);

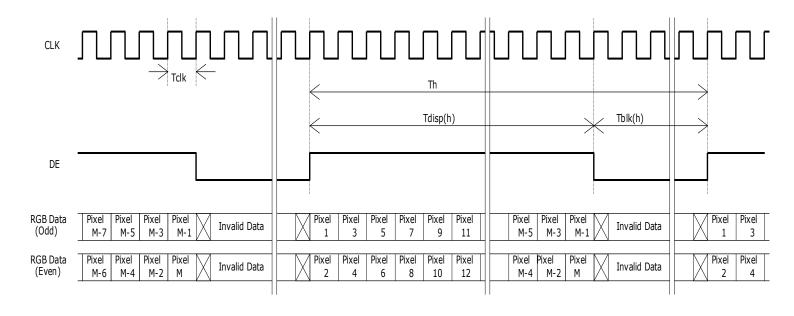
Fh (Max.)= Fclk (Max.) / Th (Min.);

*Note 3-4:* The equation is listed as following. Please don't exceed the above recommended value.

Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.); Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.); Fclk (Max.) = Fv (Max.) x Th (Typ.) x Tv (Typ.);

#### 3.4.6 Input Timing Diagram



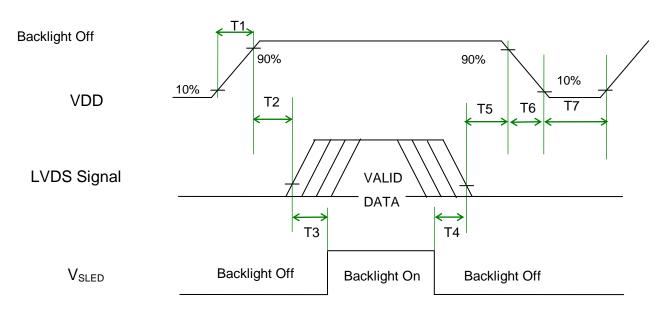




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#### 3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



#### **Power Sequence Timing**

Symbol		Value	11	Remark	
Symbol	Min.	Тур.	Max.	Unit	
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	Note 3-5 Note 3-6
Т6	0	-	200	[ms]	Note 3-6 Note 3-7
T7	1000	-	-	[ms]	

**Note 3-5**: Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

*Note 3-6 :* During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.

**Note 3-7:** Voltage of VDD must decay smoothly after power-off.(customer system decide this value)

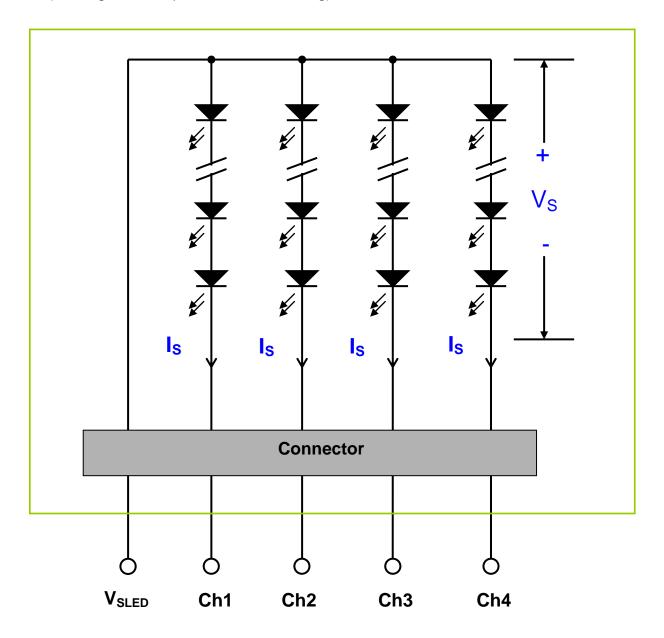


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### 4 Backlight Unit

### 4.1 Block Diagram

The following shows the block diagram of the 19 inch Backlight Unit. And it includes 44 pcs LED in the LED light bar. (4 strings and 11 pcs LED of one string).





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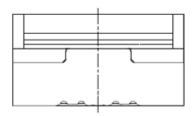
#### **4.2 Interface Connection**

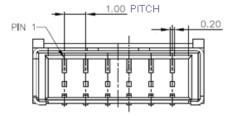
### 4.2.1 Connector Type

Backlight Connector	Manufacturer	ENTERY
Backlight Connector	Part Number	3707K-S06N-21R
Matin a Court a star	Manufacturer	ENTERY
Mating Connector	Part Number	H112K-P06N-13B (Locking type)

### **Backlight Connector dimension:**

 $H \times V \times D = 7.9 \times 3.05 \times 4.25$ , Pitch = 1.0(unit = mm)

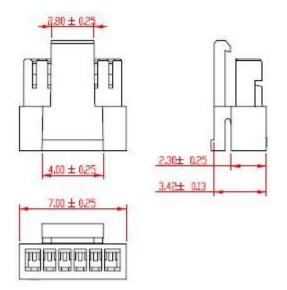


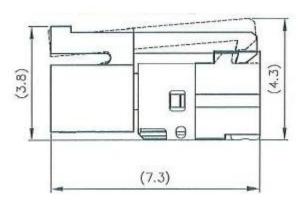




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### **Mating Connector dimension:**



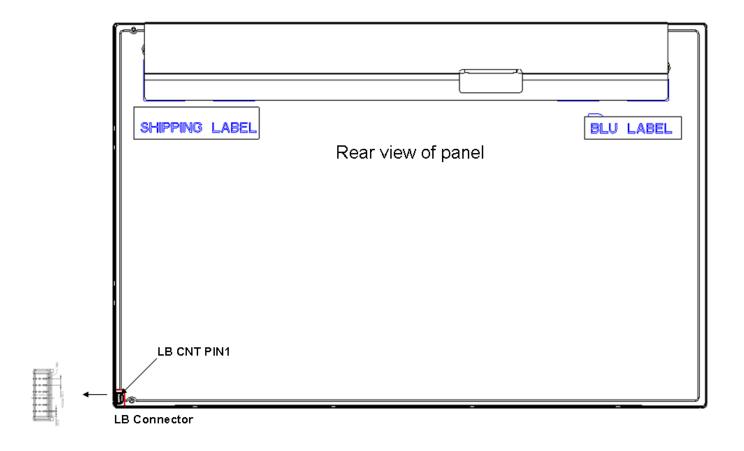




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### **4.2.2 Connector Pin Assignment**

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	$V_{\sf SLED}$	LED Power Supply Voltage Input Terminal	
4	$V_{\sf SLED}$	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	



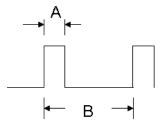
#### 4.3 Electrical Characteristics

#### 4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25°C)

Symbol	Description	Min	Max	Unit	Remark
Is	LED String Current	0	90	[mA]	100% duty ratio
			150	[mA]	Duty ratio≦ 10% Pulse time=10 ms



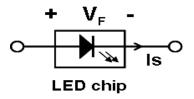
Duty ratio= (A / B) X 100%; (A: Pulse time, B: Period)

### **4.3.2 Recommended Operating Condition**

(Ta=25°ℂ)

Symbol	Description	Min.	Тур.	Max.	Unit	Remark
ls	LED String Current	-	45	50	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	27.5	31.9	36.3	[Volt]	Is=45mA @ 100% duty ratio; <i>Note 4-1, Note 4-5</i>
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	2.2	[Volt]	Is=45mA @ 100% duty ratio; <i>Note 4-2</i>
P <sub>BLU</sub>	LED Light Bar Power Consumption	-	5.742	7.26	[Watt]	Note 4-3
LT <sub>LED</sub>	LED Life Time	30,000	-	-	[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	110% Vsmax	-	-	[Volt]	Note 4-5

- **Note 4-1:** Vs (Typ.) =  $V_F$  (Typ.) X LED No. (one string);
  - a.  $V_F$ : LED chip forward voltage,  $V_F$  (Min.)=2.8V,  $V_F$ (Typ.)=3.2V,  $V_F$ (Max.)=3.6V
  - b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective  $V_F$  (Min.) &  $V_F$ (Max.);



- **Note 4-2:**  $\Delta Vs$  (Max.) =  $\Delta V_F$  X LED No. (one string);
  - a.  $\Delta V_{F}$  LED chip forward voltage deviation; (0.2 V, each Bin of LED  $V_{F}$ )
- **Note 4-3:**  $P_{BLU}$  (Typ.) = Vs (Typ.) X Is (Typ.) X 4; (4 is total String No. of LED Light bar)  $P_{BLU}$  (Max.) = Vs (Max.) X Is (Typ.) X 4;
- Note 4-4: Definition of life time:
  - a. Brightness of LED becomes to 50% of its original value
  - b. Test condition: Is = 50mA and 25 $^{\circ}$ C (Room Temperature)
- **Note 4-5:** Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

- **Note 4-6:** AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.
- **Note 4-**7: Ensure that the LED light bar is not subjected either forward or reverse voltage while monitor set is on standby mode or not in use.



#### 5 Reliability Test

AUO reliability test items are listed as following table. (Bare Panel only)

Items	Condition	Remark	
Temperature Humidity Bias (THB)	Ta= 50°C, 80%RH, 300hours		
High Temperature Operation (HTO)	Ta= 50°ℂ, 50%RH, 300hours		
Low Temperature Operation (LTO)	Ta= 0°C, 300hours		
High Temperature Storage (HTS)	Ta= 60°C, 300hours		
Low Temperature Storage (LTS)	Ta= -20°C, 300hours		
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)		
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)		
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	Note 5-1	
On/Off Test	On/10sec, Off/10sec, 30,000 cycles		
ESD (Electro Static Discharge)	Contact Discharge: $\pm$ 15KV, 150pF(330 $\Omega$ ) 1sec, 8 points, 25 times/ point.	Note 5-2	
LOD (Liectio Static Discharge)	Air Discharge: $\pm$ 15KV, 150pF(330 $\Omega$ ) 1sec 8 points, 25 times/ point.	NOIE 3-2	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft		

- **Note 5-1**: a. A cycle of rapid temperature change consists of varying the temperature from  $-20^{\circ}$ C to  $60^{\circ}$ C, and back again. Power is not applied during the test.
  - b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed

No data lost Self-recoverable No hardware failures.

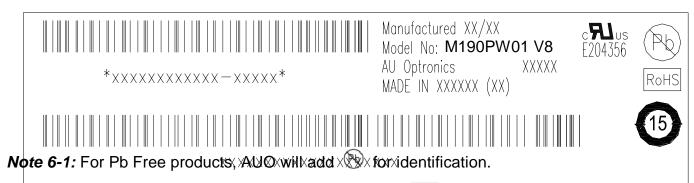


M190PW01 V8

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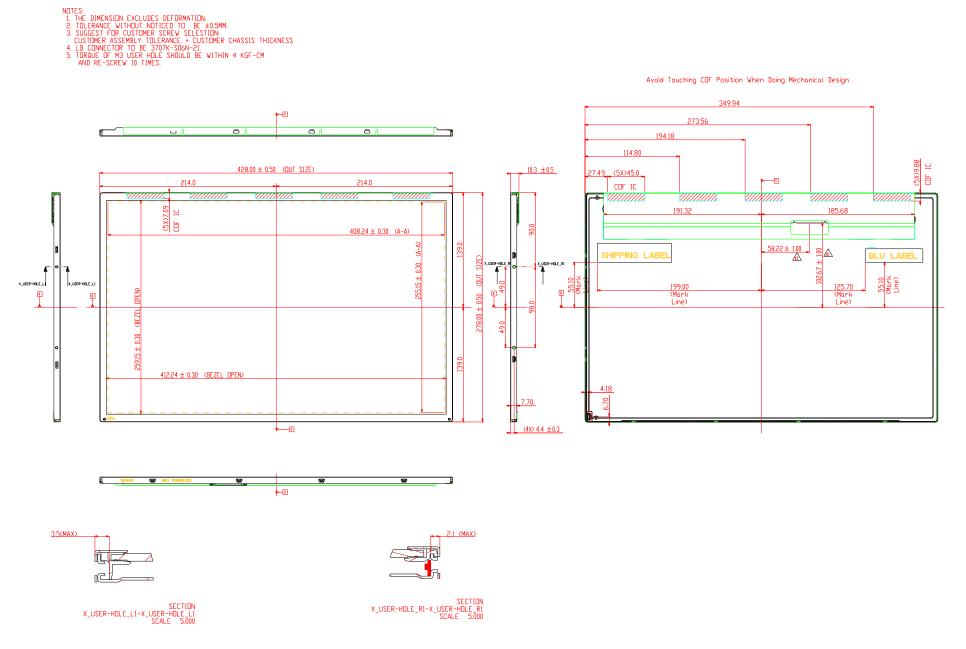
### 6 Shipping Label

The label is on the panel as shown below:



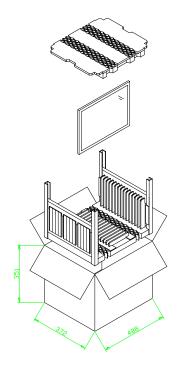
- Note 6-2: For RoHS compatible products, AUO will add RoHS for identification.
- Note 6-3: For China RoHS compatible products, AUO will add 6 for identification.
- **Note 6-4:** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

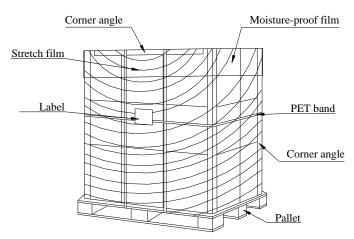
#### 7 Mechanical Characteristics



# 8 Packing Specification

# 8.1 Packing Flow





# 8.2 Pallet and shipment information

T4 ame		Downal			
Item	Q'ty	Dimension	Weight(kg)	Remark	
Panel	1	428(H)mm × 278(W)mm × 10.3(D)mm	1.52		
Cushion	1		2.178		
Box	1	485(H)mm × 372(W)mm × 325(D)mm	1.12	without Panel & cushion	
Packing Box	14 pcs/Box	485(H)mm × 372(W)mm × 325(D)mm	24.9	with panel & cushion	
Pallet	1	1150(L)mm x 980(W)mm x 132(H)mm	15.0		
Pallet after Packing	18 boxes/pallet	1150(L)mm x 980(W)mm x 1185(H)mm	463.5		