# SPECIFICATION FOR APPROVAL

(	)	<b>Preliminary Specification</b>
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(◆) Final Specification
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Title	15.0" XGA TFT LCD						
BUYER		SUPPLIER	LG.Philips LCD Co., Ltd.				
MODEL		*MODEL	LP150X05				
		Suffix	A2K3				

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
/	
/	
/	
Please return 1 copy for yo your signature and comme	

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## **RECORD OF REVISIONS**

Revision No	Revision Date	Page	Description	Note
1.0	MAR.21.2003	-	Final Specification	

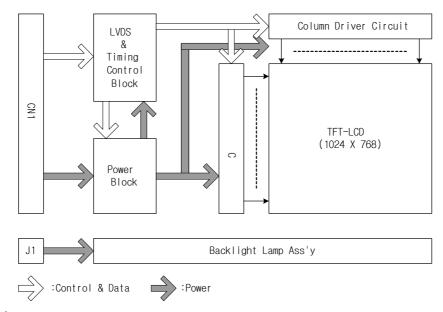


### 1. General Description

The LP150X05 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.0 inches diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150X05 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP150X05 is intended to support applications where thin thickness, low power are critical factors and graphic display are important. In combination with the vertical arrangement of the sub-pixels, the LP150X05 characteristics provide an excellent flat display for office automation products such as Notebook PC.



### **General Features**

Active Screen Size	15.0 inches(38.1cm) diagonal
Outline Dimension	317.3(H) x 241.5(V) x 5.7(D) mm(Typ.)
Pixel Pitch	0.297 mm x 0.297 mm
Pixel Format	1024 horiz. By 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	160 cd/m²(Typ.)
Power Consumption	Total 3.99 Watt(Typ.)
Weight	560 g (typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer

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### 2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

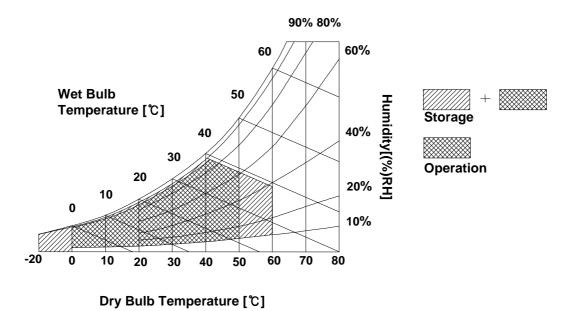
Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes		
Parameter	Symbol	Min	Max	Units	Notes		
Power Input Voltage-ON	VCC	2.7	4.0	Vdc	at 25 ± 5°C		
Power Input Voltage-OFF	GND	-0.3	0.3	Vdc	at 25 ± 5°C		
Operating Temperature	Тор	0	50	°C	1		
Storage Temperature	Тѕт	-20	60	°C	1		
Operating Ambient Humidity	Нор	10	90	%RH	1		
Storage Humidity HsT		10	90	%RH	1		
Electrostatic Durability (ESD)	VESD	± 8.0		kV	2		

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.

- 2. Condition 1) Non-operation, 150pF-330Ω, 25°C, 40~60%RH
  - 2) I/F Connector pins are subjected.
  - 3) The surface of Metal bezel and LCD are subjected.
  - 4) Discharge interval time 1sec, 10 times each place



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### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

The LP150X05 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Parameter Symbol Unit Notes Min Max Тур MODULE: Power Supply Input Voltage VCC 3.0 3.3 3.6 Vdc Power Supply Input Current 171 197 mΑ 1  $I_{CC}$ Watt **Power Consumption** Рc 0.56 0.65 1 **Differential Impedance** Zm 90 100 110 ohm 2 LAMP: Operating Voltage  $V_{BL}$ 655 685 805  $V_{\mathsf{RMS}}$ 4 **Operating Current** 3.0 5.0 6.0  $mA_{RMS}$  $I_{BL}$ Established Starting Voltage 5 Vs  $V_{\mathsf{RMS}}$ at 25 °C 1140 1370 at 0 °C  $V_{RMS}$ Operating Frequency 45 58 80 kHz 6  $f_{BL}$ Discharge Stabilization Time Ts \_ \_ 3 Min 7 3.77 **Power Consumption** 3.43 Watt 8  $P_{BL}$ 10,000 Hrs 9 Life Time

Table 2. ELECTRICAL CHARACTERISTICS

Note: The design of the inverter must have specifications for the lamp in LCD Assembly.

The performance of the Lamp in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter(no lighting, flicker, etc) never occurs. When you confirm it, the LCD – Assembly should be operated in the same condition as installed in you instrument.

- 1. The specified current and power consumption are under the VCC=3.3V,  $25^{\circ}$ C,  $f_V$ =60Hz condition whereas Mosaic pattern is displayed and  $f_V$  is the frame frequency.
- 2. This impedance value is needed to proper display and measured from LVDS  $T_X$  to the mating connector.
- 3. The duration of rush current is about 20ms.
- 4. The variance of the voltage is  $\pm$  10%.
- 5. The voltage above V<sub>S</sub> should be applied to the lamps for more than 1 second for start-up.

  Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.

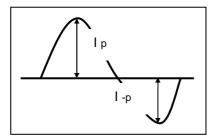
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- 6. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform.(Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave.
  Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 7. Let's define the brightness of the lamp after being lighted for 5 minutes as 100%.
  T<sub>S</sub> is the time required for the brightness of the center of the lamp to be not less than 95%.
- 8. The lamp power consumption shown above does not include loss of external inverter. The used lamp current is the lamp typical current.
- 9. The life is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the maximum lamp current( $6.0 \text{mA}_{\text{RMS}}$ ) on condition of continuous operating at 25 ± 2°C
- 10. Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.

It shall help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within  $\sqrt{2} \pm 10\%$ .
- \* Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.



#### 3-2. Interface Connections

The interface connections are compatible with ISP (Industry Standard Panels) 15.0" Mounting and Top Level Interface Requirements (Version2, June,2000) defined by SPWG (Standard Panels Working Group). This LCD employs two interface connections, a 30 pin connector is used for the module electronics and the other connector is used for the integral backlight system.

The electronics interface connector is a model GT101-30S-HR11 manufactured by LG Cable. The pin configuration for the connector is shown in the table below.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	VSS	Ground	
2	VCC	Power Supply, 3.3V Typ.	[LVDS Transmitter]
3	VCC	Power Supply, 3.3V Typ.	1.
4	VEDID	DDC 3.3V power	TI, SN75LVDS84 or equivalent
5	NC	No Connection	II V/DC Descional
6	CIkedid	DDC Clock	[LVDS Receiver]
7	DATAEDID	DDC Data	THINE, THC63LVDF64A
8	R <sub>IN</sub> 0 -	- LVDS differential data input (R0-R5, G0)	
9	R <sub>IN</sub> 0 +	+ LVDS differential data input (R0-R5, G0)	[Connector]
10	VSS	Ground	LCD : GT101-30S-HR11, LG Cable
11	R <sub>IN</sub> 1 -	- LVDS differential data input (G1-G5, B0-B1)	* JAE FI-XB30Sx-HFxx or
12	R <sub>IN</sub> 1 +	+ LVDS differential data input (G1-G5, B0-B1)	JAE FI-XB30S-HF or equivalent.
13	VSS	Ground	Matching : JAE FI-X30M or
14	R <sub>IN</sub> 2 -	- LVDS differential data input (B2-B5, HS, VS, DE)	equivalent
15	R <sub>IN</sub> 2 +	+ LVDS differential data input (B2-B5, HS, VS, DE)	
16	VSS	Ground	
17	ClkIN -	- LVDS differential clock input	[Connector pin arrangement]
18	ClkIN +	+ LVDS differential clock input	-
19	VSS	Ground	30 1
20	NC	No Connection	
21	NC	No Connection	
22	VSS	Ground	
23	NC	No Connection	LCD rear view
24	NC	No Connection	
25	VSS	Ground	
26	NC	No Connection	
27	NC	No Connection	
28	VSS	Ground	
29	NC	No Connection	
30	NC	No Connection	

Note: All GND(ground) pins should be connected together and to GND which should also be connected to the LCD's metal frame. All VCC (power input) pins should be connected together.

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 4. BACKLIGHT CONNECTOR PIN CONFIGURATION (J1)

Pin	Symbol	Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored pink and the low voltage side terminal is yellow



### 3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for it's proper operation.

**ITEM Symbol** Min Max Unit Note Тур **DCLK** Frequency fclk 65 65 65 MHz 15.4ns Period 1364 Hsync tHP 1206 1344 tclk Width 240 8 136 twH Vsync Period tvp 780 806 830 tHP Frequency fν 60 60 60 Width twv 1 6 24 Data Horizontal back porch **t**HBP 10 160 \_ tclk Enable Horizontal front porch **t**HFP 10 24

2

1

29

3

tHP

tvbp

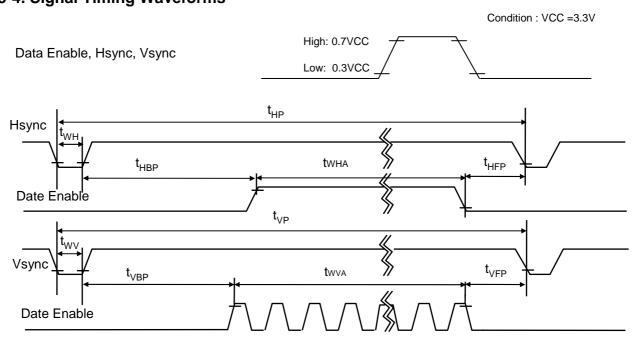
tVFP

**Table 5. TIMING TABLE** 

### 3-4. Signal Timing Waveforms

Vertical back porch

Vertical front porch



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## 3-5. Color Input Data Reference

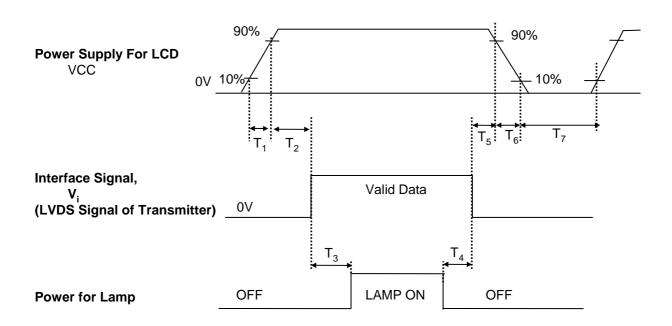
The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

**Table 6. COLOR DATA REFERENCE** 

									Inp	out Co	olor D	ata							
	Color			RE	D			GREEN					BLUE						
		MSE						MSE					LSB						LSB
	DI I	R 5	R 4	R 3	R 2		R 0		G 4	G 3	G 2	G 1	G 0	B 5	B 4	В 3	B 2	B 1	B 0
	Black	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1		0	0	0	0	0	0		0	0	0	0	0
	Green	0	0	0	0	0	0		1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																			
	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



### 3-6. Power Sequence



**Table 7. POWER SEQUENCE TABLE** 

Parameter		Value	Unit	
	Min.	Тур.	Max.	
T <sub>1</sub>	-	-	10	ms
T <sub>2</sub>	0	-	50	ms
T <sub>3</sub>	200	-	-	ms
T <sub>4</sub>	200	-	-	ms
T <sub>5</sub>	0	-	50	ms
T <sub>6</sub>	0	-	100	ms
T <sub>7</sub>	400	-	-	ms

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\Theta$  equal to  $0^{\circ}$ .

FIG. 1 presents additional information concerning the measurement equipment and method.

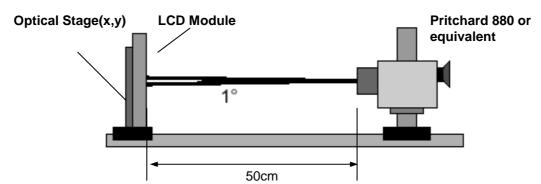


FIG. 1 Optical Characteristic Measurement Equipment and Method

**Table 8. OPTICAL CHARACTERISTICS** 

Ta=25°C, VCC=3.3V, fv=60Hz Dclk= 65MHz, IBL= 6.0mA

	_			Values		1	
	Parameter	Symbol	Min	Min Typ Max		Units	Notes
Contrast Ratio		CR	175	250	-		1
Surface Lum	inance, white (Center)	L <sub>WH</sub>	135	160	-	cd/m <sup>2</sup>	2
Luminance V	/ariation (13P)	$\delta_{\text{WHITE}}$	-	-	1.8		3
Response Ti	me						4
	Rise Time	Tr <sub>R</sub>	-	10	20	ms	
	Decay Time	Tr <sub>D</sub>	-	20	30	ms	
Color Coordi	nates						PR650 or equivalent
	RED	RX	0.536	0.566	0.596		
		RY	0.303	0.333	0.363		
	GREEN	GX	0.284	0.314	0.344		
		GY	0.505	0.535	0.565		
	BLUE	ВХ	0.127	0.157	0.187		
		BY	0.110	0.140	0.170		
	WHITE	WX	0.283	0.313	0.343		
		WY	0.299	0.329	0.359		
Viewing Ang	le						5
	x axis, right(Φ=0°)		40	45	-	degree	
	x axis, left (Φ=180°)		40	45	-	degree	
y axis, up (Φ=90°)		Θu	10	15	-	degree	
y axis, down (Φ=270°)		Θd	30	35	-	degree	
Gray Scale							6

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Notes 1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the center 1 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1., When I<sub>BI</sub> =6.0mA.
- 3. The variation in surface luminance , The Panel total variation ( $\delta_{WHITE}$ ) is determined by measuring  $L_{ON}$  at each test position 1 through 13, and then dividing the maximum  $L_{ON}$  of 13 points luminance by minimum  $L_{ON}$  of 13 points luminance. For more information see FIG 2.

$$\delta_{\text{WHITE}}$$
 = Maximum(L<sub>1</sub>,L<sub>2</sub>, ... L<sub>13</sub>) / Minimum(L<sub>1</sub>,L<sub>2</sub>, ... L<sub>13</sub>)

- 4. Response time is the time required for the display to transition from white to black(RiseTime, Tr<sub>R</sub>) and from black to white(Decay Time, Tr<sub>D</sub>). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

\* f<sub>v</sub>=60Hz

Gray Level	Luminance [%] (Typ)
LO	0.4
L7	1.4
L15	5.1
L23	10.7
L31	19.3
L39	34.2
L47	54.6
L55	77.7
L63	100



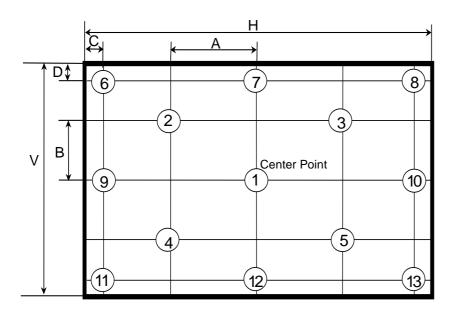
#### FIG. 2 Luminance

<measuring point for surface luminance>

<measuring point for luminance variation>

POINTS: 1 POINT (1)

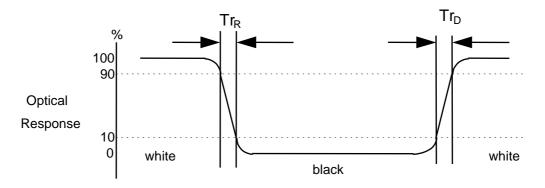
**POINTS: 13 POINTS (1~13)** 



A: H/4 mm B: V/4 mm C: 10 mm D: 10 mm H: 304.128 mm V: 228.096 mm @ H, V: Active Area

### FIG. 3 Response Time

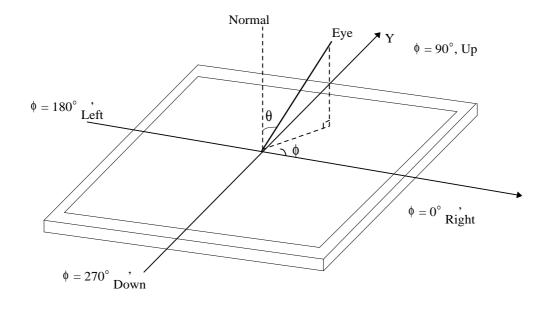
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





### FIG. 4 Viewing angle

### <Dimension of viewing angle range>





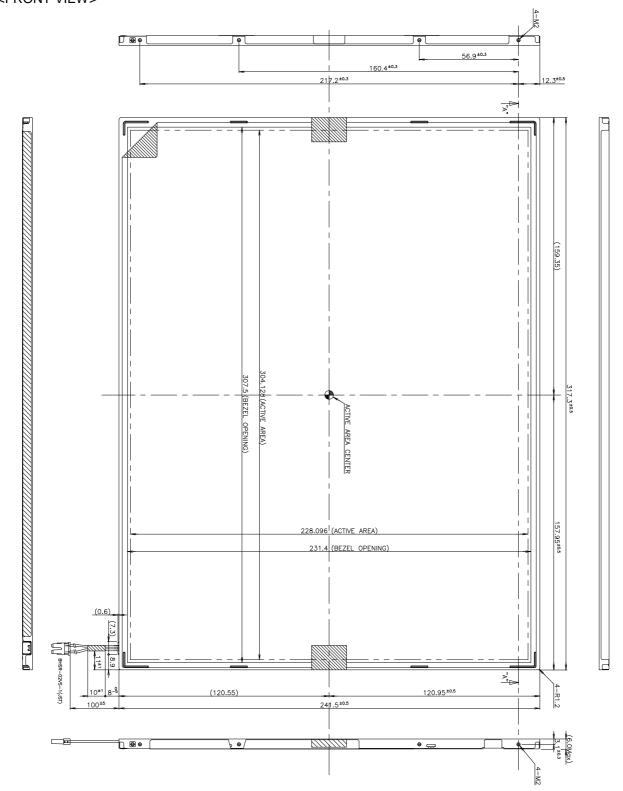
### 5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP150X05. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	317.3 ± 0.5mm			
Outline Dimension	Vertical	241.5 ± 0.5mm			
	Depth	$5.7 \pm 0.3 \text{mm}$			
Bezel Area	Horizontal	307.5 ± 0.5mm			
Dezei Alea	Vertical	231.4 ± 0.5mm			
Active Display Area	Horizontal	304.128 mm			
Active Display Area	Vertical	228.096 mm			
Weight	545g (Min.) 560g (Typ.) 575g (Max.)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

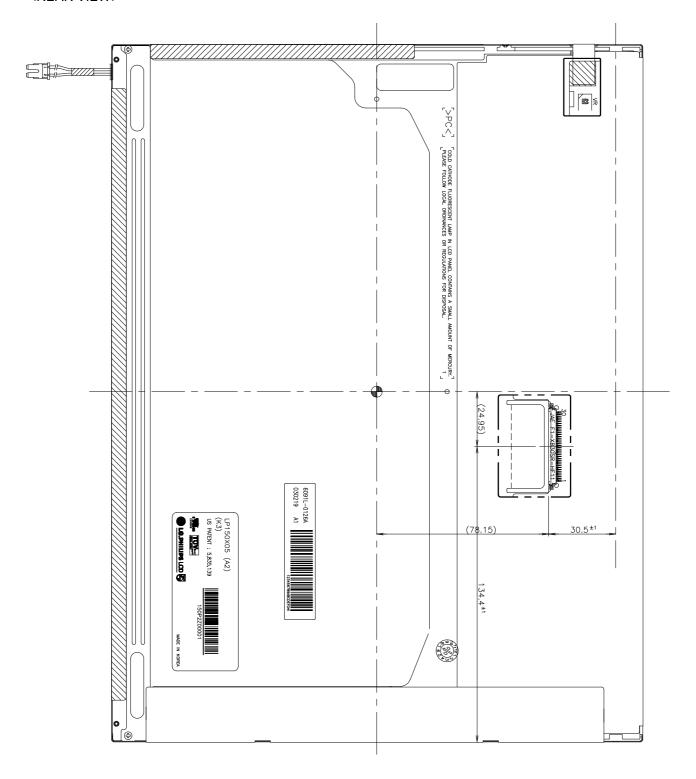


### <FRONT VIEW>



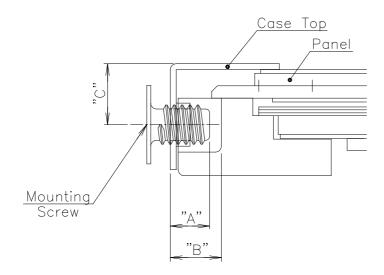


### <REAR VIEW>





### [ DETAIL DESCRIPTION OF SIDE MOUNTING SCREW ]



\* Mounting Screw depth depth Min.: "A" =2.0 depth Max: "B" =2.5

\* Mounting hole location : "C" = 3.1(typ.)

\*Torque: 2 kgf.cm(Max)

(Measurement gauge: torque meter)

Notes: 1. Screw plated through the method of non-electrolytic nickel plating is preferred to reduce possibility that results in vertical and/or horizontal line defect due to the conductive particles from screw surface.



## 6. Reliability

#### **Environment test condition**

No.	Test Item	Conditions				
1	High temperature storage test	Ta= 60°C, 240h				
2	Low temperature storage test	Ta= -20°C, 240h				
3	High temperature operation test	Ta= 50°C, 50%RH, 240h				
4	Low temperature operation test	Ta= 0°C, 240h				
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis				
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 2ms for all six faces)				
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr				

<sup>{</sup> Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



#### 7. International Standards

### 7-1. Safety

a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A4: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+A3: 1995+A4: 1996

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998



### 8. Packing

### 8-1. Designation of Lot Mark

### a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М
l I							1 1					

A,B,C : SIZE D : YEAR E : MONTH

F,G: PANEL CODE H: ASSEMBLY CODE I,J,K,L,M: SERIAL NO.

#### Note

#### 1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	Α	В	С

#### 3. Serial No

Serial No.	1 ~ 99,999	100,000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

### b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

### 8-2. Packing Form

a) Package quantity in one box: 10 pcs

b) Box Size: 374mm × 329mm × 311mm



#### 9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

#### 9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
  Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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#### 9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

#### 9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

#### 9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

#### 9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.



## APPENDIX [A] - Enhanced Extended Display Identification Data (EEDID™)

Di da#	D:#o#	T	Value	Value	
Byte#	Byte#	Field Name and Comments			Notes
(decimal)	(HEX)		(HEX)	(binary)	110100
0	00	Header	00	00000000	
1	01		FF	11111111	
2	02		FF	11111111	
3	03		FF	11111111	Header
4	04		FF	11111111	
5	05		FF	11111111	
6	06		FF	11111111	
7	07		00	00000000	
		FIGA			
8	08	EISA manufacturer code = LGP	30	00110000	
9	09		F0	11110000	
10	0A	Product code = 5CN	54	01010100	
11	0B	(Hex, LSB first)	6E	01101110	
12	0C	ID (32-bit) serial number = don't care	00	00000000	
13	0D	, , ,	00	00000000	Vender / Productor ID
14	0E		00	00000000	
15	0F		00	0000000	
16	10	Week of manufacture = don't care	00	00000000	
17	11	Year of manufacture = don't care	00	00000000	
18	12	EDID Structure version # = 1	01	00000001	
19	13	EDID Revision # = 3	03	00000011	EDID Version / Revision
20	14	Video input definition=Digital I/P, non TMDS CRGB	80	10000000	
21	15		1E	00011110	
		Max H image size(cm)=30.4128 cm			Display Parameter
22	16	Max V image size(cm)=22.8096 cm	16	00010110	Display Farameter
23	17	Display gamma = 2.2	78	01111000	
24	18	Feature support(DPMS) = Active off, RGB Color	28	00101000	
25	19	Red/Green low Bits	C7	11000111	
26	1A	Blue/White Low Bits	74	01110100	
27	1B	Red X Rx =0.566	90	10010000	
28	1C	Red Y Ry =0.333	55	01010101	
		· · · · · · · · · · · · · · · · · · ·			
29	1D	Green X Gx =0.314	50	01010000	Color Characteristic
30	1E	Green Y Gy =0.535	88	10001000	
31	1F	Blue X Bx =0.157	28	00101000	
32	20	Blue Y By =0.140	23	00100011	
33	21	White X Wx = 0.313	50	01010000	
34	22	White Y Wy = 0.329	54	01010100	
35	23	Established Timing I	00	00000000	
					Catabliah ad Timin aa
36	24	Established Timing II	00	00000000	Established Timings
37	25	Manufacturer's Timings	00	00000000	
38	26	Standard Timing Identification 1 was not used	01	0000001	
39	27	Standard Timing Identification 1 was not used	01	00000001	
40	28	Standard Timing Identification 2 was not used	01	00000001	
41	29	Standard Timing Identification 2 was not used	01	00000001	
42	2A	Standard Timing Identification 3 was not used	01	0000001	
43	2B	· ·	01	0000001	
		Standard Timing Identification 3 was not used			
44	2C	Standard Timing Identification 4 was not used	01	00000001	
45	2D	Standard Timing Identification 4 was not used	01	00000001	Standard Timing ID
46	2E	Standard Timing Identification 5 was not used	01	00000001	Standard Hilling ID
47	2F	Standard Timing Identification 5 was not used	01	00000001	
48	30	Standard Timing Identification 6 was not used	01	00000001	
49	31	Standard Timing Identification 6 was not used	01	00000001	
50	32	Standard Timing Identification 7 was not used	01	00000001	
51	33	Standard Timing Identification 7 was not used  Standard Timing Identification 7 was not used	01	0000001	
52	34	Standard Timing Identification 8 was not used	01	00000001	
53	35	Standard Timing Identification 8 was not used	01	00000001	
54	36	Detailed Timing Descriptor #1	64	01100100	
55	37	1024 x 768 @ 60 Hz mode : pixel clock = 65.00 Mb	19	00011001	
56	38	Horizontal Active = 1024 pixels	00	00000000	
57	39	Horizontal Blanking = 320 pixels	40	01000000	
					Detailed Timing
58	3A	Horizontal Active : Horizontal Blanking	41	01000001	Description #1
59	3B	Vertical Avtive = 768 lines	00	00000000	2000
60	3C	Vertical Blanking = 38 lines	26	00100110	•
61	3D	Vertical Active : Vertical Blanking	30	00110000	
62	3E	Horizontal Sync. Offset =24 pixels	18	00011000	
63	3F	Horizontal Sync Pulse Width = 136 pixels	88	10001000	
64	40	Vertical Sync Offset = 3 lines,Sync Width = 6 lines	36	00110110	
J-4	+∪	vortical Syric Oriset – 5 intes, Syric Width – 6 lines	30	00110110	



Byte#	Byte#		Value	Value	
(decimal)	(HEX)	Field Name and Comments	(HEX)	(binary)	Notes
65	41	Horizontal Vertical Sync Offset/Width upper 2bits	20	00100000	
66	42	Horizontal Image Size = 304.128 mm	30	00110000	
67	43	Vertical Image Size = 228.096 mm	E4	11100100	
68	44	Horizontal & Vertical Image Size	10	00010000	Detailed Timing
69	45	Horizontal Border = 0	00	00000000	Description #1
70	46	Vertical Border = 0	00	00000000	·
71	47		18	00011000	
/ 1	47	Non-interlaced,Normal display,no stereo, Digital separate sync,H/V pol negatives	10	00011000	
72	48	Detailed Timing Descriptor #2	00	00000000	
73	49	Detailed Tilling Descriptor #2	00	0000000	
74	49 4A		00	00000000	
75	4A 4B		00	00000000	
76	4C		00	00000000	
77	4C 4D		00	00000000	
	4E				
78			00	00000000	
79	4F		00	00000000	Detailed Timing
80	50		00	00000000	Description #2
81	51		00	00000000	
82	52		00	00000000	
83	53		00	00000000	
84	54		00	00000000	
85	55		00	00000000	
86	56		00	00000000	
87	57		00	00000000	
88	58		00	00000000	
89	59		00	00000000	
90	5A	Detailed Timing Descriptor #3	00	00000000	
91	5B		00	00000000	
92	5C		00	00000000	
93	5D		00	00000000	
94	5E		00	00000000	
95	5F		00	00000000	
96	60		00	00000000	
97	61		00	00000000	Detailed Timing
98	62		00	00000000	Description #3
99	63		00	00000000	Description #3
100	64		00	00000000	
101	65		00	00000000	
102	66		00	00000000	
103	67		00	00000000	
104	68		00	00000000	
105	69		00	00000000	
106	6A		00	00000000	
107	6B		00	00000000	
108	6C	Detailed Timing Descriptor #4	00	00000000	
109	6D		00	00000000	
110	6E		00	00000000	
111	6F		00	00000000	
112	70		00	00000000	
113	71		00	00000000	
114	72		00	00000000	
115	73		00	00000000	
116	74		00	00000000	Detailed Timing
117	75		00	00000000	Description #4
118	76		00	00000000	
119	77		00	00000000	
120	78		00	00000000	
121	79		00	00000000	
122	7A		00	00000000	
123	7B		00	0000000	
124	7C		00	0000000	
125	7D		00	0000000	
126	7E	Extension flag = 00	00	00000000	Extension Flag
126	7E 7F	· ·	4F	01001111	
127	/F	Checksum	4F	01001111	Checksum