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NV133FHM-N61 Final Product Specification Rev. P3

CHONGQING BOE OPTOELECTRONICS TECHNOLOGY CO.,LTD

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P0	-	Lable&Circu	Lable&Circuit&Mech				Liyunsong	
P1	-	Lable&E	DID		2016.10.1	14	Liyu	unsong
P2	-	Label position& h	eight of LCM		2016.10.3	31	Liyu	unsong
P3	-	Final Sp	pec		2017.02.0	9	Liyu	unsong

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1.0 General Description

1.1 Application

Notebook PC Without Touch function

1.2 General Specification

1.2.1.General LCM Specification(Table 1.)

<Table 1. General Specifications>

Devemeter	l lm:t	Domorto	
Parameter	Specification	Unit	Remarks
Active area	293.76 (H) x 165.24 (V)	mm	
Number of pixels	1920 (H) x 1080 (V)	pixels	
Pixel pitch	0.153 (H) x 0.153 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	262K	colors	
Display mode	Normally Black		
Dimensional outline	300.56(H)*188.25(V) (W/PCB)*2.45(Max) 300.56(H)*177.69(V)*2.45(Max)	mm	
Weight	215(max)	g	
Back-light	Lower Down side, 1-LED Lighting Bar type		Note 1
	Pp : 0.9	W	@mosaic pattern
Power consumption	Рв∟ :2.9(max.)	W	
	3.8(Type.)	W	

Notes: 1. LED Lighting Bar (36*LED Array)

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2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

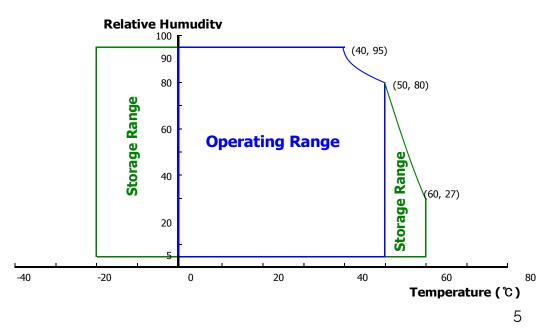
< Table 2. Absolute Maximum Ratings>

Ta=25+/-2°C

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	-0.5	4.0	V	Note 1
Logic Supply Voltage	V _{IN}	V _{ss} -0.3	V _{DD} +0.3	V	Note i
Operating Temperature	T _{OP}	0	+50	$^{\circ}$ C	Note 2
Storage Temperature	T _{ST}	-20	+60	$^{\circ}$ C	Note 2

- Notes: 1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
 - 2. Temperature and relative humidity range are shown in the figure below. 95 % RH Max. (40 °C ≥ Ta)

Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.



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3.0 ELECTRICAL SPECIFICATIONS

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3.1 Electrical Specifications

< Table 3. Electrical specifications >

Ta=25+/-2°C

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Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	3.0	3.3	3.6	V	Note 1
Permissible Input Ripple Voltage	V_{RF}	-	-	100	mV	At V _{DD} = 3.3V
Power Supply Current	I _{DD}	-	273	-	mA	Note 1
Differential Input Voltage	V_{ID}	120	-	1200	mV	
	P _D	-	0.9	1.5	W	Note 1
Power Consumption	P _{BL}	-	2.9	-	W	Note 2
	P _{total}	-	3.8	-	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for 3.3V at 25 ℃.

a) Typ: Mosaic Pattern

b) Max:RGB

2. Calculated value for reference (VLED \times ILED)

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3.2 Backlight Unit

< Table 4. LED Driving guideline specifications >

Ta=25+/-2°C

	Parameter		Min.	Тур.	Max.	Unit	Remarks
LED Forward	l Voltage	V_{F}	-	ı	2.9	V	-
LED Forward	l Current	I _F	-	23.5	-	mA	-
LED Power C	Consumption	P _{LED}		2.7	-	W	Note 1
LED Life-Tim	е	N/A	15,000	1	-	Hour	IF = 23.5mA
Power supply LED Driver	/ voltage for	V _{LED}	6	12	21	V	
EN Control	Backlight on		2.0		5.0	V	
Level	Backlight off		0		0.8	V	
PWM	PWM High Level		2.0		5.0	V	
Control Level	PWM Low Level		0		0.8	٧	
PWM Contro	l Frequency	F _{PWM}	200	-	10,000	Hz	
Duty Ratio		-	1	-	100	%	

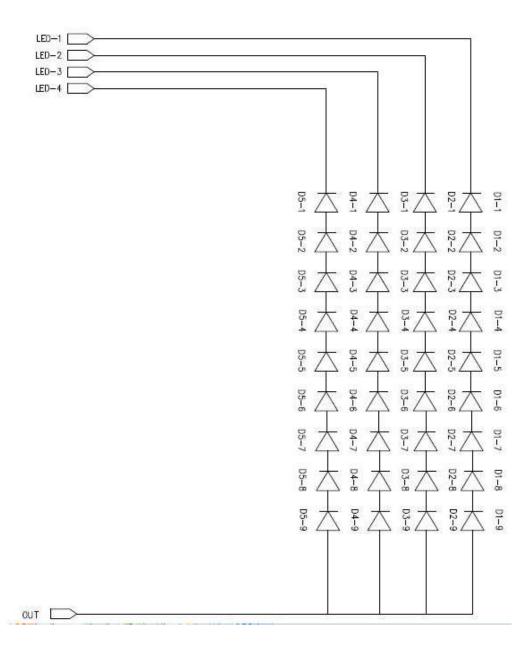
Notes : 1. Power supply voltage12V for LED Driver Calculator Value for reference IF \times VF \times 36/ efficiency = PLED

- 2. The LED Life-time define as the estimated time to 50% degradation of initial luminous.
- 3. 1% duty cycle is achievable with a dimming frequency less than 1KHz.

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3.3 LED structure



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4.0 OPTICAL SPECIFICATION

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4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta\emptyset=0$ (= $\theta3$) as the 3 o'clock direction (the "right"), $\theta\emptyset=90$ (= $\theta12$) as the 12 o'clock direction ("upward"), $\theta\emptyset=180$ (= $\theta9$) as the 9 o'clock direction ("left") and $\theta\emptyset=270$ (= $\theta6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement. VDD shall be 3.3+/- 0.3V at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

<Table 5. Optical Specifications>

Parame	eter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		-	85	-	Deg.	
Viewing Angle	попиона	Θ_9	CR > 10	-	85	-	Deg.	Note 1
range	Vertical	Θ ₁₂		-	85	-	Deg.	INOLE
	Vertical	Θ_6		-	85	-	Deg.	
Luminance Co	ntrast ratio	CR	Θ = 0°	600	800	-	-	
Luminance of White	5 Points	Y_{w}	Θ = 0°	255	300	345	-	
White	5 Points	ΔΥ5	ILED = 23.5 mA	80%	-	-	-	_
Luminance uniformity	13 Points	ΔΥ13		65%	-	-	-	Type.
White Chromaticity		X_w	Θ = 0°	0.283	0.313	0.343	-	
White Child	пансну	y_w	0-0	0.299	0.329	0.359	-	
	Red	X_R			0.650		-	
	rteu	y _R			0.345]	-	<u> </u>
Reproduction	Green	X _G	Θ = 0°	-0.03	0.329	+0.03	-	
of color	Olechi	y_{G}		-0.03	0.621	+0.03	-	
	Blue	X_R			0.151		-	
	Diue	y_B			0.056		-	
Gamı	ut	-	-	68	72	-	%	
Response (Rising + F		T _{RT}	Ta= 25° C Θ = 0°	-	30	35	Ms	Note 6
Cross T	alk	CT	⊝ = 0°	-	-	2.0	%	Note 7

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Notes:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).
- 2. Contrast measurements shall be made at viewing angle of Θ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state.

(see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

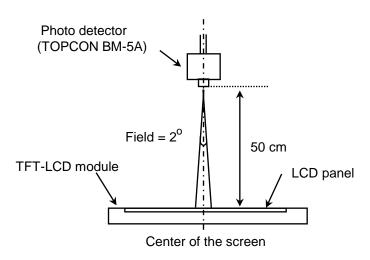
- 3. Center Luminance of white is defined as luminance values of 5 point average across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : ΔY =Minimum Luminance of 5(or 13) points / Maximum Luminance of 5(or 13) points. (see FIGURE 2 and FIGURE 3).
- 5. The color chromaticity coordinates specified in Table 5 shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 5).

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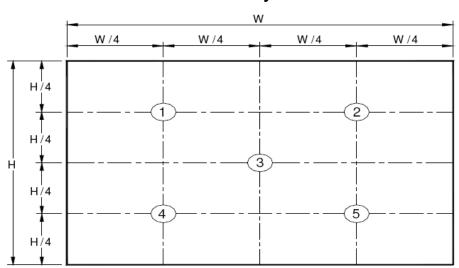
4.3 Optical measurements

Figure 1. Measurement Set Up



Optical characteristics measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (5 points)

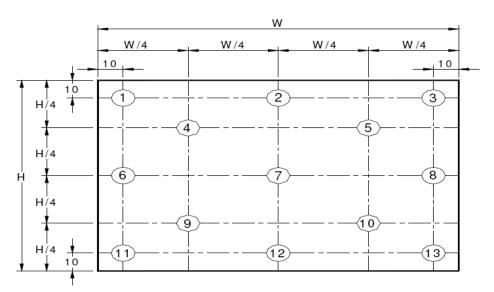


Center Luminance of white is defined as luminance values of center 5 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

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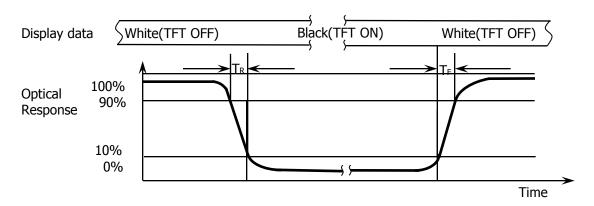
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Figure 3. Uniformity Measurement Locations (13 points)



The White luminance uniformity on LCD surface is then expressed as : $\Delta Y5$ = Minimum Luminance of five points / Maximum Luminance of five points (see FIGURE 2), $\Delta Y13$ = Minimum Luminance of 13 points /Maximum Luminance of 13 points (see FIGURE 3).

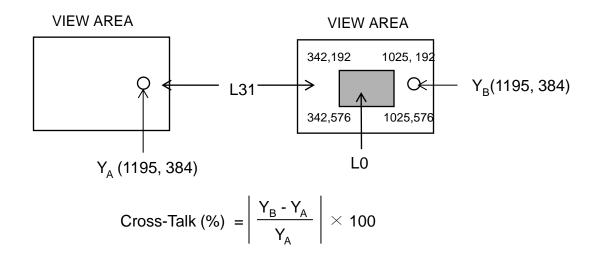
Figure 4. Response Time Testing



The electro-optical response time measurements shall be made as shown in FIGURE 4 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td and 90% to 10% is Tr.

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Figure 5. Cross Modulation Test Description



Where:

Y_A = Initial luminance of measured area (cd/m²)

Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark (Refer to FIGURE 5).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

The electronics interface connector is UJU IS050-L30B-C10 or Compatible.

The connector interface pin assignments are listed in Table 6.

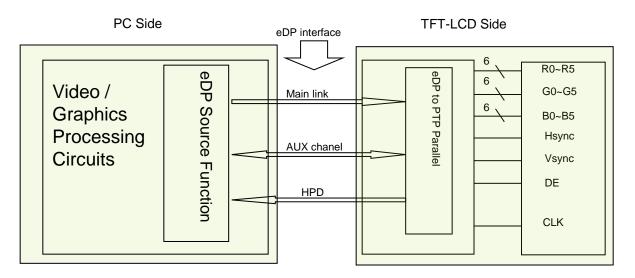
		ignments for the Interface Connector>
Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	CABC_EN	CABC reserved
2	H-GND	Ground
3	LAN1_N	Complement Signal Link _Lane1
4	LAN1_P	True Signal Link Lane1
5	H-GND	Ground
6	LAN0_N	Complement Signal Link _Lane0
7	LAN0_P	True Signal Link _Lane0
8	H-GND	High Speed Ground
9	AUXP	True Signal Link_Auxiliry Channel
10	AUXN	Complement Signal Link _Auxiliry Channel
11	H-GND	Ground
12	LCD_VCC	Power Supply, 3.3V (typ.)
13	LCD_VCC	Power Supply, 3.3V (typ.)
14	BIST	Panel self test enable (not active)
15	H-GND	Ground
16	H-GND	Ground
17	HPD	HPD(Hot Plug Detect) Signal Pin
18	BL_GND	High Speed Ground
19	BL_GND	High Speed Ground
20	BL_GND	High Speed Ground
21	BL_GND	High Speed Ground
22	BL_EN	Backlight on/off Control pin
23	BL_PWM	Back light PWM Dimming
24	Color Engine	Color Engine reserved
25	NC	No connection
26	BL_PWR	Backlight power
27	BL_PWR	Backlight power
28	BL_PWR	Backlight power
29	BL_PWR	Backlight power
30	Hsnyc	Hsnyc reserved

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5-2. eDP Interface



Note. Transmitter : Parade DP501 or equivalent.

Transmitter is not contained in Module.

5.3.eDP Input signal

Lane 0	Lane 1
R0-5:0 G0-5:4	R1-5:0 G1-5:4
G0-3:0 B0-5:2	G1-3:0 B1-5:2
B0-1:0 R2-5:0	B1-1:0 R3-5:0
G2-5:0 B2-5:4	G3-5:0 B3-5:4
B2-3:0 R4-5:2	B3-3:0 R5-5:2
R4-1:0 G4-5:0	R5-1:0 G5-5:0
B4-5:0 R6-5:4	B5-5:0 R7-5:4
R6-3:0 G6-5:2	R7-3:0 G7-5:2
G6-1:0 B6-5:0	G7-1:0 B7-5:0

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5.4 Back-light & LCM Interface Connection

<Table 7. Pin Assignments for the BLU & LCM Connector>

Pin No.	Symbol	Description	Pin No.	Symbol	Description
1	LED	LED cathode connection	6	NC	No Connection
2	LED	LED cathode connection	7	Vout	LED anode connection
3	LED	LED cathode connection	8	Vout	LED anode connection
4	LED	LED cathode connection	9	Vout	LED anode connection
5	NC	No Connection			

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6.0 SIGNAL TIMING SPECIFICATION

6.1 The NV133FHM-N61 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
Clock	Frequency	1/Tc	101	147.8	158	MHz
			1090	1120	1200	lines
Frame Period		Tv	-	60	-	Hz
			-	16.67	-	ms
Vertica	al Display Period	Tvd	-	1080	-	lines
One line Scanning Period		Th	2080	2200	2400	clocks
Horizon	tal Display Period	Thd	-	1920	-	clocks

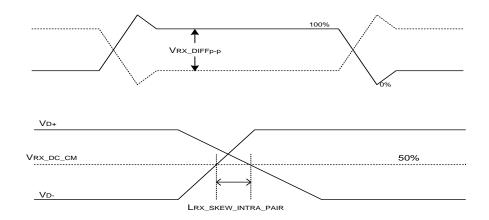
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6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 8.

<Table 8. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
Spread spectrum clock	SSC		0.5		%	
Differential peak-to-peak input volt age at package pins	VRX-DIFFp-p	120	0	1200	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	-	V	
Differential termination resistance	RRX-DIFF	80	-	120	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT	-	-	50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	60	ps	



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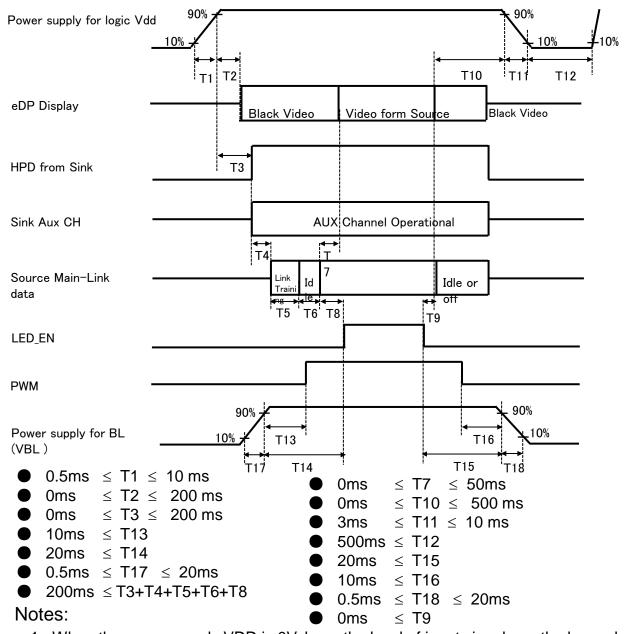
7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

	Colors &		Data signal	
	Gray scale	R0 R1 R2 R3 R4 R5	G0 G1 G2 G3 G4 G5	B0 B1 B2 B3 B4 B5
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1
Basic	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0
colors	Light Blue	0 0 0 0 0 0	1 1 1 1 1 1	1 1 1 1 1 1
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	Purple	1 1 1 1 1 1	0 0 0 0 0 0	1 1 1 1 1 1
	Yellow	1 1 1 1 1 1	1 1 1 1 1 1	0 0 0 0 0 0
	White	1 1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Δ	1 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0 0
	Darker	0 1 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0
Gray scale	Δ	↑	↑	↑
of Red		↓	↓	↓
	Brighter	1 0 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0 0
	∇	0 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0
	Red	1 1 1 1 1 1	0 0 0 0 0 0	0 0 0 0 0
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0
		0 0 0 0 0 0	1 0 0 0 0 0	0 0 0 0 0
	Darker	0 0 0 0 0 0	0 1 0 0 0 0	0 0 0 0 0 0
Gray scale		Ţ	Ţ	Ť
of Green			1	
	Brighter	0 0 0 0 0 0	1 0 1 1 1 1	0 0 0 0 0 0
	∇	0 0 0 0 0	0 1 1 1 1 1	0 0 0 0 0 0
	Green	0 0 0 0 0 0	1 1 1 1 1 1	0 0 0 0 0 0
	Black	0 0 0 0 0 0	0 0 0 0 0 0	1 0 0 0 0 0
	△ Dorlson	0 0 0 0 0 0	0 0 0 0 0 0	
Gray scale	Darker △	0 0 0 0 0 0	0 0 0 0 0	0 1 0 0 0 0
of Blue	$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	ł	↓	
of blue	Brighter	0 0 0 0 0	0 0 0 0 0 0	* 1 0 1 1 1 1
		0 0 0 0 0 0	0 0 0 0 0 0	0 1 1 1 1 1
	Blue	0 0 0 0 0 0	0 0 0 0 0 0	1 1 1 1 1 1
	Black	0 0 0 0 0 0	0 0 0 0 0 0	0 0 0 0 0
Gray	∆ ∆	1 0 0 0 0 0	1 0 0 0 0 0	1 0 0 0 0 0
scale	Darker	0 1 0 0 0 0	0 1 0 0 0 0	0 1 0 0 0 0
of	Δ	<u> </u>	1 1	<u> </u>
White		Ţ	<u> </u>	j
&	Brighter	1 0 1 1 1 1	1 0 1 1 1 1	1 0 1 1 1 1
Black		0 1 1 1 1 1	0 1 1 1 1	0 1 1 1 1 1
	White	1 1 1 1 1	1 1 1 1 1 1	1 1 1 1 1 1

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8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off seq uence shall be as shown in below



- 1. When the power supply VDD is 0V, keep the level of input signals on the low or k eep high impedance.
- 2. Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid. 20

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9.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 9. Reliability test>

No	Test Items	Conditions
1	High temperature storage test	Ta = 60 °C, 240 hrs
2	Low temperature storage test	Ta = -20 °C, 240 hrs
3	High temperature & high humidity operation test	Ta = 40 °C, 90%RH, 240 hrs
4	High temperature operation test	Ta = 50 °C, 240 hrs
5	Low temperature operation test	Ta = 0 °C, 240 hrs
6	Thermal shock	Ta = -40 $^{\circ}$ C \leftrightarrow 80 $^{\circ}$ C (0.5 hr), 100 cycle
7	Drop (non-operating)	60cm/1 corner/3 edges/6 faces
8	Shock test (non-operating)	220G, Half Sine Wave 2msec \pm X, \pm Y, \pm Z Once for each direction
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV

11.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

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(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

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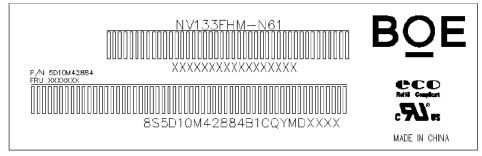
- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

12.0 LABEL

(1) LCM label



LCM ID 编码规则:

序列号	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
代码	s	L	S	Т	1	2	3	5	9	4	2	0	0	0	1	D	В
描述	G	BN	等级	lin e	1	年	月		FG-Cod	de后4位				Serial N	Number		

客户Serial Number码规则:

	YMD	####
Part Number	MFG Date	S/N
13 digit: num-alphabet	3 digit: Num-alphabet	4 digit: Num-alphabet
Follow Timi PN Rule	Skip "I,O,Q"	SN:4bit, use 0~9 and A~Z numeral-
MD10000004337	Year=last digit of year	alphabet, skip letter "I,O,Q,U";
	Month=1-9 for Jan-Sept, A=Oct,	SN must NOT be duplicated.
	B=Nov, C=Dec	
	Day=1-9 for 1^{st} thru 9^{th} , A=10,	
	B=11, etc. skip "I,O,Q,U"	

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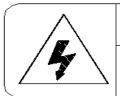
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(2) High voltage caution label

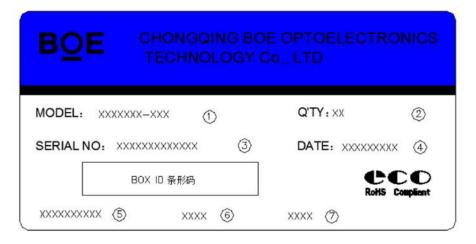


HIGH VOLTAGE CAUTION

RISK OF ELECTRIC SHOCK, DISCONNECT THE ELECTRIC POWER BEFORE SERVICING

COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY, PLEASE FOLLOW LOCAL OR-DINANCES OR REGULATIONS FOR DISPOSAL,

(3) Box label



蓝色字体为后打印标识, 说明如下:

- 1. **FG-CODE**
- Box 产品数量 2.
- Box ID, 编码规则如下 3.
- Box Packing 日期 4.
- 产品物料号(客户端) 5.
- FG-CODE 后四位 6.

Box ID 编码规则

序列号	1	2	3	4	5	6	7	8	9	10	11	12	13
代码	S	L	S	Т	1	4	3	D	0	0	1	Н	D
描述	GBN	代码	等级	TM1	年	份	月	Rev		Sei	rial Num	ber	

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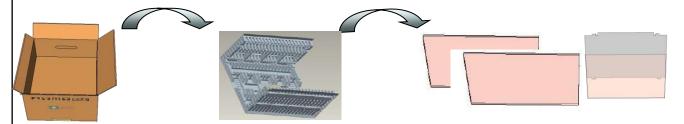
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13.0 PACKING INFORMATION

13.1 Packing order





step1

-. 将组合件(无盖)装入纸箱中



- -.将2 pcs Panel及1 ea spacer装入1 ea PE Bag中, 再将其依次装入组合件中至装满
 - -.将纸质上盖盖在组合件上
 - -.容量: 40 pcs panel /Inner box



step3

- -.将 4EA Box码放于Pallet上,共堆叠3层堆码-.单Pallet用8 ea纸护角防护,捆扎带固定,缠绕膜包裹
- -.容量: 4EA Box/层,共3层, 12 Box/Pallet。

13.2 Notes

- Box Dimension: 12 Box/Pallet
- Package Quantity in one Box: 40pcs

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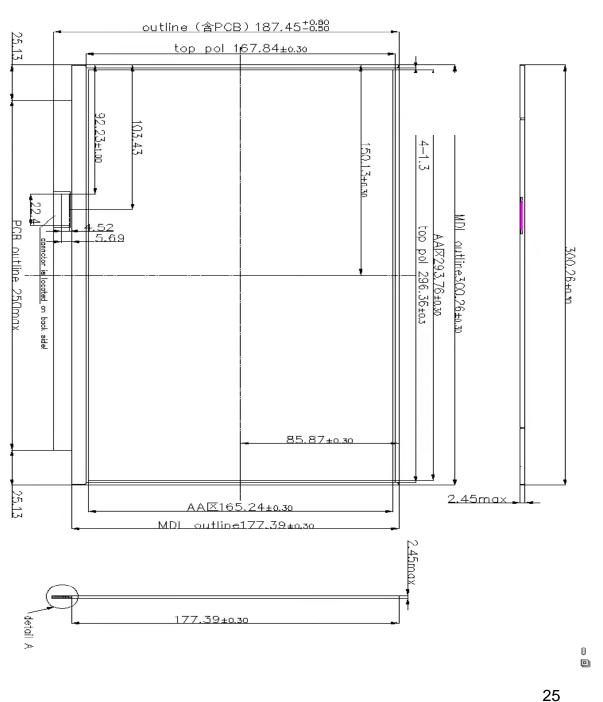
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14. MECHANICAL OUTLINE DIMENSION

14.1 Outline Dimension

Figure 6. Outline Dimensions (Front view)



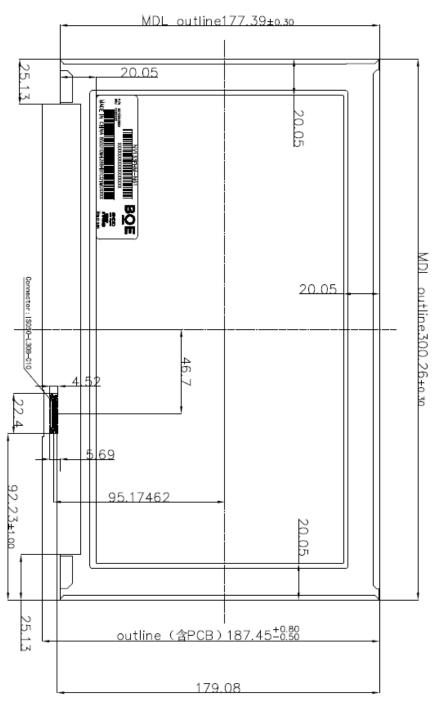
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14.2 Total Solution Outline Dimension

Figure 7. Outline Dimensions (Rear view)



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15.0 EDID Table

Address (HEX)		Function	Hex	Dec	Input values.	Notes
00			00	0	0	
01			FF	255	255	
02			FF	255	255	
03		l loodor	FF	255	255	EDID Hander
04		Header	FF	255	255	EDID Header
05			FF	255	255	
06			FF	255	255	
07			00	0	0	
08		ID Manufacturer	09	9	DOE	ID DOE
09		Name	E5	229	BOE	ID = BOE
0A	ID Product	FA	250		1706	ID _ 1796
0B	Code	06	6		1786	ID = 1786
0C			00	0		
0D		32-bit serial No.	00	0		
0E		32-bit Seriai No.	00	0		
0F			00	0		
10		Week of manufacture	01	1	1	
11		Year of Manufacture	1A	26	2016	Manufactured in 2016
12		EDID Structure Ver.	01	1	1	EDID Ver 1.0
13		EDID revision #	04	4	4	EDID Rev. 0.4
14		Video input definition	A5	165	-	digital signal/DP input
15		Max H image size	1D	29	29	29 cm (Approx)
16		Max V image size	11	17	17	17 cm (Approx)
17		Display Gamma	78	120	2.2	Gamma curve = 2.2
18		Feature support	0A	10		RGB display, Preferred Timming mode
19		Red/Green low bits	11	17	-	Red / Green Low Bits
1A		Blue/White low bits	60	96	-	Blue / White Low Bits
1B		Red x high bits	A6	166	0.649	Red $(x) = 10100110 (0.649)$
1C		Red y high bits	58	88	0.345	Red $(y) = 01011000 (0.345)$
1D		Green x high bits	54	84	0.328	Green (x) = $01010100 (0.328)$
1E		Green y high bits	9E	158	0.619	Green $(y) = 10011110 (0.619)$
1F		Blue x high bits	26	38	0.151	Blue (x) = $00100110 (0.151)$
20		BLue y high bits	0F	15	0.062	Blue (y) = 00001111 (0.062)
21		White x high bits	50	80	0.313	White $(x) = 01010000 (0.313)$
22		White y high bits	54	84	0.329	White $(y) = 01010100 (0.329)$
23		Established timing 1	00	0	-	
24		Established timing 2	00	0	-	

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25	Established timing 3	00	0	-			
26		01	1				
27	Standard timing #1	01	1		Not Used		
28		01	1				
29	Standard timing #2	01	1		Not Used		
2A		01	1				
2B	Standard timing #3	01	1		Not Used		
2C		01	1				
2D	Standard timing #4	01	1		Not Used		
2E		01	1				
2F	Standard timing #5	01	1		Not Used		
30	_	01	1				
31	Standard timing #6	01	1		Not Used		
32		01	1				
33	Standard timing #7	01	1		Not Used		
34		01	1				
35	Standard timing #8	01	1		Not Used		
36		ВС	188	4.47.0	447.0041.04.		
37		39	57	147.8	147.8MHz Main clock		
38		80	128	1920	Hor Active = 1920		
39		18	24	280	Hor Blanking = 280		
ЗА		71	113	-	4 bits of Hor. Active + 4 bits of Hor. Blanking		
3B		38	56	1080	Ver Active = 1080		
3C		28	40	40	Ver Blanking = 40		
3D		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking		
3E	Detailed	30	48	48	Hor Sync Offset = 48		
3F	timing/monitor descriptor #1	20	32	32	H Sync Pulse Width = 32		
40		36	54	3	V sync Offset = 3 line		
41		00	0	6	V Sync Pulse width : 6 line		
42		26	38	294	Horizontal Image Size = 294 mm (Low 8 bits)		
43		A5	165	165	Vertical Image Size = 165 mm (Low 8 bits)		
44		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size		
45		00	0	0	Hor Border (pixels)		
46		00	0	0	Vertical Border (Lines)		
47		1A	26		Refer to right table		

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48		00	0		MHz Main clock
49		00	0		MITIZ MAIN CIOCK
4A		00	0		Hor Active =
4B		00	0		Hor Blanking =
4C		00	0		4 bits of Hor. Active + 4 bits of Hor. Blanking
4D		00	0		Ver Active = 1080
4E		00	0		Ver Blanking =
4F		00	0		4 bits of Ver. Active + 4 bits of Ver. Blanking
50	Detailed	00	0		Hor Sync Offset =
51	timing/monitor descriptor #2	00	0		H Sync Pulse Width =
52	descriptor #2	00	0		V sync Offset = line
53		00	0		V Sync Pulse width: line
54		00	0		Horizontal Image Size = mm (Low 8 bits)
55		00	0		Vertical Image Size = mm (Low 8 bits)
56		00	0		4 bits of Hor Image Size + 4 bits of Ver Image Size
57		00	0		Hor Border (pixels)
58		00	0		Vertical Border (Lines)
59		1A	26		
5A		00	0		
5B		00	0		
5C		00	0		ASCII Data Sting Tag
5D		FE	254		
5E		00	0		
5F		42	66	В	
60		4F	79	0	
61		45	69	Е	
62	Detailed	20	32		
63	timing/monitor descriptor #3	43	67	С	
64	document we	51	81	Q	
65		0A	10		Manufacture name : BOE CQ
66		20	32		_
67		20	32		1
68		20	32		_
69		20	32		
6A		20	32		1
6B		20	32		1

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6C		00	0		
6D		00	0		
6E		00	0		Product Name Tag (ASCII)
6F		FE	254		
70		00	0		
71		4E	78	N	
72		56	86	V	
73		31	49	1	
74	Detailed	33	51	3	
75	timing/monitor descriptor #4	33	51	3	
76	accompton in t	46	70	F	Model nome: NIVACCELIM NICA
77		48	72	Н	Model name : NV133FHM-N61
78		4D	77	М	
79		2D	45	-	
7A		4E	78	N	
7B		36	54	6	
7C		31	49	1	
7D		0A	10		
7E	Extension flag	00	0		
7F	Checksum	78	120	-	

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