

# SPECIFICATION FOR APPROVAL

<b>(</b>	) Preliminary	specification
(	) Final specific	ation

Title 31.5" UHD TFT LCD
-------------------------

BUYER	General
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LM315WR2
SUFFIX	SSA1

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

	APPROVED BY	SIGNATURE DATE
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/ G.Manager	
REVIEWED BY	
PREPARED BY	
Product engineerin LG Display Co.,	

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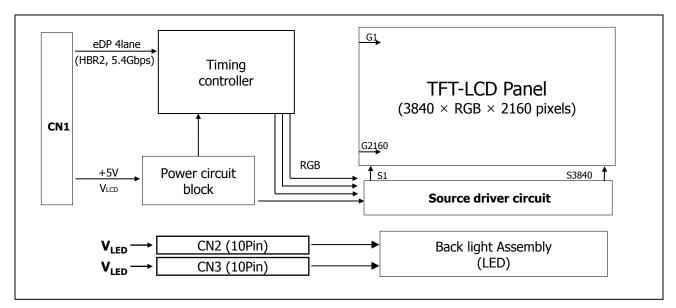
## **Record of revisions**

Revision No.	Revision Date	Page	Before	After	Application Date
0.1	Dec.17.2018	-	First Draft, Preliminary Specifications	-	-
		<u> </u>			
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## 1. General description

LM315WR2-SSA1 is a color active matrix liquid crystal display with a light emitting diode (WLED) backlight assembly without LED driver. The matrix employs a-Si thin film transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.5 inch diagonally measured active display area with UHD resolution.(3840 horizontal by 2160 vertical pixels array) Each pixel is divided into red, green and blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus, presenting a palette of more than 1.07Billion colors. It has been designed to apply eDP(HBR2, 5.4Gbps) interface. It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



[FIG. 1] Block diagram

### **General features**

Active screen size	31.5 inches(80.0051cm) (Aspect ratio 16:9)
Outline dimension	718.2(H) x 414.3(V) x 15.2(D) mm (Typ.)
Pixel pitch	0.18159(H)mm x 0.18159(V)mm
Pixel format	3840(H) x 2160(V) Pixels. RGB stripes arrangement
Color depth	1.07Billion colors (8bit + A-FRC)
Luminance (@White)	450 cd/m <sup>2</sup> ( Center 1 Point, Typ.)
Viewing angle(CR>10)	View angle free (R/L 178(Typ.), U/D 178(Typ.))
Power consumption	Total 45.2Watt (8.5Watt @V <sub>LCD</sub> , 36.7Watt @Is=85mA )
Weight	3,550g (Typ.)
Display operating mode	Transmissive mode, normally black
Panel type	Reverse type
Surface treatment	Anti-glare treatment of the front polarizer (Haze 25%, 3H)



## 2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

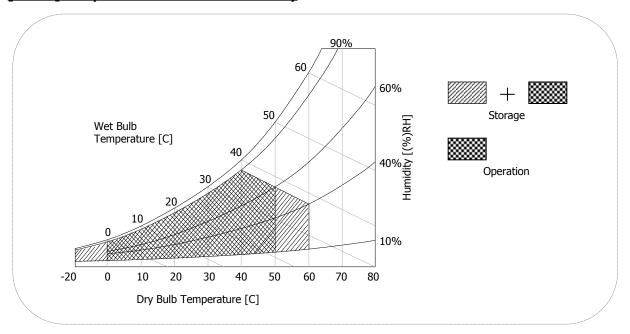
Table 1. Absolute maximum ratings

Parameter	Symbol	Val	ues	Units	Notes
raiailietei		Min.	Max.	Oilles	Notes
Power supply input voltage	V <sub>LCD</sub>	-0.3	12	V <sub>DC</sub>	<b>At 25</b> ℃
Operating temperature	T <sub>OP</sub>	0	50	°C	
Storage temperature	T <sub>ST</sub>	-20	60	°C	1 2 2
Operating ambient humidity	H <sub>OP</sub>	10	90	%RH	1,2,3
Storage humidity	H <sub>ST</sub>	10	90	%RH	
Glass surface temperature (Operation)	T <sub>Surface</sub>	0	48	°C	1, 4

#### Notes:

- 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max., and no condensation of water.
- 2. Maximum storage humidity is up to 40°C, 70% RH only for 4 corner light leakage mura.
- 3. Storage condition is guaranteed under packing condition
- 4. Glass surface temperature should be measured under the condition of  $V_{LCD}$ =10.0V, fv=60Hz,  $T_a$ =25 °C, no humidity and typical LED string current.
  - ※. T<sub>a</sub>= Ambient temperature

### [FIG. 2] Temperature and relative humidity



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## 3. Electrical specifications

### 3-1. Electrical characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

Table 2-1. Electrical characteristics

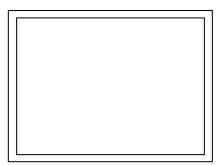
Davamatav	Creshal	Values			Units	Notes
Parameter	Symbol	Min	Тур	Max	Units	Notes
MODULE:						
Power supply input voltage	$V_{LCD}$	9.5	10.0	10.5	V	4
Permissive power input ripple	$V_{ripple}$			400	mVp-p	1
Dower supply input surrent	I <sub>LCD</sub> Typ.	-	0.85	1.06	Α	
Power supply input current	I <sub>LCD</sub> Max.	-	1.12	1.40	Α	2
Dower consumption	Рс Тур.	-	8.5	10.6	Watt	2
Power consumption	Pc Max.	-	11.2	14.0	Watt	
Rush current	Irush	-		3	Α	3

#### Notes:

- 1. Permissive power ripple should be measured under the condition of  $V_{LCD}=10.0V$ ,  $25^{\circ}C$ ,\*fv=max. Refer to page 7 for the pattern and more information.
- 2. The specified current and power consumption can be measured under the  $V_{LCD}$ =10.0V, 25°C,  $f_{V}$ =60Hz and the pattern should be changed according to the typical or maximum power condition. The max. current can be measured only with the maximum power pattern. See the page 7 for details.
- 3. Maximum condition of inrush current : The duration of rush current is about 5ms and rising time of power input is 500us  $\pm$  20%. (min.).
- 4.  $V_{LCD}$  level must be measured between two points on PCB of LCM [ $V_{LCD}$  (test point) ~ LCM Ground) (Test condition: maximum power pattern, 25°C,  $f_V$ =60Hz)
- \* fv=frame frequency



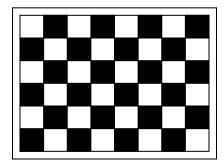
• Permissive power input ripple (V<sub>LCD</sub> = 10.0V, 25°C, fv (frame frequency)=Max. condition)



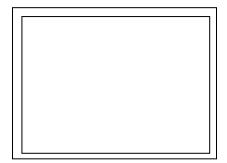
### White pattern

For the exact ripple measurement, the condition of max. 20Mhz is recommended in the bandwidth configuration of oscilloscope.

• Power consumption ( $V_{LCD} = 10V$ , 25°C, fv (frame frequency=60Hz condition)



Typical power pattern



Maximum power pattern

[FIG. 3] Mosaic pattern & White pattern for power consumption measurement



Table 2-2. Electrical characteristics of LED bar in normal operating condition

Davamatav	Cumbal	Values			Unite	Notes
Parameter	Symbol	Min.	Тур.	Max.	Units	Notes
LED string current	Is	-	85	90	mA	1, 2
LED string voltage	Vs	27.8	28.8	29.8	V	1, 3
Power consumption	PBar	-	36.7	38.0	Watt	1, 2, 5
LED life time	LED_LT	30,000	-	-	Hour	4

Notes: The LED bar consists of 75 LED packages, 15 strings (parallel) x 5 packages (serial)

- 1. The specified values are for single LED bar.
- 2. The specified current is defined as the input current for single LED string with 100% duty cycle.
- 3. The specified voltage is the input LED string voltage at typical current 100% duty cycle.
- 4. The LED life time is defined as the time when brightness of LED itself reach to the 50% of initial value under the conditions at Ta =  $25 \pm 2$ °C and typical LED string current.
- 5. The power consumption shown above does not include the loss of external LED driver. The typical power consumption is calculated as  $P_{Bar} = Vs(Typ.) \times Is(Typ.) \times No.$  of strings. The maximum power consumption is calculated as  $P_{Bar} = Vs(Max.) \times Is(Typ.) \times No.$  of strings.

Table 2-3. Absolute maximum value of LED bar

Parameter	Symbol	Values	Unit	Notes
Peak LED string current	Is	155	mA	1.2
Peak luminance	Lp	750	nit	1, 2

#### Notes:

- 1. Peak LED string voltage at peak current with 100% duty cycle is  $(30.1)\pm(1.0)$ V at Ta = 25 ± 2°C.
- 2. Peak Luminance 750nit is achieved at 155mA, while the specifications for guarantee remains under the normal operating condition specified in Table 2-2. Specifications and condition for evaluation test and mass production shall be applied with conditions specified in Table 2-2.



### 3-2. Interface connections

### **3-2-1. LCD Module**

- LCD Connector(CN1): GT05Q-30S-H10-MN (LSMtron), HD2S030HA1 (JAE), KN38B-30S-0.5H(HIROSE)

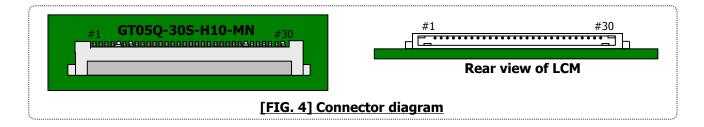
- Mating Connector: 20453-030T-## (Manufactured by I-PEX)

Table 3. Module connector (CN1) pin configration

No	Symbol	Description	No	Symbol	Description
1	VLCD	Power Supply +10.0V	16	Lane0P	True Signal for Main Link 0
2	VLCD	Power Supply +10.0V	17	Lane0N	Component Signal for Main Link 0
3	VLCD	Power Supply +10.0V	18	GND	Ground
4	VLCD	Power Supply +10.0V	19	Lane1P	True Signal for Main Link 1
5	VLCD	Power Supply +10.0V	20	Lane1N	Component Signal for Main Link 1
6	NC	No connection	21	GND	Ground
7	GND	Ground	22	Lane2P	True Signal for Main Link 2
8	NC	No Connection(I2C serial interface for LCM)	23	Lane2N	Component Signal for Main Link 2
9	NC	No Connection(I2C serial interface for LCM)	24	GND	Ground
10	GND	Ground	25	Lane3P	True Signal for Main Link 3
11	HPD	Hot Plug Detect Signal	26	Lane3N	Component Signal for Main Link 3
12	GND	Ground	27	GND	Ground
13	AUX_CHN	Component Signal for Auxiliary Channel	28	GND	Ground
14	AUX_CHP	True Signal for Auxiliary Channel	29	NC	No Connection
15	GND	Ground	30	GND	Ground

### Notes:

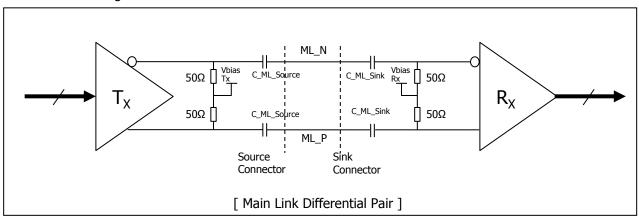
- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (input power) pins should be connected together.





## 3-2-2. eDP Signal specifications

### 1. eDP Main link signal



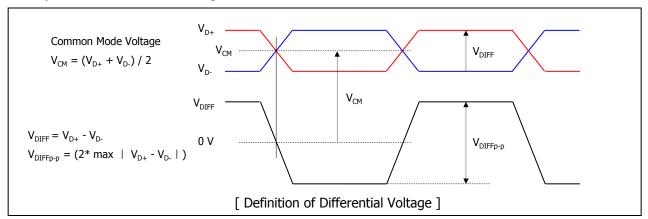
Parameter	Symbol	Min	Тур	Max	Unit	Notes
Unit Interval for high bit rate (5.4Gbps / lane)	UI_HBR2	-	185	-	ps	
Link Clock Down Spreading	Amplitude	0	-	0.5	%	
Link Clock Down Spreading	Frequency	30		33	kHz	
Differential peak-to-peak voltage at Sink side connector	V <sub>RX-DIFFp-p</sub>	-	-	1.38	V	Note 6,7)
EYE width at Sink side connector	T <sub>RX-EYE-CONN</sub>	0.38	-	-	UI	Note 6,7)
Lane-to-Lane skew	L <sub>Rx-SKEW-</sub> INTER_PAIR	-	-	4UI+ 500ps		
Lane intra-pair skew	pair skew L <sub>Rx-SKEW-</sub>		-	50	ps	
AC Coupling Capacitor	C <sub>SOURCE</sub> ML	75		200	nF	Source side

### Note)

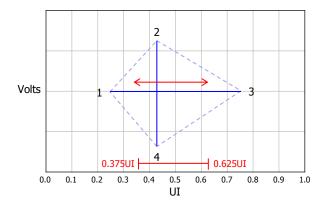
- 1. Termination resistor is typically integrated into the transmitter and receiver implementations.
- 2. In cabled embedded system, it is recommended the system designer ensure that EYE width and voltage are met at the sink side connector pins.
- 3. Mismatched common mode voltage will occur abnormal display.
- 4. All eDP electrical spec is measured at sink connector side.



### Note6) Definition of Differential Voltage



### Note7) Main Link EYE Diagram

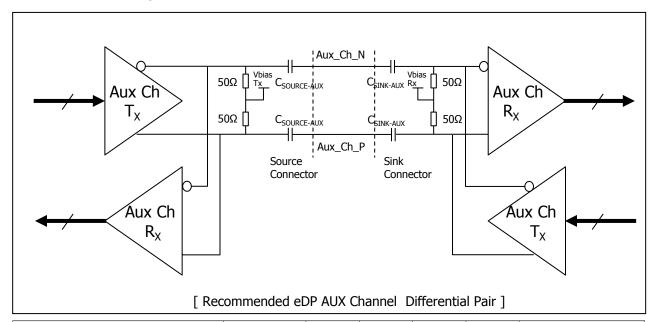


Doint	High Bit Rate 2 @ TP3 EQ									
Point	Time(UI)	Voltage(V)								
1	Any UI location (x) where the eye width is open from x to $x+0.38UI$	0.000								
2	Any passing UI location between 0.375UI-0.625UI	0.045								
3	Point 1 + 0.38UI	0.000								
4	Same as Point 2	-0.045								

[ EYE Mask Vertices at embedded DP Sink Connector Pins ]



### 2. eDP AUX Channel signal



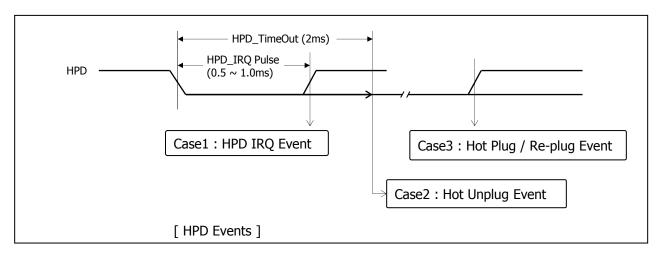
Parameter	Symbol	Min	Тур	Max	Unit	Notes
AUX Unit Interval	UI	0.4	-	0.6	us	
AUX Jitter at Tx IC Package Pins	_	-	-	0.04	UI	Equal to 24ns
AUX Jitter at Rx IC Package Pins	T <sub>jitter</sub>	-	-	0.05	UI	Equal to 30ns
AUX Peak-to-peak voltage at Connector Pins of Receiving		0.32	-	1.36	V	
AUX Peak-to-peak voltage at Connector Pins of Transmitting	V <sub>AUX-DIFFp-p</sub>	0.39	-	1.38	V	
AUX EYE width at Connector Pins of Tx and Rx		0.98	-	-	UI	
AUX AC Coupling Capacitor	C <sub>SOURCE-AUX</sub>	75		200	nF	Source side

### Note)

- 1. Termination resistor is typically integrated into the transmitter and receiver implementations.
- 2.  $V_{AUX-DIFFp-p}$  = 2\* |  $V_{AUXP}-V_{AUXN}$  | 3. Termination resistor should be  $\pm 50\pm$  5% ohm at source side to AUX level.
- 4. Mismatched common mode voltage will occur abnormal display.



### 3. eDP HDP Signal



Parameter	Symbol	Min	Тур	Max	Unit	Notes
HPD Voltage		2.25	-	3.6	٧	Sink side Driving
Hot Plug Detection Threshold	HPD	2.0	-	-	٧	Course side Detecting
Hot Unplug Detection Threshold		-	-	0.8	٧	Source side Detecting
HPD_IRQ Pulse Width	HPD_IRQ	0.5	-	1.0	ms	
HPD_TimeOut		2.0	-	-	ms	HPD Unplug Event

#### Note)

- HPD IRQ: Sink device wants to notify the Source device that Sink's status has changed so it toggles HPD line, forcing the Source device to read its Link / Sink Receiver DPCD field via the AUX-CH
- 2. HPD Unplug: The Sink device is no longer attached to the Source device and the Source device may then disable its Main Link as a power saving mode
- 3. Plug / Re-plug: The Sink device is now attached to the Source device, forcing the Source device to read its Receiver capabilities and Link / Sink status Receiver DPCD fields via the AUX-CH

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## 3-2-3. Backlight connector pin configuration

### Table 4. Backlight connector pin configuration(CN2, CN3)

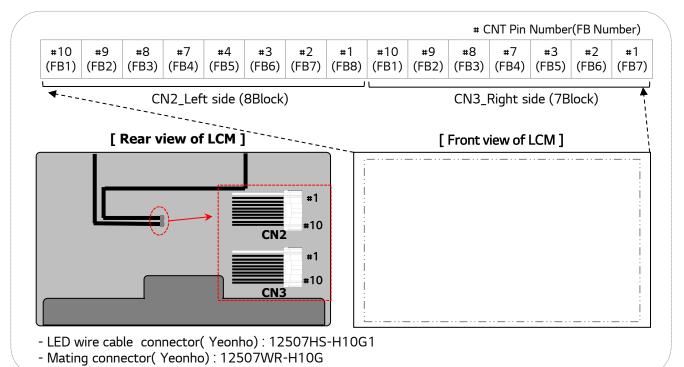
The LED wire cable connector is a model 12507HS-H10G1 Manufactured by Yeonho.

The mating connector is a 12507WR-H10G.

The pin configuration for the connector is shown in the table below.

Pin	Symbol	Pin-description (CN2)	Remark			
#1	FB8	Channe8 Current Feedback				
#2	FB7	Channel7 Current Feedback				
#3	FB6	Channel6 Current Feedback				
#4	FB5	Channel5 Current Feedback				
#5	V LED	LED power supply (FB5~FB8)	Left side			
#6	V LED	LED power supply (FB1~FB4)	in front view			
#7	FB4	Channel4 Current Feedback				
#8	FB3	B3 Channel3 Current Feedback				
#9	FB2	FB2 Channel2 Current Feedback				
#10	FB1	Channel1 Current Feedback				

Pin	Symbol	Pin-description (CN3)	Remark
#1	FB7	Channel7 Current Feedback	
#2	FB6	Channel6 Current Feedback	
#3	FB5	Channel5 Current Feedback	
#4	NC	NC	
#5	V LED	LED power supply (FB5~FB7)	Right side
#6	V LED	LED power supply (FB1~FB4)	in front view
#7	FB4	Channel4 Current Feedback	
#8	FB3	Channel3 Current Feedback	
#9	FB2	Channel2 Current Feedback	
#10	FB1	Channel1 Current Feedback	



[FIG. 5] Backlight connector view

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## 3-3. Signal timing specifications

This is signal timing requirement from the signal transmitter. All of the interface signal timing should satisfy the following specifications for its proper operation.

**Table 5. Timing table** 

ITEM	Symbol		Min.	Тур.	Max.	Unit	Note
DCLK	Period		1.82	1.875	1.93	ns	
DCLK	Frequency	-	527.92	533.25	538.58	MHz	
	Period	tHP	4000	4000	4012	tCLK	
	Horizontal Valid	tHV	3840	3840	3840	tCLK	
	Horizontal Blank	tHB	160	160	172		
Hsync	Frequency	fH	131.98	133.31	134.65	KHz	1,3,4
	Width	tWH	32	32	32	tCLK	
	Horizontal Back Porch	tHBP	80	80	88		
	Horizontal Front Porch	tHFP	48	48	52		
	Period	tVP	2220	2222	2268	tHP	
	Vertical Valid	tVV	2160	2160	2160	tHP	
	Vertical Blank	tVB	60	62	108	tHP	
Vsync	Frequency	fV	59.397	59.997	60.597	Hz	2,4
	Width	tWV	5	5	5	tHP	
	Vertical Back Porch	tVBP	52	54	100		
	Vertical Front Porch	tVFP	3	3	3		

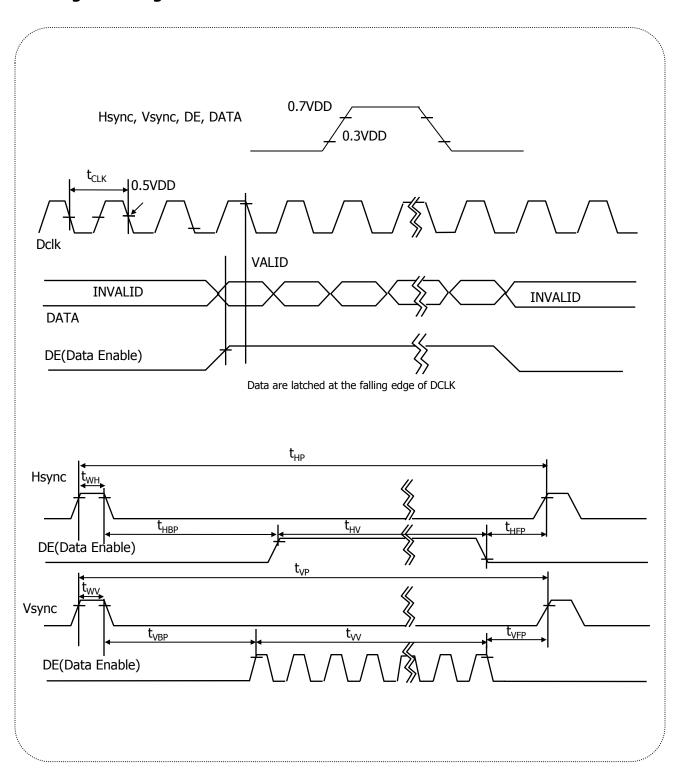
#### Notes:

- 1. The value of Hsync period, Hsync width and Hsync valid should be even number times of tCLK.

  If the value is odd number times of tCLK, it can make asynchronous signal timing and cause abnormal display.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. The value of Hsync Period, Hsync Width, and Horizontal Back Porch should be divided by 4 without a remainder.
- 4. The polarity of Hsync, Vsync is not restricted.



## 3-4. Signal timing waveforms





## 3-5. Color input data reference

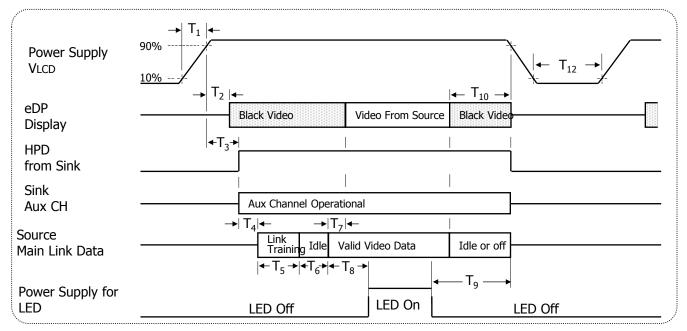
The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 6. Color data reference

											I	npu	t Co	olor	Dat	ta									
	Color				RE	D							GRE	EN							BL	UE			
	20.0.	MS							SB								SB								SB
	<u> </u>	R7		R5	R4	R3	R2	<b>R1</b>	R0						G2	G1 (					B4		B2	B1	ВО
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (1022)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (1022)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



### 3-6. Power sequence



**Table 7. Power sequence table** 

Timing	Required Limits		Units	Notes	
Tilling	Ву	Min	Max	Ullits	Notes
T <sub>1</sub>	Source	0.5	10	ms	-
T <sub>2</sub>	Sink	10	200	ms	-
T <sub>3</sub>	Sink	15	200	ms	-
T <sub>4</sub>	Source	•	•	ms	6
T <sub>5</sub>	Source	-	-	ms	6
T <sub>6</sub>	Source	-	100	ms	-
T <sub>7</sub>	Sink	0	50	ms	-
T <sub>8</sub>	Source	200	-	ms	
T <sub>9</sub>	Source	200	-	ms	7

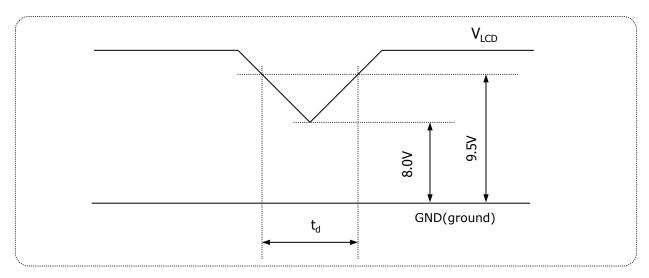
Timina	Required Limits		Uni	Notes			
Timing	Ву	Min	Max	ts	Notes		
T <sub>10</sub>	Source	0	500	ms	-		
T <sub>12</sub>	Source	1000	-	ms			

### Note:

- 1. Power sequence should be kept all the time including below cases for normal operation.
  - -.AC/DC Power On/Off
  - -. Mode change (resolution, frequency, timing, sleep mode, color depth change, etc. )
    The violation of power sequence can cause a significant trouble in display and reliability.
- 2. Please avoid floating state of interface signal during signal invalid period.
- 3. When the interface signal is invalid, be sure to pull down the VLCD.(0V)
- 4. Please turn off the power supply for LED when the level of VLCD changes to prevent noise issue.
- 5. Link training duration is dependent on the customer's system.



## 3-7. $V_{\text{LCD}}$ Power dip condition



[FIG. 6] Power dip condition

For proper operation, stable power supply of  $V_{LCD}$  is necessary and power dip is allowed only in below condition. Except this condition, power on/off should follow power sequence specification in previous page exactly.

1) Dip condition

$$8.0V \le V_{LCD} < 9.5V$$
,  $t_d \le 20ms$ 

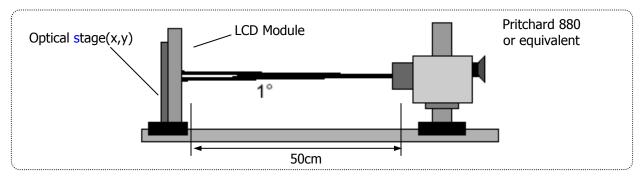
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## 4. Optical specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at  $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 ° and aperture 1 degree.

FIG. 7 presents additional information concerning the measurement equipment and method.



[FIG. 7] Optical characteristic measurement equipment and method

Table 8. Optical characteristics

(Ta=25°C,  $V_{LCD}$ =10.0V,  $f_V$ =60Hz Dclk=533.25MHz,  $I_S$ =85mA)

Parameter		Complete I		Values	II	Notes	
Parame	Symbol	Min.	Тур.	Max.	Units	Notes	
Contrast Ratio		CR	910	1300	-		1
Surface luminance, v	vhite	L <sub>wh</sub>	360	450	-	cd/m <sup>2</sup>	2
Luminance variation		δ <sub>WHITE</sub>	75	-	-	%	3
Response time	Gray To Gray	$T_{GTG\_AVR}$	-	14	25	ms	4
Color gamut (CIE1976)		DCI sRGB	90 99	98 -	- -	%	
	Red	Rx		0.680	Typ . +0.03		
	Reu	Ry	Тур.	0.313			
	Green	Gx		0.268			
Color coordinates [CIE1931]		Gy		0.685			
(By PR650)	Dive	Bx		0.151			
	Blue	Ву		0.055			
	White	Wx		0.313			
	wnite	Wy		0.329			
Color temperature		-	-	6500	-	К	
Viewing angle	Horizontal	$\theta_{H}$	170	178	-	Daguag	F
(CR>10, General)	Vertical	$\theta_{\sf V}$	170	178	-	Degree	5
Gray Scale		-		2.2			6



Notes:

1. Contrast Ratio(CR) is defined mathematically as : (By PR880)

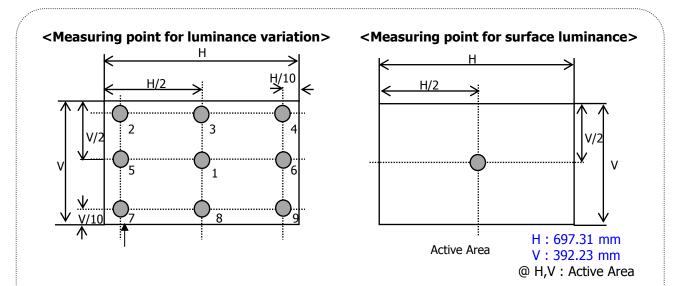
$$Contrast Ratio = \frac{Surface luminance with all white pixels}{Surface luminance with all black pixels}$$

It is measured at center point(Location P1)

- 2. Surface luminance(LwH)is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.7 (By PR880)
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as : (By PR880)

$$\delta_{\textit{WHITE}} = \frac{M \, \text{inimum}(L_{\text{P1}}, L_{\text{P2}}, \dots, L_{\text{P9}})}{M \, \text{aximum} \, (L_{\text{P1}}, L_{\text{P2}}, \dots, L_{\text{P9}})} \times 100$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations. For more information see FIG.8



[FIG.8] Measure point for luminance

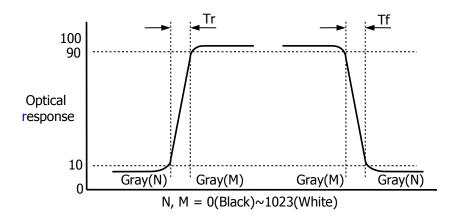


- 4. The Gray To Gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray".
  - Gray step: 5 Step
  - TGTG\_AVR is the total average time at rising time and falling time for "Gray To Gray ".
  - By RD80S

**Table 9. GTG Gray table** 

Cray To C	Rising time								
Gray 10 G	Gray To Gray		G767	G511	G255	G0			
Falling time	G1023								
	G767								
	G511								
	G255								
	G0								

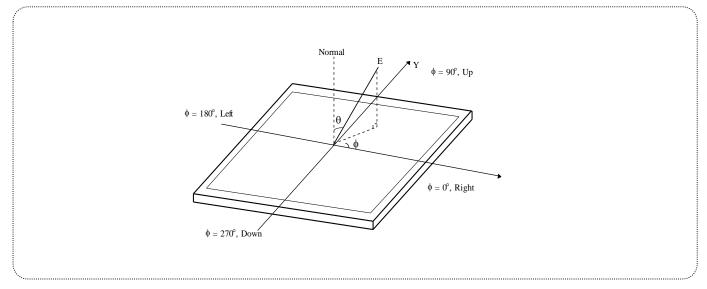
Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".



[FIG. 9] Response Time



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 (By PR880)



[FIG. 10] Viewing angle

6. Gamma Value is approximately 2.2. For more information see Table 11.

**Table 10. Gray Scale Specification** 

Gray Level	Relative Luminance [%] (Typ.)
0	0.11
127	1.08
255	4.72
383	11.49
511	21.66
639	35.45
767	53.00
895	74.48
1023	100



### 5. Mechanical characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	718.2 mm			
Outline dimension	Vertical	414.3 mm			
	Depth	15.20 mm			
Bezel area	Horizontal	-			
Dezei died	Vertical	-			
Active display area	Horizontal	697.31 mm			
Active display area	Vertical	392.23 mm			
Weight	Typ. 3,550g (Max. 3,730g)				
Surface treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

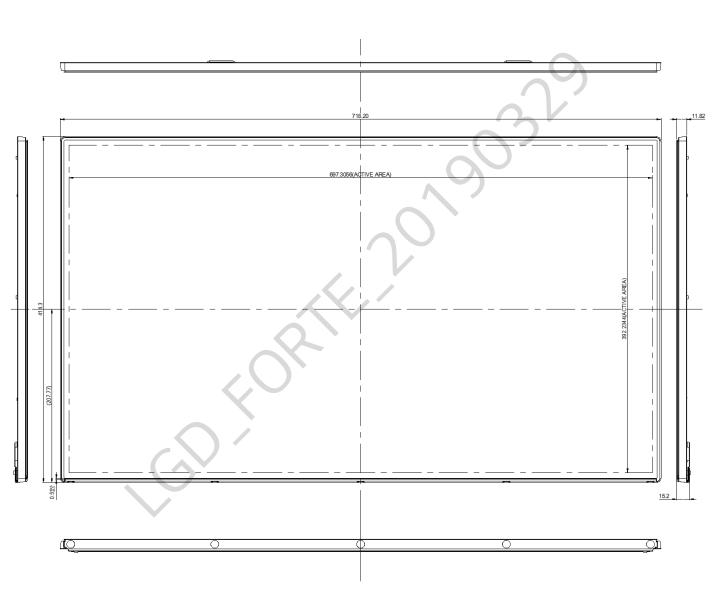
Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.

Outline dimensions (horizontal, vertical and outside depth) are measured by using vernier calipers. The inside depth dimensions are measured by using height gauge, when LCM is put face down onto a flat surface.

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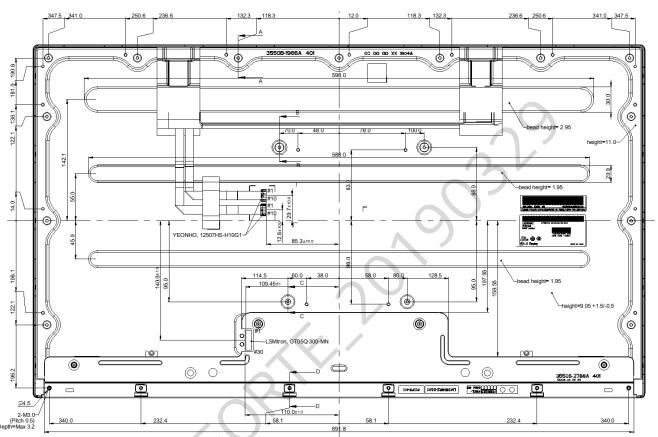
<FRONT VIEW>



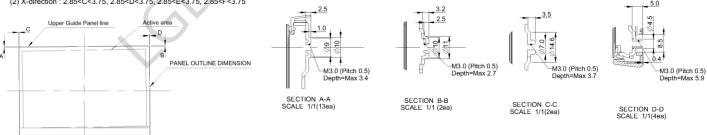


#### <REAR VIEW>

### \* Reverse Panel



- 1. I/F connector specification: LSMtron, GT05Q-30S-H10-MN 2. LED connector specification: YEONHO, 12507HS-H10G1



- 5. Unspecified tolerances is  $\pm$  0.5
- The LCM warp(warpage) is less than 1.0 on the surface plate
   The COF area is weak & sensive, so please don't press the COF area
   Undefined height should follow the 3D Modeling data

### LGD Highly recommendation:

As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.



## 6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.0G RMS Bandwidth: 10-300Hz, 0.0035G2/Hz @Test table Duration: X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm$ X, $\pm$ Y, $\pm$ Z One time each direction
7	Altitude operating storage / shipment	0 - 16,400 feet (5,000m) 0 - 40,000 feet (12,192m)

### Note 1. Result evaluation criteria:

TFT-LCD panels test should take place after cooling enough at room temperature. In the standard condition, there should be no particular problems that may affect the display function.

%.  $T_a$ = Ambient Temperature



### 7. International standards

### 7-1. Safety

- a) UL 60950-1, Underwriters Laboratories Inc.
  Information Technology Equipment Safety Part 1 : General Requirements.
- b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.
  Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1, The International Electrotechnical Commission (IEC).
  Information Technology Equipment Safety Part 1 : General Requirements

### 7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011

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## 8. Packing

## 8-1. Designation of lot mark

a) Lot mark

Α	В	С	D	Е	F	G	Н	I	J	K	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C: SIZE(INCH) D: YEAR

E: MONTH F ~ M: SERIAL NO.

Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

### b) Location of lot mark

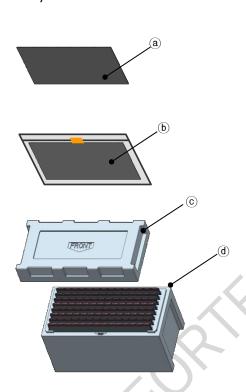
Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

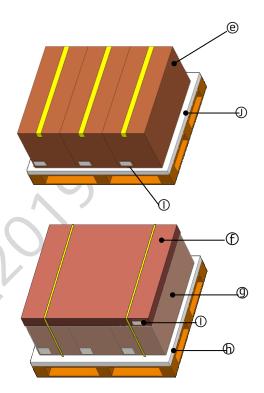


## 8-2. Packing form

a) Package quantity in one packing: 8ea/Box Package quantity in one Pallet: 24ea(3Box/Pallet)

b) Packing Size: 814mm X 358mm X 509mm C) Pallet Ass'y Size: 1140mmX870mmX702mn



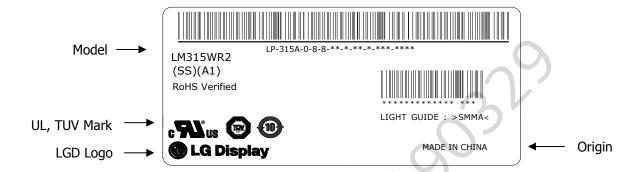


No.	Description	Material				
(a)	LCM	-				
(b)	AL-Bag	AL				
©	Packing, Top	EPS				
(d)	Packing, Bottom	EPS				
Ѳ	Box	Paper(SW)				
f	Angle Cover	Paper(SW)				
9	Wrap	LDPE				
h	Pallet	Plywood				
①	Label	YUPO				
①	Pallet cushion	EPE				



### # APPENDIX- II-1

### ■ LCM Label





### # APPENDIX- II-2

### ■ Box Label



### ■ Pallet Label





### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

## 9-1. Mounting precautions

- (1) You must mount a module using holes arranged in rear side.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

  Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

## 9-2. Operating precautions

- (1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In higher temperature, it becomes lower.)

  And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw. (If not, it causes metallic foreign material and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.
- (9) When LCMs are used for public display, defects such as Yogore & image sticking can not be guaranteed.
- (10) LCM cannot support "Interlaced scan method"
- (11) When this reverse model is used as a forward-type model (PCB on top side), LGD can not guarantee any defects of LCM.
- (12) Please conduct image sticking test after 2-hour aging with Rolling pattern and normal temperature. (25~40 $^{\circ}$ C)



## 9-3. Electrostatic discharge control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for strong light and hazardous materials exposure

Strong light exposure causes degradation of polarizer and color filter.

The LCM should be avoided direct contact with Hazardous materials such as sulfur, acetic acid, chlorine, etc. These materials may cause chemical reaction such as sulfurization, corrosion, discoloration, etc.

## 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

## 9-6. Handling precautions for protection film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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