

晶采光電科技股份有限公司 AMPIRE CO., LTD.

SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
CUSTOMER PART NO.	
AMPIRE PART NO.	AM-800480RSTMQW-TA1H
APPROVED BY	
DATE	

- ☐ Approved For Specifications
- ☑ Approved For Specifications & Sample

APPROVED BY	CHECKED BY	ORGANIZED BY

Date: 2014/03/18 AMPIRE CO., LTD.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2013/07/16		New Release	Tony
	_		

1. Features

7 inch module is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a back light system and capacitive touch panel. This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with WVGA (800 horizontal by 480 vertical pixel) resolution.

- (1) Construction: 7" a-Si TFT active matrix, White LED Backlight.
- (2) Resolution (pixel): 800(R.G.B) X 480.
- (3) Number of the Colors: 262K colors (R, G, B 6 bit digital each).
- (4) LCD type: Transmissive, normally White.
- (5) Viewing Direction 6 O'clock.
- (6) LVDS Interface.
- (7) Touch Panel with USB controller.

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
LCD size	7 inch (Diagonal)	
Outline Dimension (Include connecter)	165.0 x 104.44 x 7.76 (typ.)	mm
Number of Pixel	800 RGB (H) x 480 (V)	pixels
Pixel pitch	0.1905 (H) x 0.1905 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display mode	Normally white	
Back-light	Single LED (Side-Light type)	
Brightness	400 nit	cd/m ²
Contrast ratio	400 : 1	
Power consumption (B/L system)	4 (max.)	W

3. ABSOLUTE MAX. RATINGS

3-1 Electrical Absolute Rating

3-1-1 TFT LCD Module

Item	Symbol	Val	ues	UNIT	Note	
item	Syllibol	Min.	Max.	ONIT	Note	
Power voltage	VCC	-0.5	4.0	V	GND=0V	
Voltage range at any terminal		-0.5	VCC+0.3	V		

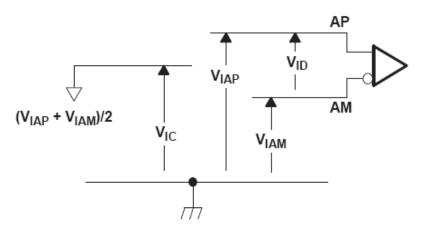
3-2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	Тора	-20	70	$^{\circ}\mathbb{C}$	
Storage Temperature	Tstg	-30	80	$^{\circ}\!\mathbb{C}$	

4. ELECTRICAL CHARACTERISTICS

4-1 TFT LCD Module voltage

		MIN	NOM	MAX	UNIT
V_{CC}	Supply voltage	3	3.3	3.6	V
V_{IH}	High-level input voltage (SHTDN)	2			V
V_{IL}	Low-level input voltage (SHTDN)			0.8	V
$ V_{ID} $	Magnitude differential input voltage	0.1		0.6	V
V _{IC}	Common-mode input voltage	$\frac{ V_{\text{ID}} }{2}$		$2.4 - \frac{ V_{\text{ID}} }{2}$	٧



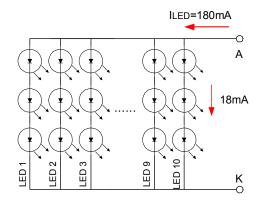
ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Power Supply Voltage For LCD	Vcc	3.0	3.3	3.6	V	
Power Supply Current For LCD	Icc	-	180	230	mA	Black pattern
Power Supply Voltage For LED Driver	VLED	ı	5.0	5.5	٧	
ADJ signal frequency	fрwм	100	50K	200K	Hz	
ADJ signal logic level High	ViH	-	3.3	5.0	V	
ADJ signal logic level Low	VIL	-	-	0.5	V	_
ADJ Input Voltage	V_{ADJ}	-	3.3	-	V	duty=100%

4-2 Backlight Unit

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
LED Backlight Voltage	V_{BL}	8.4	9.0	10.8	V	For reference
LED Backlight Current	I _{BL}	-	180	-	mA	Ta=25°ℂ
LED Life Time	-		40K		Hour	Note (2)

Note (1) The constant current source is needed for white LED back-light driving. When LCM is operated over 60 deg.C ambient temperature, the I_{LED} of the LED back-light should be adjusted to 135mA max

Note (2) Brightness to be decreased to 50% of the initial value.



LED Light Bar Circuit

Note(3) VLEDADJ is PWM signal input. It is for brightness control.

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
ADJ signal frequency	f pwм	100	50K	200K	Hz
ADJ signal logic level High	VIH	2V		VLED (5.0V)	V
ADJ signal logic level Low	VIL	0		0.5	V

4-3 Touch Panel

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Power Supply Voltage For TP USB interface	VIN		5.0		V	
Power Supply Current For TP USB interface	I_IN	-	16.3		mA	

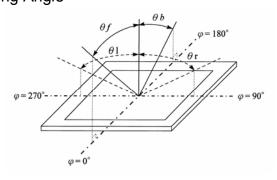
5. Optical Specifications

5-1 Optical specification

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
	Front	θf		55	60				
Viewing	Back	θb	CR≧10	55	70		deg.	(1)(2)(2)	
Angle	Left	θΙ	OR≦ IU	65	70		ueg.	(1)(2)(3)	
	Right	θr		65	70				
Contrast ratio		CR	Θ=Φ=0°	250	400			(1)(3)	
Response Tin	20	T_r			5	10	ms	(1)(4)	
Response in	iie	T_f	Θ=Φ=0°		11	16	ms	(1)(4)	
Color	White	Wx	Θ-Ψ-0	0.239	0.299	0.359		(1)	
chromaticity	vviiile	Wy		0.268	0.328	0.388		(1)	
Luminance		L	Θ=Φ=0°		400		cd/m ²	(1)(5)	
Luminance Uniformity		ΔL	Θ=Φ=0°	70			%	(1)(5)(6)	

Note 1: Ta=25°C. To be measured on the center area of panel after 10 minutes operation.

Note 2: Definition of Viewing Angle



Note 3: Definition of contrast ratio:

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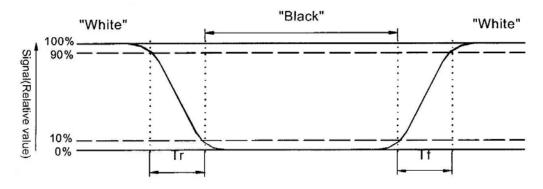
Contrast ratio is calculated with the following formula.

Contrast ratio(CR)= Photo detector output when LCD is at "White" state

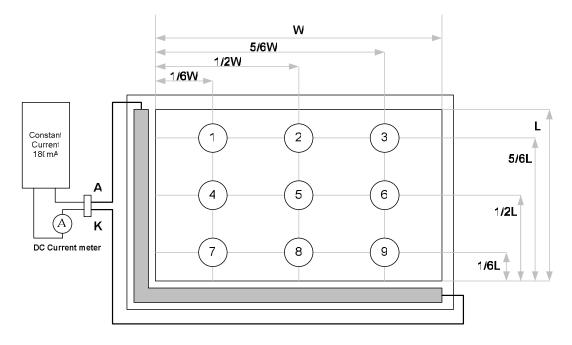
Photo detector Output when LCD is at "Black" state

Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time) respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 5: Luminance is measured at point 5 of the display.



Note 6: Definition of Luminance Uniformity

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 $\Delta L = [L(min.) \text{ of 9 points } / L(max.) \text{ of 9 points}] X 100\%$

6. Touch Panel ELECTRICAL SPECIFICATION

6-1. Basic Characteristic

ITEM	SPECIFICATION
Interface Type	Projective Capacitive Multi-Touch Panel
Activation	Two-fingers or Signal-finger

6-2. Operation Environmental Characteristic

ITEM	SPECIFICATION
Operation Temperature	-20~70°C
Storage Temperature	-30~80℃

6-3. Optical Characteristic

ITEM	SPECIFICATION
Transmittance	80% (Typ)

[Notes]

(1) UV-V is 400nm~700nm Spectrum Transmittance (Reference: Air)

(2) Definition of Transmittance

T(%)=White Luminance Touch on LCM/LCM White Luminance

7. INTERFACE

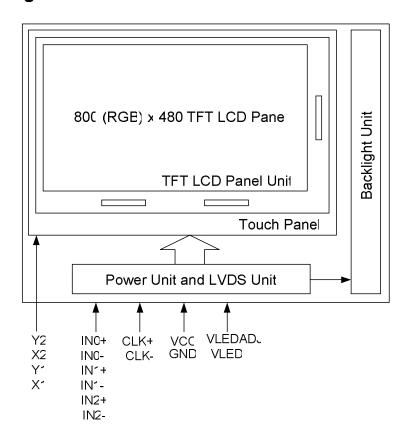
7-1 Touch Panel Interface

Pin No.	Symbol	Function					
1	GND	USB POWER GND					
2	D-	USB Data —					
3	D+	USB Data +					
4	VIN	USB POWER 5V					
5	NC	No Connection					
6	NC	No Connection					

7-2 LCD Control Interface

Pin No.	Symbol	Function
1	VDD	POWER SUPPLY:3.3V
2	VDD	POWER SUPPLY:3.3V
3	GND	Power Ground
4	GND	Power Ground
5	INO-	Transmission Data of Pixels
6	INO+	Transmission Data of Pixels
7	GND	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	GND	Power Ground
17	VLED	POWER SUPPLY for Backlight : 5V
18	VLED	POWER SUPPLY for Backlight : 5V
19	GND	Power Ground
20	LEDADJ	PWM SIGNAL

7-3 Block Diagram



8. INPUT SIGNAL:

8-1 LVDS Signal

switching characteristics over recommended operating conditions (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Setup time, D0–D20 to CLKOUT↓	0 0-5 0-5 5	5			ns
Data hold time, CLKOUT↓ to D0–D20	CL = 8 pF, See Figure 5	5			ns
Receiver input skew margin§ (see Figure 7)	t _C = 15.38 ns (±0.2%), Input clock jitter < 50 ps¶,	550	700		ps
Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7)	V _{CC} = 3.3 V, t _c = 15.38 ns (±0.2%), T _A = 25°C	3	5	7	ns
Enable time, SHTDN to phase lock	See Figure 7		1		ms
Disable time, SHTDN to off state	See Figure 8		400		ns
Transition time, output (10% to 90% t_Γ or t_f) (data only)	C _L = 8 pF		3		ns
Transition time, output (10% to 90% t_Γ or t_f) (clock only)	C _L = 8 pF		1.5		ns
Pulse duration, output clock			0.50 t _C		ns
	Setup time, D0–D20 to CLKOUT↓ Data hold time, CLKOUT↓ to D0–D20 Receiver input skew margin§ (see Figure 7) Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7) Enable time, SHTDN to phase lock Disable time, SHTDN to off state Transition time, output (10% to 90% t _f or t _f) (data only) Transition time, output (10% to 90% t _f or t _f) (clock only)	Setup time, D0–D20 to CLKOUT↓ $C_L = 8 \text{ pF}$, See Figure 5 Data hold time, CLKOUT↓ to D0–D20 $t_C = 15.38 \text{ ns } (\pm 0.2\%)$, [Input clock jitter] < 50 ps¶,	Setup time, D0–D20 to CLKOUT↓ $C_L = 8 \text{ pF}$, See Figure 55Data hold time, CLKOUT↓ to D0–D20 $t_C = 15.38 \text{ ns } (\pm 0.2\%)$, Input clock jitter < 50 ps¶,	Setup time, D0–D20 to CLKOUT↓ $C_L = 8 \text{ pF}$, See Figure 55Data hold time, CLKOUT↓ to D0–D20 $t_C = 15.38 \text{ ns } (\pm 0.2\%),$ Input clock jitter < 50 ps¶,	Setup time, D0–D20 to CLKOUT↓ $C_L = 8 \text{ pF}$, See Figure 55Data hold time, CLKOUT↓ to D0–D20 $t_C = 15.38 \text{ ns} (\pm 0.2\%),$ Input clock jitter < 50 ps¶,

PARAMETER MEASUREMENT INFORMATION

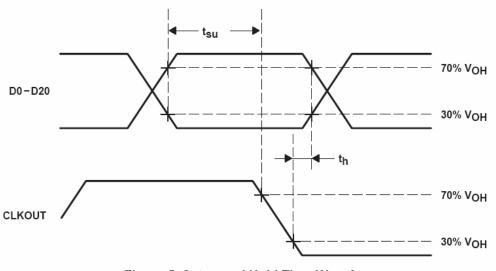
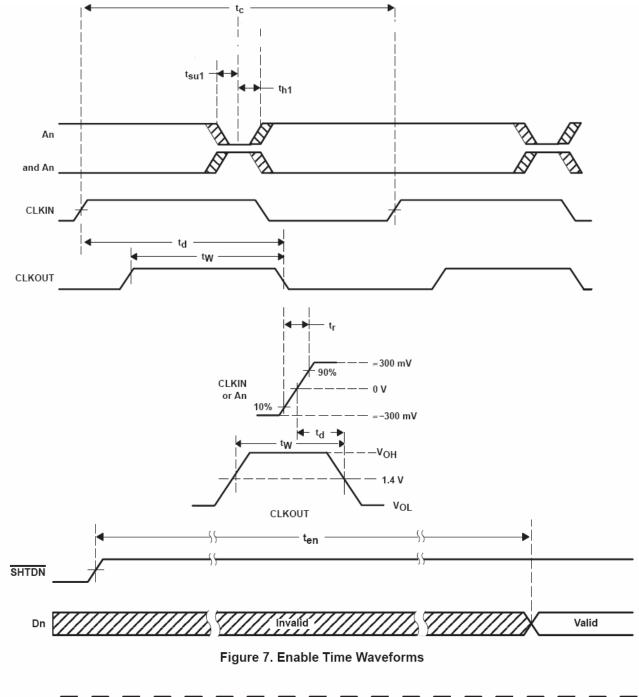


Figure 5. Setup and Hold Time Waveforms

[†] All typical values are at V_{CC} = 3.3 V, T_A = 25°C.

§ The parameter t_(RSKM) is the timing margin available to allocate to the transmitter and interconnection skews and clock jitter. The value of this parameter at clock periods other than 15.38 ns can be calculated from t_{RSKM} = tc/14 – 550 ps.

¶ |Input clock jitter| is the magnitude of the change in input clock period.



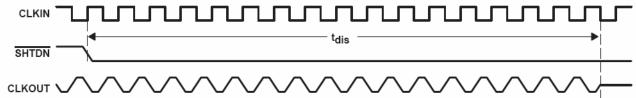
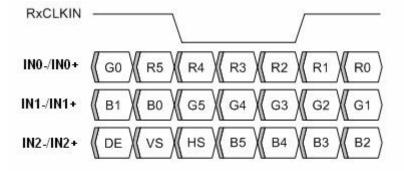


Figure 8. Disable Time Waveforms

LVDS Input Data Format



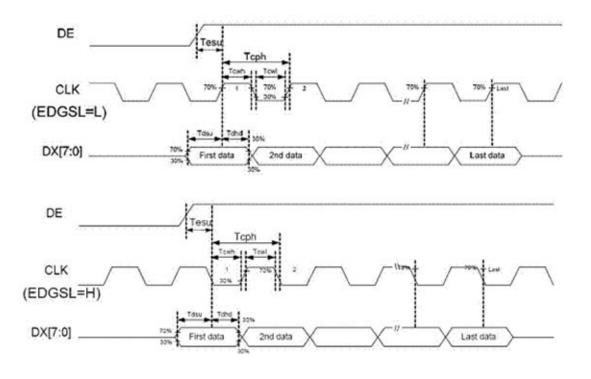
Note: R/G/B data 5: MSB, R/G/B data 0: LSB

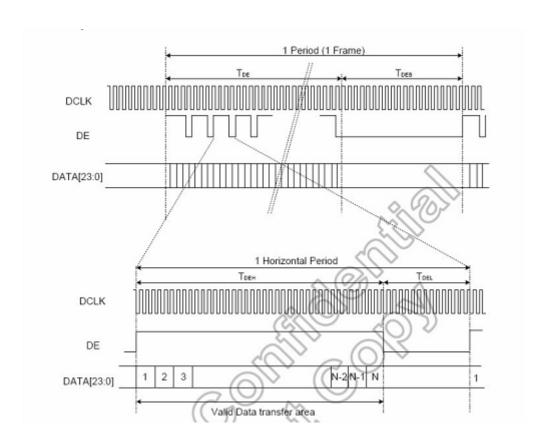
Signal Name	Description	Remark
R5 R4 R3 R2 R1 R0	Red Data 5 (MSB) Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 6 bits pixel data.
G5 G4 G3 G2 G1 G0	Green Date 5 (MSB) Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 6 bits pixel data.
B5 B4 B3 B2 B1 B0	Blue Data 5 (MSB) Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 6 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

8.2 Timing characteristic of Panel

Parameter	Symbol		Unit			
Parameter	Symbol	Min.	Тур.	Max.	Oill	
Data setup time	Tdsu	6	-	-	ns	
Data hold time	Tdhd	6	-	-	Tcph	
DE setup time	Tesu	6	-	-	Tcph	
CLK frequency	Fсрн		33.26		MHz	
CLK period	Тсрн		30.06		ns	
CLK pulse duty	Tcwn	40	50	60	%	
DE period	TDEH+TDEL	1000	1056	1200	Тсрн	
DE pulse width	TDEH	-	800	-	Тсрн	
DE frame blanking	TDEB	10	45	110	TDEH+TDEL	
DE frame width	TDE	-	480	-	TDEH+TDEL	

Note: We suggest using the typical value, so it can have better performance.





9. DISPLAYED COLOR AND INPUT DATA

	Color &		DATA SIGNAL																
	Gray Scale	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	В5	В4	ВЗ	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:			:	:	:	:		:	:	:	:
Neu	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Green	:	:	:	:	:							:	***	:		:	***	:	:
Green	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:							:		:		:	***	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Dide	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

10. Reliability Test Items

Date: 2014/03/18

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency: 10 ~ 55 ~ 10 Hz/1min Amplitude: 0.75mm Test direction: X.Y.Z/3 axis Duration: 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

11. USE PRECAUTIONS

11.1 Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1M\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

11.4 Operating precautions

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC dive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

11.5 Other

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) The residual image may exist if the same display pattern is shown for hours. This residual image, however, disappears when another display pattern is shown or the drive is interrupted and left for a while. But this is not a problem on reliability.
- 3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

12 OUTLINE DIMENSION

