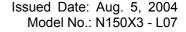


# **TFT LCD Approval Specification**

# **MODEL NO.: N150X3 - L07**

Approv							
Note:	1	10 10		4-			
Note.							

Liquid Crystal	Display Division
QRA Division.	OA Head Division.
Approval	Approval
93. 8. 10	93. 8. 5

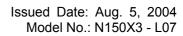




### - CONTENTS -

#### **REVISION HISTORY**

- 1. GENERAL DESCRIPTION
  - 1.1 OVERVIEW
  - 1.2 FEATURES
  - 1.3 APPLICATION
  - 1.4 GENERAL SPECIFICATIONS
  - 1.5 MECHANICAL SPECIFICATIONS
- 2. ABSOLUTE MAXIMUM RATINGS
  - 2.1 ABSOLUTE RATINGS OF ENVIRONMENT
  - 2.2 ELECTRICAL ABSOLUTE RATINGS
    - 2.2.1 TFT LCD MODULE
    - 2.2.2 BACKLIGHT UNIT
- 3. ELECTRICAL CHARACTERISTICS
  - 3.1 TFT LCD MODULE
  - 3.2 BACKLIGHT UNIT
- 4. BLOCK DIAGRAM
  - 4.1 TFT LCD MODULE
  - 4.2 BACKLIGHT UNIT
- 5. INPUT TERMINAL PIN ASSIGNMENT
  - 5.1 TFT LCD MODULE
  - 5.2 BACKLIGHT UNIT
  - 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL
  - 5.4 COLOR DATA INPUT ASSIGNMENT
- 6. INTERFACE TIMING
  - 6.1 INPUT SIGNAL TIMING SPECIFICATIONS
  - 6.2 POWER ON/OFF SEQUENCE
- 7. OPTICAL CHARACTERISTICS
  - 7.1 TEST CONDITIONS
  - 7.2 OPTICAL SPECIFICATIONS
- 8. PRECAUTIONS
  - 8.1 ASSEMBLY AND HANDLING PRECAUTIONS
  - 8.2 SAFETY PRECAUTIONS
- 9. DEFINITION OF LABELS
  - 9.1 CMO MODULE LABEL
  - 9.2 CMO CARTON LABEL
- 10. PACKAGING
  - 10.1 Packing Specification
  - 10.2 Packing Method





# **REVISION HISTORY**

Version	Date	Page (New)	Section	Description
1.0	Apr,16,'04	All		Preliminary specification was first issue.
2.0	Jun,28,'04	-	Attached	Change short Cu tape to long Cu tape.
2.1	Aug,05,'04	13		Change optical instrument name.



#### 1. GENERAL DESCRIPTION

#### 1.1 OVERVIEW

N150X3 - L07 is a 15.0" TFT Liquid Crystal Display module with single CCFL Backlight unit and 30 pins LVDS interface. This module supports 1024 x 768 XGA mode and can display 262,144 colors. The optimum viewing angle is at 6 o'clock direction. The inverter module for Backlight is not built in.

#### 1.2 FEATURES

- Thin and Light Weight
- XGA (1024 x 768 pixels) resolution
- DE only mode
- 3.3V LVDS (Low Voltage Differential Signaling) interface with 1 pixel/clock

#### 1.3 APPLICATION

- TFT LCD Notebook

#### 1.4 GENERAL SPECIFICATIONS

Item Specification		Unit	Note
Active Area	ve Area 304.1 (H) x 228.1 (V) (15.0" diagonal)		(1)
Bezel Opening Area	307.8 (H) x 231.6 (V)	mm	(1)
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1024 x R.G.B. x 768	pixel	-
Pixel Pitch	0.297 (H) x 0.297 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262,144	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	Hard coating (3H), Anti-glare (Haze 25 %)	-	-

#### 1.5 MECHANICAL SPECIFICATIONS

It	em	Min.	Тур.	Max.	Unit	Note
	Horizontal(H)	316.8	317.3	317.8	mm	
Module Size	Vertical(V)	241.5	242	242.5	mm	(1)
	Depth(D)	5.4	5.7	6.0	mm	
We	eight	535	550	565	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.



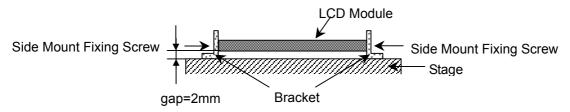
#### 2. ABSOLUTE MAXIMUM RATINGS

#### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Va	lue	Unit	Note	
item	Symbol	Min.	Max.	Offic		
Storage Temperature	T <sub>ST</sub>	-20	+60	°C	(1)	
Operating Ambient Temperature	T <sub>OP</sub>	0	+50	°C	(1), (2)	
Shock (Non-Operating)	S <sub>NOP</sub>	-	200	G	(3), (5)	
Vibration (Non-Operating)	$V_{NOP}$	-	2.0	G	(4), (5)	

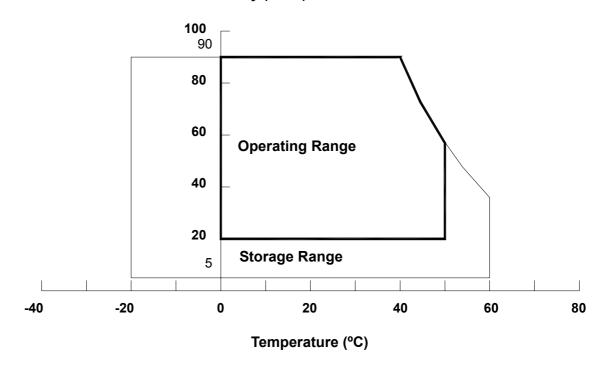
Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. (Ta  $\leq$  40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.
- Note (2) The ambient temperature means the temperature of panel surface.
- Note (3) 2ms, half sine wave, 1 times for  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ .
- Note (4) 10 ~ 500 Hz, 0.5 Hr/Cycle,(4) cycles each X, Y, Z. The fixing condition is shown as below:



Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

# Relative Humidity (%RH)





### 2.2.1 TFT LCD MODULE

Item	Symbol	Va	lue	Unit	Note	
item	Symbol	Min.	Max.	Offic	NOLE	
Power Supply Voltage	V <sub>CC</sub>	-0.3	+4.0	V	(1)	
Logic Input Voltage	V <sub>IN</sub>	-0.3	V <sub>CC</sub> +0.3	V	(1)	

#### 2.2.2 BACKLIGHT UNIT

Item	Symbol	Va	lue	Unit	Note
item	Symbol	Min.	Max.	Offic	Note
Lamp Voltage	V <sub>L</sub>	-	2.5K	$V_{RMS}$	$(1)$ , $(2)$ , $I_L = 6.0 \text{ mA}$
Lamp Current	ΙL	-	7.5	mA <sub>RMS</sub>	(1) (2)
Lamp Frequency	FL	-	80	KHz	(1), (2)

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).

#### 3. ELECTRICAL CHARACTERISTICS

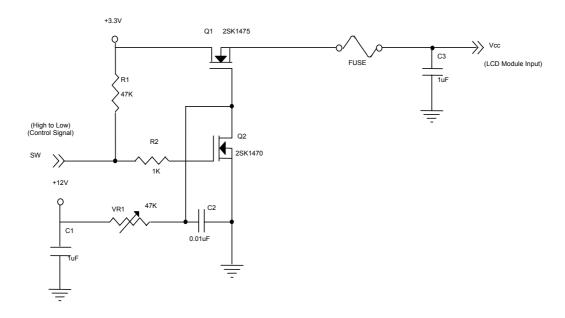
#### 3.1 TFT LCD MODULE

Ta = 25 ± 2 °C

Daramo	Parameter			Value	Unit	Note	
Farame	Symbol	Min.	Тур.	Max.	Offic	Note	
Power Supply Voltage		Vcc	3.0	3.3	3.6	V	-
Ripple Voltage	$V_{RP}$	-	50	-	mV	-	
Rush Current	I <sub>RUSH</sub>	-	1.2	1.5	Α	(2)	
Power Supply Current	White	Icc	-	300	-	mA	(3)a
Fower Supply Current	Black	100	-	400	-	mA	(3)b
Logical Input Voltage	"H" Level	$V_{IL}$	-	-	+100	mV	-
Logical Input Voltage	"L" Level	$V_{IH}$	-100	-	-	mV	-
Terminating Resistor		R <sub>T</sub>	-	100	-	Ohm	-

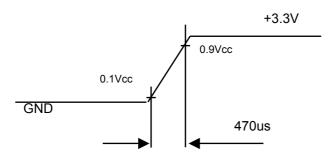
Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



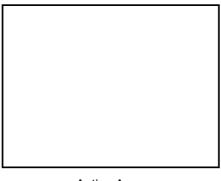


# Vcc rising time is 470us



Note (3) The specified power supply current is under the conditions at Vcc = 3.3 V, Ta =  $25 \pm 2$  °C,  $f_v = 60$  Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern



Active Area

b. Black Pattern



Active Area

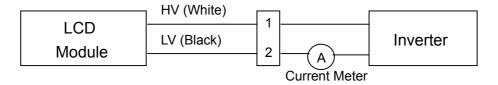
Issued Date: Aug. 5, 2004 Model No.: N150X3 - L07

#### 3.2 BACKLIGHT UNIT

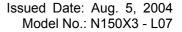
Ta = 25 ± 2 °C

Parameter	Symbol		Value	Unit	Note	
r arameter	Syllibol	Min.	Тур.	Max.	Offic	Note
Lamp Input Voltage	$V_L$	608	675	743	$V_{RMS}$	$I_{L} = 6.0 \text{ mA}$
Lamp Current	ΙL	2.0	-	6.5	$mA_{RMS}$	(1)
Lamp Turn On Voltage	Vs	-		1360 (25 °C)	$V_{RMS}$	(2)
Lamp rum on voltage		-		1500 (0 °C)	$V_{RMS}$	(2)
Operating Frequency	$F_L$	40	50	67	KHz	(3)
Lamp Life Time	$L_BL$	10,000		-	Hrs	(5)
Power Consumption	$P_L$	-	4.4	-	W	$(4)$ , $I_L = 6.5 \text{ mA}$

Note (1) Lamp current is measured by utilizing a high frequency current meter as shown below:



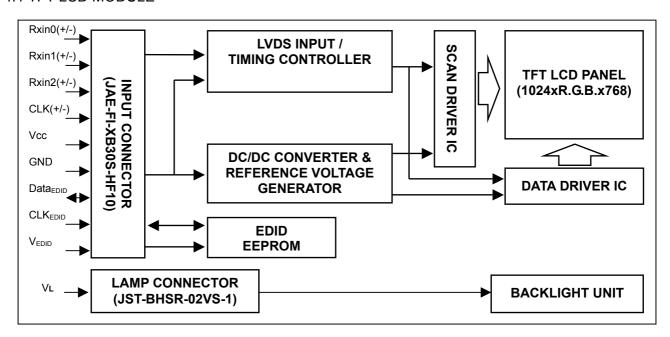
- Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.
- Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.
- Note (4)  $P_L = I_L \times V_L$
- Note (5) The lifetime of lamp can be defined as the time in which it continues to operate under the condition Ta = 25  $\pm 2$  °C and I<sub>L</sub> = 6.5 mArms until one of the following events occurs:
  - (a) When the brightness becomes or lower than 50% of its original value.
  - (b) When the effective ignition length becomes or lower than 80% of its original value. (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)
- Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.



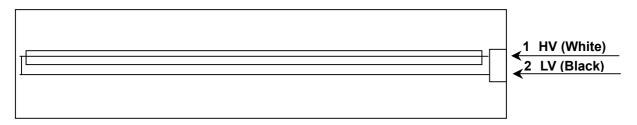


#### 4. BLOCK DIAGRAM

#### 4.1 TFT LCD MODULE



#### **4.2 BACKLIGHT UNIT**



# 5. INPUT TERMINAL PIN ASSIGNMENT

### 5.1 TFT LCD MODULE

Pin	Symbol	Description	Polarity	Remark
1	Vss	Ground		
2	Vcc	Power Supply +3.3 V (typical)		
3	Vcc	Power Supply +3.3 V (typical)		
4	$V_{EDID}$	DDC 3.3V Power		DDC 3.3V Power
5	NC	Non-Connection		
6	CLK <sub>EDID</sub>	DDC Clock		DDC Clock
7	DATA <sub>EDID</sub>	DDC Data		DDC Data
8	Rxin0-	LVDS Differential Data Input	Negative	R0~R5,G0
9	Rxin0+	LVDS Differential Data Input	Positive	_
10	Vss	Ground		
11	Rxin1-	LVDS Differential Data Input	Negative	G1~G5, B0, B1
12	Rxin1+	LVDS Differential Data Input	Positive	-
13	Vss	Ground		
14	Rxin2-	LVDS Differential Data Input	Negative	B2~B5, DE, Hsync, Vsync



15	Rxin2+	LVDS Differential Data Input	Positive	
16	Vss	Ground		
17	CLK-	LVDS Clock Data Input	Negative	LVDS Level Clock
18	CLK+	LVDS Clock Data Input	Positive	LVD3 Level Clock
19	Vss	Ground		
20	NC	Non-Connection		
21	NC	Non-Connection		
22	Vss	Ground		
23	NC	Non-Connection		
24	NC	Non-Connection		
25	Vss	Ground		
26	NC	Non-Connection		
27	NC	Non-Connection		
28	Vss	Ground		
29	NC	Non-Connection		
30	NC	Non-Connection		

Note (1) Connector Part No.: JAE-FI-XB30S-HF10 or equivalent

Note (2) User's connector Part No: FI-X30M or equivalent

Note (3) The first pixel is even.

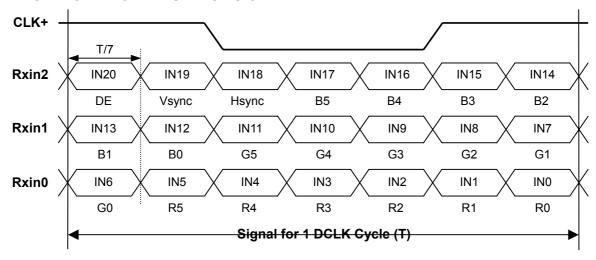
#### 5.2 BACKLIGHT UNIT

Pin	Symbol	Description	Color
1	HV	High Voltage	White
2	LV	Ground	Black

Note (1) Connector Part No.: JST- BHSR-02VS-1 or equivalent

Note (2) User's connector Part No.: SM02B-BHSS-1-TB or equivalent

# 5.3 TIMING DIAGRAM OF LVDS INPUT SIGNAL





#### 5.4 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

	rodo data iripati								[	Data	Sign	al							
Color		Red			Green			Blue											
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale Of	:	:	:	:					:	:				:			:	:	
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Reu	Red(61)	1		1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1			1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	Ö	ő	0	0	Ö	0	0	0	0	0	0	1	ő	ő	ő	Ö	0	0
Gray	Green(2)	Ö	0	0	0	0	0	0	0	0	0	1	0	Ö	ő	Ö	Ö	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:		:	:	:	:	:	:	:
Of	:	:	:	:	:		:	:	:	:	:			:	:	:	:	:	
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

#### 6. INTERFACE TIMING

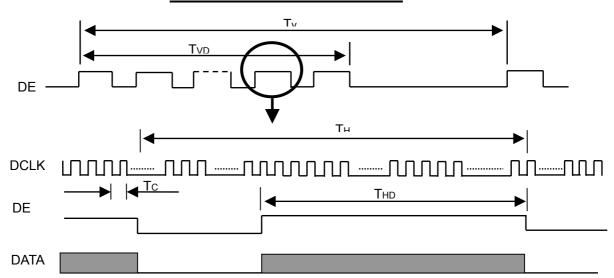
# 6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The specifications of input signal timing are as the following table and timing diagram.

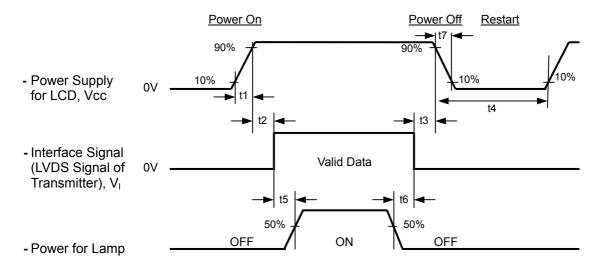
Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
DCLK	Frequency	1/Tc	50	65	68	MHz	-
DE	Vertical Total Time	TV	771	806	850	TH	-
	Vertical Addressing Time	TVD	768	768	768	TH	-
	Horizontal Total Time	TH	1200	1344	1500	Tc	-
	Horizontal Addressing Time	THD	1024	1024	1024	Tc	-



#### **INPUT SIGNAL TIMING DIAGRAM**



#### 6.2 POWER ON/OFF SEQUENCE



# **Timing Specifications:**

 $0.5 < t1 \leq 10 \text{ msec}$ 

 $0 < t2 \le 50 \text{ msec}$ 

 $0 < t3 \le 50 \text{ msec}$ 

 $t4 \ge 500 \text{ msec}$ 

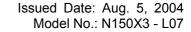
t5  $\geq$  200 msec

 $t6 \ge 200 \, msec$ 

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD Vcc to 0 V.

Note (3) The Backlight inverter power must be turned on after the power supply for the logic and the





interface signal is valid. The Backlight inverter power must be turned off before the power supply for the logic and the interface signal is invalid.

Note (4) Sometimes some slight noise shows when LCD is turned off (even backlight is already off). To avoid this phenomenon, we suggest that the Vcc falling time had better to follow

 $t7 \geq 5 \, \text{msec}$ 

# 7. OPTICAL CHARACTERISTICS

#### 7.1 TEST CONDITIONS

Item	Symbol	Value	Unit				
Ambient Temperature	Та	25±2	°C				
Ambient Humidity	Ha	50±10	%RH				
Supply Voltage	V <sub>CC</sub>	3.3	V				
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"						
Inverter Current	IL	6.0	mA				
Inverter Driving Frequency	F∟	F <sub>L</sub> 50					
Inverter	H05-4915						

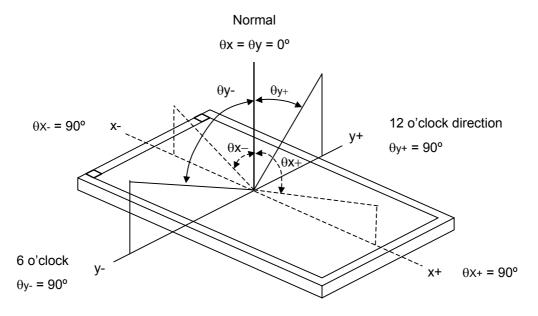
The relative measurement methods of optical characteristics are shown in 6.2. The following items should be measured under the test conditions described in 6.1 and stable environment shown in Note (6).

#### 7.2 OPTICAL SPECIFICATIONS

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
	Red	Rx			0.591				
	Neu	Ry			0.341	TYP		,	
	Green	Gx			0.318			(1), (6)	
Color	Oreen	Gy		TYP	0.532				
Chromaticity	Blue	Bx		-0.03	0.150	+0.03			
Chilomaticity	Dide	Ву	$\theta_{x}$ =0°, $\theta_{Y}$ =0°		0.128				
	White	Wx	CS-1000T		0.313				
	vviille	Wy			0.329				
	Color Gamut	C.G%			45		%	(9)	
Average lumina	Average luminance of white			140	170		cd/m <sup>2</sup>	(7)	
Contrast Ratio		CR		200	250			(2), (6)	
Response Time	Deep and Time		$\theta_x = 0^\circ, \theta_Y = 0^\circ$		6	10	ms	(3)	
Response Tim		$T_F$	$\theta_X$ =0 , $\theta_Y$ =0		17	25	ms	(3)	
Cross Talk	Cross Talk		$\theta_x$ =0°, $\theta_Y$ =0°			3.0	%	(5), (6)	
White Variation		δW	BM-5A		1.25	1.40		(8)	
Viewing	Horizontal	$\theta_x$ +		40	45				
		$\theta_{x}$ -	CR≥10	40	45		Dog		
Angle	\/ortical	θ <sub>Y</sub> +	BM-5A	15	20		Deg.		
	Vertical	θ <sub>Y</sub> -		35	40				



# Note (1) Definition of Viewing Angle ( $\theta x$ , $\theta y$ ):



# Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63 / L0

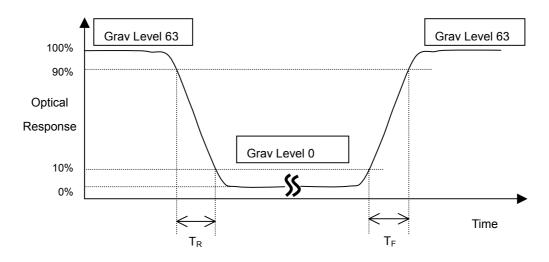
L63: Luminance of gray level 63

L 0: Luminance of gray level 0

CR = CR(5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (7).

# Note (3) Definition of Response Time (T<sub>R</sub>, T<sub>F</sub>):





Note (4) Definition of Average Luminance of White (L<sub>AVE</sub>):

Measure the luminance of gray level 63 at 5 points

$$L_{AVE} = [L(1) + L(2) + L(3) + L(4) + L(5)] / 5$$

L (x) is corresponding to the luminance of the point X at Figure in Note (7).

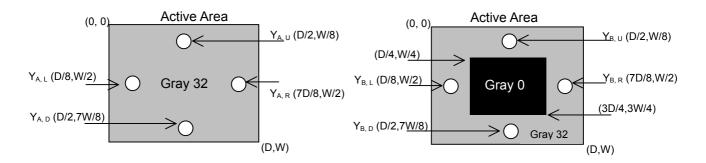
#### Note (5) Definition of Cross Talk (CT):

$$CT = | Y_B - Y_A | / Y_A \times 100 (\%)$$

Where:

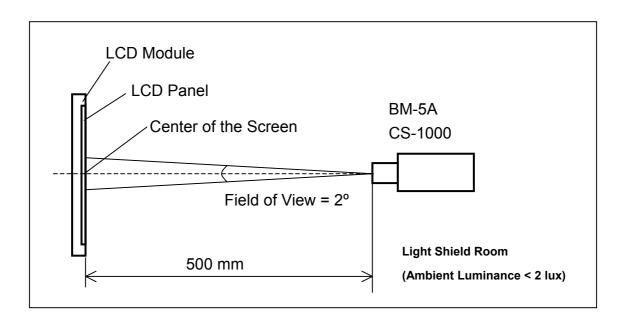
Y<sub>A</sub> = Luminance of measured location without gray level 0 pattern (cd/m<sup>2</sup>)

Y<sub>B</sub> = Luminance of measured location with gray level 0 pattern (cd/m<sup>2</sup>)



### Note (6) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.

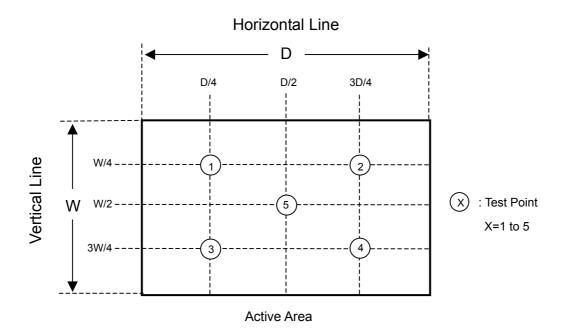


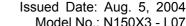


Note (7) Definition of White Variation ( $\delta W$ ):

Measure the luminance of gray level 63 at 5 points

 $\delta W = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]$ 







Model No.: N150X3 - L07

#### 8. PRECAUTIONS

#### 8.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly, and the starting voltage of CCFL will be higher than room temperature.

## 8.2 SAFETY PRECAUTIONS

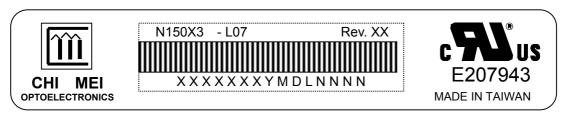
- (1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

Issued Date: Aug. 5, 2004 Model No.: N150X3 - L07

### 9. DEFINITION OF LABELS

#### 9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.

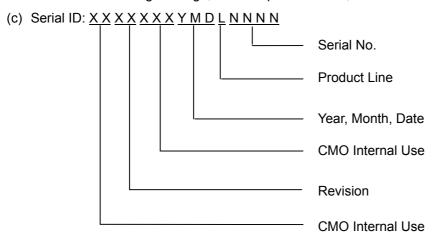


(a) Model Name: N150X3 - L07

(b) Revision: Rev. XX: ES : A1, A2, A3,..., etc.

CS & MP: C1, C2, C3,...,etc.

Note: It will happen that revision code changed without product changed in developing duration because of CMO internal stage change, for example:  $AX \rightarrow B1$ ,  $BX \rightarrow C1$ .



Serial ID includes the information as below:

(a) Manufactured Date: Year: 0 ~ 9, for 2000 ~ 2009.

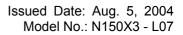
Month: 1 ~ 9, A ~ C, for Jan. ~ Dec.

Day:  $1 \sim 9$ ,  $A \sim Y$ , for  $1^{st}$  to  $31^{st}$ , exclude I, O and U.

(b) Revision Code: cover all the changes.

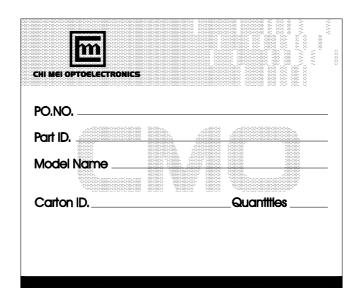
(c) Serial No.: Manufacturing sequence of product.

(d) Product Line: 1 -> Line1, 2 -> Line 2, ...etc.





# 9.2 CMO CARTON LABEL





#### 10. PACKAGING

#### **10.1 PACKING SPECIFICATIONS**

(1) 20 LCD modules / 1 Box

(2) Box dimensions : 511(L) X 420(W) X 360(H) mm

(3) Weight: approximately 14Kg (20 modules per box)

# **10.2 PACKING METHOD**

(1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
Vibration	Random, 2-200 Hz, follow ISTA standard	Non Operation
Dropping Test	1 Angle, 3 Edge, 6 Face, 60cm	Non Operation

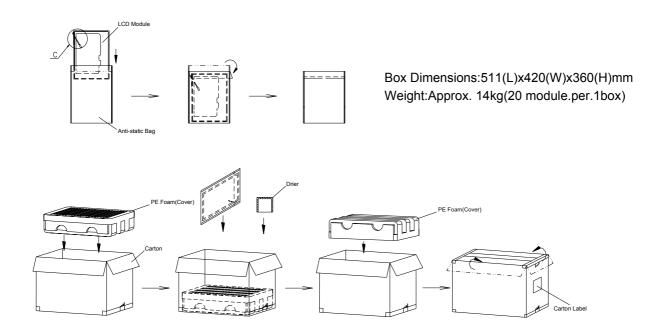
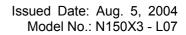


Figure. 10-1 Packing method





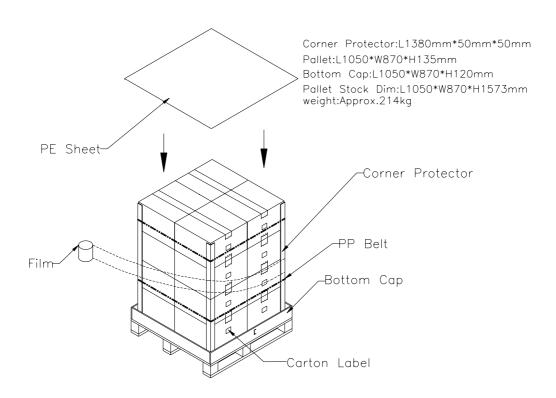


Figure. 10-2 Packing method

