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TITLE: B3 HT185WX1-300

Product Specification

Rev. 0

HEFEI BOE OPTOELECTRONICS TECHNOLOGY

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REVISION HISTORY

REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
0		Initial Release	2011.1.14	丁渊

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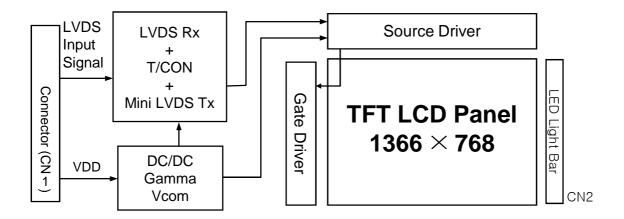
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1.0 GENERAL DESCRIPTION

1.1 Introduction

HT185WX1-300 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 18.5 inch diagonally measured active area with WXGA resolutions (1366 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- LVDS Interface with 1 pixel / clock
- High-speed response
- Low power consumption
- 6-bit (Hi-FRC) color depth, display 16. 7M colors
- Incorporated edge type back-light (One Light Bar)
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS /TCO 5.0 Compliant

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1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model HT185WX1-300.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	$409.8(H) \times 230.4(V)$	mm	
Number of pixels	1366(H) ×768(V)	pixels	
Pixel pitch	$0.3(H) \times 0.3(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally White		
Dimensional outline	$430.4(H) \times 254.6(V) \times 10.9(D)$ typ.	mm	
Weight	1330 (typ.)	g	
Surface Treatment	Haze 25%, 3H		
Back-light	right edge side, 1- LED Light bar		

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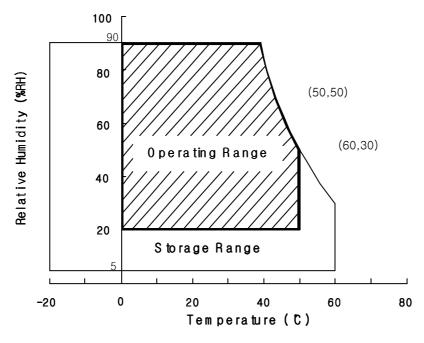
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> [VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.5	5.5	V	
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	V	Ta = 25 °C
LED Channel Current	I_{BL}	-	80	mA	
Operating Temperature	T_{OP}	0	+50	$^{\circ}\!\mathbb{C}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}\!$	1)

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

 $[Ta = 25 \pm 2 \, ^{\circ}C]$

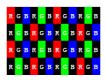
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	4.5	5.0	5.5	V	Note1
Power Supply Current	I_{DD}	-	600	1000	mA	Note1
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V _{RF}	-	-	100	mV	$V_{DD} = 5.0V$
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	$V_{\rm IL}$	-100	-	-	mV	
Differential input voltage	V _{ID}	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		$V_{IH}=100 \text{mV},$ $V_{IL}=-100 \text{mV}$
LED Channel Voltage	V_L	37.7	41.6	46.8	V	
LED Channel Current	I_L	57	60	63	mA	
LED Lifetime		30,000	-	-	Hrs	I
	P_{D}	-	3	4.5	W	
Power Consumption	P_{BL}	-	9.98	11.23	W	I _L =60 mA, Note 5
	P_{total}	-	12.98	15.73	W	

Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz and Clock frequency = 95MHz. Test Pattern of power supply current

a) Typ: Color Bar pattern

b) Max: Skip Sub Pixel Pattern



- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %
- 3. The lamp frequency should be selected as different as possible from the horizontal synchronous frequency and its harmonics to avoid interference, which may cause line flow on the display
- 4. The voltage above this value should be applied to the lamps for more than 1 second to start-up. Otherwise the lamps may not be turned on.
- 5. Calculated value for reference ($V_L \times I_L$) \times 4(channel) excluding driver loss. (LED Light bar: 13S4P)

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2^{\circ}$ °C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 78MHz, I_{BL} = 7.5mA, Ta =25 \pm 2 °C]

Parame	ter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Y	Horizontal	Θ_3		70	85	-	Deg.	
	поптан	Θ_9	GD 10	70	85	-	Deg.	
Viewing Angle range	Vertical	Θ_{12}	CR > 10	70	80	-	Deg.	
	vertical	Θ_6		70	80	-	Deg.	Note 1
	Horizontal	Θ_3		85	-	-	Deg.	Note 1
Viewing Angle range	Horizontai	Θ_{9}	CR > 5	85	-	-	Deg.	
Viewing Angle range	Vertical	Θ_{12}	CR > 3	85	-	-	Deg.	
	vertical	Θ_6		85	-	-	Deg.]
Luminance Contrast r	atio	CR		700	1000			Note 2
Luminance of White		$Y_{\rm w}$		200	250		cd/m ²	Note 3
White luminance unif	ormity	ΔΥ		75	80		%	Note 4
	White	$\mathbf{W}_{\mathbf{x}}$	$\Theta - 0^{\circ}$	0.283	0.313	0.343		
	Willte	\mathbf{W}_{y}		0.299	0.329	0.359		
	Red	R_x	Normal	0.606	0.636	0.666		
Reproduction	Red	R_y	Viewing Angle	0.313	0.343	0.373		Note 5
of color	Green	G_{x}	C	0.297	0.327	0.357		11010 3
	Green	G_{y}		0.599	0.629	0.659		
	Blue	B_x		0.124	0.154	0.184		
	Diue	\mathbf{B}_{y}		0.018	0.048	0.078		
Response	Rising	T_{r}			1.5	2.5	ms	Note 6
Time	Falling	$T_{\rm f}$			3.5	5.5	ms	11010 0
Cross Ta	ılk	CT		-	-	2.0	%	Note 7

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Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 9points / Maximum Luminance of 9points) * 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Module Side Connector : UJU IS100-30O-C23 or Equivalent User Side Connector : JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	NC	No connection	
2	CE	No connection	internal use
3	CTL	No connection	internal use
4	GND	GND Ground	
5	RX0-	Negative LVDS differential data input. Channel 0	
6	RX0+	Positive LVDS differential data input. Channel 0	
7	GND	Ground	
8	RX1-	Negative LVDS differential data input. Channel 1	
9	RX1+	Positive LVDS differential data input. Channel 1	
10	GND	Ground	
11	RX2-	Negative LVDS differential data input. Channel 2	
12	RX2+	Positive LVDS differential data input. Channel 2	
13	GND	Ground	
14	RXCLK-	Negative LVDS differential clock input.	
15	RXCLK+	Positive LVDS differential clock input.	
16	GND	Ground	
17	RX3-	Negative LVDS differential data input. Channel 3	
18	RX3+	Positive LVDS differential data input. Channel 3	
19	GND	Ground	
20	NC	Not connection, this pin should be open.	
21	NC	Not connection, this pin should be open.	
22	NC	Not connection, this pin should be open.	
23	GND	Ground	
24	GND	Ground	
25	GND	Ground	
26	VCC	5V Power supply	
27	VCC		
28	VCC		
29	VCC		
30	VCC		

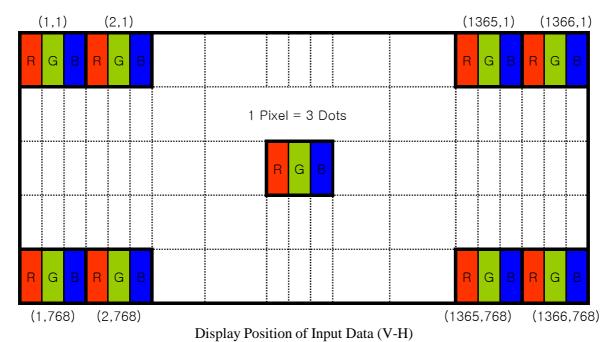
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5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent) 5.2.1 LVDS Interface

	Input	Trans	mitter	Inter	face	HT185WX1-300 (CN11)	Remark
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.	
	OR0	51					
	OR1	52					
	OR2	54	48	OUT0-	RXO0-	1	
	OR3	55	48 47	OUT0+	RXO0- RXO0+	1 2	
	OR4	56	''	00101	101001	2	
	OR5	3					
	OG0	4					
	OG1	6					
	OG2	7			RXO1- RXO1+	3 4	
	OG3	11	4.6	OLUM1			
	OG4 OG5 OB0	12	46 45	OUT1- OUT1+			
		14] 73				
		15					
-	OB1	19					
L V	OB2	20					
Ď	OB3	22			RXO2- RXO2+	5 6	
S	OB4	23]	O.V.T.			
	OB5	24	42 41	OUT2- OUT2+			
	Hsync	27	41	0012+			
	Vsync	28					
	DE	30					
	MCLK	31	40 39	CLK OUT- CLK OUT+	RXO CLK- RXO CLK+	8 9	
	OR6	50					
	OR7	2					
	OG6	8	20	OLUT2	RXO3-	10	
	OG7	10	38 37	OUT3- OUT3+	RXO3+	10 11	
	OB6	16		00131		11	
	OB7	18					
	RSVD	25					

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5.3 Data Input Format



5.4 Back-light Interface Connection

●CN 2 LED LightBar Connector: CI1406M1HRL-NH or equivalent

Pin	Function
1	Channel 1 Current Feedback
2	Channel 2 Current Feedback
3	LED Power Supply
4	LED Power Supply
5	Channel3 Current Feedback
6	Channel4 Current Feedback

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6.0 SIGNAL TIMING SPECIFICATION

6.1 The HT185WX1-300 is operated by the DE only.

Item		Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	50	78	95	MHz
Clock	High Time	Tch	-	4/7Tc	-	
	Low Time		-	4/7Tc	-	
·			778	806	888	lines
Fı	Frame Period	Tv	50	60	75	Hz
			20	16.7	13.3	ms
Vertical Display Period		Tvd	1	768	-	lines
One line Scanning Period		Th	1446	1560	1936	clocks
Horizon	tal Display Period	Thd	-	1366	-	clocks

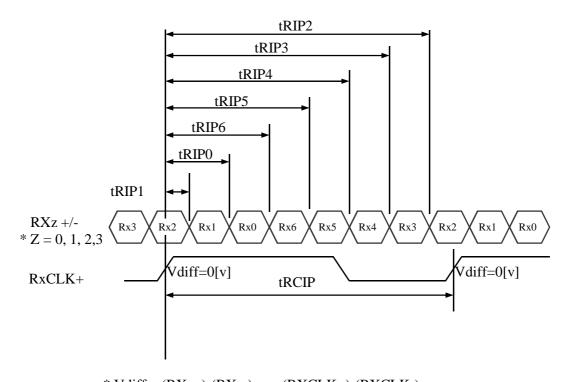
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6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
CLKIN Period	tRCIP	10.60	13.25	20.00	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	$2 \times tRCIP/7 + 0.4$	nsec	
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	$3 \times tRCIP/7 + 0.4$	nsec	
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	$4 \times tRCIP/7 + 0.4$	nsec	
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 × tRCIP/7	$5 \times tRCIP/7 + 0.4$	nsec	
Input Data 6	tRIP2	6 ×tRCIP/7-0.4	6 ×tRCIP/7	$6 \times tRCIP/7 + 0.4$	nsec	

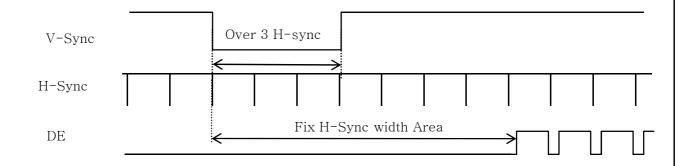


* $Vdiff = (RXz+)-(RXz-), \dots, (RXCLK+)-(RXCLK-)$

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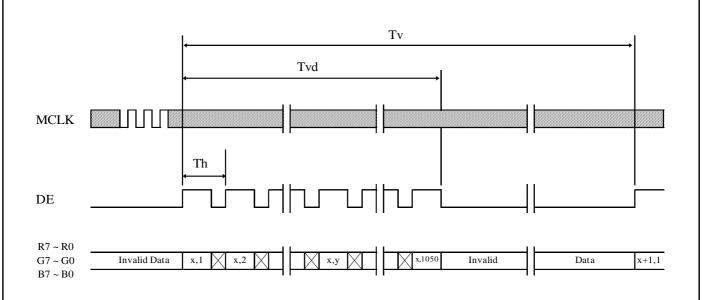
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms



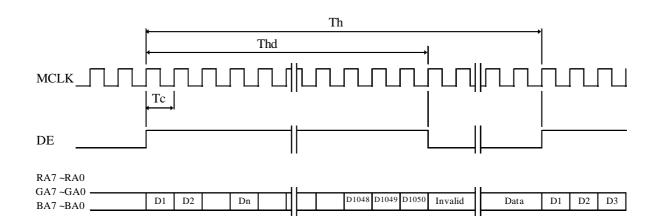
- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

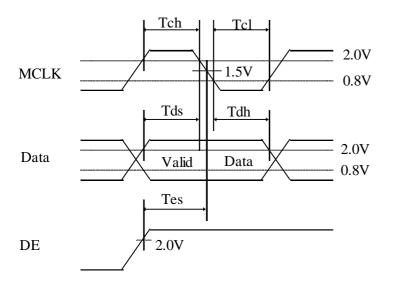
7.2 Vertical Timing Waveforms



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7.3 Horizontal Timing Waveforms





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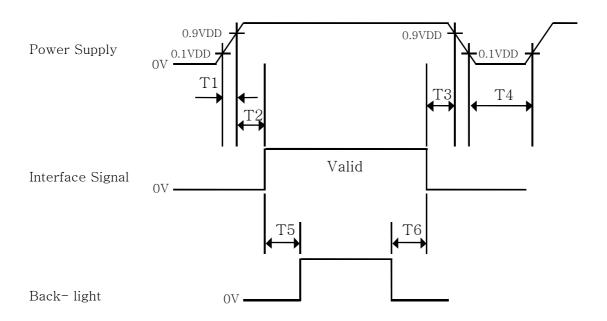
8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

				RF	ED I	DAT	ГΑ				(GRI	EEN	I DA	λΤΑ					BL	UE	DA	TA		
Color & C	Gray Scale	R7	R6			R3		R1	R0	G7	G6			G3			G0	В7	B6					В1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Davis Calam	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\triangle					<u> </u>								<u> </u>								<u> </u>			
of RED	∇					ļ								ļ								ļ			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	Δ				,	1							,	1							,				
OI GREEN	∇				,	ļ							,	ļ							,	ļ			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	\triangle				,	<u> </u>								<u> </u>											
Of BECE	∇				,	_							,								,				
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
of WHITE	<u> </u>					<u> </u>								<u> </u>							,	<u> </u>			
OI WIII IE	∇				,																,	_			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
I	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- \bullet 0.5 ms \leq T1 \leq 10 ms
- \bullet 0 \leq T2 \leq 50 ms
- \bullet 0 \leq T3 \leq 50 ms
- \bullet 1 sec \leq T4
- \bullet 200 ms \leq T5
- \bullet 200 ms \leq T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HT185WX1-300. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	$430.4 \times 254.6 \times 10.9$	mm
Weight	(1330) (typ.)	gram
Active area	$409.8(H) \times 230.4(V)$	mm
Pixel pitch	$0.3(H) \times 0.3(V)$	mm
Number of pixels	$1366(H) \times 768(V)$ (1 pixel = R + G + B dots)	pixels
Back-light	Right edge side 1-LED Light bar Type	

10.2 Mounting

See FIGURE 5. (shown in Appendix)

10.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

10.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

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11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below. <Table 6. Reliability Test Parameters >

No	Test Items		Conditions	
1	High temperature storage test	$Ta = 60 ^{\circ}\text{C}, 240 \text{h}$	nrs	
2	Low temperature storage test	$Ta = -20 ^{\circ}\text{C}, 240 ^{\circ}$	hrs	
3	High temperature & high humidity operation test	Ta = 50 °C, 80%RH, 240hrs		
4	High temperature operation test	$Ta = 50 ^{\circ}\text{C}, 240\text{hr}$	rs	
5	Low temperature operation test	$Ta = 0 ^{\circ}C$, 240hrs	3	
6	Thermal shock	$Ta = -20 \degree C \leftrightarrow 60 \degree C (0.5 \text{ hr}), 100 \text{ cycle}$		
7	Vibration test (non-operating)	Frequency Gravity / AMP Period	10 ~ 300 Hz, Sweep rate 30 min 1.5 G X, Y, Z 30 min	
		Gravity	50G	
8	Shock test (non-operating)	Pulse width	11msec, sine wave	
		Direction	$\pm X$, $\pm Y$, $\pm Z$ Once for each	
9	Electro-static discharge test (non-operating)	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV		
10	Altitude test		16400ft, 0 to 40°	
_		Non Operating:	0 to 40000ft, -20 to 40°	

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12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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13.0 PRODUCT SERIAL NUMBER









MADE IN CHINA

X X

 \mathbf{x}

X

x x

B3

5 X

x x x x

x x x x x x x

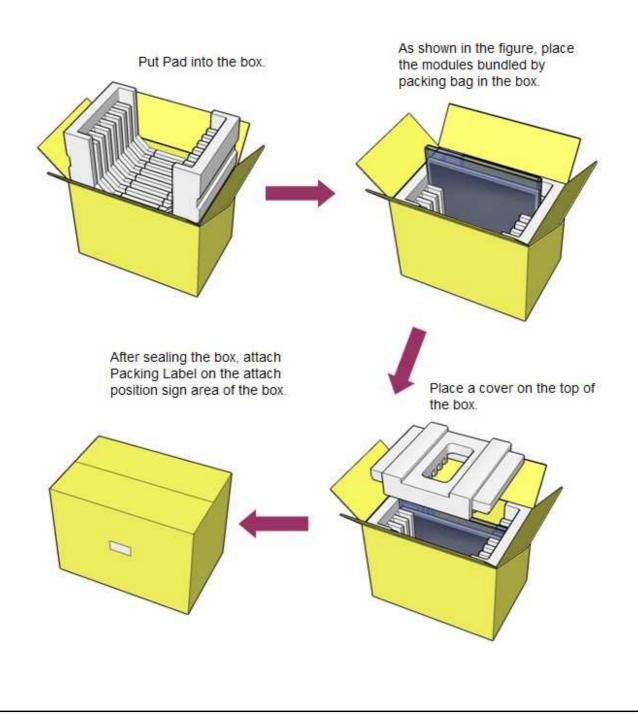
- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001: 01, 2002: 02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

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14.0 Packing

14.1 Packing Order



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14.2 Packing Note

• Box Dimension : 346mm(W) $\times 521$ mm(L) $\times 403$ mm(H)

• Package Quantity in one Box: 9 pcs

14.3 Box label

• Label Size : 108 mm (L) × 56 mm (W)

• Contents

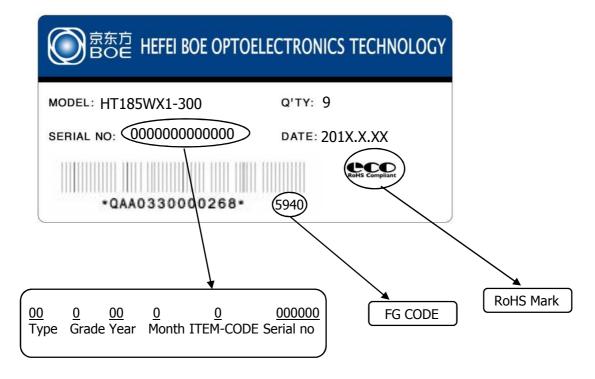
Model: HT185WX1

Q`ty: Module Q`ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date

FG Code: FG Code of Product



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15.0 APPENDIX

Figure 1. Measurement Set Up

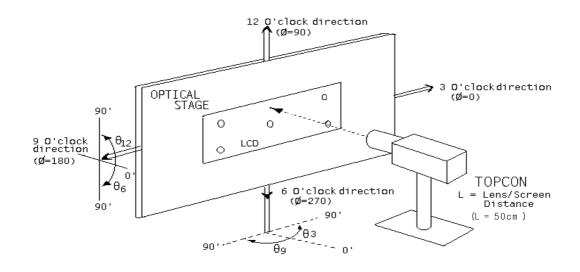
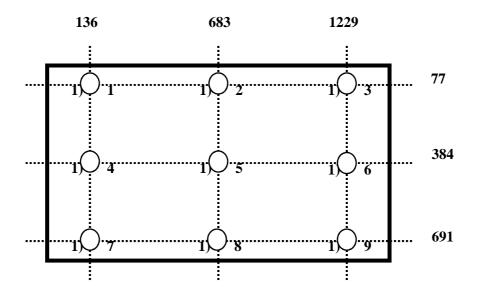


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



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Figure 3. Response Time Testing

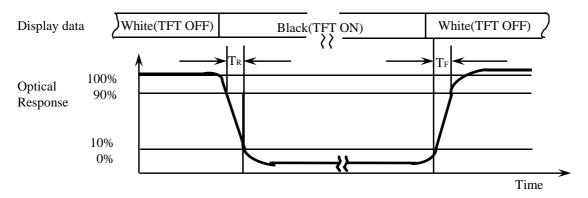
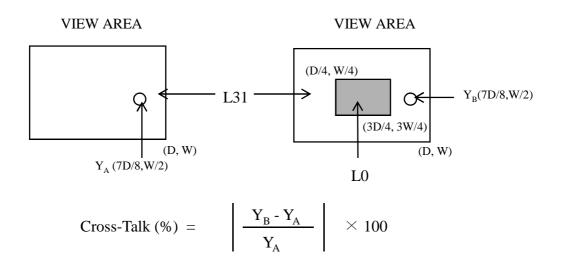


Figure 4. Cross Modulation Test Description



Where: Y_A = Initial luminance of measured area (cd/m²) Y_B = Subsequent luminance of measured area (cd/m²) The location measured will be exactly the same in both patterns

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Figure 5. TFT-LCD Module Outline Dimensions (Front view) 409.8(ACTIVE AREA)
413.4(BEZEL OPENING) 230.4(ACTIVE AREA) 234.0(BEZEL OPENING) HT185WX1 0 Z1

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Figure 6. TFT-LCD Module Outline Dimensions (Rear view)

