

CUSTOMER APPROVAL SHEET

С	ompany Name				
	MODEL	C070VW04 V1			
	CUSTOMER	Title:			
	APPROVED	Name :			
	APPROVAL FOR SPECIFICATIONS ONLY (Spec. Ver. 0.8) APPROVAL FOR SPECIFICATIONS AND ES SAMPLE (Spec. Ver.0.8) APPROVAL FOR SPECIFICATIONS AND CS SAMPLE (Spec. Ver.0.8)				
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Doc. Version	0.8
Total Page	19
Date	2011/05/12

Product Specification

7" COLOR TFT-LCD MODULE

Model Name: C070VW04 V1

Planned Lifetime: From 2009/Apr To 2016/Apr

Phase-out Control: TBD EOL Schedule: TBD

< >Preliminary Specification

< ◆ >Final Specification

Note: The content of this specification is subject to change.

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Record of Revision

Version	Revise Date	Page	Content
0.0	2009/4/09		First draft.
0.1	2009/04/16	6	Mechanical Drawing → Page 6
		8	LED Maximum Rating
		8	BL Driving Condition
0.2	2009/07/22	6	Outline Dimension Drawing update
		16	Packing Form Updates
0.3	2009/09/07	6	Mechanical Drawing update
		13	Definition of Rising and Falling response time
0.4	2009/12/30	6	Outline Dimension drawing update
		9	Power On/Off sequence update, Define DEN signal.
		14	Update Note.10 Luminance uniformity definition
		16	Update Packing form drawing
0.5	2010/01/22	8	Modify vertical cycle minimum value
		13	Add Luminance, Contrast, Res onse time, Viewing angle Max or min value
		5	Update module weight
		7	Correct PIN6 Description
0.6	2010/02/24	12	Update Timing Diagram (HV Mode)
0.7	2010/08/06	6	Update Mechanical Drawing(Customer asked to change the length of power cord from 85 to 90mm).
		13	Update response time and definition of response time
		17	Update Packing Form and Marking
8.0	2011/05/04	15	Update Reliability Test Items



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A. General Description

C070VW04 V1 is an a-Si type Thin Film Transistor Liquid crystal Display (TFT-LCD) with AMVA-Mobile (Advanced MVA) technology. This model is composed of a TFT-LCD, a driver, an FPC (flexible printed circuit), and a backlight unit . TCON (timing controller) is also embedded in source driver.

B. Features

- 7-inch (15:9) display
- 800RGB x 480 resolution in RGB stripe dot arrangement
- High brightness
- Interfaces: parallel RGB 18-bit
- Advanced MVA -- wide view technology
- RoHs compliance

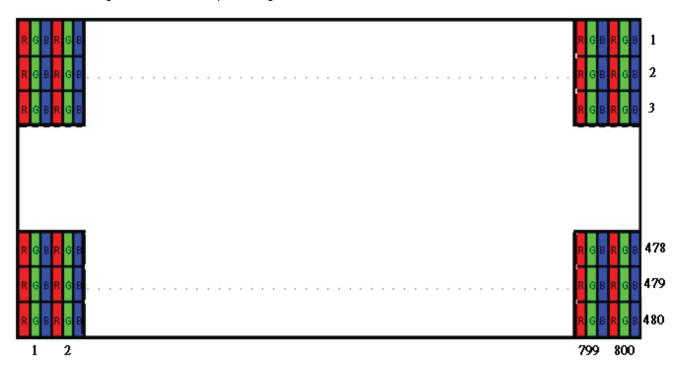


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C. Physical Specifications

NO.	Item	Unit	Specification	Remark
1	Display Resolution	dot	800 RGB (H)×480(V)	
2	Active Area	mm	152.4(H)×91.44(V)	
3	Screen Size	inch	7(Diagonal)	
4	Dot Pitch	mm	0.0635(H)×RGBx0.1905(V)	
5	Color Configuration		R. G. B. Stripe	Note 1
6	Color Depth		262K Colors	
7	Overall Dimension	mm	164.55(H) × 105.01(V) × 6.67(T)	Note 2
8	Weight	g	145	
9	Display Mode		Normally Black	
10	Surface Treatment		AG	

Note 1: Below figure shows dot stripe arrangement.

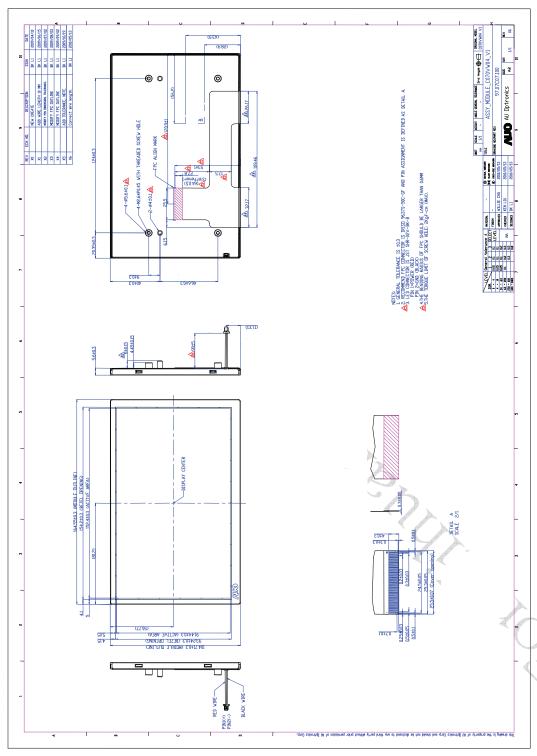


Note 2: Not including FPC. Please refer to the drawing in page 6 for further information.



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D. Outline Dimension –



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E. Electrical Specifications

1. Pin Assignment Connector=HRS FH28D-50S-0.5SH(05)

Connector=HRS FH28D-50S-0.5SH(05) No. Pin Name								
No.		Description	Remarks					
1	VCOM	PI	Common electrode driving voltage					
2	VGH	PI	Positive power supply voltage for TFT					
3	VGL	PI	Negative power supply voltage for TFT					
4	VDD	PI	Digital power supply voltage.					
5	GND	G	Ground					
6	V1	1	Gamma correction voltage					
7	V2	1	Gamma correction voltage					
8	V3	1	Gamma correction voltage					
9	V4	1	Gamma correction voltage					
10	V5	I	Gamma correction voltage					
11	V6	I	Gamma correction voltage					
12	V7	ı	Gamma correction voltage					
13	V8	ı	Gamma correction voltage					
14	V9	ı	Gamma correction voltage					
15	V10	i	Gamma correction voltage					
16	V11	i	Gamma correction voltage					
17	V12	i	Gamma correction voltage					
18	V13	i	Gamma correction voltage					
	V14	i	Gamma correction voltage					
	AVDD	PI	Analog power supply voltage					
21	AVDD	PI	Analog power supply voltage					
22	GND	G	Ground					
23	VS	ı	Vertical Sync input					
	HS	ı	Horizontal Sync input					
<u>24</u> 25	DE	l I	Data Enable Input (High Active)					
<u>25</u> 26	DCLK	1	Data clock Input					
27	GND	G	Ground					
28	DB5	G I	Blue data input(MSB)	+				
<u>20</u> 29	DB4	1	Blue data input	+				
30	DB3	1	Blue data input					
30 31	DB2	- 1						
32	DB1	- 1	Blue data input					
	II.	1	Blue data input	+				
33 34	DB0	1	Blue data input(LSB)					
	DG5	I.	Green data input (MSB)	1				
35	DG4	I.	Green data Input	1				
	DG3	l I	Green data Input	1				
37	DG2	l I	Green data Input	1				
38	DG1	l	Green data Input	1				
39	DG0		Green data Input (LSB)	1				
40	DR5		Red data input (MSB)	1				
41	DR4		Red data input					
42	DR3		Red data Input					
43	DR2		Red data Input	_				
44	DR1		Red data Input	1				
45	DR0	l	Red data Input (LSB)	1				
	Mode	I	DE/SYNC mode selection. "H" for DE mode. "L" for HV mode.					
47	STBYB	1	Standby mode. "H" for normal operation. "L" for standby mode.					
48	RSTB	1	Global reset pin. (low active)					
49	GND	G	Ground					
50	V-COM	PI	Common electrode driving voltage					

I: Digital signal input, O: Digital signal output, G: GND, PI: Power input



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2. Absolute Maximum Ratings

Items	Symbol	Va	lues	Unit	Condition
iteilis	Syllibol	Min. Max.		Offic	Condition
	VDD	-0.3	4.5	V	
	AVDD	-0.5	13.5	V	
Power Voltage	VGH	-0.3	20	V	
	VGL	-15	0.3	V	
	VGH-VGL	0	35	V	
	Vi	-0.3	Vdd+ 0.3	V	
Input Signal Voltage	Vref(V1~V7)	0.4AVDD	AVDD-0.3	V	
input Signal Voltage	Vref(V8~V14)	0.1	0.6AVDD	V	
	VCOM	-0.3	10	V	
Operation Temperature	Topa	-30	+85	$^{\circ}\mathbb{C}$	Ambient
Storage Temperature	Tstg	-40	+95	$^{\circ}\mathbb{C}$	Ambient
LED	Vf	-0.5	16	V	
LED	If	0	90	mA	

Note 1: Functional operation should be restricted under normal ambient temperature.

3. Electrical Characteristics

The following items are measured under stable condition and suggested application circuit.

a. TFT- LCD Panel

Parameter	Symbol	Min	Тур	Max	Unit	Notes
	VDD	3.1	3.3	3.5	V	
Power Supply	AVDD	12.2	12.5	12.8	٧	
	VGH	14.5	15	15.5	٧	
	√ VGL	-9.5	-9	-8.5	V	
	VCOM	5.11	5.31	5.51	٧	
	Vi	-0.3	-	Vdd+ 0.3	V	
Input Signal Voltage	Vref(V1~V7)	0.4AVdd	-	AVdd-0.3	V	
	Vref(V8~V14)	0.1	-	0.6AVdd	V	
Input high voltage	Vh	0.7Vdd	-	Vdd	V	
Input low voltage	VI	0	1	0.3Vdd		
Vertical cycle	f _V	50	60	80	Hz	
Horizontal cycle	f _H	28.9	31.5	42	kHz	
Dot Frequency	f _{DCLK}		33.3	45	MHz	

b. Recommend Gamma Voltage

Parameter	Symbol	Min	Тур	Max	Unit	Notes
Gamma Voltage	V1	-	12.24	-	V	
	V2	-	11.62	-	V	
	V3	-	10.40	-	V	
	V4	-	9.78	-	V	
	V5	-	9.38	-	V	
	V6	-	8.46	-	V	
	V7	-	6.83	-	V	
	V8	-	6.40	-	V	
	V9	-	4.58	-	V	
	V10	-	3.74	-	V	
	V11	-	3.27	-	V	



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V12	-	2.60	-	V	
V13	-	1.18	-	V	
V14	-	0.28	-	V	

c. Backlight Driving Conditions (Note 1)

zacking it zirring conditions (rector)										
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark				
LED Supply Current	Ι <u>ι</u>	-	80	90	mA	single serial				
LED Supply Voltage	V_L	1	14	16	٧	Note 3				
LED Life Time	LL	10000			Hr	Note 2				

Note 1: LED backlight is 12 LEDs (3 strings, 4pcs for each string).

Note 2: The LED lifetime 10000hrs means , after normal use at 80mA, room temperature +25 the brightness decreases to 50% of original level.

Note 3: The LED supply power is for 3 string of LED



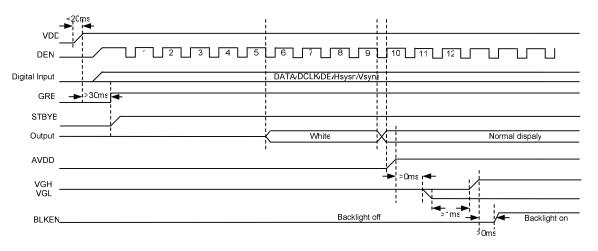
4. AC Timing

a. Power on/off sequence

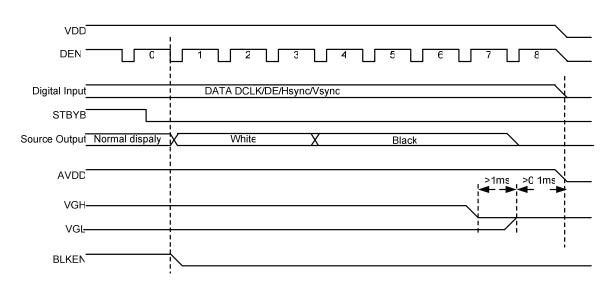
The LCD adopts high voltage driver IC, so it could be permanently damaged under a wrong power on/off sequence. The suggested LCD power sequence is below:

Power on sequence:

VDD -> GRB -> STBYB -> Digital input (Data/DCLK/DE) -> AVDD -> VGL -> VGH -> BLKEN



Power-Off STBYB -> BLKEN -> VGH -> VGL -> Digital input (Data/DCLK/DE)&Power(VDD/AVDD)



DEN: Defined a frame period and created internally by DE. It is similar vertical sync.

GRB: Global reset, normally pulled high.

Suggest to connecting with an RC reset circuit for stability. Normally pull high.

STBYB: Standby mode, normally pulled high.

STBYB ="1", normal operation

STBYB="0",timing controller, source driver will turn off, all output are High-Z



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b. Timing Condition

Item	Symbol	Min	Тур	Max	Unit	Remark
Clock frequency	dclk	-	33.3	45	MHZ	
DCLK cycle time	Tdclk	22	30	Ī	ns	
DCLK pulse duty	Tcwh	40	50	60	%	
Vertical display area	Tvd	480	480	480	Ι	
Vertical period area	Tv	522	525	762	Η	
Vertical blanking area	Tvb	42	45	282	Ι	
Vertical pulse width	Tvw	-	3	ı	Η	
Vertical back porch	Tve	-	32	Ī	Ι	
Vertical front porch	Tvf	10	13	250	Н	
Horizontal display area	Thd	800	800	800	dclk	
Horizontal period area	Th	910	1056	1138	dclk	
Horizontal blanking area	Thb	110	256	338	dclk	
Horizontal pulse width	Thw	1	1	ı	dclk	
Horizontal back porch	The	88	88	88	dclk	
Horizontal front porch	Thf	22	168	250	dclk	
Data setup time	Tds	8	-		ns	
Data hold time	Tdh	8	- ,	-	ns	

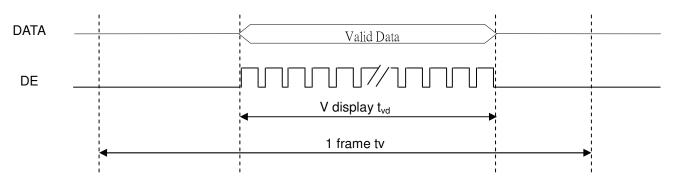


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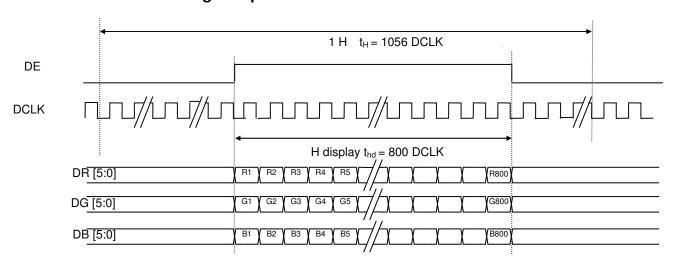
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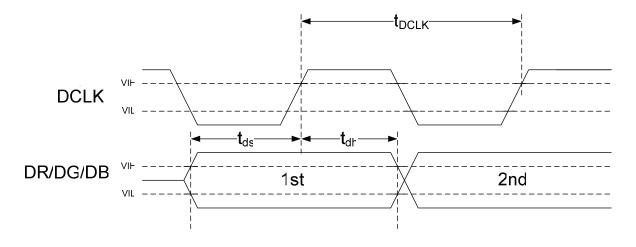
c. Timing Diagram (DE Mode) Vertical Timing of Input



Horizontal Timing of Input



Clock and Data Timing of Input

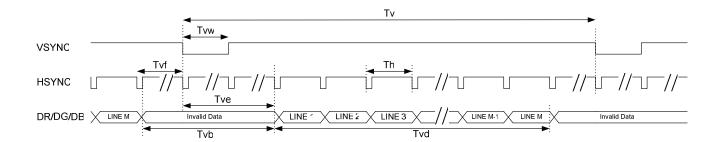




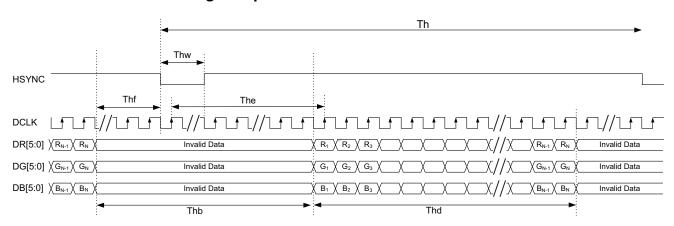
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d. Timing Diagram (HV Mode)

Vertical Timing of Input



Horizontal Timing of Input





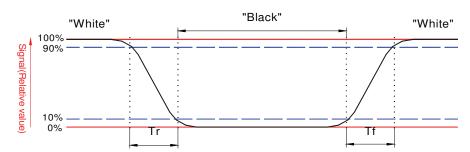
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F. Optical specifications (Note 1, 2)

	- · · · · · · · · · · · · · · · · · · ·							
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	
Response Time Rise Fall	Tr Tf	θ =0°		10 25	15 30	ms ms	Note 3	
Contrast ratio	CR	At optimized viewing angle	800	1500	-		Note 4, 5, 6	
Viewing Angle Top Bottom Left Right		CR≧10	75 75 75 75	80 80 80 80	- - -	deg.	Note 7, 8	
Brightness	YL	θ = 0 °	390	450	-	cd/m ²	Note 9	
White Chromaticity	Х	θ = 0 °	0.268	0.318	0.368		Note 8	
	Υ	θ =0°	0.273	0.323	0.373			
Uniformity		-	80	-	-	%	Note 10	

- Note 1: Measurement should be performed in the dark room, optical ambient temperature =25 $^{\circ}$ C, and backlight current I_L =80 mA
- Note 2: To be measured on the center area of panel with a field angle of 1 by Topcon luminance meter BM-7, after 10 minutes operation.
- Note 3: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (Tr, Rising time) and from "white" to "black" (Tf, Falling time), respectively.



Note 4. From liquid crystal characteristics, response time will become slower and the color of panel will become darker when ambient temperature is below 25°C.

 $Contrastratio = \frac{Photo \ detector \ output \ when \ LCD \ is \ at \ "White" \ state}{Photo \ detector \ output \ when \ LCD \ is \ at \ "Black" \ state}$

Note 5. Contrast ratio is calculated with the following formula.

Note 6. White Vi=Vi50 + 1.5V Black Vi=Vi50 ± 2.0V

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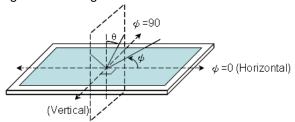
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- "±" means that the analog input signal swings in phase with COM signal.
- "7" means that the analog input signal swings out of phase with COM signal.

Vi50 :The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

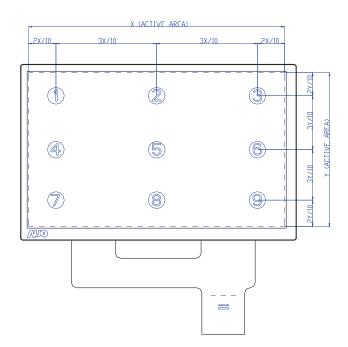
Note 7. Definition of viewing angle: refer to figure as below.



Note 8. The viewing angles are measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 9. Brightness is measured at the center of the display with white pattern in 80mA.

Note 10. Luminance Uniformity is defined as following within the 9 measurements (L1~L9), Luminance Uniformity(%) =Minimum luminance(brightness)/Maximum luminance(brightness)





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G. Reliability Test Items (Note 2)

No.	Test items	Condition	Remark	
1	High temperature storage	Ta= 95°C	240Hrs	
2	Low temperature storage	Ta= -40°C	240Hrs	Note1
3	High temperature operation	Ta= 85°C	240Hrs	
4	Low temperature operation	Ta= -30°C	240Hrs	Note1, 3
5	High temperature and high humidity	Ta= 60°C , 90% RH	240Hrs	Operation
6	Heat shock	-30°C ~85°C /100 cycle	Non-operation	
7	Electrostatic discharge	Contact = \pm 8 kV, class B (Air = \pm 15 kV, class B (Reported in the content of the content	Operation	
8		Frequency range	8~33.3Hz	JIS D1601,A10 Condition A
		Stoke	1.3mm	
	Vibration	Sweep	2.9G, 33.3~400Hz	
		Cycle	15min.	
		2 hours for each direct 4 hours for Y direct to the control of the		
9	Mechanical shock	100G, 6ms, ±X, 3 times for each o		
10	Random vibration: Vibration (with carton) 0.015G²/Hz from 5~200Hz -6dB/Octave from 200~500Hz			IEC 68-34
11	Drop (with carton)	Height: 60c 1 corner, 3 edges, 6		

Note 1: Ta: Ambient temperature.

Note 2: In the standard condition, there is not display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.

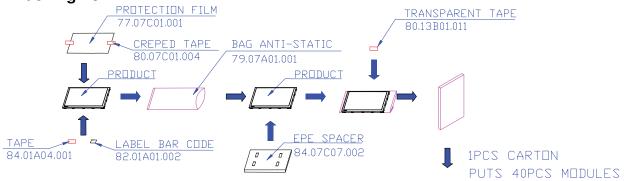
Note 3: Short time operation between -40 $^{\circ}$ C $^{\sim}$ -30 $^{\circ}$ C doesn't provide full performance but a correct image on the LCD. The LCD is guaranteed to suffer no permanent damage.

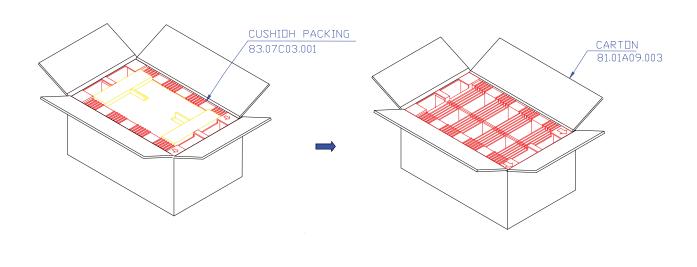


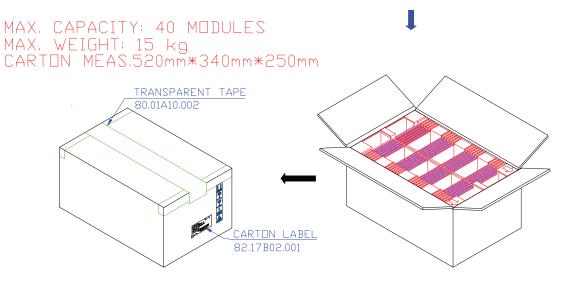
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H. Packing Form and Marking

1. Packing Form









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2. Module/Panel Label Information

The module/panel (collectively called as the "Product") will be attached with a label of Shipping Number which represents the identification of the Product at a specific location. Refer to the Product outline drawing for detailed location and size of the label. The label is composed of a 22-digit serial number with the following definition:

ABCDEFGHIJKLMNOPQRSTUV

For internal system usage and production serial numbers.

LAUO Module or Panel factory code, represents the final production factory to complete the Product Product version code, ranging from 0~9 or A~Z (for Version after 9)

-Week Code, the production week when the product is finished at its production process

Example:

501M06ZL06123456781Z05:

Product Manufacturing Week Code: WK50

Product Version: Version 1

Product Manufacturing Factory: L3A (The manufacturing site at Taiwan)

Note:

M06: The manufacturing site in Taiwan
 S16: The manufacturing site in Xiamen

3. Carton Label Information

The packing carton will be attached with a carton label where packing Q'ty, AUO Model Name, AUO Part Number, Customer Part Number (Optional) and a series of Carton Number in 13 or 14 digits are printed. The Carton Number is appearing in the following format:

ABC-DEFG-HIJK-LMN

DEFG appear after first "-" represents the packing date of the carton

L Month, ranging from 1∼9, A∼C. A for Oct, B for Nov and C for Dec.

► A.D. year, ranging from 1~9 and 0. The single digit code reprents the last number of the year

Refer to the drawing of packing format for the location and size of the carton label.