# Toshiba Matsushita Display Technology

26.4cm COLOUR TFT-LCD MODULE (10.4 TYPE)

LTD104KA1S (p-Si TFT)

PRODUCT INFORMATION

#### **FEATURES**

- (1) 10.4 XGA display size
- (2) LVDS interface with 6bit color data
- (3) Light weight(195g Typical) and Thinner design(6.4mm Max.)
- (4) Digitizer easy insertion design
- (5) Mechanical Compatible with LTM10C321K/T Series

# **TENTATIVE**

### **MECHANICAL SPECIFICATIONS**

Item	Specifications
Dimensional Outline (typ.)	238.6(W) x 173.2(H) x 6.4max(D) mm
Number of Pixels	1024(W) x 768(H) pixels
Active Area	210.432(W) x 157.824(H) mm
Pixel Pitch	0.2055(W) x 0.2055(H)
Weight (approximately)	(195g)
Backlight	CCFL, Sidelight type

#### **ABSOLUTE MAXIMUM RATINGS**

Item		Min.	Max.	Unit
Supply Voltage	$(V_{DD})$	-0.3	4.0	V
	$(V_{FL})$	0	2.0	kV(rms)
FL Driving Frequ	ency (f <sub>FL</sub> )	-	100	kHz
Input Signal Volta	age (V <sub>IN</sub> )	-0.3	V <sub>DD</sub> +0.3	V
Operating Ambie	nt Temperature	0	50	°C
Storage Tempera	ature	-20 60		°C
Storage Humidity	/	10	90	%RH

# **ELECTRICAL SPECIFICATION**

Item	Min.	Тур.	Max.	Unit	Remarks	
Supply Voltage	upply Voltage (V <sub>DD</sub> )		3.3	3.6	V	
	(V <sub>FL</sub> )	540	590	640	V(rms)	$I_{FL}$ =(5.0)mA(rms)
FL Start Voltage (Ta=0°C)	1200		1600	V(rms)		
Differntial Input High Threshol	Differntial Input High Threshould (V <sub>TH</sub> )			V <sub>OS</sub> +0.1	V	
Differntial Input Low Threshou	ıld (V <sub>TL</sub> )	V <sub>os</sub> -0.1			V	
Current Consumption	Current Consumption *1 (I <sub>DD</sub> )		210	360	mA	
*3 (I <sub>FL</sub> )		3.0	5.0	6.0	mA(rms)	
*2 *3 Power Consumption		(3.68)		W	@170 cd/m <sup>2</sup>	

<sup>\*2 : 8</sup> color bars pattern

# **OPTICAL SPECIFICATION** (Ta=25°C)

Item		Min.	Typ.	Max.	Unit	Remarks
Contrast Ratio (C	R)	100	250			
Viewing Angle	(Upper+Lower)		50		deg.	
(CR>=10)	(Left+Right)		90		deg.	
Luminance			(170)		cd/m <sup>2</sup>	$I_{FL}$ =(5.0)mA(rms)

<sup>\*3 :</sup> Excepting the efficiency FL inverter

<sup>\*</sup>The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by Toshiba Matsushita Display technology Co.,Ltd or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Toshiba Matsushita Display technology Co.,Ltd or others.

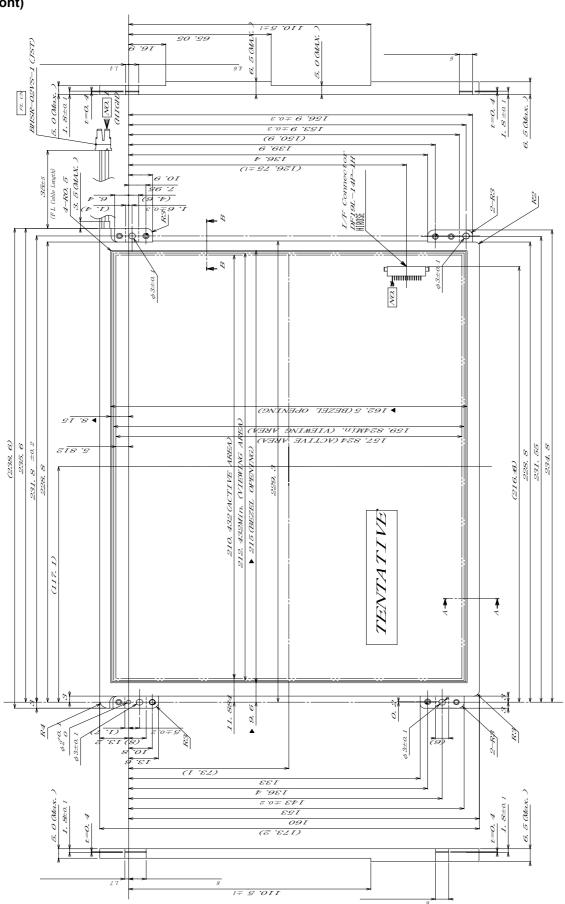
<sup>\*</sup>The information contained herein may be changed without prior notice. It is therefore advisable to contact Toshiba Matsushita Display technology Co.,Ltd before proceeding with the design of equipment incorporating this product.

DIMENSIONAL OUTLINE (Front)

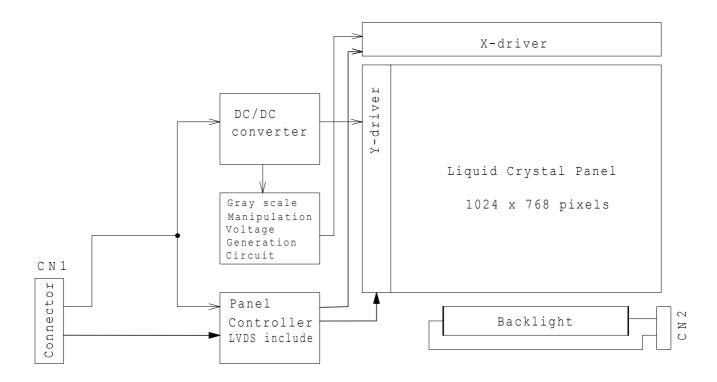
TENTATIVE

Unit : mm

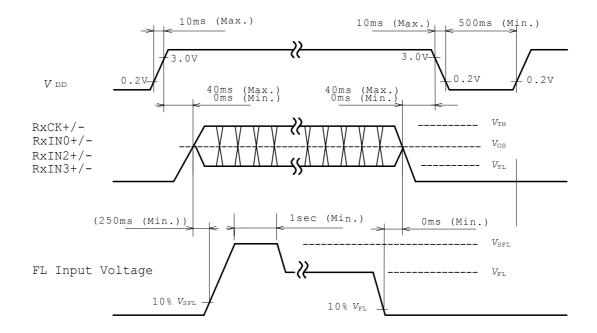
Standard tolerance :  $\pm 0.5$ 



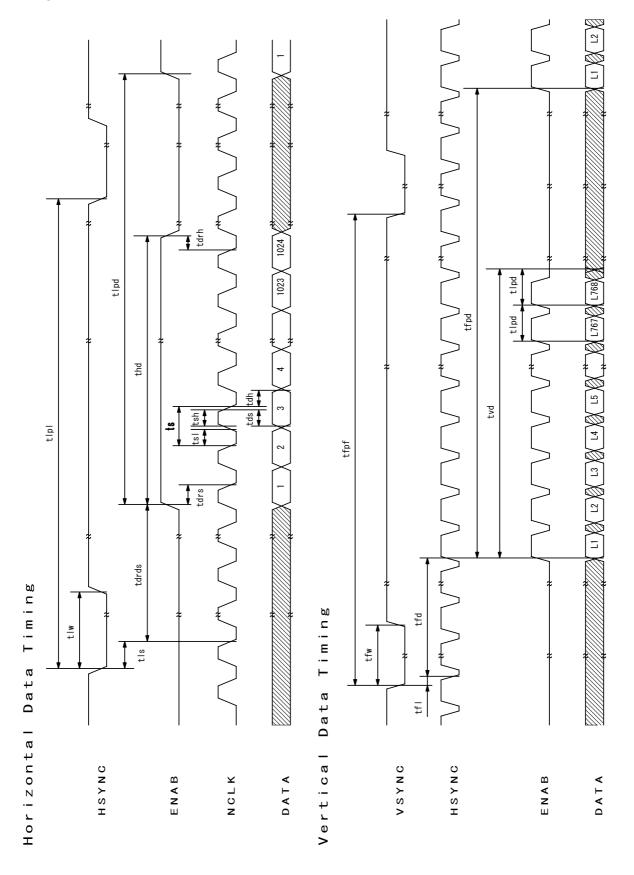
# **BLOCK DIAGRAM**



# **Sequence of Power Supplies and Signals**



# **TIMING CHART**



# TIMING SPECIFICATION 1) 2) 3) 4) 5) 6)

Signal	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
NCLK	Clock Period	ts	15	15.38	-	ns	
	Frequency	1/ <i>ts</i>	-	65	66.6	MHz	
	High Time	tsh	6	-	-	ns	5)
	Low Time	tsl	7	-	-	ns	5)
HSYNC	Setup to NCLK	tls	7	-	-	ns	5)
	Pulse Width	tlw	8 x ts	-	-	-	
VSYNC	Pulse Width	tfw	3 x tlpd	-	7 x tlpd	-	
	VSYNC to DATA	tfd	7 x tlpd	-	-	-	
	Setup to HSYNC	tfl	16	-	-	ns	5)
	Line Period	tlpd=tlpl	1319 x ts	1344 x ts	1462 x ts	-	
			20.04	20.68		μS	
	Horizontal Display Time	thd	1024 x ts	1024 x ts	1024 x ts	-	
	Frame Frequency	1/tfpd	58	60	1	Hz	
	Frame Period	tfpd=tfpf	778 x tlpd	806 x <i>tlpd</i>	860 x tlpd	-	
	Vertical Display Time	tvd	768 x tlpd	768 x tlpd	768 x tlpd	-	
DATA	Setup	tds	5	-	-	ns	5)
	Hold	tdh	7	-	-	ns	5)
ENAB	Setup	tdrs	10	-	-	ns	5)
	Hold	tdrh	10	-	-	ns	5)
	Display Start	tdrds	-	-	400 x ts	-	

- Note 1) Refer to TIMING CHART and LVDS (DF90CF364) specifications by National Semiconductor.
- Note 2) If NCLK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.
- Note 3) If Hsync or Vsync or ENAB is Fixed to "H" or "L" level for certain period while NCLK is supplied, the panel became display shifted.
- Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality.

  There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving

  Condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating conditions shown in 3.
- Note 5) Do not hold NCLK on "H" level nor "L" level during VDD(+3.3V) Is supplied. When it holds on, DC voltage supplies to liquid crystal materials and it may cause damage to liquid crystal materials.
- Note 6) In case of using the long frame period, the deterioration of
- Note 7) These values are determined by the LCD module.

# **CONNECTOR PIN ASSIGNMENT FOR INTERFACE**

# CN1 INPUT SIGNAL

Using Connector: DF19L-14P-1H / HIROSE ELECTRIC CO.,LTD.

Mating Connector(Corresponding Connector)

: DF19G-14S-1C / HIROSE ELECTRIC CO.,LTD.

Terminal No.	Symbol	Function
1	$V_{DD}$	Power Supply: +3.3V
2	$V_{ m DD}$	Power Supply: +3.3V
3	GND <sup>1)</sup>	
4	GND <sup>1)</sup>	
5	INO-	Transmission Data of Pixels(Differential data) 0 (Negative : - )
6	IN0+	Transmission Data of Pixels(Differential data) 0 (Positive: +)
7	IN1-	Transmission Data of Pixels(Differential data) 1 (Negative : - )
8	IN1+	Transmission Data of Pixels(Differential data) 1 (Positive: +)
9	IN2-	Transmission Data of Pixels(Differential data) 2 (Negative : - )
10	IN2+	Transmission Data of Pixels(Differential data) 2 (Positive: +)
11	CLK-	Sampling Clock (Negative : - )
12	CLK+	Sampling Clock (Positive : + )
13	GND <sup>1)</sup>	
14	GND <sup>1)</sup>	

# CN2 CCFL POWER SOURCE

Using Connector : BHSR-02VS-1 / Japan SOLDERLESS TERMINAL MFG CO.,LTD

Mating Connector(Corresponding Connector)

: SM02B-BHSS-1 / Japan SOLDERLESS TERMINAL MFG CO.,LTD

Terminal No.	Symbol	Function
1	<b>V</b> FLH	CCFL Power Supply ( high voltage)
2	VFLL	CCFL Power Supply (low voltage)

Note 1) Please connect NC pin to nothing. Don't connect it to grand nor to other signal input.

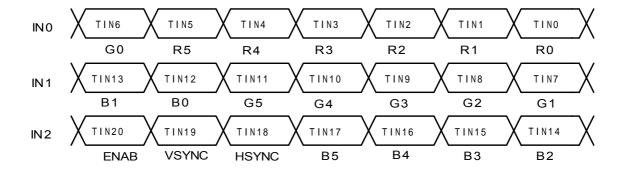
Please connect GND to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) 262,144 colors are displayed by the combinations of 18 bits data. (See next page)

# RECOMMENDED TRANSMITTER (DS90CF363) TO LTD104KA1S INTERFACE ASSIGNMENT

# 6bit Transmitter

DS90CF363					LTD104KA1S			
Input To	rminal No		Input Signal	Output	Inte	rface		
iliput le	Input Terminal No.		(Graphics controller output signal)	Signal	Signal (CN1)			
Symbol	Terminal	Symbol	Function	Symbol	Terminal	Symbol		
TIN0	44	R0	Red Pixels Display Data (LSB)					
TIN1	45	R1	Red Pixels Display Data					
TIN2	47 R2		Red Pixels Display Data	TOUT0-	No.5	INO-		
TIN3	48	R3	Red Pixels Display Data	TOUT0+	No.6	IN0-		
TIN4	1	R4	Red Pixels Display Data	10010+	140.0	IINOT		
TIN5	3	R5	Red Pixels Display Data (MSB)					
TIN6	4	G0	Green Pixels Display Data (LSB)					
TIN7	6	G1	Green Pixels Display Data			IN1- IN1+		
TIN8	7	G2	Green Pixels Display Data		No.7 No.8			
TIN9	9	G3	Green Pixels Display Data	TOUT1-				
TIN10	10	G4	Green Pixels Display Data	TOUT1+				
TIN11	12	G5	Green Pixels Display Data (MSB)	10011+		IIN I T		
TIN12	13	B0	Blue Pixels Display Data (LSB)					
TIN13	15	B1	Blue Pixels Display Data					
TIN14	16	B2	Blue Pixels Display Data					
TIN15	18	B3	Blue Pixels Display Data					
TIN16	19	B4	Blue Pixels Display Data	TOUT2-	No.9	IN2-		
TIN17	20	B5	Blue Pixels Display Data (MSB)	TOUT2+	No.10	IN2-		
TIN18	22	HSYNC	H-Sync	10012+	140.10	IINZŦ		
TIN19	23	VSYNC	V-Sync					
TIN20	25	ENAB	Compound Synchronization Signal					
CLK IN	26	CLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.11 No.12	CLK- CLK+		



# **Colors Combination Table**

	Display	R5 R4 R3 R2 R1 R0	G5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0	Gray ScaleLevel	
	Black			-	
	Blue		LLLLLHHHHHH	-	
	Green		H H H H H H L L L L L L	-	
Basic	Light Blue		<b>H H H H H H H H H H</b>	-	
Color	Red	H H H H H		-	
	Purple	H H H H H		-	
	Yellow	H H H H H	H H H H H H L L L L L L	-	
	White	H H H H H H	<b>нинини нини</b>	-	
	Black			L 0	
		LLLLLH		L 1	
Gray	Dark	LLLLHL		L 2	
Scale of	<b>↑</b>	:	: :	L3	
Red	$\downarrow$	:	: : :	L60	
rtcu	Light	H $H$ $H$ $H$ $L$ $H$		L61	
		H $H$ $H$ $H$ $L$		L62	
	Red	H H H H H		Red L63	
	Black			L 0	
				L 1	
Gray	Dark	LLLLL		L 2	
Scale of	<u> </u>	:	: :	L3 L60	
Green	↓	:	: : :		
0.00	Light		H H H H L H L L L L L	L61	
			H H H H H L L L L L L	L62	
	Green		H H H H H H L L L L L L	Green L63	
	Black			L 0	
				L 1	
Gray	Dark			L 2	
Scale of	<u> </u>	:	: :	L3 L60	
Blue	↓.	:	: :		
	Light			L61	
				L62	
	Blue			Blue L63	
	Black			L 0	
	Darde			L 1 L 2	
Gray	Dark			L3	
Scale of	<u> </u>			L3 L60	
White &	↓ 	· · · · · · · · · · · · · · · · · · ·			
Black	Light	H H H H L H		L61	
	\ \ \ /  - : 4 -	<u> </u>	H	L62 White L63	
	White			WIIILE LOS	

Note1 L: Low level voltage, H: High level voltage



LCD module is generally designed with precise parts to achieve light weighted thin mechanical dimensions.

In using our Modules, make certain that you fully understand and put into practice the warnings and safety precautions detailed in Engineering Information No.EE-N001,"CAUTIONS AND INSTRUCTIONS FOR TOSHIBA MATSUSHITA DISPLAY TECHNOLOGY CO.,LTD LCD MODULES".

Refer to individual specifications and TECHNICAL DATA sheets (hereinafter called "TD") for more detailed technical information.

#### 1) SPECIAL PURPOSES

- A) Toshiba Matsushita Display technology Co.,Ltd's Standard LCD Modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
- B) Since Toshiba Matsushita Display technology Co.,Ltd's Standard LCD Modules have not been designed for operation in extreme environments, they must never be used in devices that will be exposed to abnormally high levels of vibration or shock which exceed Toshiba Matsushita Display technology Co.,Ltd's published specification limits.
- C) In addition, since Toshiba Matsushita Display technology Co.,Ltd Standard LCD Modules have not been designed for use in applications where performance failures could be life-threatening or catastrophic, they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision Avoidance System and Air Traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.

## 2) DISASSEMBLING OR MODIFICATION

DO NOT DISASSEMBLE OR MODIFY the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display.

Toshiba Matsushita Display technology Co., Ltd doses not warrant the module, if customer disassembled or modified it.

## 3) BREAKAGE OF LCD PANEL

DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT CONTACT the material with skin, if LCD panel is broken and liquid crystal material spills out.

If liquid crystal material comes into mouth or eyes, rinse mouth or eyes out with water immediately.

If this material contact with skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

## 4) GLASS OF LCD PANEL

BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.

#### 5) ELECTRIC SHOCK

DISCONNECT POWER SUPPLY before handling LCD module.

DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's connector or cables in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.

#### 6) ABSOLUTE MAXIMUM RATINGS AND POWER PROTECTION CIRCUIT

DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, environmental temperature, etc., otherwise LCD module may be damaged.

Employ protection circuit for power supply, whenever the specification or TD specifies it.

Suitable protection circuit should be applied for each system design.

# 7) DISPOSAL

When dispose LCD module, obey to the applicable environmental regulations.