

# ( ) Preliminary Specifications(V ) Final Specifications

Module	12.1 Inch Color TFT-LCD panel
Model Name	G121EAN01.2

Customer Date	Approved by Date
Checked & Approved by	Prepared by
	Ken Wang
	General Display Business Division / AU Optronics corporation



# **Contents**

1. Operating Precautions	4
2. General Description	5
2.1 Display Characteristics	5
2.3 Optical Characteristics	6
3. Functional Block Diagram	9
4. Absolute Maximum Ratings	10
4.1 Absolute Ratings of TFT LCD Module	10
4.2 Absolute Ratings of Environment	10
5. Electrical Characteristics	11
5.1 TFT LCD Module	11
5.2 Backlight Unit	13
6. Signal Characteristics	14
6.1 Pixel Format Image	14
6.2 Signal Description	15
6.3 The Input Data Format	16
6.4 TFT-LCD Interface Timing	17
6.5 Power ON/OFF Sequence	18
7. Reliability Test Criteria	19
8. Mechanical Characteristics	20
8.1 LCM Outline Dimension	20
9. Label and Packaging	21
9.1 Shipping Label (on the rear side of TFT-LCD display)	21
9.2 Carton Package	21
TBD10 Safety	21
10 Safety	22
10.1 Sharp Edge Requirements	22
10.2 Materials	22
10.3 Capacitors	22
10.4 National Test Lab Requirement	22



1 70	cora or iv	01101	OII .												
Versi	on and Date	Page	Page Old description New Description							cription New Description					
0.0	2017/05/02	All	First Edition												
0.1	2018/02/12	11	Update Remark Wording	Fre	om Blac	ck pa	itter	n to	Whi	te p	attern				
				Symbol	Paramete	er-	Min.₽	Typ.∂	Max.₽	Unit-	Remark-				
				VI FDe	Input Volta	age/	10₽	120	15₽	[Volt]₽					
				lvcc.	Input Curre	ente	٥	0.633₽	-0	[A] <i>₽</i>	VCC-12V & 100% PWM Duty				
				Pycce	Power Consur	mption@	۰	7.60	8.8₽	[Watt]₽	VCC=12V & 100% PWM Dutye				
					_	BL On∂	2.5₽	-4	40	[Volt]₽	e e				
		13	Modify the F.2.1 Decemeter guideline for LED table	V <sub>EN+</sub> /		DL Offe	0.0	-0	0.5₽	[Volt]	12				
		13	Modify the 5.2.1 Parameter guideline for LED table	F <sub>PWM</sub> ₽	Dimming Freq	quency@	200₽	-47	20K₽	[Hz]₽	<i>→</i>				
				Dewm∞	Dimming Duty	/ Cycle₽	10	-0	100€	160	1~5% with non-linea				
			V power!	Dimming H	ligh Level⊬	3+-		5.5⊬	[Volt]						
				Control Level	Low Lovele	0.0	0	0.50	[Volt]₽						
				lee-	LED Forward (	Current₽	-47	61e_<	- 4	[mA]+	Ta = 25°Ce				
				LED life time	0 0		50,000+	~	×	Hrs⊬	IF = 61 mA, Ta = 25				
0.2	2018/02/21	11	LCD Input Current : 458 mA		LCD I	nput	Cui	rrent	: 50	10 m	nA				



#### 1. Operating Precautions

- 1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
- 5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
- 6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
- 7) Do not open nor modify the module assembly.
- 8) Do not press the reflector sheet at the back of the module to any direction.
- 9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
- 10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
- 11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
- 12) Small amount of materials without flammability grade are used in the TFT-LCD module. The TFT-LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
- 13) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
- 14) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
- 15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
- 16) Continuous displaying fixed pattern may induce image sticking. It's recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.



#### 2. General Description

This specification applies to the Color Active Matrix Liquid Crystal Display G121EAN01.2 composed of a TFT-LCD display, a driver and power supply circuit, and a LED backlight system.

The screen format is intended to support WXGA (1280(H) x 800(V)) screen and 16.7M (8-bits) or 262k colors (6-bits). LED driving board for backlight unit is included in G121EAN01.2. G121EAN01.2 designed with wide viewing angle; wide temperature and long life LED backlight is well suited for industial applications. G121EAN01.2 is a RoHS product.

#### 2.1 Display Characteristics

Items	Unit	Specifications
Screen Diagonal	[inch]	12.1
Active Area	[mm]	261.12(H) x 163.2(V)
Pixels H x V		1280 x 3(RGB) x 800
Pixel Pitch	[mm]	0.204 x 0.204
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Normally Black
Nominal Input Voltage VDD	[Volt]	3.3 (typ.)
Typical Power Consumption	[Watt]	9.25 W @ White pattern
Weight	[Grams]	390
Physical Size	[mm]	278.0(H) x 184.0(V) x 9.4(D) (Typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		Anti-glare, Hardness 3H
Support Color		16.7M / 262K colors
Temperature Range Operating Storage (Non-Operating)	[°C] [°C]	-30 to +85 -30 to +85
RoHS Compliance		RoHS Compliance



# 2.3 Optical Characteristics

The optical characteristics are measured under stable conditions at 25°C (Room Temperature):

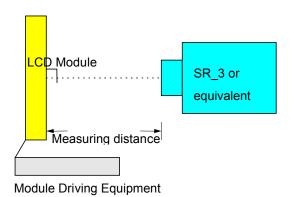
Item	Unit	Condition	s	Min.	Тур.	Max.	Remark	
White Luminance	[cd/m2]	ILED= 61mA (center point)		850	1000	-	Note 1	
Uniformity	%	5 Points		75	-	-	Note 2, 3	
Contrast Ratio				800	1000	-	Note 4	
	[msec]	Rising		-	-	-		
Response Time	[msec]	Falling		-	-	-	Note 5	
	[msec]	Raising + Falling	3	-	25	-		
	[degree]		ight)	80	89	-		
Viewing Angle	[degree]	CR = 10 (L	CR = 10 (Left)		89	-	Note 6	
Viewing Angle	[degree]	Vertical (Upper) CR = 10 (Lower)		80	89	-		
	[degree]			80	89	-		
		Red x		0.581	0.631	0.681		
		Red y		0.279	0.329	0.379		
		Green x		0.249	0.299	0.349		
Color / Chromaticity Coordinates		Green y		0.569	0.619	0.669		
(CIE 1931)		Blue x		0.105	0.155	0.205		
		Blue y		0.004	0.054	0.104		
		White x		0.263	0.313	0.363		
		White y		0.279	0.329	0.379		
Color Gamut	%				72	-		

Note 1: Measurement method : (before touch panel)

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR\_3 or equivalent)

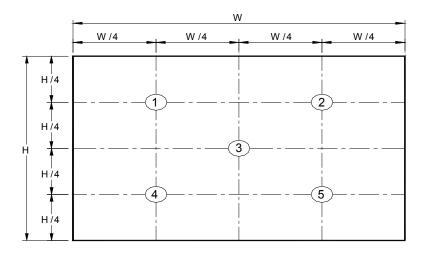
Aperture 1° with 50cm viewing distance

Test Point Center
Environment < 1 lux





Note 2: Definition of 5 points position (Display active area: 261.12mm (H) x 163.2mm (V))



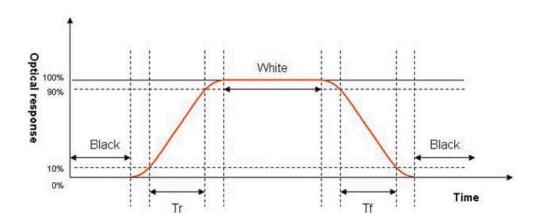
Note 3: The luminance uniformity of 5 points is defined by dividing the minimum luminance values by the maximum test point luminance

$$\delta_{\text{W9}} = \frac{\text{Minimum Brightness of five points}}{\text{Maximum Brightness of five points}}$$

Note 4: Definition of contrast ratio (CR):

Note 5: Definition of response time:

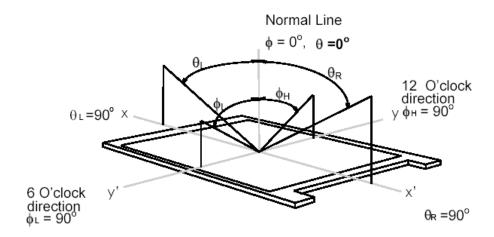
The output signals of photo detector are measured when the input signals are changed from "White" to "Black" (falling time) and from "Black" to "White" (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.





#### Note 6: Definition of viewing angle

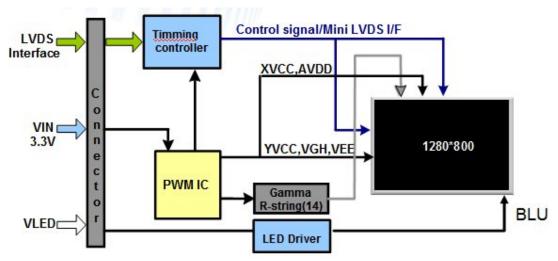
Viewing angle is the measurement of contrast ratio ≥ 10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90°  $(\theta)$  horizontal left and right, and 90°  $(\Phi)$  vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.





# 3. Functional Block Diagram

The following diagram shows the functional block of the 12.1 inch color TFT/LCD module:





# 4. Absolute Maximum Ratings

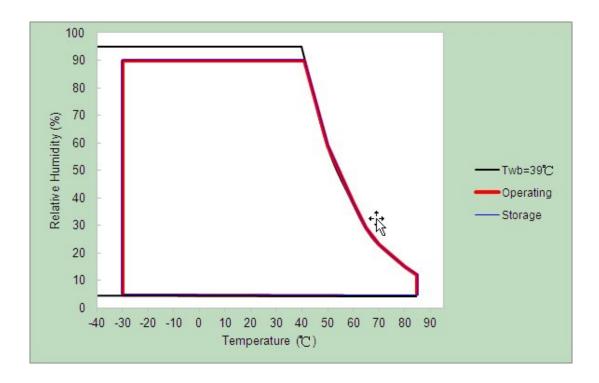
# 4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit
Logic/LCD Drive Voltage	VDD	-0.3	+3.8	[Volt]
Contorl Signals	SEL68	-0.3	VDD	[Volt]
LVDS Signals	CLKIN -/+, RIN0 ~ 3-/+	-0.3	0.6	[Volt]
LED Input Voltage	VLED	-0.3	15	[Volt]
LED Control Signals	EN, PWM	-0.3	+6	[Volt]

# **4.2 Absolute Ratings of Environment**

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	5	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	90	[%RH]

Note: Maximum Wet-Bulb should be 39°C and no condensation.





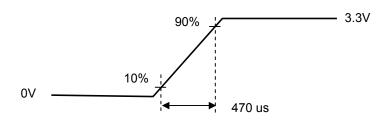
### 5. Electrical Characteristics

### **5.1 TFT LCD Module**

5.1.1 LCD Electrical Specification

Symbol	Parameter		Min	Тур	Max	Units	Remark
VDD	Logic/LCD Input Voltage		3.0	3.3	3.6	[Volt]	
I <sub>VDD</sub>	LCD Input Cu	urrent	ı	ı	500	[mA]	VDD=3.3V at 60 HZ, all White Pattern
P <sub>VDD</sub>	LCD Power com	sumption	ı	ı	1.65	[Watt]	VDD=3.3V at 60 HZ, all White Pattern
I <sub>rush LCD</sub>	LCD Inrush C	urrent	ı	ı	1.5	[A]	VDD=3.3V at 60 HZ, all White Pattern Note 1 VDD rising time ≥470us
VDD <sub>rp</sub>	Allowable Logic/LCD Drive Ripple Voltage		-	-	100	[mV] p-p	VDD=3.3V at 60 HZ, all White Pattern
VIH	High Control Signals		0.7* VDD	-	VDD	[Volt]	SEL68
VIL	Voltage	Low Level	0	-	0.3* VDD	[Volt]	SEL68

Note 1: Measurement condition:



VDD rising time



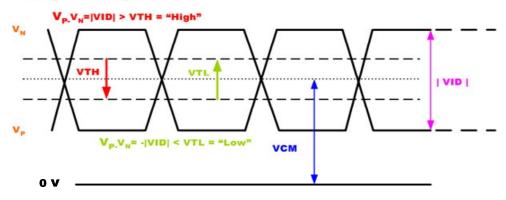
#### **5.1.2 LVDS Electrical Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Unit	Remark
VTH	Differential Input High Threshold	-	-	100	[mV]	VCM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V
VID	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.1	-	1.45	[V]	VTH/VTL=+-100mV

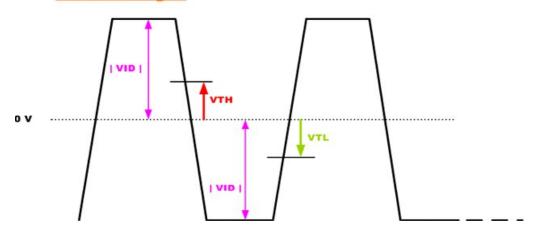
Input signals shall be low or Hi-Z state when VDD is off.

Note: LVDS Signal Waveform.

# Single-end Signal



#### Differential Signal





#### 5.2.1 Parameter guideline for LED

Following characteristics are measured under stable condition at 25 °C . (Room Temperature):

Symbol	Parameter		Min.	Тур.	Max.	Unit	Remark
VLED	Input Vo	oltage	10	12	15	[Volt]	
I <sub>vcc</sub>	Input Co	urrent		0.633	-	[A]	VCC=12V & 100% PWM Duty
P <sub>vcc</sub>	Power Con	sumption		7.6	8.8	[Watt]	VCC=12V & 100% PWM Duty
	EN Control	BL On	2.5	-	4	[Volt]	
V <sub>EN</sub>	Level	BL Off	0	-	0.5	[Volt]	
F <sub>PWM</sub>	Dimming F	requency	200	-	20K	[Hz]	
D <sub>PWM</sub>	Dimming D	uty Cycle	1	-	100	%	1~5% with non-linear
V	Dimming	High Level	3	-	5.5	[Volt]	
V <sub>PWM</sub>	Control Level	Low Level	0	-	0.5	[Volt]	
I <sub>F</sub>	LED Forward Current		ı	61		[mA]	Ta = 25°C
LED life time			50,000	70,000	-	Hrs	$I_F = 61 \text{ mA}, \text{ Ta} = 25^{\circ}\text{C}$

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: VCC, I<sub>VCC</sub>, P<sub>VCC</sub> are defined for LED backlight.(100% duty of PWM dimming)

Note 3: I<sub>F</sub> are defined for one channel LED. There are four LED channel in back light unit.

Note 4: If G121EAN01.2 module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

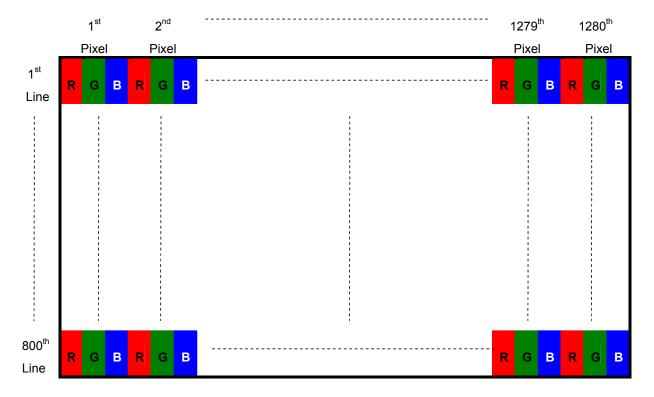
Note 5: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.



# 6. Signal Characteristics

# **6.1 Pixel Format Image**

Following figure shows the relationship between input signal and LCD pixel format.





**6.2 Signal Description**The module using a LVDS receiver embaded in AUO's ASIC. LVDS is a differential signal technology for LCD interface and a high-speed data transfer device.

#### 6.2.1 TFT LCD Module: LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	Starconn
Connector Model Number	093G30-B0001A-1

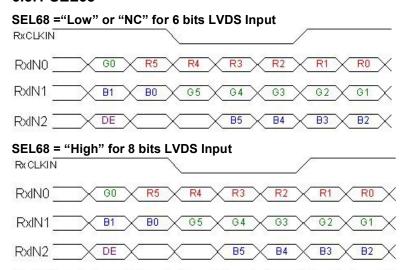
### 6.2.2 Input Signal Description

Pin NO	Signal Name	Description
1	VLED	LED Power Input
2	VLED	LED Power Input
3	VLED	LED Power Input
4	VLED	LED Power Input
5	EN	LED Driver Enable
6	PWM	LED Driver Backlight Adjust
7	GND	Ground
8	GND	Ground
9	VDD	Power supply:+3.3V
10	VDD	Power supply:+3.3V
11	GND	Ground
12	GND	Ground
13	RXin0N	-LVDS differential data (0N)
14	RXin0P	+LVDS differential data (0P)
15	GND	Ground
16	RXin1N	-LVDS differential data (1N)
17	RXin1P	+LVDS differential data (1P)
18	GND	Ground
19	RXin2N	-LVDS differential data (2N)
20	RXin2P	+LVDS differential data (2P)
21	GND	Ground
22	LVDS_RX_N	-LVDS differential clock input
23	LVDS_RX_P	+LVDS differential clock input
24	GND	Ground
25	RXin3N	-LVDS differential data (3N)
26	RXin3P	+LVDS differential data (3P)
27	GND	Ground
28	SEL 6/8	Low or NC>6 bit input mode High>8 bit input mode
29	GND	Ground
30	GND	Ground



### 6.3 The Input Data Format

#### 6.3.1 SEL68



В6

G7

G6

Note 1: Please follow PSWG.

RxIN3

Note 2: R/G/B data 7:MSB, R/G/B data 0:LSB

RSV

B7

Signal Name	Description	Remark
R7	Red Data 7	Red-pixel Data
R6	Red Data 6	
R5	Red Data 5	For 8Bits LVDS input
R4	Red Data 4	MSB: R7 ; LSB: R0
R3	Red Data 3	
R2	Red Data 2	For 6Bits LVDS input
R1	Red Data 1	MSB: R5 ; LSB: R0
R0	Red Data 0	
G7	Green Data 7	Green-pixel Data
G6	Green Data 6	
G5	Green Data 5	For 8Bits LVDS input
G4	Green Data 4	MSB: G7 ; LSB: G0
G3	Green Data 3	
G2	Green Data 2	For 6Bits LVDS input
G1	Green Data 1	MSB: G5 ; LSB: G0
G0	Green Data 0	
B7	Blue Data 7	Blue-pixel Data
B6	Blue Data 6	
B5	Blue Data 5	For 8Bits LVDS input
B4	Blue Data 4	MSB: B7 ; LSB: B0
B3	Blue Data 3	
B2	Blue Data 2	For 6Bits LVDS input
B1	Blue Data 1	MSB: B5 ; LSB: B0
B0	Blue Data 0	
RxCLKIN	LVDS Data Clock	The typical frequency is 40MHz.
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.
RSV	Reserved Signal	"High" or "Low" is acceptable

R7

R6

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

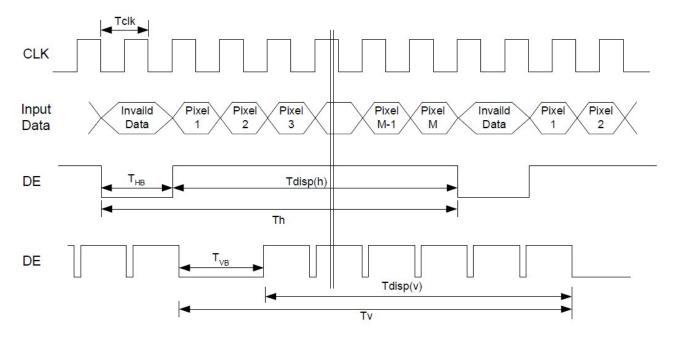


# **6.4 TFT-LCD Interface Timing**

#### **6.4.1 Timing Characteristics**

Signal	Parameter		Symbol	Min.	Тур.	Max.	Unit
Clock Timing	Clock fre	Clock frequency		60	74.4	90	MHz
Vsync Timing	Vertical	Period	T <sub>V</sub>	808	838	900	Vsync
	Section	Active	T <sub>VD</sub>	-	800	-	Timing
		Blanking	T <sub>VB</sub>	8	38	100	
Hsync Timing	Horizontal	Period	T <sub>H</sub>	1350	1480	1680	Hsync
	Section	Active	T <sub>HD</sub>	-	1280	-	Timing
		Blanking	T <sub>HB</sub>	70	200	400	
Frame Rate		F	50	60	75	Hz	

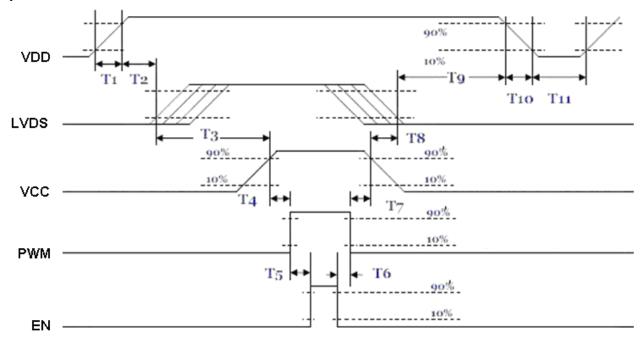
#### **6.4.2 Input Timing Diagram**





#### 6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



### Power ON/OFF sequence timing

Parameter	Min. Typ.		Max.	Units	
T1	0.5	-	10	[ms]	
T2	30	40	50	[ms]	
Т3	175	-	-	[ms]	
T4	10	-	-	[ms]	
T5	10	-	-	[ms]	
Т6	0	-	-	[ms]	
Т7	10	-	-	[ms]	
Т8	100	-	-	[ms]	
Т9	0	16	50	[ms]	
T10	-	-	10	[ms]	
T11	1000	-	-	[ms]	

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.



### 7. Reliability Test Criteria

Items	Required Condition	Note	
Temperature Humidity Bias	60 °C, 90%RH, 300 hours		
High Temperature Operation	85 °C, 300 hours		
Low Temperature Operation	-30 °C, 300 hours		
Hot Storage	85 °C, 300 hours		
Cold Storage	-30 °C, 300 hours		
Thermal Shock Test	-20 °C / 30 min, 60 °C / 30 min, 100cycles, 40 °C minimun ramp rate		
Hot Start Test	85 °C / 1Hr min. power on/off per 5 minutes, 5 times		
Cold Start Test	-30 °C / 1Hr min. power on/off per 5 minutes, 5 times		
Shock Test (Non-Operating)	g) 50G, 20ms, Half-sine wave, ( ±X, ±Y, ±Z)		
Vibration Test (Non-Operating)	1.5G, (10~200Hz, Sine wave)		
	30 mins/axis, 3 direction (X, Y, Z)		
On/off test	On/10 sec, Off/10 sec, 30,000 cycles		
	Contact = ± 8 kV, class B (R=330,C=150pF)		
ESD	Air = ± 15 kV, class B (R=330,C=150pF)	Note 1	
	1sec, 8 points, 25times/point		
ЕМІ	30-230 MHz, limit 40 dBu V/m, 230-1000 MHz, limit 47 dBu V/m		

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost Self-recoverable. No hardware failures.

#### Note2:

- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.

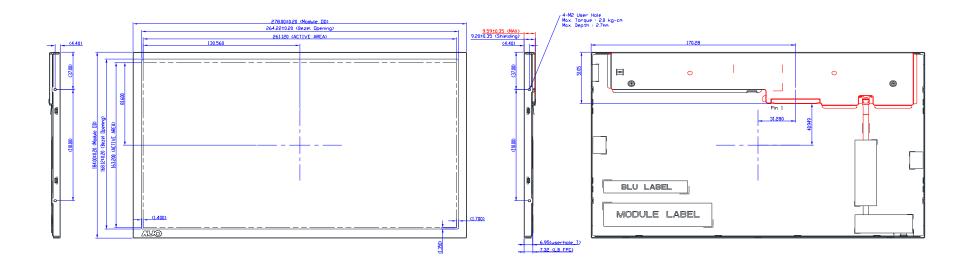
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.
- In the standard condition, there is not display function NG issue occurred.



#### 8. Mechanical Characteristics

#### **8.1 LCM Outline Dimension**

NOTES: 1.TOLERANCE IS ±0.5mm IF NOT SPECIFIED.

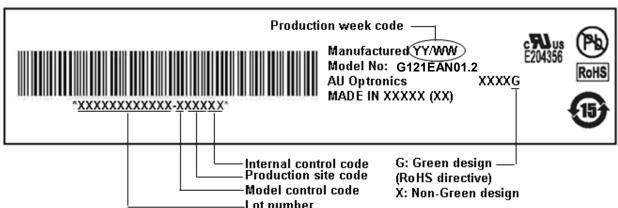


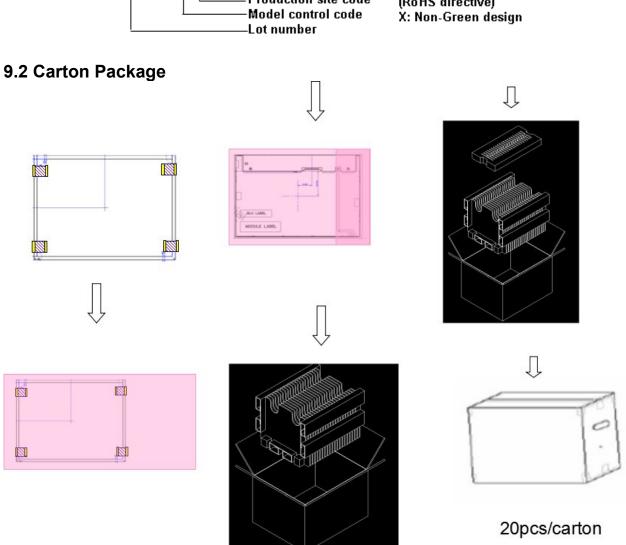




### 9. Label and Packaging

**9.1 Shipping Label** (on the rear side of TFT-LCD display)







#### 10.1 Sharp Edge Requirements

There will be no sharp edges or comers on the display assembly that could cause injury.

#### 10.2 Materials

#### 10.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO toxicologist.

#### 10.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

# 10.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

#### 10.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 1950, First Edition

U.S.A. Information Technology Equipment