

T215HVN01.2

AU OPTRONICS CORPORATION

(V)	Preliminary Specification
()	Final Specification

Module	21.5" Color TFT-LCD
Model Name	T215HVN01.2

	_
Customer Date	Approved by
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Note: This Specification is subject to change without notice.	AU Optronics cor

Date Aug 28, 2014 **Date** Aug 28, 2014 orporation



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Record of Revision

Version	Date	Page	Old description	New Description	Remark
0.1	2014/8/5	all			
0.2	2014/08/28	13	Pin 1 Pin 30	TIEN IN CONCUE	
0.2	2014/08/28	25	PIN 1	100 POON	
0.2	2014/08/28	30	(1) Avoid touching COF position when doing mechanical design (2) The user hold eight of the light bar side is different from the other side	A COLUMN STATE OF THE PARTY OF	
0.2	2014/08/28	31	N/A	THE DATE OF THE PARTY OF THE PA	
0.3	2014/10/01	27	4.3.1 Absolute Maximum Rating Permanent damage may occur if exceeding the following maximum rating. Symbol Description Min Max Unit Is LED String Current 0 TBD [mA] TBD TBD	4.3.1 Absolute Maximum Rating Permanent damage may occur if exceeding the following maximum rating. Symbol Description Min Max Unit	



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2014/10/01 27				4.3.2 Re	ecommen	ded Operatin	g Condition					4.3.2 Recommended Operating Condition																
0.3 2014/10/01 27	0.3			Symbo	I D	escription	Min.	Туј	p. I	Max.	Unit	Symbo	I D	escription	Min.	Ту	/p.	Max.	Unit									
Avs Maximum Vs Voltage Avs Avs Maximum Vs Voltage Avs Deviation of light bar Deviation of light bar Avs Deviation of light bar				Is	LED Str	ing Current	-	65	5	72	[mA]	Is	LED Str	ring Current	-	6	5	72	[mA]									
Deviation of light bar - - 4.5		2014/10/01	27	Vs	LED Str	ing Voltage	-	46	.5	51	[Volt]	Vs	LED Str	ring Voltage	-	46	5.5	51	[Volt]									
Psu Consumption - 12.1 14.7 [Watt] Psu Consumption - 12.1 13.3 [Watt]	i			ΔVs	Maximu	m Vs Voltage n of light bar	-	-		4.5	[Volt]	ΔVs			-	-	-	4.5	[Volt]									
1 1 1 1 1 1 1 1 1 1	ı			P _{BLU}			-	12	.1	14.7	[Watt]	Pau			-	12	2.1	13.3	[Watt]									
O.3 2014/10/01 34				8.2 Pal							8.2 Pa	llet and s	shipment in	formation	-													
Panel 1 495.6(F)mm × 292.2(V)mm × 10.6(D)mm Cushion 4			34	I .	tem			Speci	ficatio	1]	[tem			Speci	ificatio	on										
Cushion 4	0.3	2014/10/01						Dimension							Dimension													
Box	0.3						495.6(H)mm	495.6(H)mm × 292.2(V)mm × 10.6(D)mm			6(D)mm				495.6(H)mn	1 × 292.	.2(V)n	nm × 10	.6(D)mm									
Packing Box 13 pc/Box 556(L)mm x 345(W)mm x 375(H)mm Packing Box 13 pc/Box 557(L)mm x 345(W)mm x 375(H)mm				1				-							-	-												
Symbol Description Min. Typ. Max. Unit Limit Luminance (Center of screen) 200 250 - Lot/s2 Lumi Luminance (Uniformity (9 points) 75 80 - [%] Luminance Uniformity (9 points					_			17 17											• •									
Local Processing Contrast Ratio (Center of screen) 200 250 -				Pack	ing Box	13 pcs/Box	556(L)mm	x 345(W)mm	x 375	(H)mm	Pack	ing Box	13 pcs/Box	557(L)mn	1 x 345((W)mr	n x 375	(H)mm									
1.00				Symbol		Description	1	Min.	Тур.	Max.	Unit	Symbol		Description	ı	Min.	Тур.	Max.	Unit									
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0.3 2014/10/01 8 \$\begin{array}{c ccccccccccccccccccccccccccccccccccc				Luni	Lumin	ance Uniformity	(9 points)	75	80	-	[%]	Luni	Lumir	nance Uniformity	(9 points)	75	80	-	[%]									
0.3 2014/10/01 8													CR	Contra	st Ratio (Center	r of screen)	2000	3000	-	-	CR	Contra	ast Ratio (Center	of screen)	2000	3000	-	-
O. 3 20 14 / 10 / 0 1 8 O _L (CR=10) Left 75 89 . O _L (CR=10) Left 75 89 . O _L (CR=10) O _L O _L	0.0	0014/10/01		θ _R	Horizontal \	/iewing Angle	Right	75	89	-		θ _R	Horizontal	Viewing Angle	Right	75	89	-										
CRE-10 Down 75 89 - (GR=10 Down 75 89 - (GR=10 GR=10 GR	0.3	2014/10/01	8	-	(CF	R=10)	Left	75	89	-	1	θL	(C	R=10)	Left	75	89	-	1									
B _R Horizontal Viewing Angle Right 75 89 - B _R Horizontal Viewing Angle CR=5 Left TBD				Фн	Vertical V	ewing Angle			_	-	1					_	_	_]									
O, C(R=5) Left TBD					(CF	R=10)				-	[degree]	Ψt		R=10)		_	_	-	[degree]									
Фн Vertical Viewing Angle Up TBD TBD - Фн Vertical Viewing Angle Up 75 89 -					Horizontal \	/iewing Angle	-			-								-	1									
Vertical Viewing Angle Op 100 100 100 100 100 100 100 100 100 10				_		-				-	-			-		_	_	+	-									
								$\overline{}$		<u> </u>	-					_	-	H	1									



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1 Handling Precautions

- 1) Since front polarizer is easily damaged, pay attention not to scratch it.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
- 10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
- 11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
- 12) Please avoid touching COF position while you are doing mechanical design.
- 13) When storing modules as spares for a long time, the following precaution is necessary:

 Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 and 35 at normal humidity.



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2 General Description

This specification applies to the 21.5 inch wide Color a-Si TFT-LCD Module T215HVN01.2. The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (8 bits RGB data input). The input interface is Dual channel LVDS and this module doesn't contain an driver board for backlight.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	546.86 (21.53")
Active Area	[mm]	476.64 (H) x 268.11 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	248.25 (per one triad) ×248.25
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	VA Mode, Normally Black
White Luminance (Center)	[cd/m ²]	250 (Typ.)
Contrast Ratio	-	3000 (Typ.)
Response Time	[msec]	18ms (Typ., on/off)
Power Consumption	[Watt]	15.2 (Typ.)
(LCD Module + Backligh unit)		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
		LCD module : PDD (Typ.)= 3.1 @ all white pattern,Fv=60Hz
		Backlight unit : P _{BLU} (Typ.) =12.1 @Is=65mA
Weight	[Grams]	1670
Outline Dimension	[mm]	495.6(H) × 292.2(V) × 10.6(D) Typ.
Electrical Interface	-	Dual channel LVDS, 8 bits RGB data input
Support Color	-	16.7M colors
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 6.0 Compliance



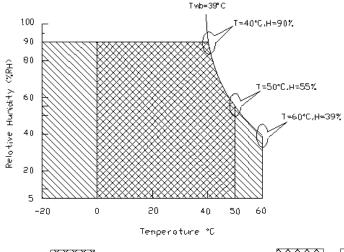
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[°C]	Note 2-1 Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

- 1.90% RH Max (Ta 39)
- 2. Max wet-bulb temperature at 39 or less. (Ta 39)
- 3. No condensation



Operating Range

 \otimes

Storage Range

+



2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

Test Condition:

1. Equipment setup: Please refer to Note 2-2.

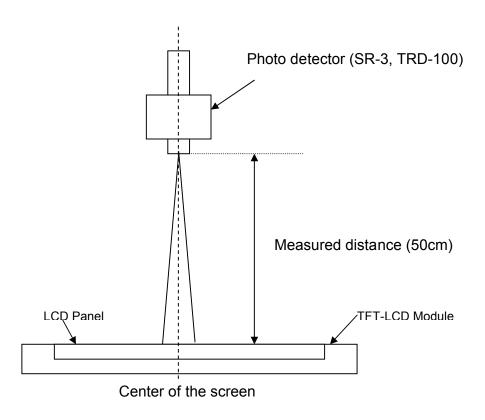
2. Panel Lighting time: 30 minutes

3. VDD=5.0V, Fv=60Hz,Is=65mA,Ta=25

Symbol	Descriptio	n	Min.	Тур.	Max.	Unit	Remark
L _w	White Luminance (Cen	200	250	-	[cd/m2]	Note 2-2 By SR-3	
L _{uni}	Luminance Uniformit	75	80	-	[%]	Note 2-3 By SR-3	
CR	Contrast Ratio (Cente	er of screen)	2000	3000	-	-	Note 2-4 By SR-3
θ_{R}	Horizontal Viewing Angle	Right	75	89	-		,
θ_{L}	(CR=10)	Left	75	89	-		
Фн	Vertical Viewing Angle	Up	75	89	-		
Φ_{L}	(CR=10)	Down	75	89	-	[degree]	Note 2-5
θ_{R}	Horizontal Viewing Angle	Right	75	89	-		By SR-3
θ_{L}	(CR=5)	Left	75	89	-		
Φ_{H}	Vertical Viewing Angle	Up	75	89	-		
Φ_L	(CR=5)	Down	75	89	-		
T_R		Rising Time	-	13	28		
T_F	Response Time	Falling Time	-	5	8	[msec]	Note 2-6
-		Rising + Falling	-	18	36		By TRD-100
R _x		Red x	0.622	0.652	0.682		
R _y		Red y	0.305	0.335	0.365		
G _x		Green x	0.291	0.321	0.351		
Gy	Color Coordinates	Green y	0.595	0.625	0.655		
B _x	(CIE 1931)	Blue x	0.123	0.153	0.183	-	By SR-3
By		Blue y	0.037	0.067	0.097		
W _x		White x	0.283				
W _v		White y	0.299	0.329	0.359		
,	NTSC Area Ratio	1		72		[%]	By SR-3
O.T.					F0/ 1	Note 2-7	
СТ	Crosstalk		-	-	2.0	[%]	By SR-3
F_{dB}	Flicker (Center of	screen)	-	ı	TBD	[dB]	Note 2-8 By SR-3



Note 2-2: Equipment setup :

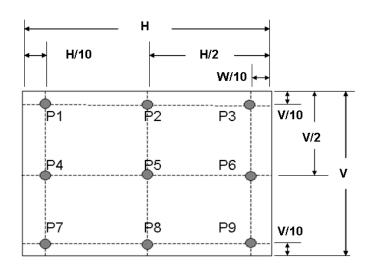


Note 2-3: Luminance Uniformity Measurement

Definition:

Luminance Uniformity = $\frac{\text{Minimum Luminance of 9 Points (P1 } \sim P9)}{\text{Maximum Luminance of 9 Points (P1 } \sim P9)}$

a. Test pattern: White Pattern





Note 2-4: Contrast Ratio Measurement

Definition:

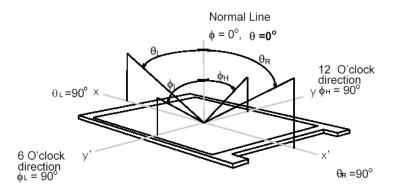
 $Contrast Ratio = \frac{Luminance of White pattern}{Luminance of Black pattern}$

a. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta=\Phi=0^{\circ}$)

Note 2-5: Viewing angle measurement

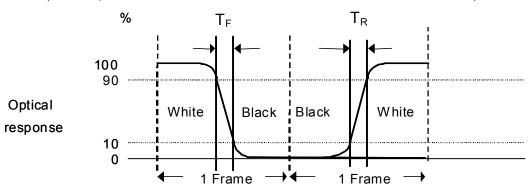
Definition: The angle at which the contrast ratio is greater than 10 & 5.

a. Horizontal view angle: Divide to left & right ($\theta_L \& \theta_R$) Vertical view angle: Divide to up & down ($\Phi_H \& \Phi_L$)



Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Black" to "White" (rising time, T_R), and from "White" to "Black" (falling time, T_F), respectively. The response time is interval between the 10% and 90% of optical response. (*Black & White color definition: Please refer section 3.4.3*)





Note 2-7: Crosstalk measurement

Definition:

 $CT = Max. (CT_H, CT_V);$

Where

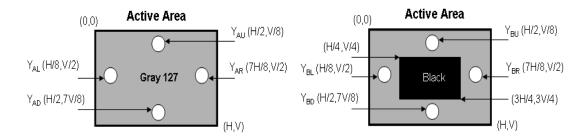
a. Maximum Horizontal Crosstalk:

$$CT_H = Max. (| Y_{BL} - Y_{AL} | / Y_{AL} \times 100 \%, | Y_{BR} - Y_{AR} | / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

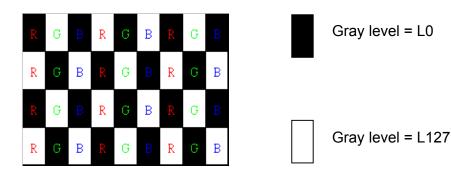
$$CT_V = Max. (|Y_{BU} - Y_{AU}|/Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}|/Y_{AD} \times 100 \%);$$

b. Y_{AU} , Y_{AD} , Y_{AL} , Y_{AR} = Luminance of measured location without Black pattern Y_{BU} , Y_{BD} , Y_{BL} , Y_{BR} = Luminance of measured location with Black pattern



Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.



R: Red, G: Green, B:Blue

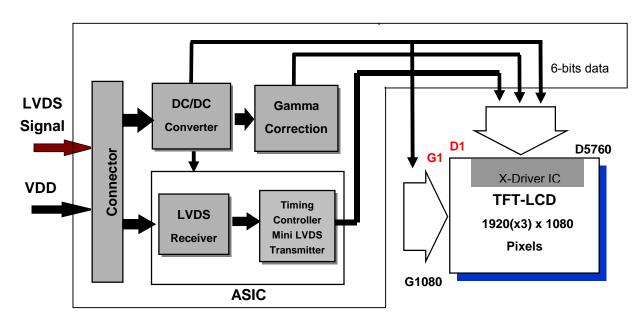
b. Measured position: Center of screen (P5) & perpendicular to the screen $(\theta=\Phi=0^{\circ})$



3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 21.5 inch Color TFT-LCD Module.



Control Board



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3.2 Interface Connection

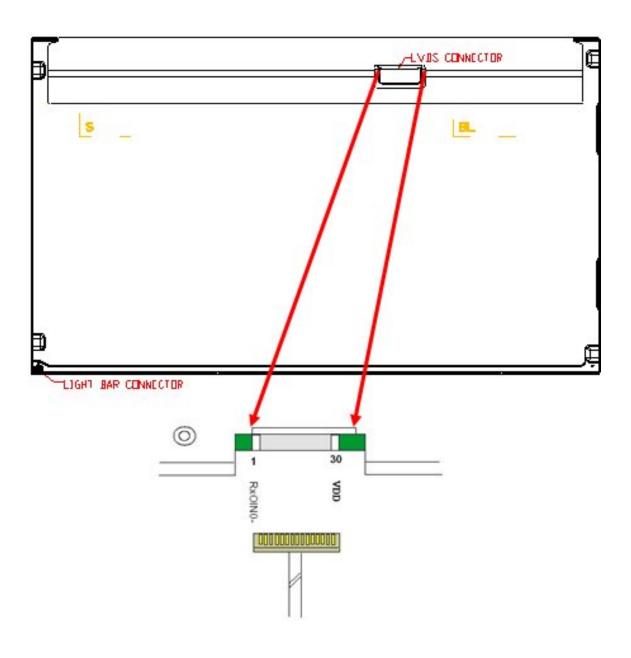
3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM	
TI 1-LOD CONNECTOR	Part Number	AL230F-A0G1D-P	MSCKT2407P30HB	
Mating Connector	Manufacturer	JAE		
Wating Connector	Part Number	FI-X30HL (Locked Type)		

3.2.2 Connector Pin Assignment

PIN#	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	GND	Ground	
25	NC	No connection (for AUO test only. Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	

27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	





3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

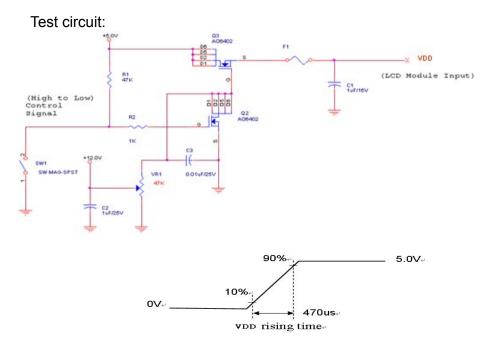
Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Тур	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply	ı	0.62	0.74	[A]	VDD= 5.0V, Black Pattern, Fv=60Hz
טטו	Input Current (RMS)		0.7	0.84	[A]	VDD= 5.0V, Black Pattern, Fv=75Hz
PDD	VDD Power	ı	3.1	3.7	[Watt]	VDD= 5.0V, Black Pattern, Fv=60Hz
	Consumption		3.5	4.2	[Watt]	VDD= 5.0V, Black Pattern, Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	Note 3-1
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, Black Pattern, Fv=75Hz

Note 3-1: Inrush Current measurement:

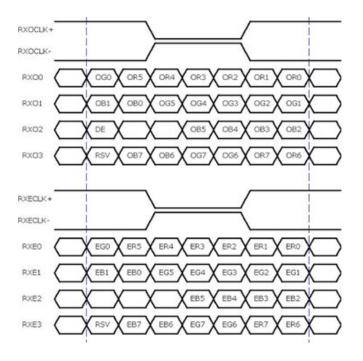


The duration of VDD rising time: 470us.

3.4 Signal Characteristics

3.4.1 LCD Pixel Format

3.4.2 LVDS Data Format



8 Bit Color Bit Order										
MSB	MSB R7 G7 B7									
	R6	G6	B6							
	R5	G5	B5							
	R4	G4	B4							
	R3	G3	В3							
	R2	G2	B2							
	R1	G1	B1							
LSB	R0	G0	B0							

Note 3-2:

- a. O = "Odd Pixel Data" E = "Even Pixel Data"
- b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

		Color Input Data																								
Color Gray Level		RED data (MSB:R7, LSB:R0)				GREEN data (MSB:G7, LSB:G0)				BLUE data (MSB:B7, LSB:B0)					Remark											
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	B5	B4	ВЗ	В2	B1	В0	
Black	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
	Ω	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Red	:			:	:	:	:	:	:	:		:	:	:	:	:	:		:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Ш	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Ш	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	



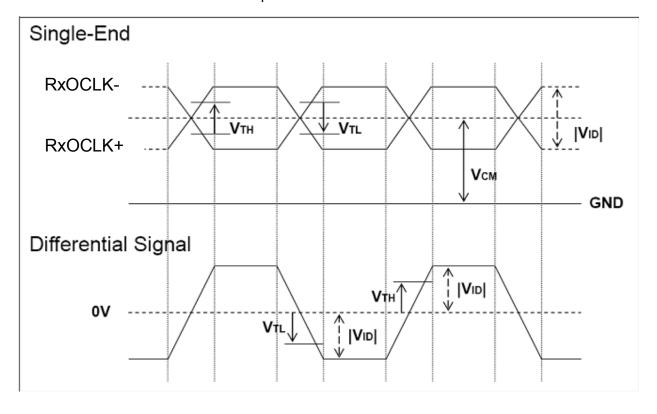
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Тур	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	1	-	+100	[mV]	V _{CM} = 1.2V
V_{TL}	LVDS Differential Input Low Threshold	-100	-	ı	[mV]	V _{CM} = 1.2V
V _{ID}	LVDS Differential Input Voltage	100	-	600	[mV]	
V _{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH} - V_{TL} = 200mV

LVDS Signal Waveform:

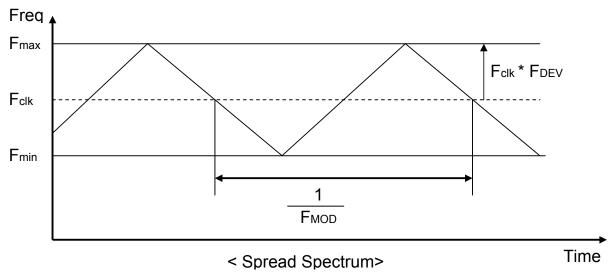
Use RxOCLK- & RxOCLK+ as example.





b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F _{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	•	± 3	%	
F _{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



Fclk: LVDS Clock Frequency



3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Descript	Min.	Тур.	Max.	Unit	Remark	
Tv		Period	1092	1130	1818	Th	
Tdisp (v)	Vertical Section	Active	1080	1080	1080	Th	
Tblk (v)		Blanking	12	50	738	Th	
Fv		Frequency	50	60	76	Hz	
Th		Period	1034	1050	1100	Tclk	
Tdisp (h)	Horizontal Section	Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	55	68	91	KHz	Note 3-3
Tclk	LVDS Clock	Period	10.6	14.0	17.7	ns	1/Fclk
Fclk		Frequency	56.5	71.2	94.0	MHz	Note 3-4

Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

Fh (Min.) = Fclk (Min.) / Th (Min.);

Fh(Typ.) = Fclk(Typ.) / Th(Typ.);

Fh (Max.) = Fclk (Max.) / Th (Min.);

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

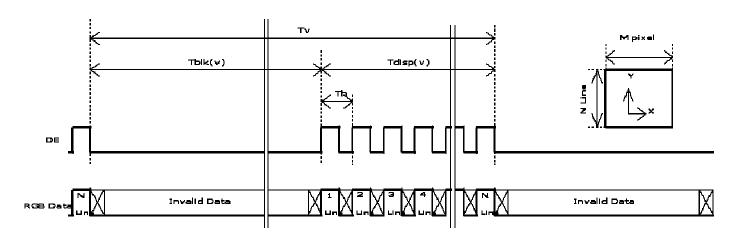
Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.);

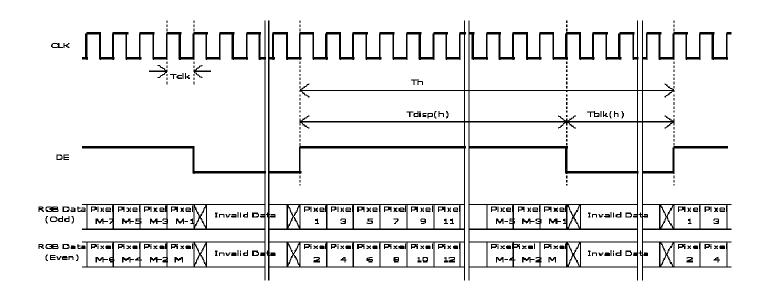
Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);

Fclk (Max.) = Fv (Max.) x Th (Typ.) x Tv (Typ.);



3.4.6 Input Timing Diagram



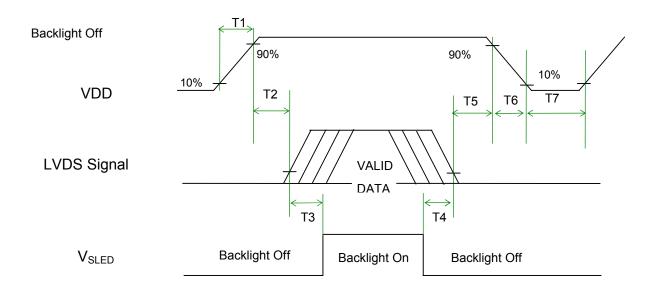




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3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol		Value	I I m i f	Remark		
Symbol	Min.	Тур.	Max.	Unit		
T1	0.5	-	10	[ms]		
T2	0	-	50	[ms]		
T3	500	-	-	[ms]		
T4	100	-	-	[ms]		
T5	0		50	[ms]	Note 3-5 Note 3-6	
T6	0	-	100	[ms]	Note 3-6	
T7	1000	-	-	[ms]		

Note 3-5: Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

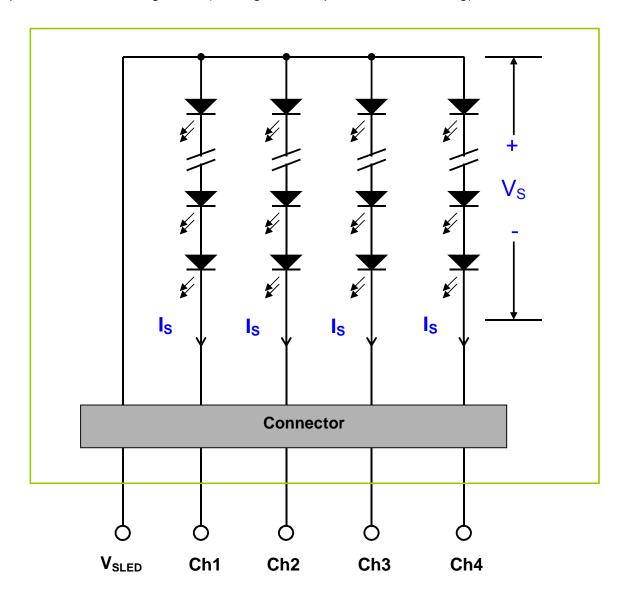
Note 3-6: During T5 and T6 period, please keep the level of input LVDS signals with Hi-Z state.



4 Backlight Unit

4.1 Block Diagram

The following shows the block diagram of the 21.5 inch Backlight Unit. And it includes 60 pcs LED in the LED light bar. (4 strings and 15 pcs LED of one string).





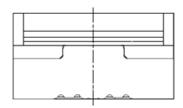
4.2 Interface Connection

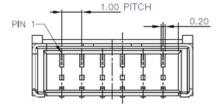
4.2.1 Connector Type

Backlight Connector	Manufacturer	ENTERY				
Bushing it Confidence	Part Number	3707K-S06N-21R				
Mating Comparts	Manufacturer	ENTERY				
Mating Connector	Part Number	H112K-P06N-00B (Non-Locking type) H112K-P06N-03B (Locking type)				

Backlight Connector dimension:

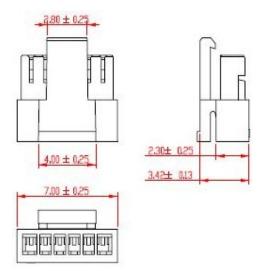
 $H \times V \times D = 13.9 \times 3.00 \times 4.25$, Pitch = 1.0(unit = mm)

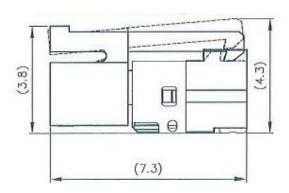






Mating Connector dimension:

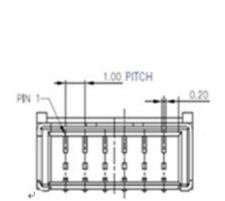


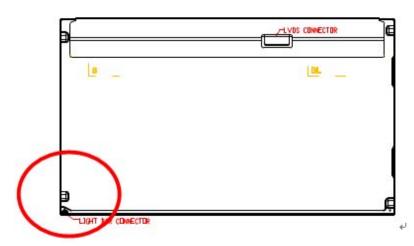




4.2.2 Connector Pin Assignment

Pin#	Symbol	Description	Remark
1	Ch1	LED Current Feedback Terminal (Channel 1)	
2	Ch2	LED Current Feedback Terminal (Channel 2)	
3	V_{SLED}	LED Power Supply Voltage Input Terminal	
4	V_{SLED}	LED Power Supply Voltage Input Terminal	
5	Ch3	LED Current Feedback Terminal (Channel 3)	
6	Ch4	LED Current Feedback Terminal (Channel 4)	





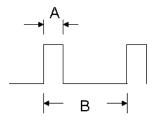
4.3 Electrical Characteristics

4.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

(Ta=25)

Symbol	Description	Min	Max	Unit	Remark
			90	[mA]	100% duty ratio
Is	LED String Current	0	150	[mA]	Duty ratio 10% Pulse time=10 ms



Duty ratio= (A / B) X 100%; (A: Pulse time, B: Period)

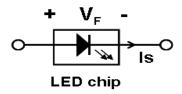
4.3.2 Recommended Operating Condition

(Ta=25

Symbol	Description	Min.	Тур.	Max.	Unit	Remark
Is	LED String Current	ı	65	72	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	-	46.5	51	[Volt]	Is=65mA @ 100% duty ratio; <i>Note 4-1, Note 4-5</i>
ΔVs	Maximum Vs Voltage Deviation of light bar	-	-	4.5	[Volt]	Is=65mA @ 100% duty ratio; <i>Note 4-2</i>
P _{BLU}	LED Light Bar Power Consumption	-	12.1	13.3	[Watt]	Note 4-3
LT _{LED}	LED Life Time	30,000	-	-	[Hour]	Note 4-4
OVP	Over Voltage Protection in system board	110% Vsmax	-	-	[Volt]	Note 4-5



- **Note 4-1:** Vs (Typ.) = V_F (Typ.) X LED No. (one string);
 - a. V_F: LED chip forward voltage, V_F (Min.)= 2.8V, V_F(Typ.)=3.1V, V_F(Max.)=3.4V
 - b. The same euqation to calculate Vs(Min.) & Vs (Max.) for respective V_F (Min.) & V_F (Max.);



- **Note 4-2:** ΔVs (Max.) = $\Delta V_F X$ LED No. (one string);
 - a. $\Delta V_{F:}$ LED chip forward voltage deviation; (0.3 V , each Bin of LED V_F)
- **Note 4-3:** P_{BLU} (Typ.) = Vs (Typ.) X Is (Typ.) X 4; (4 is total String No. of LED Light bar) P_{BLU} (Max.) = Vs (Max.) X Is (Typ.) X 4;
- **Note 4-4:** Definition of life time:
 - a. Brightness of LED becomes to 50% of its original value
 - b. Test condition: Is = 65mA and 25 (Room Temperature)
- **Note 4-5:** Recommendation for LED driver power design:

Due to there are electrical property deviation in LED & monitor set system component after long time operation. AUO strongly recommend the design value of LED driver board OVP (over voltage protection) should be 10% higher than max. value of LED string voltage (Vs) at least.

Note 4-6: AUO strongly recommend "Analog Dimming" method for backlight brightness control for Wavy Noise Free. Otherwise, recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency.

5 Reliability Test

AUO reliability test items are listed as following table. (Bare Panel only)

Items	Condition	Remark	
Temperature Humidity Bias (THB)	Ta= 50 , 80%RH, 300hours		
High Temperature Operation (HTO)	Ta= 50 , 50%RH, 300hours		
Low Temperature Operation (LTO)	Ta= 0 , 300hours		
High Temperature Storage (HTS)	Ta= 60 , 300hours		
Low Temperature Storage (LTS)	Ta= -20 , 300hours		
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)		
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)		
Thermal Shock Test (TST)	-20 /30min, 60 /30min, 100 cycles	Note 5-1	
On/Off Test	On/10sec, Off/10sec, 30,000 cycles		
ESD (Electro Static Discharge)	Contact Discharge: \pm 15KV, 150pF(330 Ω) 1sec, 8 points, 25 times/ point.	- Note 5-2	
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.		
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft		

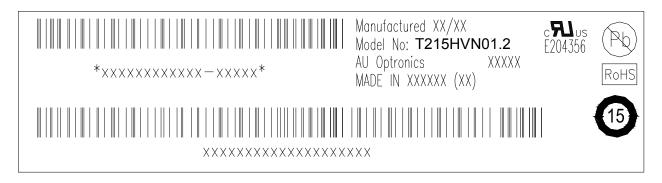
- **Note 5-1**: a. A cycle of rapid temperature change consists of varying the temperature from -20 to 60 , and back again. Power is not applied during the test.
 - b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed

No data lost Self-recoverable No hardware failures.

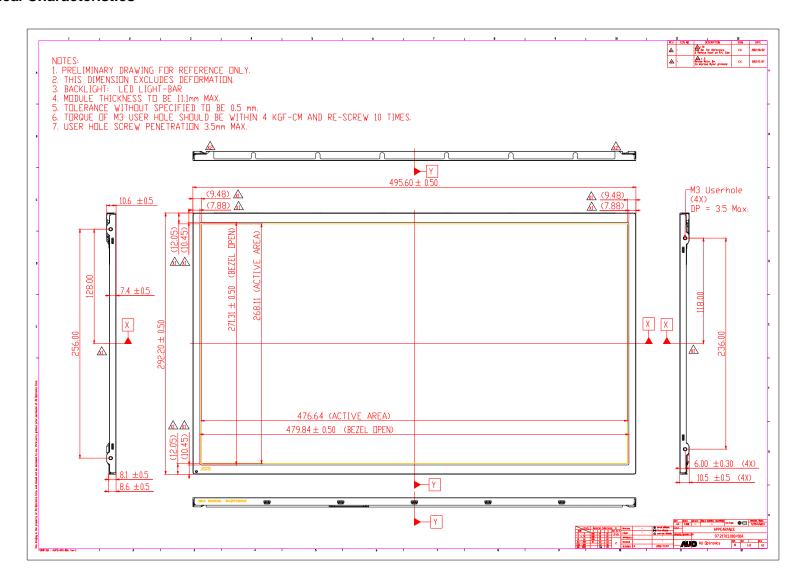
6 Shipping Label

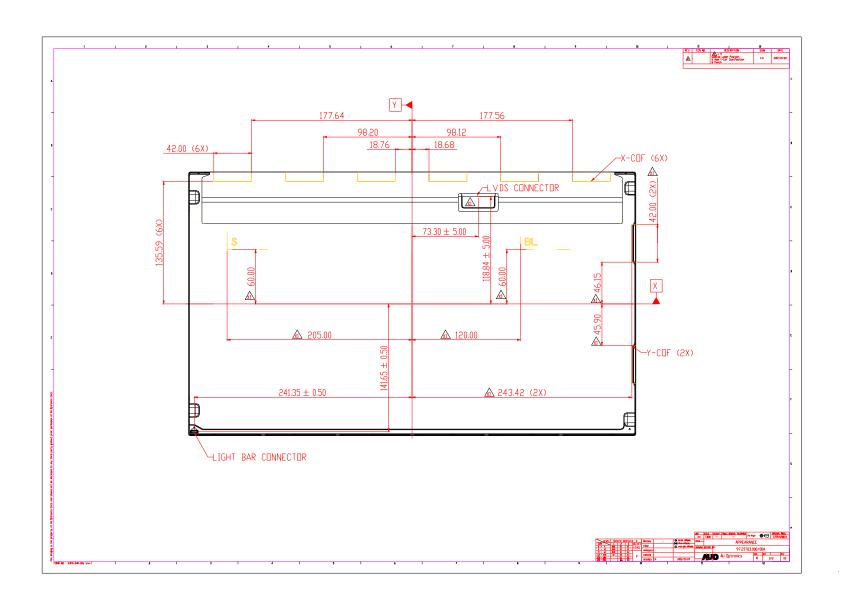
The label is on the panel as shown below:



- Note 6-1: For Pb Free products, AUO will add for identification.
- Note 6-2: For RoHS compatible products, AUO will add RoHS for identification.
- Note 6-3: For China RoHS compatible products, AUO will add 6 for identification.
- **Note 6-4:** The Green Mark will be presented only when the green documents have been ready by AUO Internal Green Team.

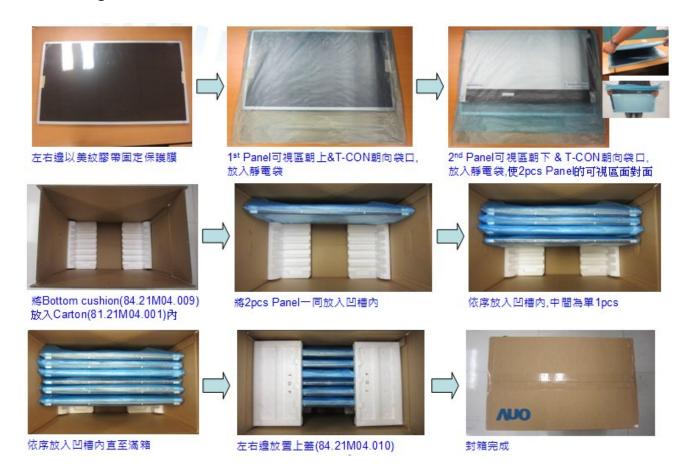
7 Mechanical Characteristics

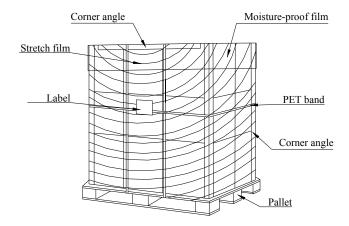




8 Packing Specification

8.1 Packing Flow





8.2 Pallet and shipment information

Item	Specification			Remark
itelli	Q'ty	Dimension	Weight(kg)	Kemark
Panel	1	495.6(H)mm × 292.2(V)mm × 10.6(D)mm	1.75	
Cushion	4	-	0.24	
Box	1	557(L)mm x 345(W)mm x 375(H)mm	1.37	without Panel & cushion
Packing Box	13 pcs/Box	557(L)mm x 345(W)mm x 375(H)mm	25.7	with panel & cushion
Pallet	1	1150(L)mm x 1070(W)mm x 132(H)mm	14.20	
Pallet after Packing	18 boxes/pallet	1150(L)mm x 1070(W)mm x 1257(H)mm	476.8	