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Document No.	DC140-000609	Revision	3.1

TO:

Date: Jul., 20, 2011

HannStar Product Specification (Formal)

Model: **HSD100IFW2**-A00

Note: 1.Please contact HannStar Display Corp. before designing your product based on this module specification.

2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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	Record of Revisions				
Rev.	Date	Sub-Model	Description of change		
1.0	Mar., 06, 2009		Formal Product Specification was first released		



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1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD100IFW2-A00 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 10.1 (17:10) inch diagonally measured active display area with WSVGA (1024 horizontal by 600 vertical pixel) resolution.

1.2 Features

- 10.1 (17:10 diagonal) inch configuration
- One channel LVDS interface
- 262K color by 6 bit R.G.B signal input
- RoHS Compliance
- Halogen Free

1.3 Applications

- Mobile NB
- Digital Photo frame
- Display terminal for AV application

1.4 General information

Item		Specification	Unit
Outline Dimension	on	235 x 145.8 x 4.8 (Typ.)	mm
Display area		220.42(H) x 129.15(V)	mm
Number of Pixel		1024 RGB (H) x 600(V)	pixels
Pixel pitch		0.2153(H) x 0.2153(V)	mm
Pixel arrangement		RGB Vertical stripe	
Display mode		Normally white	
Surface treatmer	nt	Antiglare, Hard-Coating (3H) with EWV film	
Weight		185(typ)	g
Back-light		Single LED (Side-Light type)	
Power	Power Logic System 0.7(Max.)		W
Consumption	B/L System	2.0(Max.)	W

1.5 Mechanical Information

	Item	Min.	Тур.	Max.	Unit
Madula	Horizontal (H)	234.7	235	235.3	mm
Module Size	Vertical (V)	145.5	145.8	146.1	mm
Size	Depth (D)	_	4.8	5.1	mm
Weight	•	_	185	195	g



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2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
LED Power Supply voltage	V_{LED}	-0.3	6.0	V	GND=0
Logic Supply voltage	V_{DD}	-0.3	6.0	V	

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T _{opa}	0	50	$^{\circ}\!\mathbb{C}$	
Storage Temperature	T_{stg}	-20	60	$^{\circ}$ C	



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3.0 OPTICAL CHARACTERISTICS

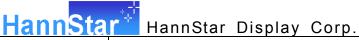
3.1 Optical specification

5.1 Optical specification								
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		400	500	_		(1)(2)
		Tr		_	3	6	maga	
Response tim	ie	Tf			9	18	msec	(1)(3)
White lumina (5 points)	nce	Y _L		160	200	_	cd/m ²	(1)(4)(5)
	Dod	R _x	⊖=0	0.553	0.603	0.653		
	Red	R _Y	Normal	0.302	0.352	0.302		
	0	G _x	viewing angle	0.260	0.310	0.360		
Color	Green	G _Y		0.496	0.546	0.596		
chromaticity (CIE1931)	Blue	B _x		0.099	0.149	0.199		
(OIL 1991)		B _Y		0.059	0.109	0.159		
	White	W _x		0.260	0.310	0.360		
	vviile	W _y		0.280	0.330	0.380		
	l lan	θL		60	70	_		
Viewing	Hor.	Θ_{R}	CR>10	60	70	_		(1)(4)
angle	Vor	θu	CK-10	40	50	_		
	Ver.	θр		50	60	_		
Brightness uniformity		В	⊖=0	_	_	1.25		(6)
		B _{UNI}	(5points)			1.20		(6)
Brightness uniformity		B _{UNI}	⊖=0	_	_	1.5		(6)
Digitiless ui	inornity	DUNI	(13points)			1.0		(0)

3.2 Measuring Condition

■ Measuring surrounding : dark room
 ■ Ambient temperature : 25±2°C

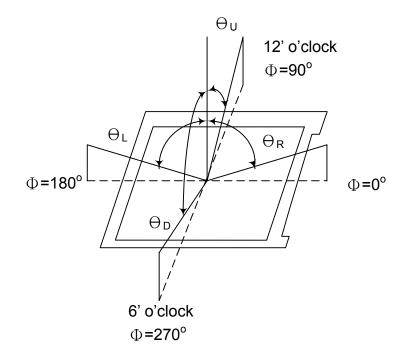
■ 15min. warm-up time.



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3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20 ~ 21 mm Note (1) Definition of Viewing Angle:



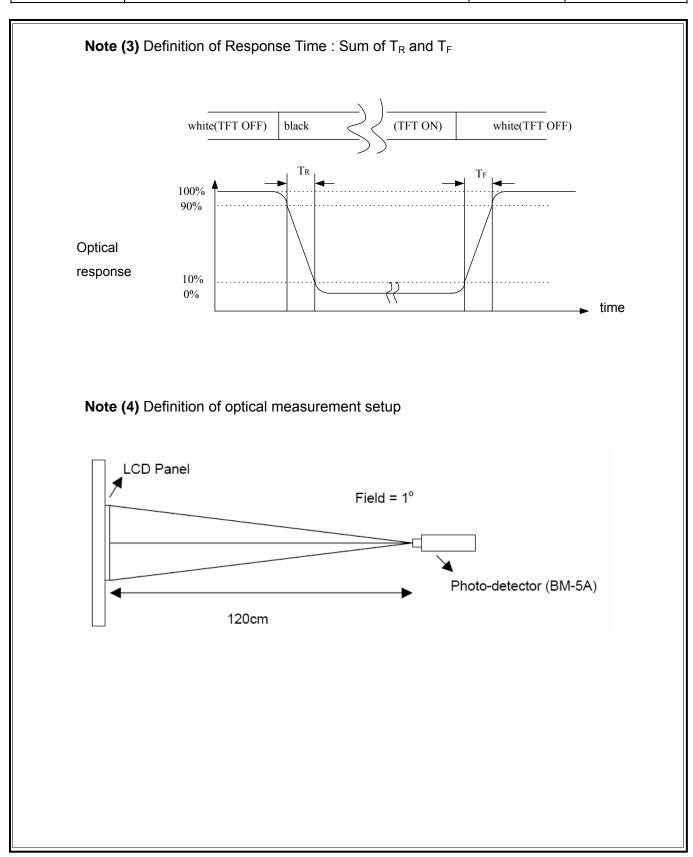
Note (2) Definition of Contrast Ratio (CR) : measured at the center point of panel

CR = Luminance with all pixels white

Luminance with all pixels black

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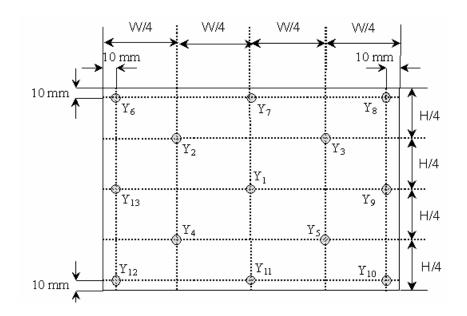




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Note (5) Definition of Average Luminance Uniformity of White (5 Point)

Average Luminance Uniformity =
$$\frac{Y_1 + Y_2 + Y_3 + Y_4 + Y_5}{5}$$



Note (6) Definition of brightness uniformity

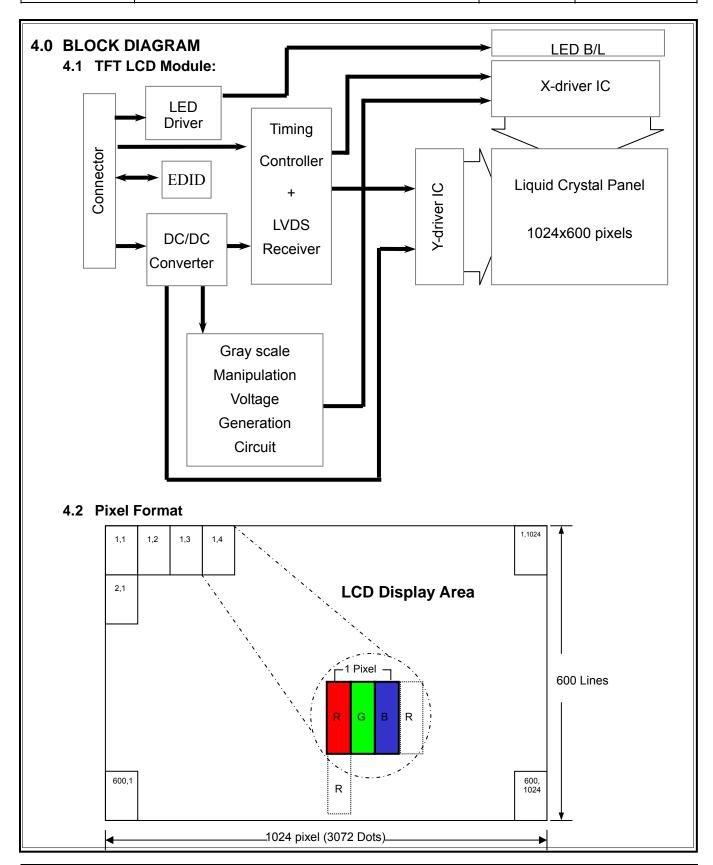
Luminance uniformity =
$$\frac{\text{(Max Luminance of 5 points)}}{\text{(Min Luminance of 5 points)}}$$
(Max Luminance of 13 points)

Luminance uniformity = (Max Luminance of 13 points)

(Min Luminance of 13 points)

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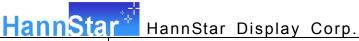
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4.3 Relationship Between Displayed Color and Input MSB LSB MSB LSB MSB LSB Gray scale R1 R0 G5 G4 R5 R4 R3 R2 G3 G2G1 G0B5 B4 B2 B1 B0 Display В3 level Black LL L LH Blue L L L L L Н Н Н Н Н Green L LH Н Н Η Н HIL L Basic Light Blue LH Н Н Н Н HHΗ Н Η Η Н color Red Η Η Н LL L Purple Н Н Н Н HIL LH Η Н Н Н Н Yellow Η Η Н Н H|HН Η Η HIL L L L L L White Н Н Н Η Н HH Η Н Η Н H|HΗ Н Н Н Н Black L L L L L L L L L LL L L L L L L0 Н L L L L L L L LL L L L L L1 LL L Dark Gray L3...L60 scale of Red Light Η Н Н Н L ΗL L L L L LL L L L L L L61 LL Η Н Н Η L L L L L L L L L L L62 Red Н Н Н L L L LL L L L L Red L63 Н Black L LL L0 HL L L1 L Н LL L L2 Dark Gray scale of L3...L60 Green Light LH Н Н HIL L61 LH Н Н Н Н LL Ι L L62 LΗ Н Green L63 Green L L L Н Н Н HL L L L L L Г Black LL L L0 1 L L Т Т L L Н ı LL L1 Dark Gray 1 scale of L3...L60 Blue Light L61 L L L LH Н Н Η L Н LH L Н Н Н Η 1 L62 Blue LH Blue L63 L L L L L L Η Η Н Н Н Black L L L L L L LL L L0 Ι ı L L L Н L L HL L L L L Н L1 L Н L2 Gray Dark scale of 1 L3...L60 White & Black Light Н Н НН Н Н НН Н L61 Η Н Н Н LH Η Η Н Н LH Н Н Η Н L62 White Н Н Н Н Н Н Н Н Н Н Н Н Н Н H White L63 Н



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5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD Module:

Connector CN1 is used for electronics interface. The recommended model is FI-XB30SRL-HF10 manufactured by JAE or other equivalent.

Pin No.	Signal	Description	
1	GND	Ground	
2	VDD	Power Supply, 3.3V (typical)	
3	VDD	Power Supply, 3.3V (typical)	
4	V_EDID	EDID 3.3V power	
5	ADJ	Adjust for LED backlight brightness	
6	CLK_EDID	EDID Clock	
7	DATA_EDID	EDID Data	
8	RXIN0-	- LVDS differential data input (R0-R5, G0)	
9	RXIN0+	+ LVDS differential data input (R0-R5, G0)	
10	GND	Ground	
11	RXIN1-	- LVDS differential data input (G1-G5, B0-B1)	
12	RXIN1+	+ LVDS differential data input (G1-G5, B0-B1)	
13	GND	Ground	
14	RXIN2-	- LVDS differential data input (B2-B5, HS, VS, DE)	
15	RXIN2+	+ LVDS differential data input (B2-B5, HS, VS, D	
16	GND	Ground	
17	RXCLKIN-	- LVDS differential clock input	
18	RXCLKIN+	+ LVDS differential clock input	
19	GND	Ground	
20	NC	NC	
21	NC	NC	
22	GND	Ground	
23	GND	Ground	
24	VLED	LED Power Supply, 5V	
25	VLED	LED Power Supply, 5V	
26	VLED	LED Power Supply, 5V	
27	NC	NC	
28	NC	NC	
29	NC	NC	
30	NC	NC	

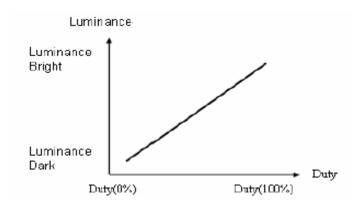
Note: The brightness of LCD panel could be changed by adjusting ADJ



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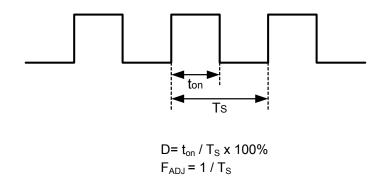
[Note]

(1) ADJ can adjust brightness to control Pin. Pulse duty the bigger the brighter.



(2) ADJ Signal=0~3.3V, Operation Conditions:

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
ADJ Logic-High Level	V_{ADJH}		1.8	3.3	3.6	V
ADJ Logic-Low Level	V_{ADJL}		0	0	0.4	V
Dimming Frequency	F _{ADJ}		18	20	22	kHz
Dimming Duty Cycle	D		20		100	%





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6.0 ELECTRICAL CHARACTERISTICS

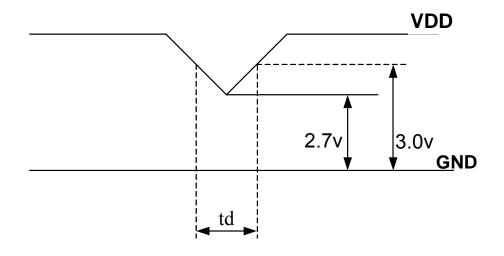
6.1 TFT LCD Module

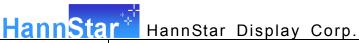
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Supply Voltage	V_{DD}	3.0	3.3	3.6	V	Note (1)
Supply Voltage	V_{LED}	4.7	5.0	5.3	V	
Current of power supply	IDD	-	0.192	-	Α	V _{DD} =3.3V ⋅ L0 pattern

Note : (1) V_{DD} -dip condition :

When VDD operating within 2.7V \leq VDD<3.0V , td \leq 10ms , the display may momentarily become abnormal.

VDD<2.7V, VDD dip condition should also follow the Power On/Off conditions for supply voltage.



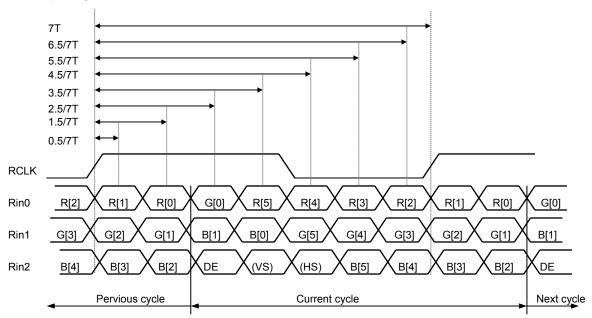


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6.2 Switching Characteristics for LVDS Receiver

Item	Symbol	Min.	Тур.	Max.	Unit	Conditions
Differential Input High Threshold	Vth			100	mV	\/ -1.2\/
Differential Input Low Threshold	Vtl	-100	_	_	mV	V _{CM} =1.2V
Input Current	I _{IN}	-10	_	+10	uA	
Differential input Voltage	$ V_{ID} $	0.1	_	0.6	٧	
Common Mode Voltage Offset	V _{CM}	(V _{ID} /2)	1.25	1.8-0.4-(V _{ID} /2)	V	

6.3 Bit Mapping & Interface Definition



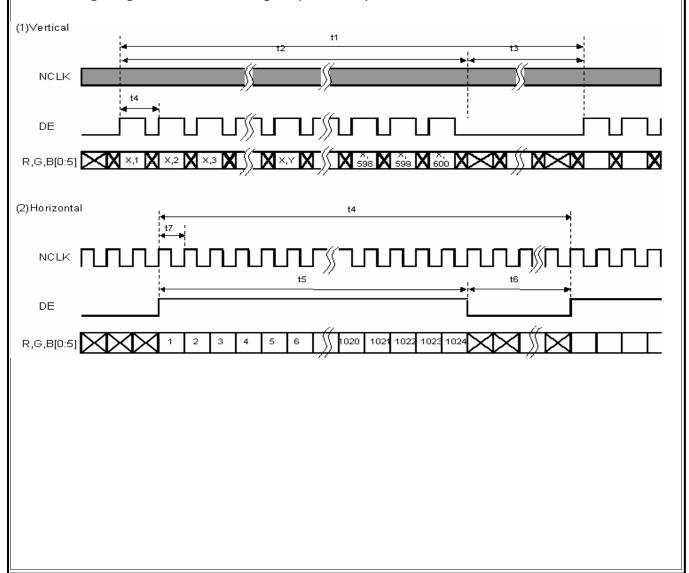
LVDS Receiver Input Timing Definition for 6bits LVDS input

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6.4 Interface Timing (DE mode)

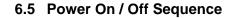
		,			
Item	Symbol	Min.	Тур.	Max.	Unit
Frame Rate		55	60	65	Hz
Frame Period	t1	612	625	638	line
Vertical Display Time	t2	600	600	600	line
Vertical Blanking Time	t3	12	25	38	line
1 Line Scanning Time	t4	1160	1200	1240	clock
Horizontal Display Time	t5	1024	1024	1024	clock
Horizontal Blanking Time	t6	136	176	216	clock
Clock Rate	t7	39	45	51.42	MHz

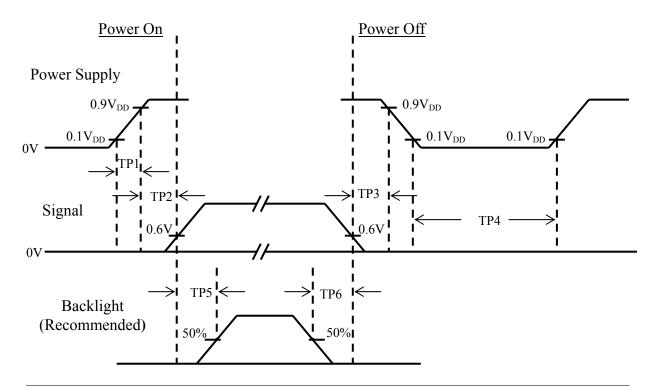
Timing Diagram of Interface Signal (DE mode)





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Item	Min.	Тур.	Max.	Unit	Remark
TP1	0.5		10	msec	
TP2	0		50	msec	
TP3	0	-	50	msec	
TP4	500	-	-	msec	
TP5	200	-	-	msec	
TP6	200			msec	

Note:(1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

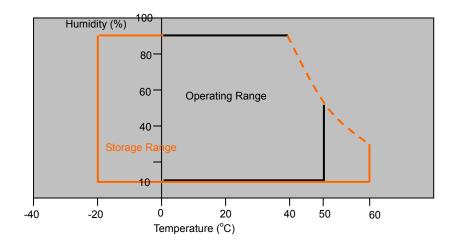


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7.0 Reliability test items

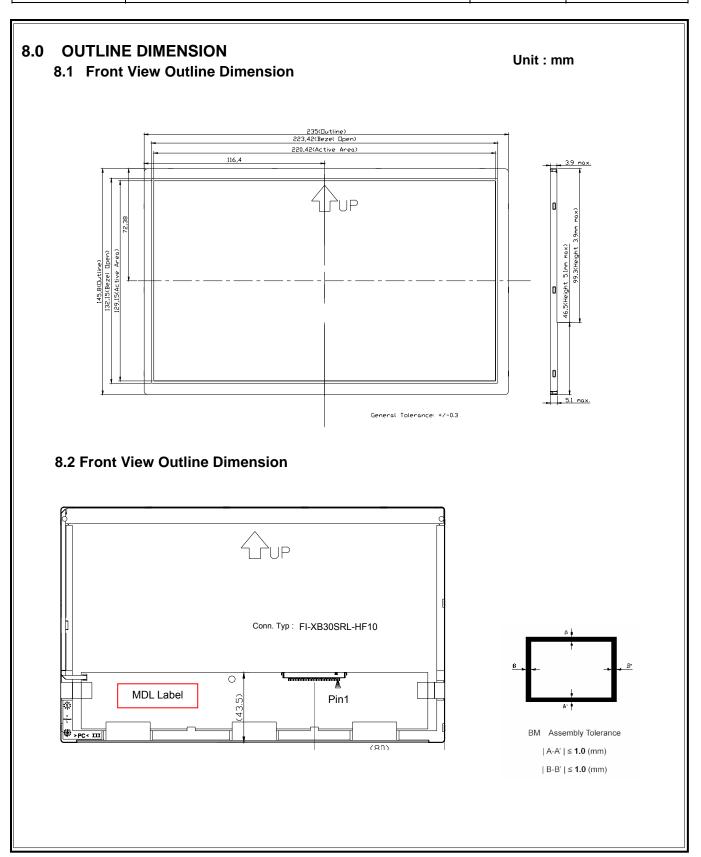
No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+60°C, 240hrs	
2	Low Temperature Storage	Ta=-20°C, 240hrs	
3	High Temperature Operation	Ta=+50°C, 300hrs	
4	Low Temperature Operation	Ta=0°C, 300hrs	
5	Thermal Cycling Test (non operation)	-20°C(30min)→+60°C(30min),100 cycles	
	Vibration	Sine Wave	
6		1.5G, 5~500Hz, XYZ	
		30min/each direction	
7	Shock	Half-Sine, 200G, 2ms, ±XYZ, 1time	

Storage / Operating temperature



Note .Max wet bulb temp.=39°C

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9.0 LOT MARK

9.1 Lot Mark

 1
 2
 3
 4
 5
 6
 7
 8
 9
 10
 11
 12
 13
 14
 15

Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location. Code 8: production year.

Code 9: production year.

Code 10,11,12,13,14,15: serial number.

Note (1) Production Year

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Mark	6	7	8	9	0	1	2	3	4	5

Note (2) Production Month

. ,												
Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

9.2 Location of Lot Mark

- (1) Location : The label is attached to the backside of the LCD module. See Product back view. (Section 8.0 : OUTLINE DIMENSION)
- (2) Detail of the Mark: As attached below
- (3) This is subject to change without prior notice.





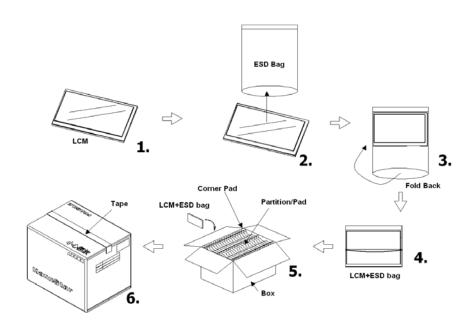
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10.0 PACKAGE SPECIFICATION

10.1 Packing form

LCM Model	LCM Qty. in the box	Inner Box Size (mm)	Notice
HSD100IFW2-A	50 pcs/box	460 x 316 x 321 ^H	

10.2 Packing assembly drawings



HSD100IFW2-A00	Material	Notice
Box	Corrugated Paper Board	(AB Flute)
Partition/Pad	Corrugated Paper Board	(B Flute)
Corner Pad	Corrugated Paper Board	(AB Flute)
ESD bag	PE	

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11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

11.3 Breakage of LCD Panel

- 11.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.4 Electric Shock

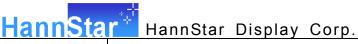
- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.



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- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

11.10 Disposal

When disposing LCD module, obey the local environmental regulations.