INNOLUX DISPLAY CORPORATION LCD MODULE

SPECIFICATION

Customer:			_
Model Name SPEC NO.: Date: Version:	A10	02TN03 \ 2-03-TT-1 6/02/06	
Preliminary Final Speci	ification		ti.
Approved b	у		Comment
Approved by	Revie	wed by	Prepared by
是数据	せな		70)



InnoLux copyright 2004 All rights reserved, Copying forbidden.

Record of Revision

Version	Revise Date	Page	Content
1	2006/02/06		Initial Release



Contents:

1.	General Specifications	1
2.	Pin Assignment	2
	2.1. TFT LCD Panel Driving Section	2
	2.2. Backlight Unit Section	6
3.	Operation Specifications	7
	3.1. Absolute Maximum Rating	7
	3.1.1. Typical Operation Conditions	8
	3.1.2. Current Consumption	9
	3.1.3. Backlight Driving Conditions	9
	3.2. Timing Characteristics	10
	3.2.1. Timing Conditions	10
	3.2.2. Timing Diagram	11
4.	Optical Specifications	16
5.	Reliability Test Items	20
6.	General Precautions	21
	6.1. Safety	21
	6.2. Handling	21
	6.3. Static Electricity	21
	6.4. Storage	21
	6.5. Cleaning	21
7.	Mechanical Drawing	22
8.	Package Drawing	23
	8.1. Packaging Material Table	23
	8.2. Packaging Quantity	23
	8.3. Packaging Drawing	24



PAGE: 1/24

1. General Specifications

No.	Item	Specification	Remark
1	LCD size	10.2 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	800X3(RGB)X480	
4	Display mode	Normally white, Transmissive	
5	Dot pitch	0.0925(W)X0.276(H) mm	
6	Active area	222.0(W)X132.48(H) mm	
7	Module size	235.0(W)X145.8(H)X6.1(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	2.57W(Typ.)	
12	Panel power consumption	250mW(Typ.)	
13	Weight	TBD	

Note 1: Refer to Mechanical Drawing.

PAGE: 2/24

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

Pin No.	Symbol	I/O	Function	Remark
1	POL	I	Polarity selection	
2	STVD	I/O	Vertical start pulse input when U/D= H	Note 1
3	OEV	I	Output enable	
4	CKV	I	Vertical clock	
5	STVU	I/O	Vertical start pulse input when U/D= L	Note 1
6	GND	Р	Power ground	
7	EDGSL	I	Select rising edge or rising/falling edge	
8	V _{CC}	Р	Power supply for digital circuit	
9	V9	I	Gamma voltage level 9	
10	V_{GL}	Р	Gate OFF voltage	
11	V2	I	Gamma voltage level 2	
12	V_{GH}	Р	Gate ON voltage	
13	V6	I	Gamma voltage level 6	
14	U/D	I	Up/down selection	Note 1,2
15	V _{COM}	I	Common voltage	
16	GND	Р	Power ground	
17	AV _{DD}	Р	Power supply for analog circuit	
18	V14	I	Gamma voltage level 14	
19	V11	I	Gamma voltage level 11	
20	V8	I	Gamma voltage level 8	

The copyright belongs to InnoLux. Any unauthorized use is prohibited.



PAGE: 3/24

			PAGE:	3/24
21	V5	I	Gamma voltage level 5	
22	V3	I	Gamma voltage level 3	
23	GND	Р	Power ground	
24	R5	I	Red data(MSB)	
25	R4	I	Red data	
26	R3	I	Red data	
27	R2	I	Red data	
28	R1	I	Red data	
29	R0	I	Red data(LSB)	
30	GND	Р	Power ground	
31	GND	Р	Power ground	
32	G5	I	Green data(MSB)	
33	G4	I	Green data	
34	G3	I	Green data	
35	G2	I	Green data	
36	G1	I	Green data	
37	G0	I	Green data(LSB)	
38	STHL	I/O	Horizontal start pulse input when R/L = H	Note 1
39	REV	Р	Control signal are inverted or not	
40	GND	I	Power ground	
41	DCLK	I	Sample clock	
42	DV _{DD}	Р	Voltage for digital circuit	
43	STHR	I/O	Horizontal start pulse input when R/L = L	Note 1
44	LD	I	Latches the polarity of outputs and switches the new data to outputs	
45	B5	I	Blue data (MSB)	
		_		



PAGE: 4/24

			-	1/4 1
46	B4	I	Blue data	
47	В3	I	Blue data	
48	B2	I	Blue data	
49	B1	I	Blue data	
50	В0	I	Blue data (LSB)	
51	R/L	I	Right/ left selection	Note 1,2
52	V1	I	Gamma voltage level 1	
53	V4	I	Gamma voltage level 4	
54	V7	I	Gamma voltage level 7	
55	V10	I	Gamma voltage level 10	
56	V12	I	Gamma voltage level 12	
57	V13	I	Gamma voltage level 13	
58	AV _{DD}	Р	Voltage for analog circuit	
59	GND	Р	Power ground	
60	V _{COM}	I	Common voltage	

I: input, O: output, P: Power

Note 1: Selection of scanning mode

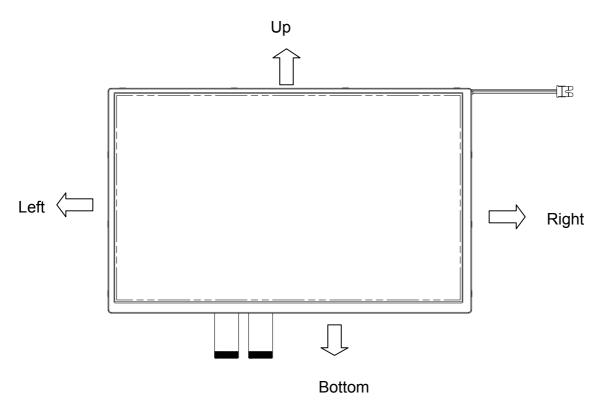
Setting of scan control input		IN/OUT	state for s	tart pulse	Scanning direction	
U/D	R/L	STVD	STVU	STHR	STHL	
GND	V _{CC}	0	I	0	1	Up to down, left to right
V_{CC}	GND	I	0	I	0	Down to up, right to left
GND	GND	0	I	1	0	Up to down, right to left
V _{CC}	V _{CC}	I	0	0	1	Down to up, left to right



PAGE: 5/24

Note 2: Definition of scanning direction.

Refer to the figure as below:





PAGE: 6/24

2.2. Backlight Unit Section

Pin No.	Symbol	I/O	Function	Remark
1	н	Р	Power supply for backlight unit	Pink
2	GND	Р	Ground for backlight unit	White



PAGE: 7/24

3. Operation Specifications

3.1. Absolute Maximum Rating

(GND=AV_{SS}=0V, Note 2)

	(6142 71755 67, 11616 2)					
Item	Symbol	Val	Unit	Remark		
item	Syllibol	Min.	Max.	Onit	Remark	
	V _{CC}	-0.3	5	V		
	AV _{DD}	-0.5	12	V		
Power voltage	V _{GH}	-0.3	18	V		
	V _{GL}	-15	0.3	V		
	V _{GH} -V _{GL}	-	33	V		
Input signal valtage	V1~V7	0.4 AV _{DD}	AV _{DD} -0.1	V	Note 1	
Input signal voltage	V8~V14	-0.3	0.6AV _{DD}	V		
Operation temperature	T _{OP}	-30	85	$^{\circ}\!\mathbb{C}$		
Storage temperature	T _{ST}	-30	85	°C		

Note 1: AVDD - $0.1 \ge V1 \ge V2 \ge V3 \ge V4 \ge V5 \ge V6 \ge V7 \ge V8 \ge V9 \ge V10 > V11 \ge V12 \ge V13 \ge V14 \ge AVSS + 0.1$

Note 2: The absolute maximum rating values of the module should not be exceeded. Once exceeded absolute maximum rating values, the characteristics of the module may not be recovered. Even in an extreme condition, may result in module permanently destroyed.



PAGE: 8/24

3.1.1. Typical Operation Conditions

(GND=AV_{SS}=0V, Note 1)

Itama	Cumbal		Values	Hnit	Domonic	
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
	V _{CC}	3.0	3.3	3.6	V	
Power voltage	AV _{DD}	9.0	9.2	9.4	V	
Power voltage	V_{GH}	14.3	15	15.7	V	
	V _{GL}	-10.5	-10	-9.5	V	
	V _{COM}	3.5	3.7	3.9	V	(V1+V14)/2 =4.5V
Input signal voltage	V1~V7	0.4 A _{VDD}	-	A _{VDD} -0.1	V	
	V8~V14	0.1	-	0.6 A _{VDD}	V	
Input logic high voltage	V _{IH}	0.7V _{CC}	-	V _{CC}	V	
Input logic low voltage	V _{IL}	0	-	0.3V _{CC}	V	

Note 1: Be sure to apply GND, V_{CC} , and V_{GL} , to the LCD first, and then apply V_{GH} .



PAGE: 9/24

3.1.2. Current Consumption

 $(GND=AV_{SS}=0V)$

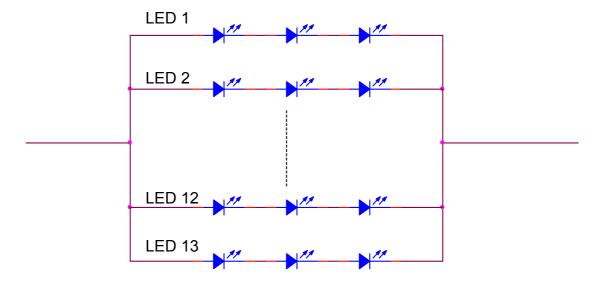
Item	Symbol	Values			Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Onit	Remark	
	I _{GH}	-	0.3	0.5	mA	V _{GH} =15V	
Current for Driver	I _{GL}	-	0.2	1.0	mA	V _{GL} = -10V	
Current for Driver	I _{CC}	-	4	10	mA	V _{CC} =3.3V	
	I _{DD}	-	25	50	mA	AV _{DD} =9.2V	

3.1.3. Backlight Driving Conditions

ltem	Symbol	Values			Unit	Remark
item	Symbol	Min.	Тур.	Max.	Offic	Kelliaik
LED voltage	V _L	-	9.9	10.5	V	Note 2
LED current	IL	-	20	-	mA	Note 2
LED life time	-	20,000	-	-	Hr	Note 1

Note 1: The "LED life time" is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25° C and I_L =20mA.

Note 2: The LED driving condition is defined for each LED module.(3 LED Serial)



The copyright belongs to InnoLux. Any unauthorized use is prohibited.



PAGE: 10/24

3.2. Timing Characteristics

3.2.1. Timing Conditions

ltem	Symbol	Values			Unit	Remark
item		Min.	Тур.	Max.	Unit	Remark
DCLK frequency	Fclk	-	40	45	MHz	
DCLK cycle	Tcph	22	25	-	ns	
DCLK pulse width	Tcw	22	-	-	ns	
Data set-up time	Tsu	4	-	-	ns	
Data hold time	Thd	2	-	-	ns	
Time that the last data to LD	Tld	1	-	-	Tcph	
Pulse width of LD	Twld	3	-	-	Tcph	
Time that LD to STVU/D	Tlds	5	-	-	Tcph	
POL set-up time	Tpsu	6	-	-	ns	
POL hold time	Tphd	6	-	-	ns	
OEV pulse width	Toev	1	-	-	us	
CKV pulse width	Tckv	0.5	-	-	us	
Horizontal display timing range	Tdh	-	800	-	Tcph	
STVD/U setup time	Tsuv	400	-	-	ns	
STVD/U hold time	Thdv	400	-	-	ns	
STVD/U pulse width	Tstv	-	-	1	Tdh	
Horizontal lines per field	Tv	512	525	610	Tdh	
Vertical display timing range	Tdv	_	480	-	Tdh	

PAGE: 11/24

3.2.2. Timing Diagram

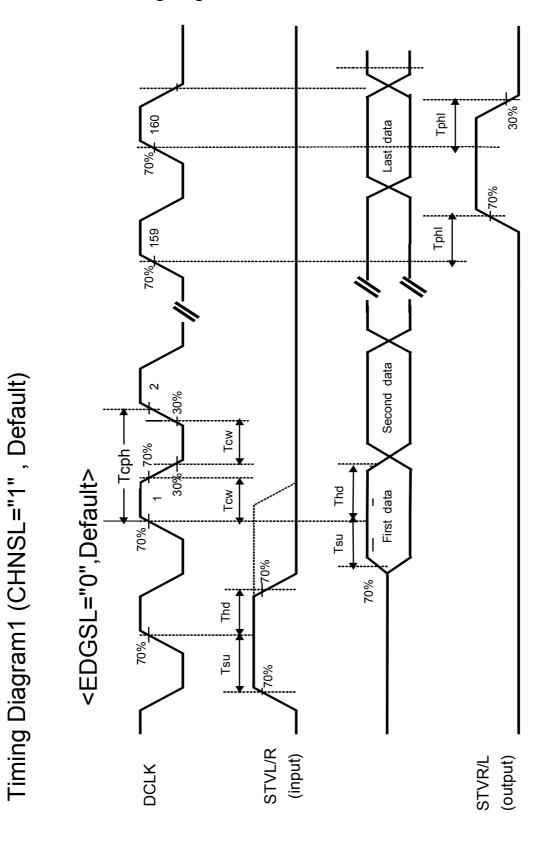


Fig.3-1 operation model 1

PAGE: 12/24

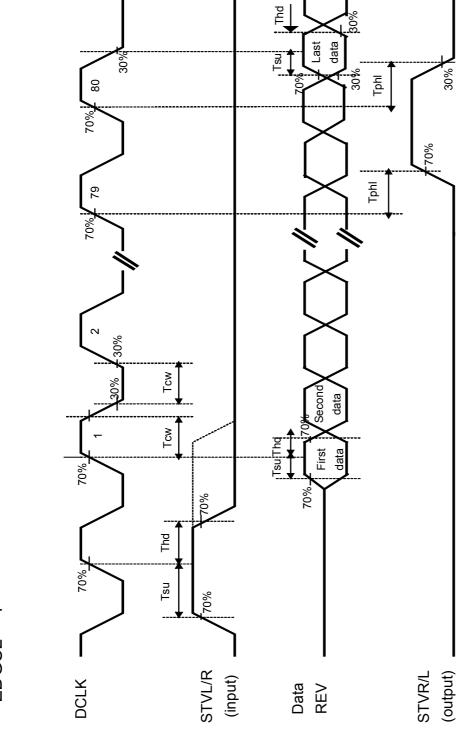


Fig.3-2 operation model 2

PAGE: 13/24

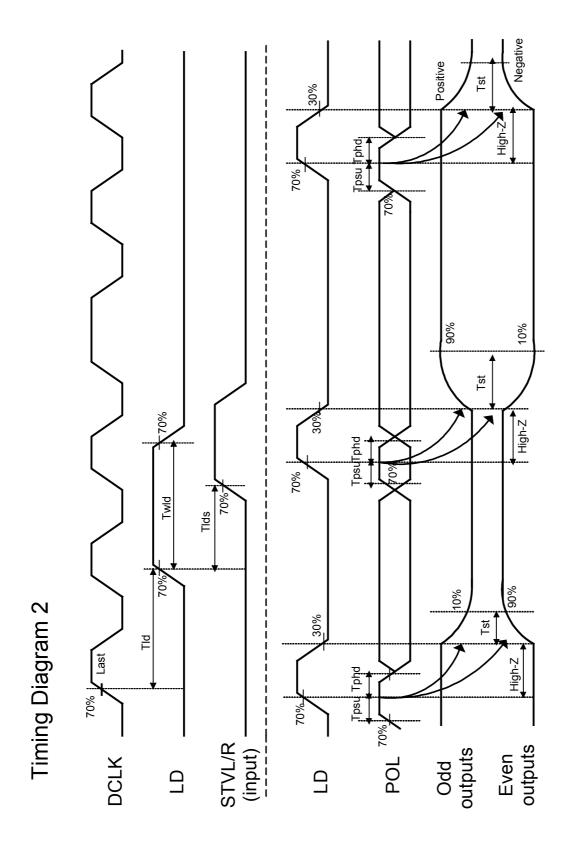


Fig.3-3 Horizontal timing

PAGE: 14/24

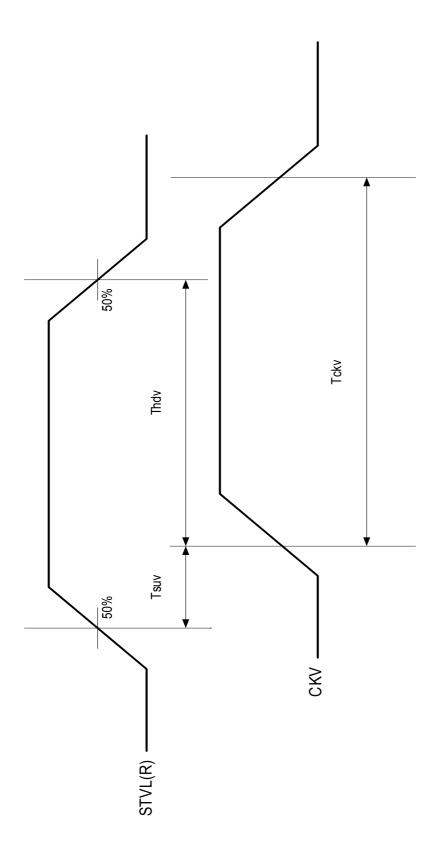
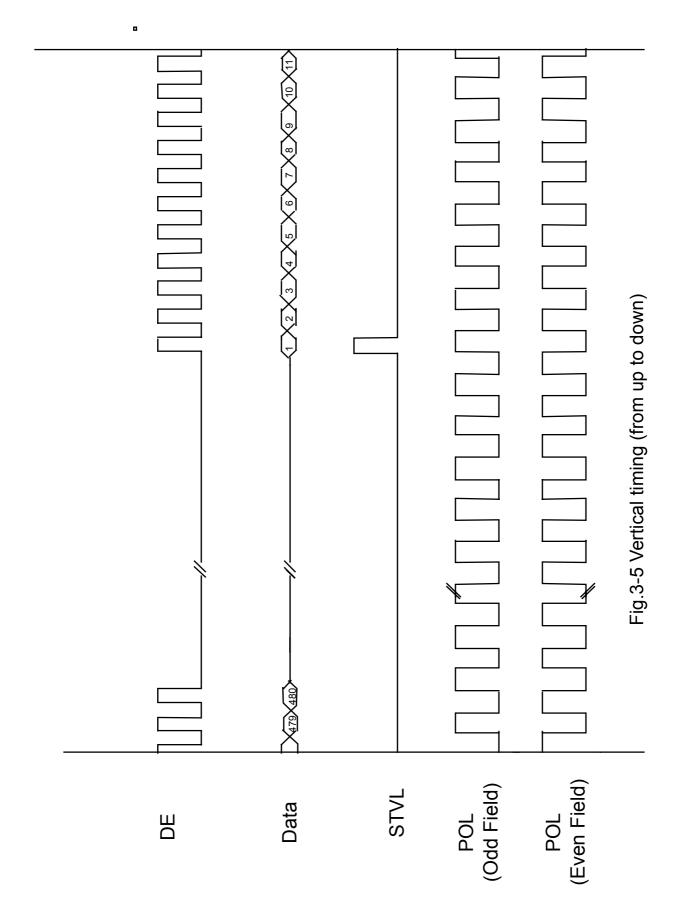


Fig.3-4 Vertical shift clock timing

PAGE: 15/24





PAGE: 16/24

4. Optical Specifications

ltem	Symbo	Condition	Values			Unit	Remark
item	ı	Condition	Min.	Тур.	Max.	Oilit	Remark
	θ_{L}	Φ=180°(9 o'clock)	55	65	-		Note 1
Viewing angle (CR≥10)	θ_{R}	Ф=0°(3 o'clock)	55	65	-	degre	
	θτ	Φ=90°(12 o'clock)	35	45	-	е	
	θв	Φ=270°(6 o'clock)	55	65	-		
Dognongo timo	T _{ON}	Normal θ=Φ=0°	-	15	30	msec	Note 3
Response time	T _{OFF}		-	20	40	msec	Note 3
Contrast ratio	CR		250	300	-	-	Note 4
	W _X		(0.25)	(0.30)	(0.35)	-	Note 5
Color chromaticity	W _Y		(0.28)	(0.33)	(0.38)	-	Note 6
Luminance	L		200	250	-	cd/m ²	Note 6
Luminance uniformity	Yu		70	75	-	-	Note 7

Test Conditions:

- 1. V_{CC} =3.3V, AV_{DD} =9.2V, I_L =20mArms (Backlight current), the ambient temperature is 25 $^{\circ}$ C.
- 2. The test systems refer to Note 2.



PAGE: 17/24

Note 1: Definition of viewing angle range

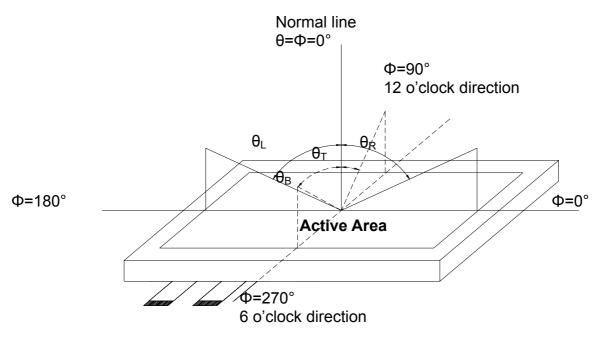


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

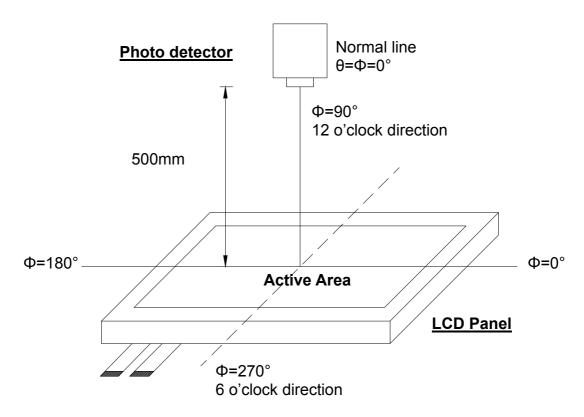


Fig. 4-2 Optical measurement system setup

The copyright belongs to InnoLux. Any unauthorized use is prohibited.



PAGE: 18/24

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

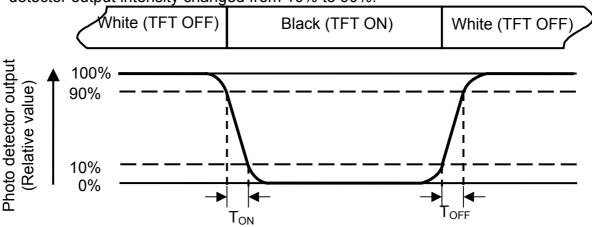


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

Contrast ratio (CR) = $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground when measuring the center area of the panel.



PAGE: 19/24

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) = $\frac{B_{min}}{B_{max}}$

L-----Active area length W----- Active area width

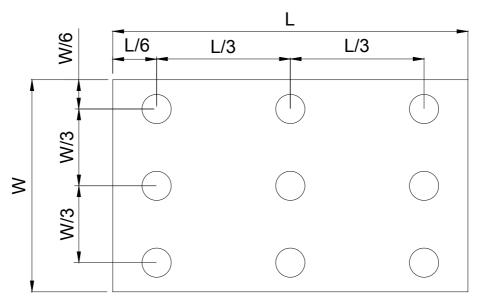


Fig. 4-4 Definition of measuring points

 \mathbf{B}_{max} : The measured maximum luminance of all measurement position. \mathbf{B}_{min} : The measured minimum luminance of all measurement position.



PAGE: 20/24

5. Reliability Test Items

(Note3)

Item	Test	Remark	
High Temperature Storage	Ta = 85℃	240 hrs	Note 1
Low Temperature Storage	Ta = -30°C	240hrs	Note 1
High Temperature Operation	Ts = 85°℃	240hrs	Note 2
Low Temperature Operation	Ta = -30°C	240hrs	Note 1
Operate at High Temperature and Humidity	+60℃, 90%RH	240 hrs	
Thermal Shock	=	$^{\circ}\mathbb{C}/30$ min for a total 100 old temperature and end ure	
Vibration Test	Frequency range:1 Stroke:1.5mm Sweep:10Hz~55Hz 2 hours for each did (6 hours for total)		
Mechanical Shock	100G 6ms,±X, ±Y, direction	±Z 3 times for each	
Package Vibration Test	Random Vibration: 0.015G*G/Hz from from 200-500HZ 2 hours for each dir (6 hours for total)		
Package Drop Test	Height:60 cm 1 corner, 3 edges, 0	3 surfaces	
Electro Static Discharge	± 2KV, Human Bo	ody Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function.



PAGE: 21/24

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
 - 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
 - 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

- 1. Store the module in a dark room where must keep at +25±10° and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
 - 3. Store the module in an anti-electrostatic container or bag.

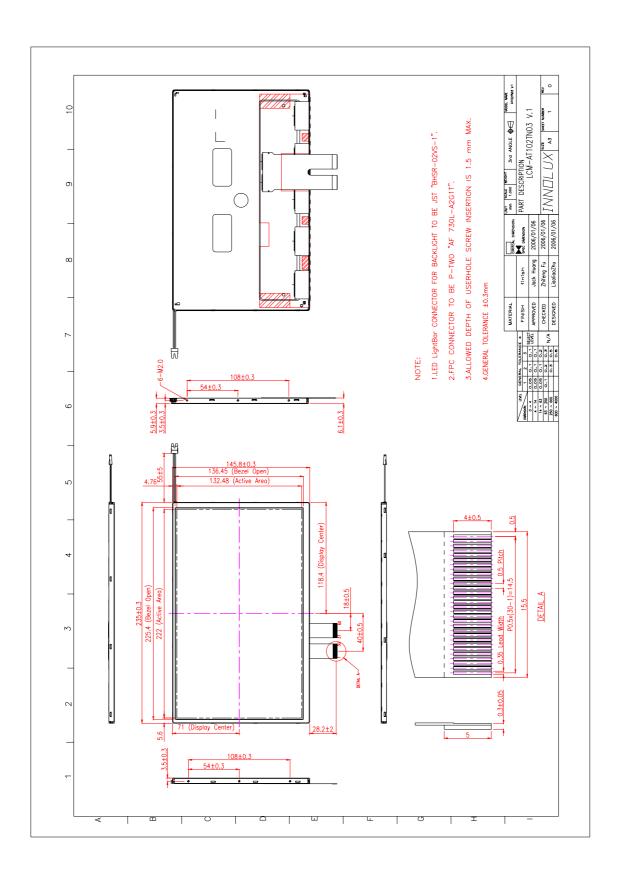
6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.



PAGE: 22/24

7. Mechanical Drawing





PAGE: 23/24

8. Package Drawing

8.1. Packaging Material Table

No.	Item	Model (Material)	Dimensions(mm)	Unit Weight (kg)	Quantity	Remark
1	LCM module	AT102TN03 V.1	235X145.8X6.1	TBD	25	
2	Partition	BC Corrugated paper	512X349X226	1.35	1	
3	Partition Paper	B Corrugated paper	510X350X7	0.148	2	
4	Corrugated Bar	B Corrugated paper	512X370X7	0.11	2	
5	Dust-Proof Bag	PE	900X700X0.05	0.01	1	
6	A/S Bag	PE	280X200X0.05	0.001	25	10^{9} ~ 10^{11} Ω/sq
7	Carton	Corrugated paper	530X355X255	2.3	1	
8	Total weight	TI	BD Kg ± 5%			

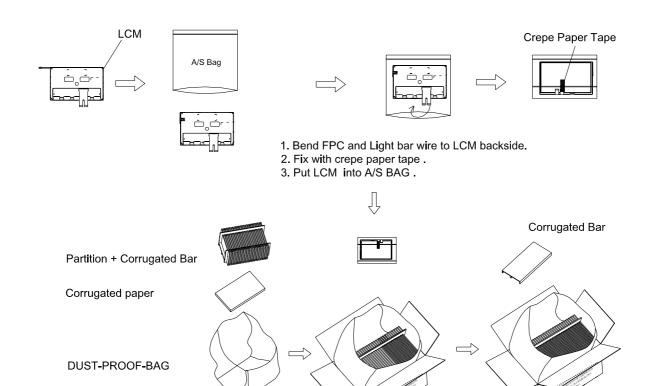
8.2. Packaging Quantity

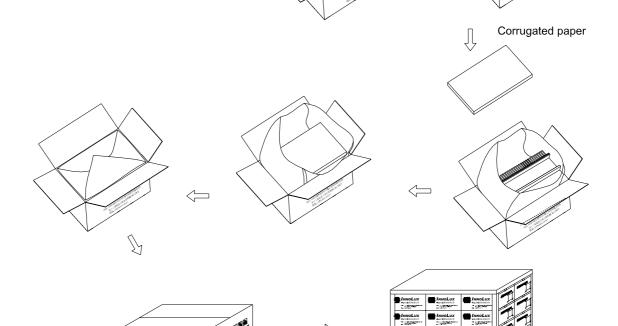
Total LCM quantity in Carton: no. of Partition 1 Rows x quantity per Row 25 = 25



PAGE: 24/24

8.3. Packaging Drawing





InnoLux