

PRODUCTION SPECIFICATION OF LCD MODULE MODULE NO. TL040HDS03CT-T1238A

Customer Name:		
Customer Part Number:		
Approved By:	Date:	

Prepared By	Checked By	Approved By

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Rev. 1.0

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Revision History

Rev	Issued Date	Description	Page	Editor
1.0	Aug.28,2018	First release	All	

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1 General Specifications

	Feature	Specifications
	LCD type	4.0 inch
	Resolution (H*V)	720(RGB)×720
	Technology Type	a-Si TFT
Display Spec.	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	Normally Black
	Viewing Direction	ALL
	Gray Scale Inversion Direction	-
	OutlineDimensions (W x H x T) (mm)	84.0*84.0*2.6
	Active Area(mm)	71.93*71.93
 Mechanical	CTP View Area(mm)	72.53*72.53
Characteristics	With /Without Touch screen	With CTP
	Connector Type	0.5mm pitch-30pin ZIF
	Backlight Type	LED
	Weight (g)	TBD
	Display Interface	MIPI
Fig. 4 to 1	Touch Interface	I2C
Electrical Characteristics	Number of color	16.7M
Onaracteristics	Display Driver IC	\
	Touch Driver IC	FT6336U

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2 Pin Assignment

NO.	PIN NAME	Description
1	LED A	LED ANODE
2	LED K1	LED CATHODE
3	LED K2	LED CATHODE
4	VCI	LCD analog power supply
5	IOVCC	LCD I/O power supply
6	RESET	Reset Signal ,Active Low
7	TE	Tearing effect
8	PWM	LCD backlight control PWM (It can be NC)
9	GND	Ground
10	D0P	MIPI DSI differential data pair (Data lane 0)
11	D0N	MIPI DSI differential data pair (Data lane 0)
12	GND	Ground
13	D1P	MIPI DSI differential data pair (Data lane 1)
14	D1N	MIPI DSI differential data pair (Data lane 1)
15	GND	Ground
16	CLKP	MIPI DSI differential clock pair
17	CLKN	MIPI DSI differential clock pair
18	GND	Ground
19	D2P	MIPI DSI differential data pair (Data lane 2)
20	D2N	MIPI DSI differential data pair (Data lane 2)
21	GND	Ground
22	D3P	MIPI DSI differential data pair (Data lane 3)
23	D3N	MIPI DSI differential data pair (Data lane 3)
24	GND	Ground
25	TP_INT	Touch Interrupt
26	TP_SDA	Touch IIC Data signal
27	TP_SCL	Touch IIC Clock signal
28	TP_RESET	Touch Reset Signal
29	TP_VCI	Touch IC analog power supply (3.3V)
30	TP_IOVCC	Use 1.8V or 3.3V,It must be same with user
		Touch IIC I/O voltage

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3 Absolute Maximum Ratings

GND=0V, Ta= 25°C

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Item	Symbol	Value	Unit
Power supply voltage for logic	VCI	0.3~3.6	V
Input voltage	Vin	V _{CI} +0.3	٧
Operating temperature	Topr	-20 to 70	°C
Storage temperature	Tstg	-30 to 80	°C

Note: Note1: Absolute maximum rating is the limit value beyond which the IC maybe broken.

They do not assure operations.

Note2: Background color changes slightly depending on ambient temperature. This Phenomenon is reversible.

Ta ≤ 70°C: 75% RH max

Ta>70°C: absolute humidity must be lower than the humidity of 75%RH at 70°C

Note3: Ta at -30° C will be <48hrs, at 80° C will be <120hrs

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

Item	Symbol	Min	Тур	Max	Unit	Remark
LCD Analog operation voltage	VCI	3.0	3.3	3.6	V	
LCD Logic operation voltage	IOVCC	1.65	1.8	3.3	V	
CTP I/O voltage	TP_IOV CC		1.8 or 3.3		V	It must be same with user touch IIC voltage
CTP analog voltage	TP_VCI	2.8	3.3	3.6	V	

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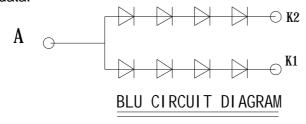
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4.2 Driving Backlight

Ta=25°C

Item	Symbol	Min	Тур	Max	Unit	Remark
Forward Current	I _F		40	50	mA	
Forward Voltage	V_{F}	-	12.8	-	V	
Connection mode			4S2P	-		
LED number	/		8		pcs	

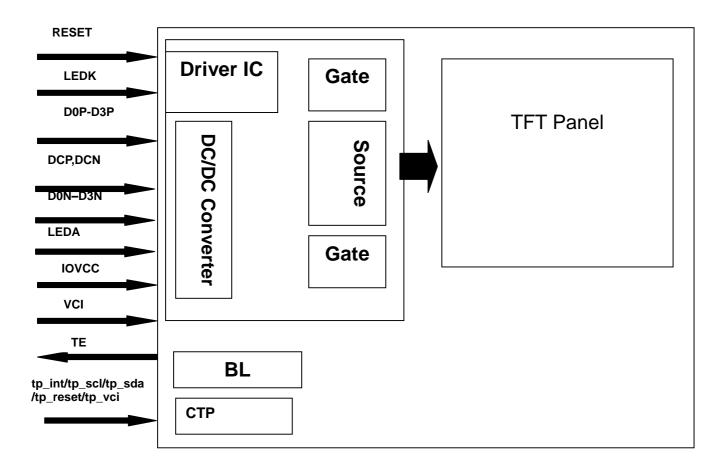
Note1: Optical performance should be evaluated at $Ta=25^{\circ}C$ only .If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



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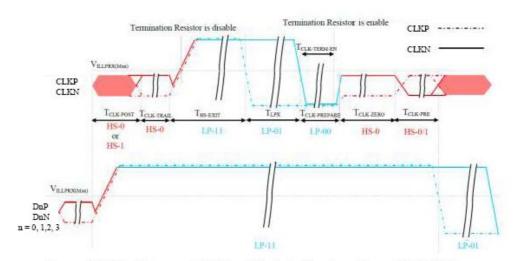
4.3 Block Diagram



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5 INTERFACE TIMING

5.1 System Bus Read/Write Characteristics.



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Figure 123: Clock Lanes - High Speed Mode to/from Low Power Mode Timings

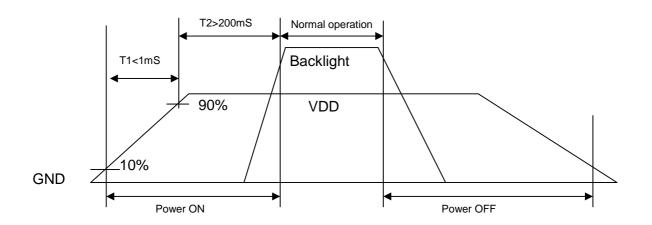
Table 46: Clock Lanes - High Speed Mode to/from Low Power Mode Timings

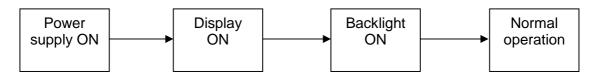
Signal	Symbol	Description	Min	Max	Unit
CLKP/N	T _{CLK-POST}	Time that the MCU shall continue sending HS clock after the last associated Data Lanes has transitioned to LP mode	60+52xUI	98	ns
CLKP/N	T _{CLK-TRAIL}	Time to drive HS differential state after last payload clock bit of a HS transmission burst	60	98	ns
CLKP/N	T _{HS-EXIT}	Time to drive LP-11 after HS burst	100	82	ns
CLKP/N	Tolk-Prepare	Time to drive LP-00 to prepare for HS transmission	38	95	ns
CLKP/N	T _{CLK-TERM-EN}	Time-out at Clock Lane to enable HS termination	134	38	ns
CLKP/N	Tolk-prepare + Tolk-zero	Minimum lead HS-0 drive period before starting Clock	300	12	ns
CLKP/N	T _{CLK-PRE}	Time that the HS clock shall be driven prior to any associated Data Lane beginning the transition from LP to HS mode	8xUI	35	ns

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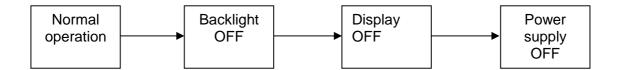


5.2 Power ON/OFF Timing





Power ON sequence



Power OFF sequence

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6 Optical Characteristics

Ta=25°C

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Item	Symbol	Condition	Min	Тур	Max	Unit	Remark	
	θТ		ı	80	ı			
View Angles	θВ	CR≧10	-	80	1	Dograda	Note 2	
view Arigies	θL	CK = 10	-	80	-	Degree	Note 2	
	θR		-	80	ı			
Contrast Ratio	CR	θ=0°	700	900	-	-	Note1 Note3	
Response Time	T _{ON}	25 ℃	25℃		30	40	ms	Note1
Response fille	T _{OFF}		•	30	40	1115	Note4	
Uniformity	U	-	70	80	-	%	Note1 Note6	
NTSC	-	-	-	70	-	%	Note 5	
Luminance	L		250	300	-	cd/m ²	Note1 Note7	

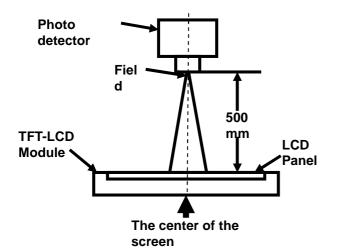
Test Conditions:

- 1. $V_F=12.8V$, $I_F=40mA$, the ambient temperature is 25° C.
- 2. The test systems refer to Note 1 and Note 2.

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Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio		
Luminance	SR-3A	1°
Chromaticity	SK-SA	ı
Lum Uniformity		
Response Time	BM-7A	2°

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Note 2: Definition of viewing angle range and measurement system. viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

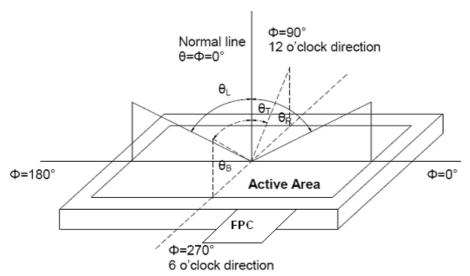


Fig. 1 Definition of viewing angle

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Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state Luminance measured when LCD is on the "Black" state

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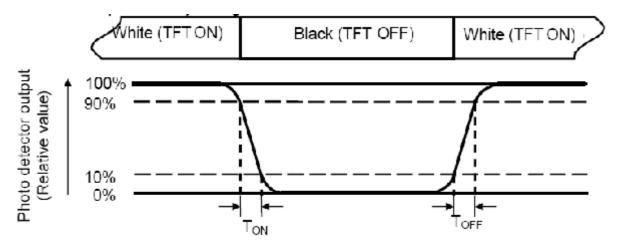
"White state ":The state is that the LCD should be driven by Vwhite.

"Black state": The state is that the LCD should be driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

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Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

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Luminance Uniformity (U) = Lmin/Lmax

L-----Active area length W----- Active area width

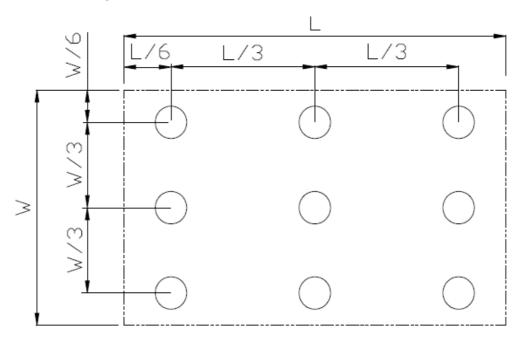


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

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7 Environmental / Reliability Test

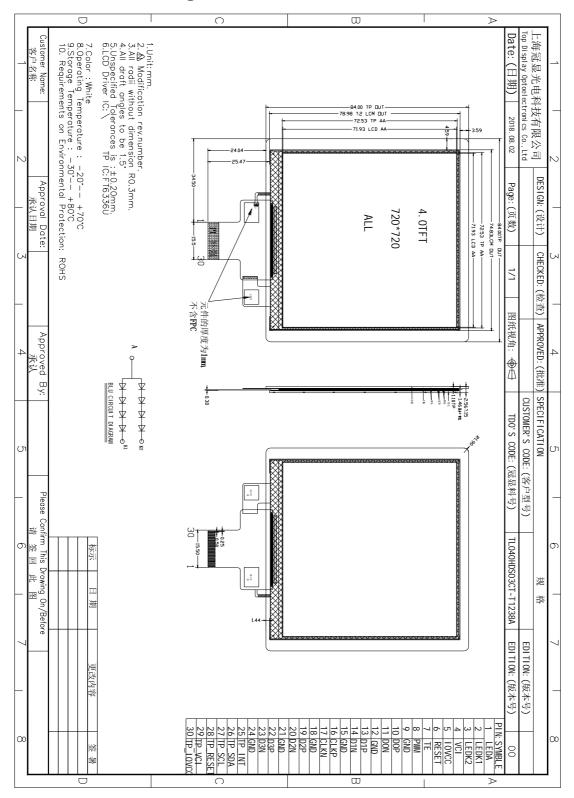
Item	Condition	Time (hrs)	Assessment
High temp. Storage	80°C	120	No abnormalities in functions and appearance
High temp. Operating	70°C	120	
Low temp. Storage	-30°C	120	
Low temp. Operating	-20°C	120	
Humidity	40°C/ 90%RH	120	
Thermal Shock(Non-operation)	-20°C ¬ 25°C ® 70°C (0.5 hour ¬ 5 min ® 0.5 hour)	10cycles	

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8 Mechanical Drawing



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9 Precautions For Use of LCD Modules

- 9.1 Handling Precautions
- 9.1.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

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- 9.1.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 9.1.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 9.1.1.9 Be sure to ground the body when handling the LCD Modules.
- 9.1.1.10 Tools required for assembly, such as soldering irons, must be properly ground.
- 9.1.1.11 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 9.1.1.12 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.
- 9.1.1.13 Storage precautions
- 9.1.1.14 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.1.1.15 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
- 9.1.1.16 Temperature : 0° C \sim 40°C Relatively humidity: \leq 80%
- 9.1.1.17 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- **9.2** Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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