SPECIFICATION FOR APPROVAL

() Preliminai	y Specification
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() I	Final	Sp	ecif	icat	tio	r

Title	1	15.4" WXGA TFT LCD			
BUYER		SUPPLIER	LG.Philips LCD Co., Ltd.		
MODEL		*MODEL	LP154W01		
		Suffix	TLB5		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
/	
Please return 1 copy for your your signature and comments	

APPROVED BY	SIGNATURE
J. H. Lee / S.Manager	
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Product Engineering LG. Philips LCD Co	

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	AUG.08. 2005	-	First Draft	0.0

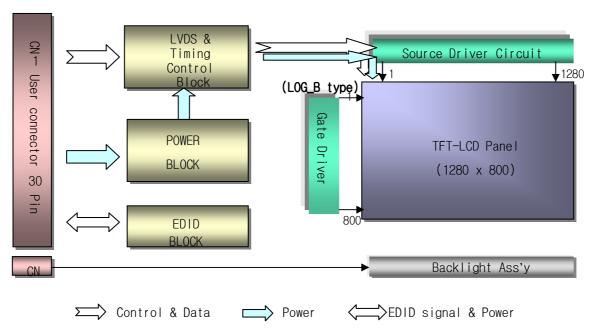


1. General Description

The LP154W01 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.4 inches diagonally measured active display area with WXGA resolution(1280 horizontal by 800 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP154W01 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP154W01 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP154W01 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.4 inches diagonal
Outline Dimension	344.0 (H) × 222.0 (V) × 6.5(D, max) mm
Pixel Pitch	0.25875 mm × 0.25875 mm
Pixel Format	1280 horiz. by 800 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	200 cd/m ² (Typ.) , 5 point
Power Consumption	Total 5.3 Watt(Typ.) @ LCM circuit 1.1 Watt(Typ.), B/L input 4.2 Watt(Typ.)
Weight	540 g (Typ.)
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Glare & hard coating 2H

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2. Absolute Maximum Ratings

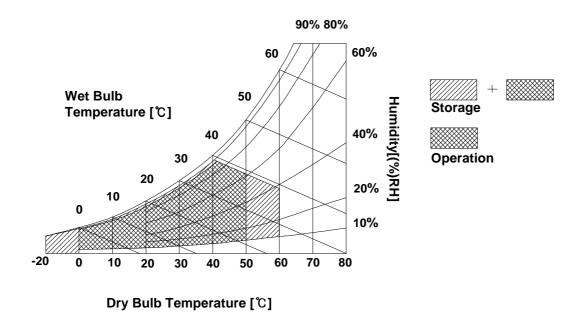
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min Max		Offics	INOLES	
Power Input Voltage	V _{cc}	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP154W01 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Values			Unit	Notes	
Parameter	Symbol	Min	Тур	Max	Offic	Notes	
MODULE :							
Power Supply Input Voltage	V _{cc}	3.0	3.3	3.6	V _{DC}		
Power Supply Input Current	I _{cc}	280	330	380	mA	1	
Power Consumption	Pc	-	1.1	1.3	Watt	1	
Differential Impedance	Zm	90	100	110	Ohm	2	
LAMP:							
Operating Voltage	V_{BL}	3.5	6	6.5	V_{RMS}	3	
Operating Current	I _{BL}	680(6.5mA)	695(6.0mA)	810(3.5mA)	mA _{RMS}	4	
Power Consumption	P _{BL}	-	4.2	4.42		9	
Operating Frequency	f _{BL}	45	60	80	kHz	7	
Discharge Stabilization Time	Ts	-	-	3	Min	5	
Life Time		10,000	.	-	Hrs	6	
Established Starting Voltage at 25 ℃ at 0 ℃	Vs			1170 1400	$V_{ m RMS}$ $V_{ m RMS}$	8	

Note)

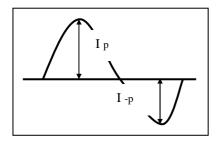
- 1. The specified current and power consumption are under the Vcc = 3.3V, 25 °C, fv = 60Hz condition whereas full black pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The typical operating current is for the typical surface luminance (L_{WH}) in optical characteristics.
- 5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7. The output of the inverter must have symmetrical(negative and positive) voltage waveform and symmetrical current waveform. (Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 9. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

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Note)

- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

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3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model IS100-C30R-C15 manufactured by UJU Elec.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	NC	Reserved for supplier test point	1.1 LCD: SW0602_U(LCD Controller)
6	CIK EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	(UMC, FXLVRX085H90A) 1.2 System : THC63LVD63A or equivalent
8	R _{IN} 0-	Negative LVDS differential data input	* Pin to Pin compatible with THINE LVDS
9	R _{IN} 0+	Positive LVDS differential data input	
10	GND	Ground	2. Connector 2.1 LCD : IS100-C30R-C15 ,UJU Elec. or
11	R _{IN} 1-	Negative LVDS differential data input	its compatibles
12	R _{IN} 1+	Positive LVDS differential data input	2.2 Mating : FI-X30M or equivalent.
13	GND	Ground	2.3 Connector pin arrangement 30 1
14	R _{IN} 2-	Negative LVDS differential data input	│
15	R _{IN} 2+	Positive LVDS differential data input	
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	[LCD Module Rear View]
18	CLKIN+	Negative LVDS differential clock input	
19	GND	Ground	
20	NC	No connect	
21	NC	No connect	
22	NC	No connect	
23	NC	No connect	
24	NC	No connect	
25	NC	No connect	
26	NC	No connect	
27	NC	No connect	
28	NC	No connect	
29	NC	No connect	
30	NC	No connect	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

			. ,
Pin Symbol		Description	Notes
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored White and the low voltage side terminal is Blue.

Condition: VCC =3.3V



Product Specification

3-3. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	66.9	71.1	75.4	MHz	
Hsync	Period	tHP	1380	1440	1496		
	Width		16	32	40	tclk	
	Active	twha	1280	1280	1280		
Vsync	Period	tvp	808	823	840		
	Width	tw∨	2	6	6	tHP	
	Active	twva	800	800	800		
Data	Horizontal back porch	tHBP	68	80	120	tour	
Enable	Horizontal front porch	tHFP	16	48	56	tCLK	
	Vertical back porch	tvbp	5	15	32	tHP	
	Vertical front porch	tvfp	1	2	2	I IHP	

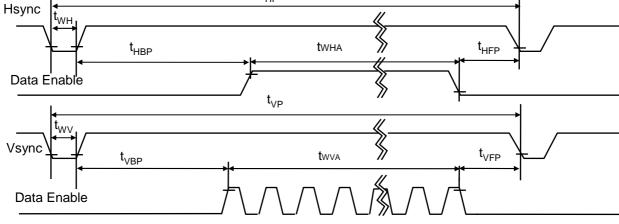
3-4. Signal Timing Waveforms

0.5 Vcc

Data Enable, Hsync, Vsync

DCLK

High: 0.7VCC
Low: 0.3VCC



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3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	00101		3				LSB		3				LSB	MSE	3				LSB
	,	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	B 3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0		0	0	0	0	0		0	0	0
	Red	1	1	.1	. 1	. 1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED																			
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN																			
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE											 								
ļ.	BLUE (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1	 1	 1		0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	 1		 1	1	1
	- (/							<u> </u>											

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3-6. Power Sequence

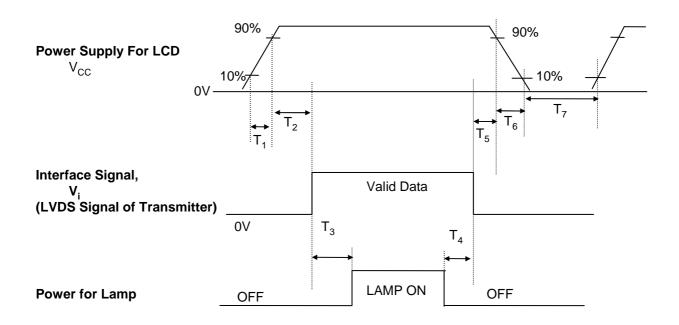


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	-	•	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	•	1	(ms)
T ₄	200	•	-	(ms)
T ₅	0	•	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

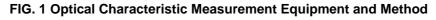
- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



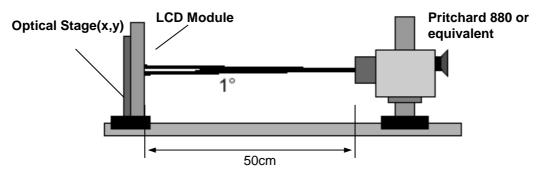


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, V_{CC} =3.3V, f_{V} =60Hz, f_{CLK} = 71.1MHz, lout = 6.0mA

Down to the second	0		Values	CLK	Links	Mataa
Parameter	Symbol	Min	Тур	MAx	Units	Notes
Contrast Ratio	CR	300	550	-		1
Surface Luminance, white	L _{WH}	170	200		cd/m ²	2
Luminance Variation	δ_{WHITE}	-	1.8	2.0		3
Response Time						4
Rise Time+Decay Time	$Tr_{R+}Tr_{D}$		16	25	ms	
Color Coordinates						±0.03
RED	RX	0.560	0.590	0.620		
	RY	0.314	0.344	0.374		
GREEN	GX	0.294	0.324	0.354]	
	GY	0.505	0.535	0.565		
BLUE	ВХ	0.127	0.157	0.187		
	BY	0.108	0.138	0.168		
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle						5
x axis, right(Φ =0°)	Θr	60	65	-	degree	
x axis, left (Φ=180°)	Θl	60	65	-	degree	
y axis, up (Φ =90°)	Θu	45	50	-	degree	
y axis, down (Φ=270°)	Θd	45	50	-	degree	
Gray Scale						6

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Note)

1. Contrast Ratio(CR) is defined mathematically as Surface Luminance with all white pixels

Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the 5point (1~5) average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2. When I_{BL} = 6.0mA, L_{WH} =200cd/m²(typ.)
- 3. Luminance % uniformity is measured for 13 point For more information see FIG 2. δ WHITE = Maximum(LN1,LN2, LN13) ÷ Minimum(LN1,LN2, LN13)
- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

* f_{\/}=60Hz

Gray Level	Luminance [%] (Typ)
LO	0.12
L7	0.69
L15	4.46
L23	11.6
L31	21.4
L39	35.7
L47	54.9
L55	77.3
L63	100



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

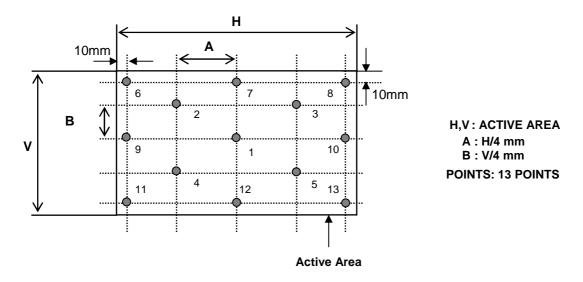
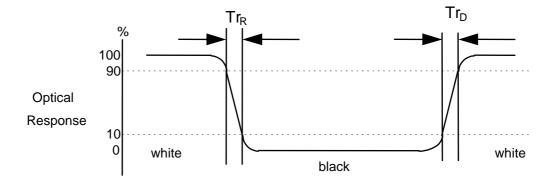


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

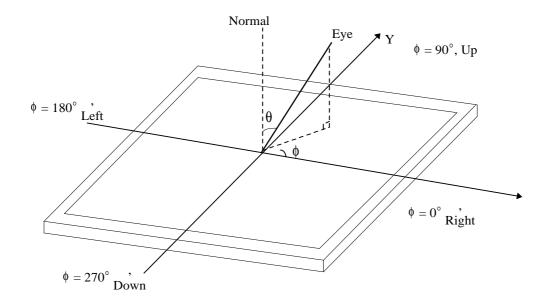


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FIG. 4 Viewing angle

<Dimension of viewing angle range>





5. Mechanical Characteristics

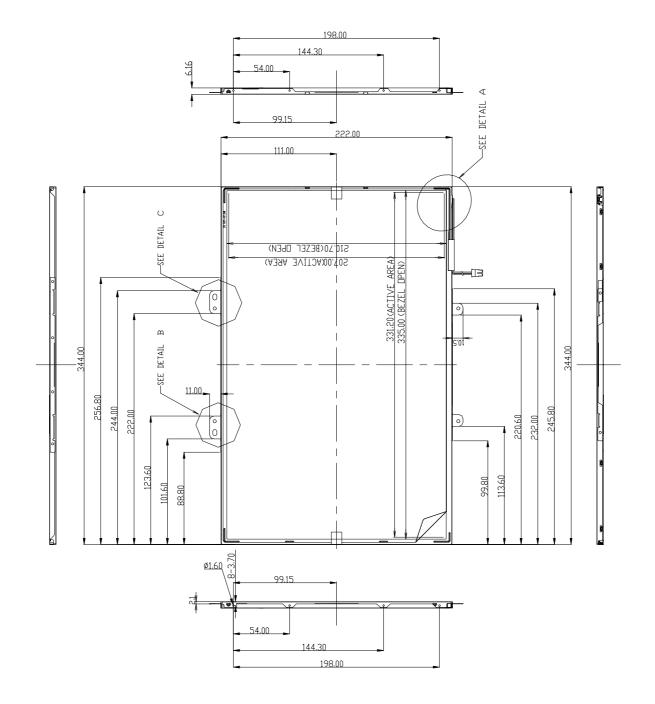
The contents provide general mechanical characteristics for the model LP154W01-TLB5. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	344.0 ± 0.5mm				
Outline Dimension	Vertical	222.0 ± 0.5mm				
	Depth	6.5(MAX)				
Bezel Area	Horizontal	335.0 ± 0.5mm				
bezei Alea	Vertical	210.7 ± 0.5mm				
Active Display Area	Horizontal	331.2 mm				
Active Display Area	Vertical	207.0 mm				
Weight	555g (MAX)					
Surface Treatment	Glare & hard coating 2H					



<FRONT VIEW>

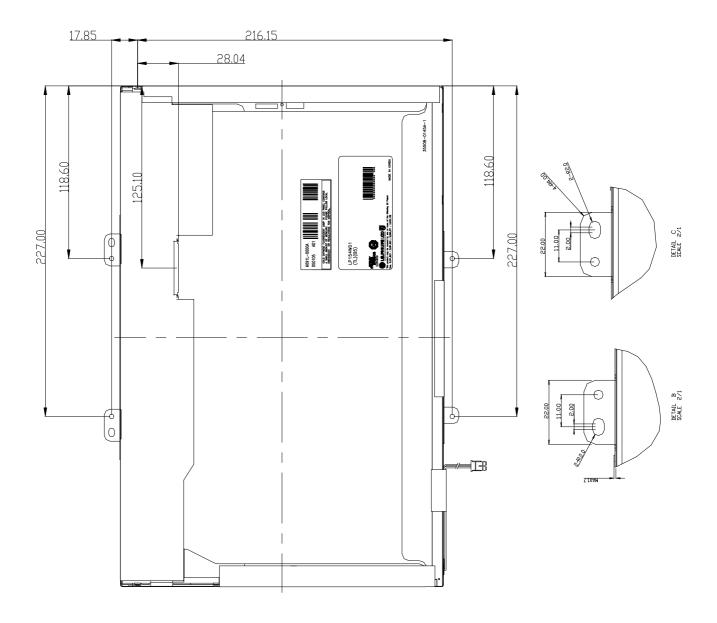
Note) Unit:[mm], General tolerance: \pm 0.5mm





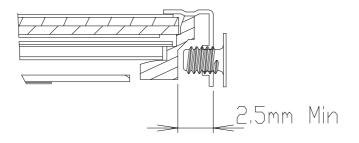
<REAR VIEW>

Note) Unit:[mm], General tolerance: \pm 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]



*SCREW(8EA) TORQUE : 2.0kgf.cm max *Screw Hole Depth : 2.5mm min *Screw Length : max 2.5, min2.0

Note) Unit:[mm], General tolerance: ± 0.5mm

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6. Reliability

Environment test condition

No.	Test Item	Conditions					
1	High temperature storage test	Ta= 60°C, 240h					
2	Low temperature storage test	Ta= -20°C, 240h					
3	High temperature operation test	Ta= 50°C, 50%RH, 240h					
4	Low temperature operation test	Ta= 0°C, 240h					
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis					
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays					
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr					

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R. "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

А	В	С	D	Е	F	G	Н	I	J	К	L	М
							1 1			1 1	1 1	

A,B,C : SIZE(INCH) D : YEAR

E: MONTH F: FACTORY CODE

G: ASSEMBLY CODE H, I, J, K, L, M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Ī	Mark	1	2	3	4	5	6	7	8	9	Α	В	С

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG
Mark	K	С	D

4. SERIAL NO.

Mark	100001~199999, 200001~299999, 300001~399999,, A00001~A99999,, Z00001~Z99999
------	---

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 10 pcs

b) Box Size: 441mm ×373mm × 348mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

		LP154W01-TLB5 E-EDID DATA (ver0.1)			2005-04-01
Byte#	Byte#	Field News and Comments	Val	ue	Value	
(decimal)	(HEX)	Field Name and Comments	(HE	X)	(binary)	
0	00	Header	0	0	0000 0000	
1	01		F	F	1111 1111	
2	02		F	F	1111 1111	
3	03		F	F	1111 1111	Header
4	04		I E I	F.	1111 1111	
5	05		I F	F.	1111 1111	
6 7	06 07		F 0	F 0	1111 1111 0000 0000	
8	08	EISA manufacturer code = LPL	3	2	0011 0010	
9	09	Compressed ASCII	0	C	0000 1100	
10	0A	Panel Supplier Reserved - Product code	l ö l	ŏ	0000 0000	
11	0B		101	0	0000 0000	
		(Hex, LSB first)	10			Uandar/
12	0C	LCD Module Serial No. = 0(If not used)	++	0	0000 0000	Vender/
13	OD OE	LCD Module Serial No. = 0(If not used)	0		0000 0000	Product ID
14	OE	LCD Module Serial No. = 0(If not used)	<u> </u>	0	0000 0000	
15	0F	LCD Module Serial No. = 0(If not used)	0	0	0000 0000	
16	10	Week of manufacture	0	0	0000 0000	
17	11	Year of manufacture = 2005	+	F	0000 1111	EDID # : ·
18	12	EDID Structure version # = 1		<u>1</u> 2	0000 0001	EDID Version/
19	13	EDID Revision # = 2	-		0000 0010	Revision
20 21	14 15	Video input definition = Digital I/p,non TMDS CRGB	2	1	1000 0000 0010 0001	Dioplan
22	16	Max H image size(cm) = 33.12cm(33) Max V image size(cm) = 20.70cm(21)	1	5	0001 0101	Display Parameter
23	17	Display gamma = 2.2	171	8	0111 1000	ı arameter
24	18	Feature support(DPMS) = Active off, RGB Color	tit	Ā	0000 1010	
25	19	Red/Green low Bits	10	F	0000 1111	
26	1A	Blue/White Low Bits	1	0	0001 0000	
27	1B	Red X Rx = 0.590	9	7	1001 0111	
28	10	Red Y Ry = 0, 344	5	8	0101 1000	
29	1D	Green X Gx = 0.324	5	2	0101 0010	Color
30	1E	Green Y Gy = 0.535	8	8	1000 1000	Characteristic
31	1F	Blue X Bx = 0.157	2	8	0010 1000	
32	20	Blue Y By = 0.138	2	3	0010 0011	
33	21	White X	5	0	0101 0000	
34	22	White Y Wy = 0.329	5	4	0101 0100	Catabliahad
35 36	23 24	Established Timing I Established Timing II		0	0000 0000 0000 0000	Established
	t			 O	0000 0000	Timings
37	25	Manufacturer's Timings	+ - +	_		
38 39	26 27	Standard Timing Identification 1 was not used	0	<u>1</u> 1	0000 0001 0000 0001	
		Standard Timing Identification 1 was not used				
40	28	Standard Timing Identification 2 was not used	0	1	0000 0001	
41	29	Standard Timing Identification 2 was not used	101	1	0000 0001	
42	2A	Standard Timing Identification 3 was not used	10	1	0000 0001	
43	2B	Standard Timing Identification 3 was not used	-++-	1	0000 0001	
44	2C	Standard Timing Identification 4 was not used	0	1	0000 0001	Standard
45	2D	Standard Timing Identification 4 was not used	101	1	0000 0001	Timing ID
46	2E	Standard Timing Identification 5 was not used	-++-	1	0000 0001	
47	2F	Standard Timing Identification 5 was not used	0	1	0000 0001	
48	30	Standard Timing Identification 6 was not used	0	1	0000 0001	
49	31	Standard Timing Identification 6 was not used	0	1	0000 0001	
50	32	Standard Timing Identification 7 was not used	0	1	0000 0001	
51	33	Standard Timing Identification 7 was not used	0	1	0000 0001	
52	34	Standard Timing Identification 8 was not used	0	1	0000 0001	
53	35	Standard Timing Identification 8 was not used	0	1	0000 0001	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 2/3

Byte#	Byte#	Field Name and Comments	Va	lue	Value	
(decimal)	(HEX)	Field Name and Comments	(HE	X)	(binary)	
54	36	Detailed Timing Descriptor #1	D	5	1101 0101	
55	37	1280 × 800 @ 60Hz mode : pixel clock = 71.25Mb	1	В	0001 1011	
56	38	Horizontal Active = 1280 pixels	0	0	0000 0000	
57	39	Horizontal Blanking = 160 pixels	Α	0	1010 0000	
58	3A	Horizontal Active : Horizontal Blanking = 1280 : 160	5	0	0101 0000	
59	3B	Vertical Avtive = 800 lines	2	0	0010 0000	
60	3C	Vertical Blanking = 23 lines	1	7	0001 0111	Detailed
61	3D	Vertical Active: Vertical Blanking = 800: 23	3	0	0011 0000	Timing
62	3E	Horizontal Sync. Offset = 48 pixels	3	0	0011 0000	Description
63	3F	Horizontal Sync Pulse Width = 32 pixels	2	0	0010 0000	# 1
64	40	Vertical Sync Offset = 4 lines, Sync Width = 6 lines	2	6	0010 0110	
65	41	Horizontal Vertical Sync Offset/Width upper 2bits = 0	0	0	0000 0000	
66	42	Horizontal Image Size = 33.12cm(33)	2	1	0010 0001	
67	43	Vertical Image Size = 207mm(21)	1	5	0001 0101	
68	44	Horizontal & Vertical Image Size	1	0	0001 0000	
69	45	Horizontal Border = 0	0	0	0000 0000	
70	46	Vertical Border = 0	0	0	0000 0000	
71	47	Hon−interlaced, Hormal display, no stereo, Digital separate sync, H/V pol negatives	1	9	0001 1001	
72	48	Detailed Timing Descriptor #2	0	0	0000 0000	
73	49		0	0	0000 0000	
74	4A		0	0	0000 0000	
75	4B		0	0	0000 0000	
76	4C		0	0	0000 0000	
77	4D		0	0	0000 0000	
78	4E		0	0	0000 0000	Detailed
79	4F		0	0	0000 0000	Timing
80	50		0	0	0000 0000	Description
81	51		0	0	0000 0000	# 2
82	52		0	0	0000 0000	
83	53		0	0	0000 0000	
84	55		0	0	0000 0000	
85	55		0	0	0000 0000	
86	56		0	0	0000 0000	
87	57		0	0	0000 0000	
88	58		0	0	0000 0000	
89	59		0	0	0000 0000	
90		Detailed Timing Descriptor #3	0	0	0000 0000	
91	5B		0	0	0000 0000	
92	5C		<u></u>	0	0000 0000	
93	5D		F.	E	1111 1110	
94	5E		0	0	0000 0000	
95	5F	L	4	<u> </u>		
96	60	G	4	7	0100 0111	Detailed
97	61	P	5	0	0101 0000	Timing
98	62	H	6	. 8	0110 1000	Description
99	63		6	9	0110 1001	# 3
100	64	L	6	<u>. c</u>	0110 1100	
101	65	 	6	9	0110 1001	
102	66	P	7	<u>0</u>	0111 0000	1
103	67	<u> </u>	7	3	0111 0011	
104	68	L .	4	<u></u>	0100 1100	
105	69	<u>C</u>	4	3	0100 0011	
106	6A	D	4	4	0100 0100	
107	6B	LF	0	Α	0000 1010	



APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

Byte#	Byte#	Field Name and Comments	Value		Value	
(decimal)	(HEX)	Field Name and Comments	(HE	EX)	(binary)	
108	6C	Detailed Timing Descriptor #4	0	0	0000 0000	
109	6D		0	0	0000 0000	
110	6E		0	0	0000 0000	
111	6F		F	E	1111 1110	
112	70		0	0	0000 0000	
113	71	L	4	C	0100 1100	
114	72	P	5	0	0101 0000	Detailed
115	73	1	3	1	0011 0001	Timing
116	74	5	3	5	0011 0101	Description
117	75	4	3	4	0011 0100	#4
118	76	₩	5	7	0101 0111	
119	77	0	3	0	0011 0000	
120	78	1	3	1	0011 0001	
121	79	-	2	D	0010 1101	
122	7A	T	5	4	0101 0100	
123	7B	L	4	С	0100 1100	
124	7C	В	4	2	0100 0010	
125	7D	5	3	5	0011 0101	
126	7E	Extension flag = 00	0	0	0000 0000	Extension Flag
127	7F	Checksum	0	4	0000 0100	Checksum

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