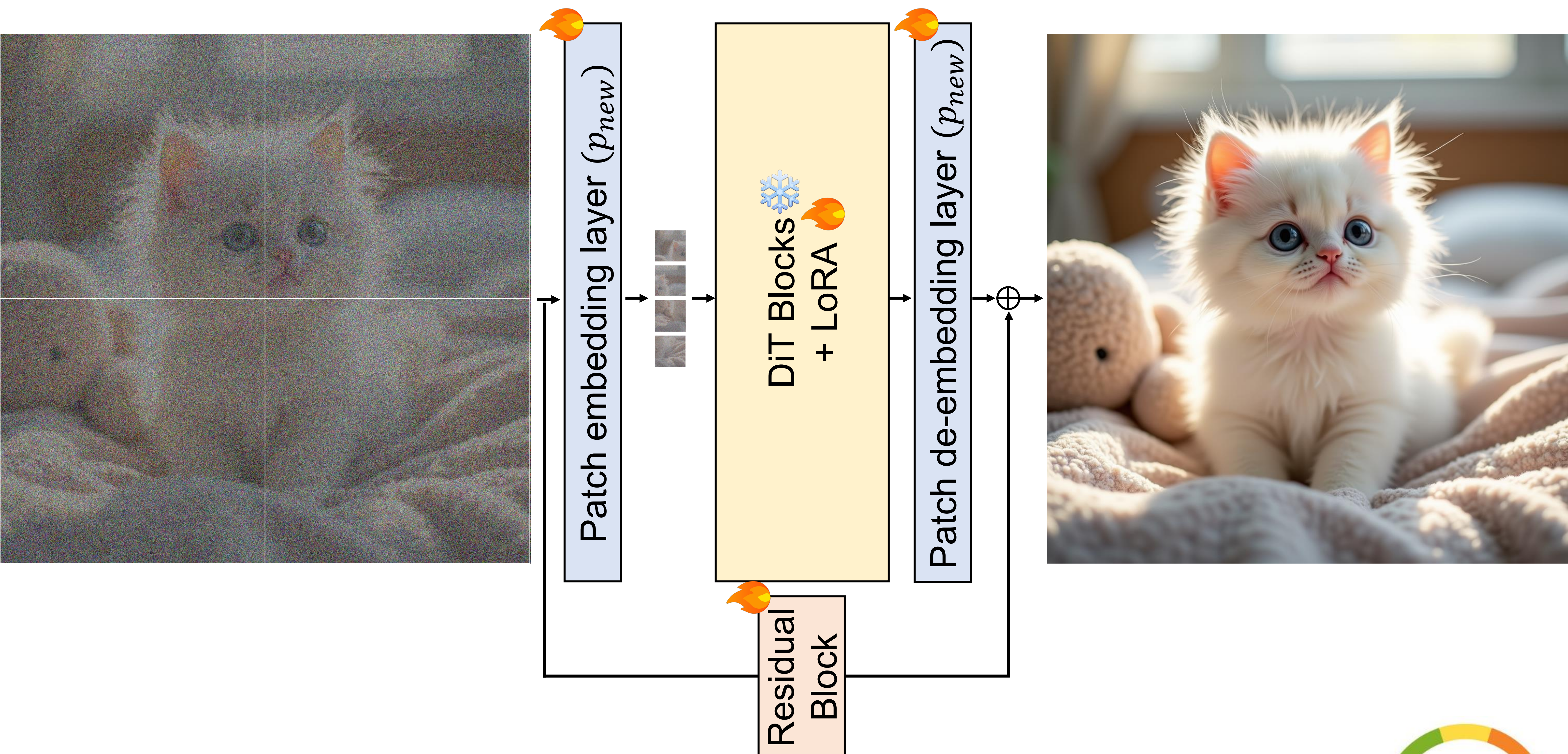


(a) patch-embedding layer of standard diffusion transformers (DiTs)



(b) patch-embedding layer of DDiT (Ours)

