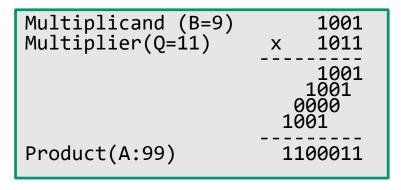
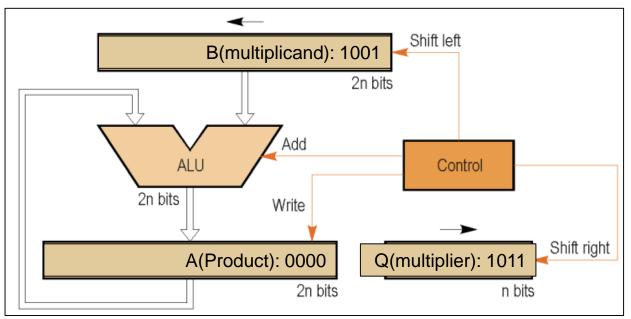
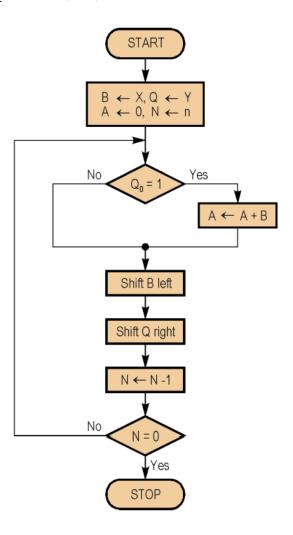
n-bit Add & Shift Multiplier Lab (ver1)

Product(A) Multiplicand(B) * Multiplier(Q)





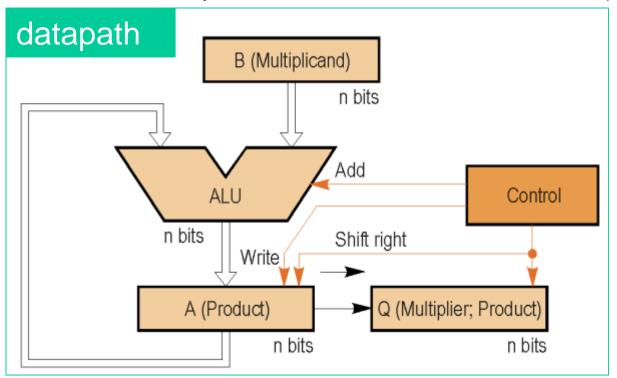


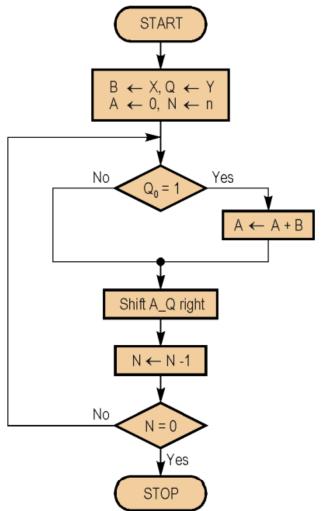
n-bit Add & Shift Multiplier Lab (ver2)

• {N, Q}

← A * B

Step	Α	Q	В	Operation
0	0000	110 <u>0</u>	1001	Initialization
1	0000	011 <u>0</u>	1001	Shift right A_Q
2	0000	001 <u>1</u>	1001	Shift right A_Q
3	1001	001 <u>1</u>	1001	Add B to A
	0100	100 <u>1</u>	1001	Shift right A_Q
4	1101	100 <u>1</u>	1001	Add B to A
	0110	1100	1001	Shift right A_Q





N-bit Multiplier Extension Lab

