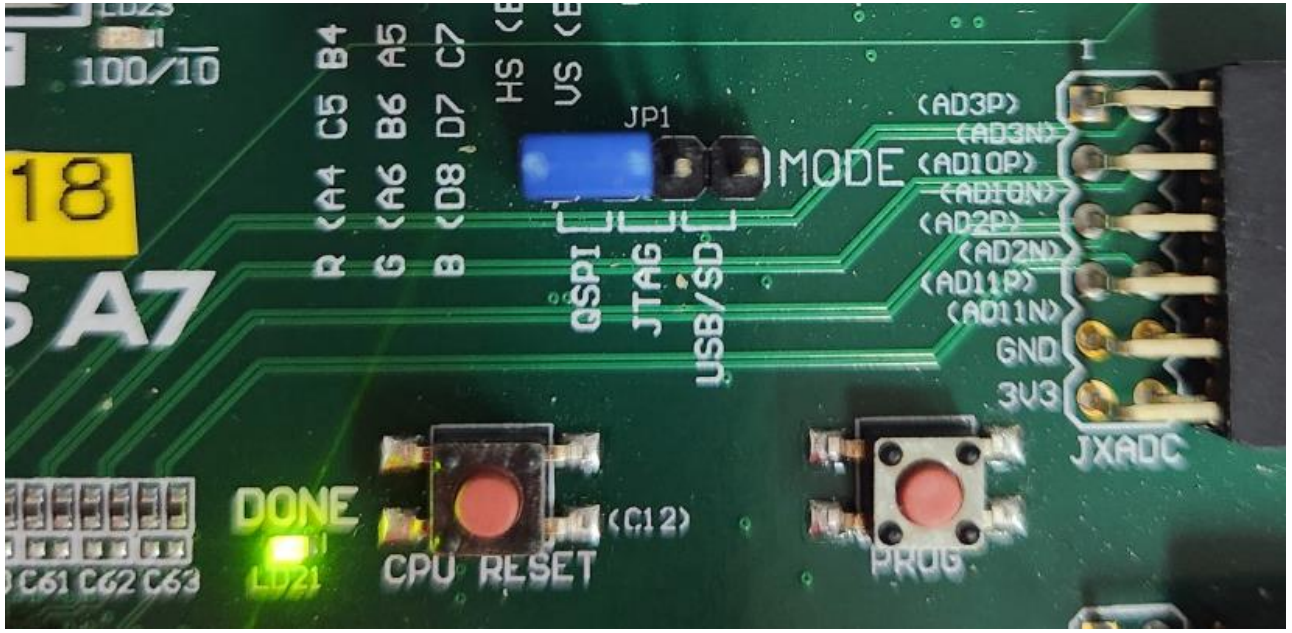
How to Program Board with QSPI flash

1. Board Setting: Change board JP1 jumper Setting
2. Software Setting : Change Vivado Setting to generate bin file
3. Generate Bin file: Run Synthesis, Implemenation, & bitstream Generation
4. Connect FPGA board and PC with USB and turn ON the power of the board
5. Open Hardware Manager and Program the board
    1) Connect Board
    2) Add Configuration Memory Device
    3) Program Configuration Memory
6. Run the design
    1) Push the board Programming button (PROG)
    2) Wait a few seconds !
    3) Watch and enjoy the demo

1. FPGA board Setting (JP1)

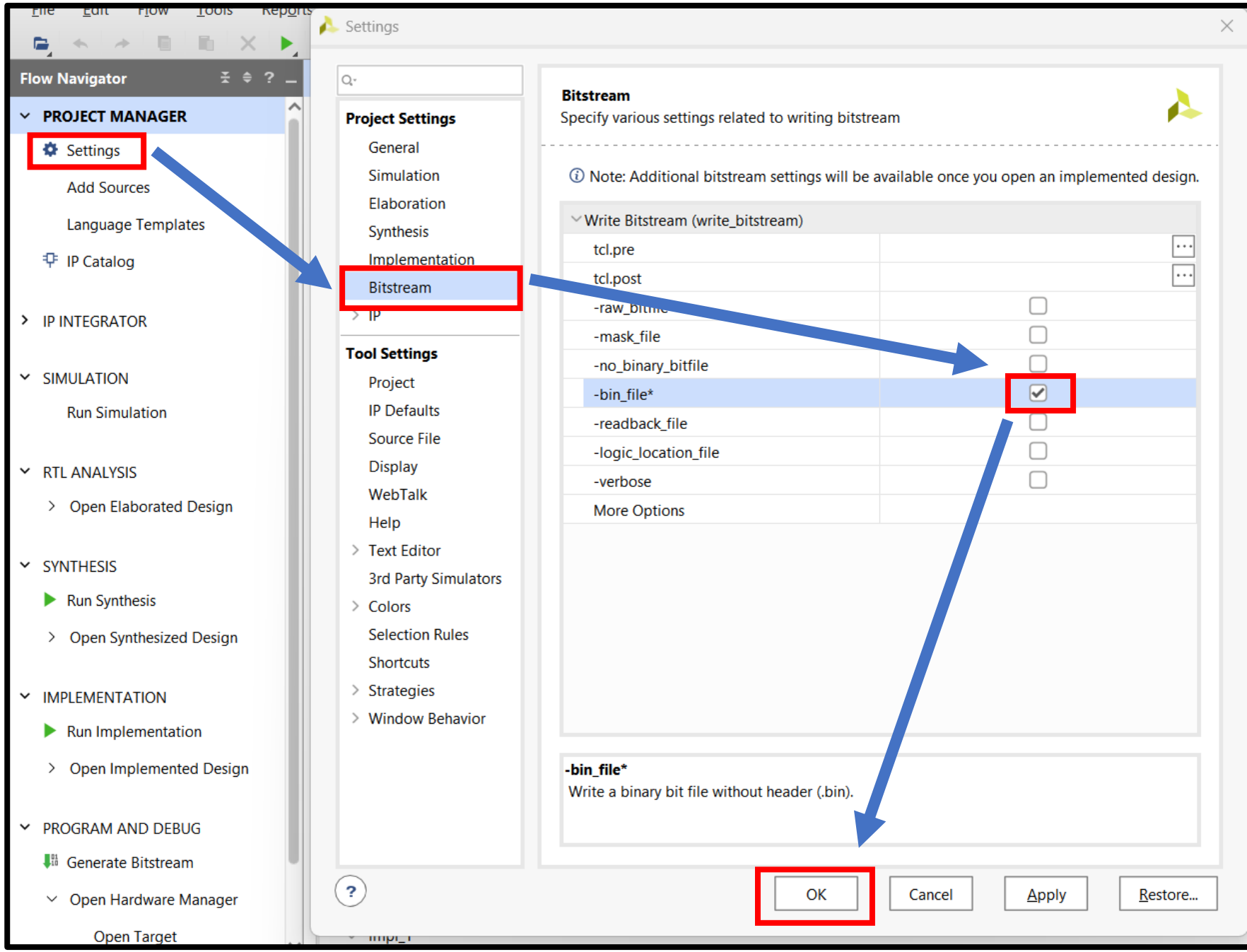
- JP1 : set Jumper as QSPI mode



2. Vivado Setting

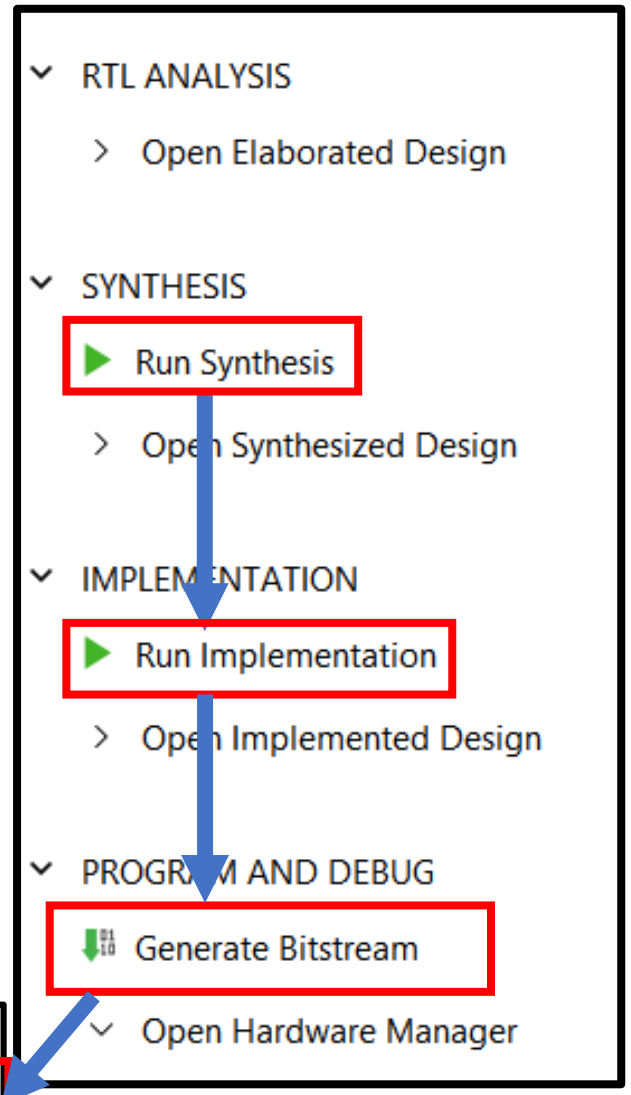
- Project Setting
  → Bitstream
  → Check **-bin_file**

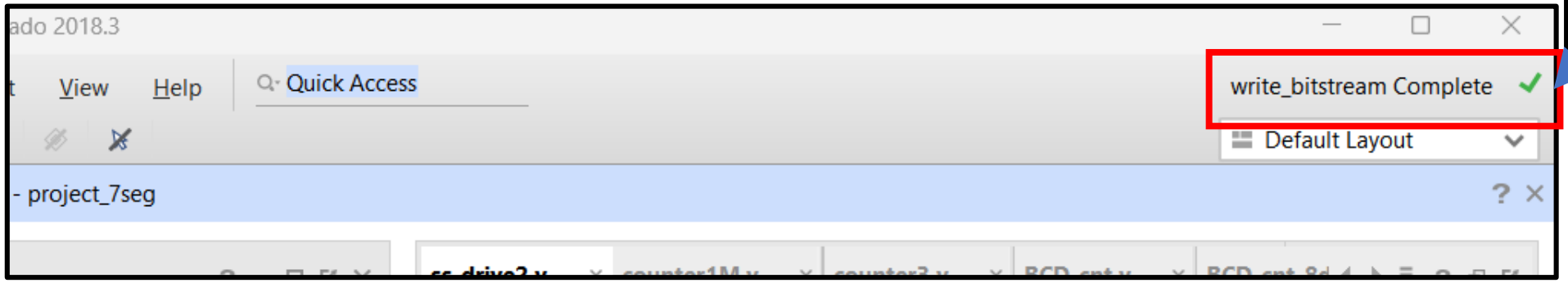
- It will result in both *bit* and *bin* file generation



3. Generate Bitstream

- Run Synthesis
  → Implemenation
  → Generate Bitstream
- Under the *project*/*project*.runs/Impl_1 folder: you can find both **bit** and **bin** files
  1) ***top_module*.bin**
  2) top_module.bit

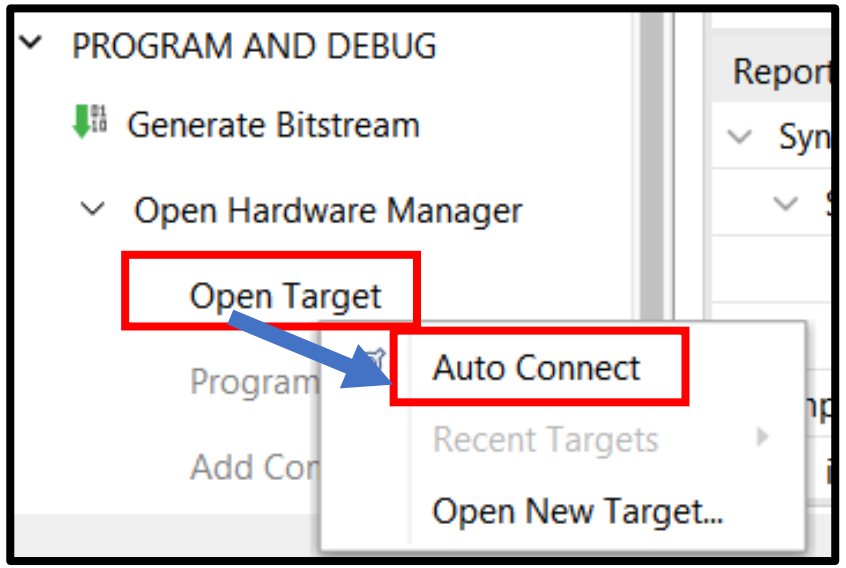


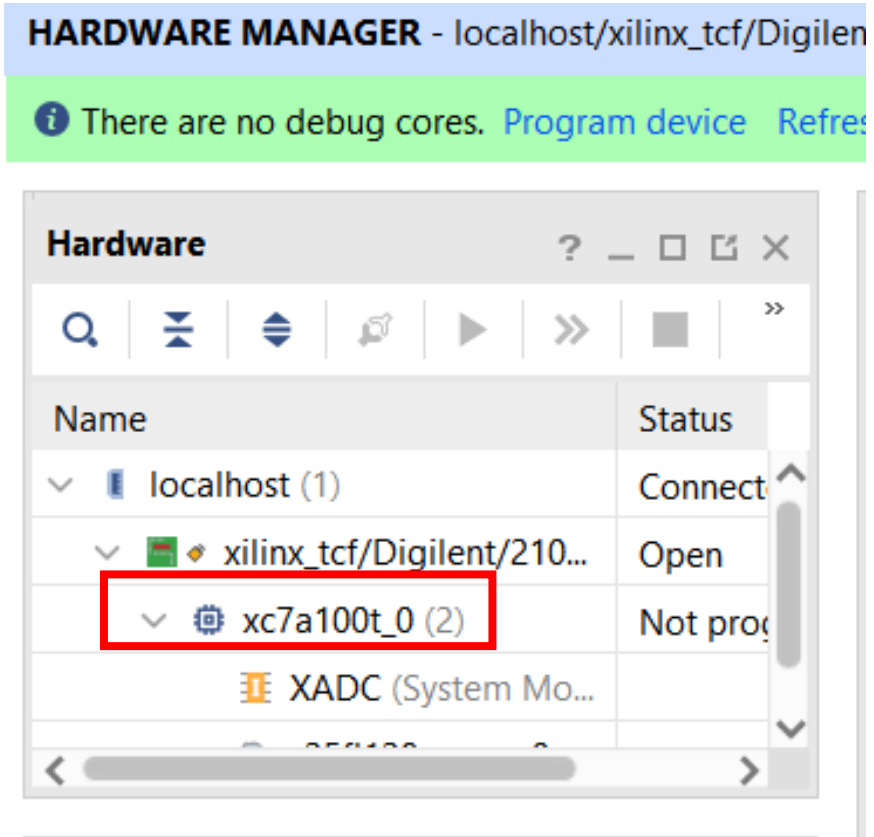


4. Connect and Turn ON the Power

5. Open Hardware Manager
1) Connect Board

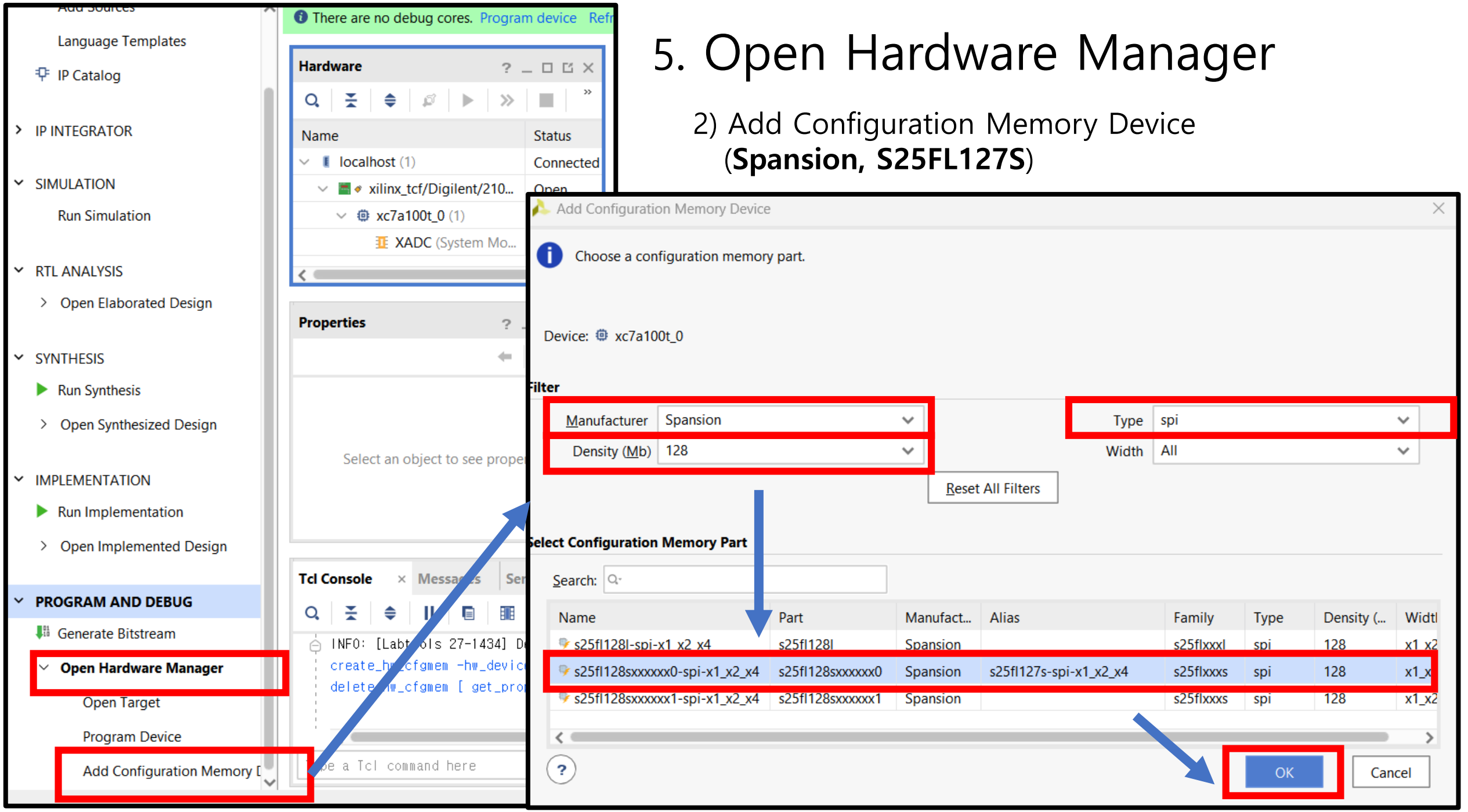
- Open Hardware Manager → Open Target → Auto Connect
- Confirm the Proper Board type and FPGA Device were detected





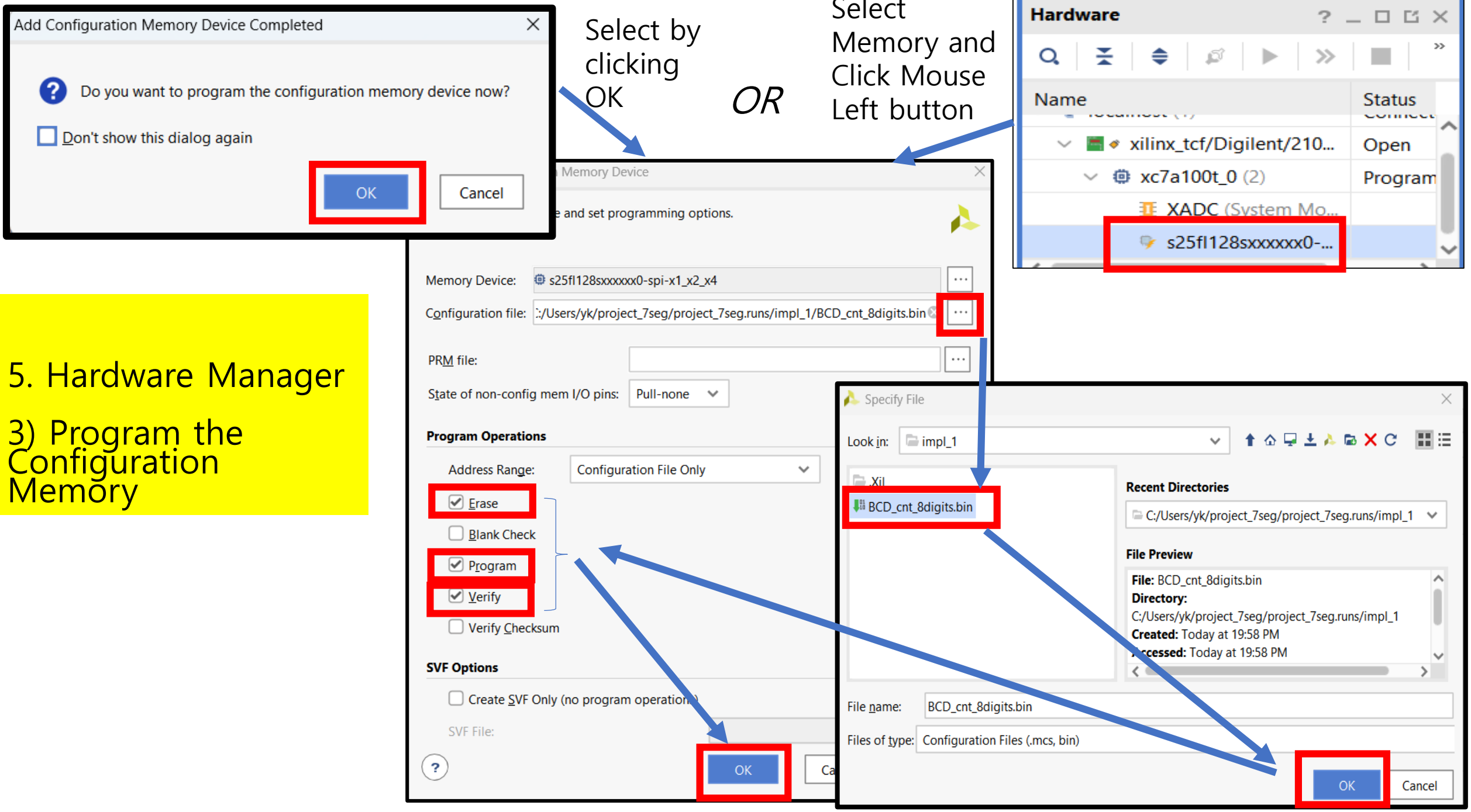
5. Open Hardware Manager

2) Add Configuration Memory Device (**Spansion, S25FL127S**)



5. Hardware Manager

3) Program the Configuration Memory



6. Run

1) PRESS PROG button of FPGA Borad

2) Wait for 10 seconds