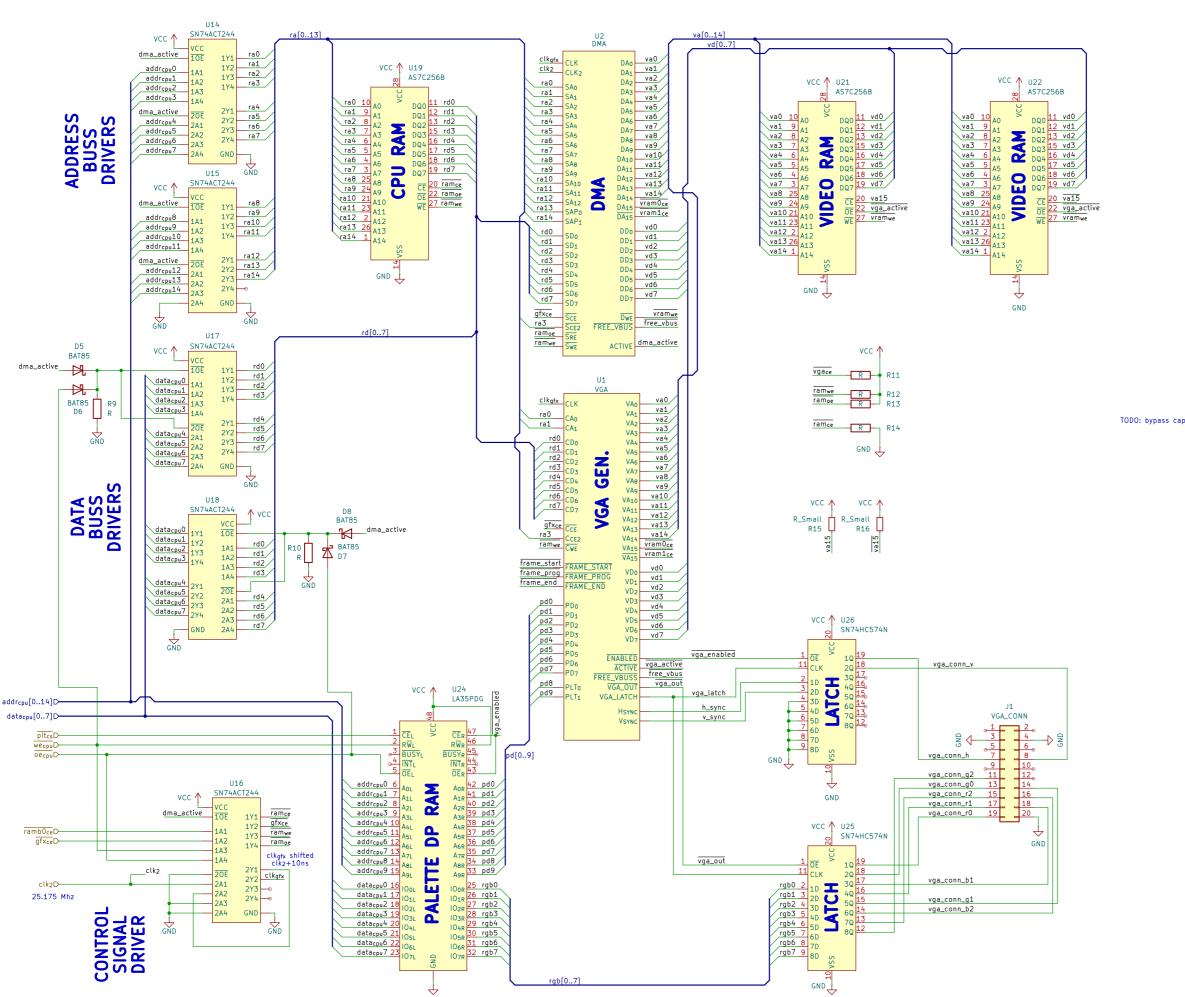
>clk2	
_	
>addr _{cpu} [015]	
⊃datacpu[07]	gfxfr_start[
	gfxfr_prog□
	gfxfr_end[
	gf×dma_act
<mark>pltcs</mark>	
oramb0ce	
gfxce	
Wecpu	
Oecpu	

File: gfx_module.kicad_sch



GND

TODO: bypass caps, coupling caps, resistance conrete values

frame_start	D -6
frame_prog	Dgfxfr_start Dgfxfr_prog
frame_end	Dqfxfr_prog
dma_active	Dafydma ac