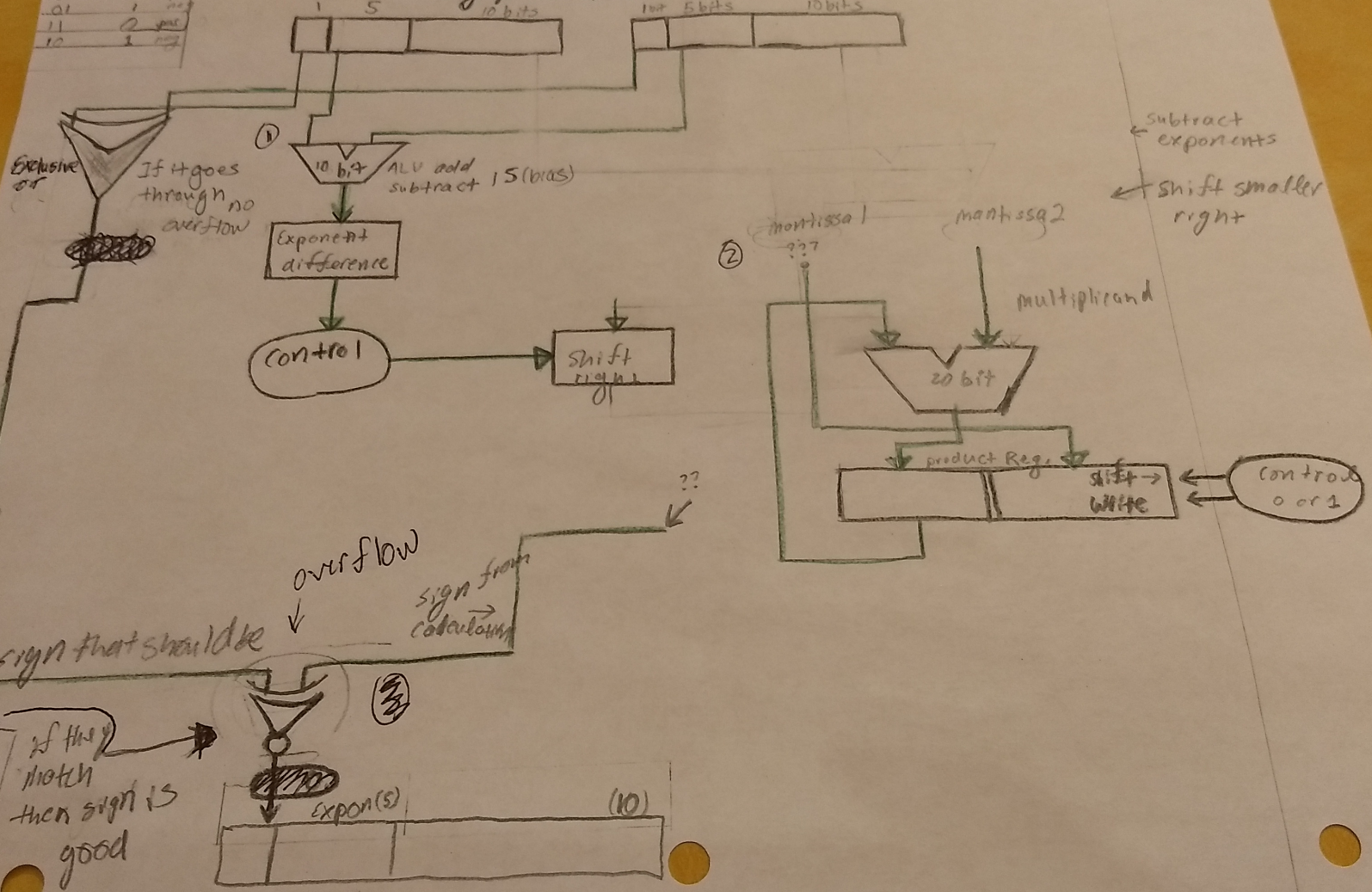


Why OR gate??

00	0	neg
01	1	neg
11	0	pos
10	1	pos

# floating point V.1

Steps



Ex1 NOR

00	1
01	0
10	0
11	1