1.测试结果输入输出说明

（1）测试1

输出：(调用init()函数)"-------init-------" "please enter int & char values:"

输入：一个整型 一个字符型(分别存进：\_global\_int，\_GLOBAL\_cHAr)(测试是输入3，d)

输出："int: "3 "char: "d "am I right"\* "-------fibonacci-------"21 (调用fib函数，传入的整型为\_GLOBAL\_ConstInt，在程序中为const常量，值为7)调用if\_test函数) "-------if test-------" "please enter a tester value:"

输入：一个整型(测试时输入4)

输出："if-test\_1""if-test\_2""if-test\_3""if-test\_4""if-test\_5""if-test\_6""if-test\_7""if-test\_8""if-test\_9""if-test\_10""if-test passed!""please check input tester value:"4

(调用loop函数)"-------loop test-------" "please input 3 int(the 3rd int must >0):" "original value: "1"finally, i value: "51"finally, j value:"0"after loop, original value changes to: "51

输入：三个整型(测试时输入1,10,5)

输出： (调用case\_test) "-------case test-------" "please input int(0-9) & char values(a-e):"

输入：一个整型，一个字符(测试时输入3，b)

输出："case 3 test: "101"test\_num passed!""case 'b' test: "B"test\_char passed!" (调用array\_test函数) "-------array test------" "please input 10 char:"

输入：abcdefghij(随后调用setAverage函数)

输出："set average ended""average of input char: "101"array test passed!" "ALL TESTS FINISHED!" "-------program end-------"

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| mars结果 |
| Reset: reset completed.  "-------init-------""please enter int & char values:"3  d"int: "3"char: "d"am I right"\*"-------fibonacci-------"21"-------if test-------""please enter a tester value:"4  "if-test\_1""if-test\_2""if-test\_3""if-test\_4""if-test\_5""if-test\_6""if-test\_7""if-test\_8""if-test\_9""if-test\_10""if-test passed!""please check input tester value:"4"-------loop test-------""please input 3 int(the 3rd int must >0):"1  10  5  "original value: "1"finally, i value: "51"finally, j value:"0"after loop, original value changes to: "51"-------case test-------""please input int(0-9) & char values(a-e):"3  b"case 3 test: "101"test\_num passed!""case 'b' test: "B"test\_char passed!""-------array test------""please input 10 char:"abcdefghji"set average ended""average of input char: "101"array test passed!""ALL TESTS FINISHED!""-------program end-------"  -- program is finished running -- |

（2）测试2

输出：(调用test\_constdecl\_constdef) "test\_constdecl\_constdef success!\n"（调用test\_vardecl\_vardef）"test\_vardecl\_vardef success!\n""test\_vardecl\_vardef end!\n”（调用test\_array）"a[10]="1"a[i]="100hi"test\_array success!\n""test\_array end!\n"（调用test\_array2）cc#$%&'()\*+,-./0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^\_`abcdefghijklmnopqrstuvwxyz{|}~"a""a""a""test\_array2 end!\n"（调用test\_charfunc）c"test\_charfunc and return success!\n"（调用test\_intfunc）12"test\_intfunc and return success!\n"（调用test\_voidfunc）"test\_voidfunc success: var1="2"test\_void\_func end!\n"

输入：2个整型(测试时输入3,4)

输出：（调用test\_scanf）7"test\_scanf success!\n""test\_scanf end!\n"（调用test\_case1）"test\_case1 success!\n""test case1 end!\n"

输入：一个整型(测试时输入3)

输出：（调用test\_case2）"scanf test\_case2 number:\n"

输入：2个整型(测试时输入10,5)

输出："input an operator: char::\n"

输入：+

输出："result is "15"input an operator: char::\n"

输入：-

输出："result is "5"input an operator: char::\n"

输入：/

输出："result is "2"input an operator: char::\n"

输入：8

输出："x+y="15"-x+y="-5"-(x+y)="-15" test case2 end!\n""\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*simple test end\*\*\*\*\*\*\*\*\*\*\*\*""\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*start complex test\*\*\*\*\*\*\*\*\*"

输入：4

输出：（调用factor）24"factor success!\n"

输入：8

输出：（调用fib）34"fib success!\n"

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| mars结果 |
| "test\_constdecl\_constdef success!\n""test\_constdecl\_constdef end!\n""test\_vardecl\_vardef success!\n""test\_vardecl\_vardef end!\n""a[10]="1"a[i]="100hi"test\_array success!\n""test\_array end!\n"cc#$%&'()\*+,-./0123456789:;<=>?@ABCDEFGHIJKLMNOPQRSTUVWXYZ[\]^\_`abcdefghijklmnopqrstuvwxyz{|}~"a""a""a""test\_array2 end!\n"c"test\_charfunc and return success!\n"12"test\_intfunc and return success!\n""test\_voidfunc success: var1="2"test\_void\_func end!\n"3  4  7"test\_scanf success!\n""test\_scanf end!\n""test\_case1 success!\n""test case1 end!\n"3  "scanf test\_case2 number:\n"10  5  "input an operator: char::\n"+"result is "15"input an operator: char::\n"-"result is "5"input an operator: char::\n"/"result is "2"input an operator: char::\n"8"x+y="15"-x+y="-5"-(x+y)="-15"test case2 end!\n""\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*simple test end\*\*\*\*\*\*\*\*\*\*\*\*""\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*start complex test\*\*\*\*\*\*\*\*\*"4  24"factor success!\n"8  34"fib success!\n" |

2.mars代码

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| .text  ori $fp $sp 0  li $t9 0x7fffeffc #global stack bottom  li $t8 0x10010000 #save word  li $t0 7 #\_global\_constint  sw $t0 ($sp)  subi $sp $sp 4  li $t0 42 #\_global\_constint  sw $t0 ($sp)  subi $sp $sp 4  subi $sp $sp 40  li $t0 0 #\_global\_constint  sw $t0 ($sp)  subi $sp $sp 4  subi $sp $sp 40  li $t0 0 #\_global\_constint  sw $t0 ($sp)  subi $sp $sp 4  j \_\_main  init:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 38  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 5  syscall  sw $v0 -48($t9)  li $v0 12  syscall  sw $v0 -92($t9)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -48($t9)  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -92($t9)  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 109  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 73  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -4($t9)  syscall  \_\_FEND\_LAB\_1:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  fib:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_1  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_2  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_3  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_4  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_5  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_6  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_7  sw $t0 ($sp)  subi $sp $sp 4  lw $t0 4($fp)  li $t1 1  slt $t0 $t1 $t0  bne $t0 1 \_LABEL\_1  lw $t0 4($fp)  li $t1 1  sub $t0 $t0 $t1  sw $t0 -8($fp)  lw $t0 -8($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal fib  nop  sw $v0 -12($fp)  lw $t0 4($fp)  li $t1 2  sub $t0 $t0 $t1  sw $t0 -16($fp)  lw $t0 -16($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal fib  nop  sw $v0 -20($fp)  lw $t0 -12($fp)  lw $t1 -20($fp)  add $t0 $t0 $t1  sw $t0 -24($fp)  li $t0 1  li $t1 0  mul $t0 $t0 $t1  sw $t0 -28($fp)  lw $t0 -24($fp)  lw $t1 -28($fp)  sub $t0 $t0 $t1  sw $t0 -32($fp)  lw $v0 -32($fp)  j \_\_FEND\_LAB\_2  \_LABEL\_1:  lw $t0 4($fp)  li $t1 0  bne $t0 $t1 \_\_tLABEL0  li $t0 1  j \_\_tLABEL1  \_\_tLABEL0:  li $t0 0  \_\_tLABEL1:  bne $t0 1 \_LABEL\_2  li $v0 1  j \_\_FEND\_LAB\_2  \_LABEL\_2:  lw $t0 4($fp)  li $t1 1  bne $t0 $t1 \_\_tLABEL2  li $t0 1  j \_\_tLABEL3  \_\_tLABEL2:  li $t0 0  \_\_tLABEL3:  bne $t0 1 \_LABEL\_3  li $v0 1  j \_\_FEND\_LAB\_2  \_LABEL\_3:  \_\_FEND\_LAB\_2:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  if\_test:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_8  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_9  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_10  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_11  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_12  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_13  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_14  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_15  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_16  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_17  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_18  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_19  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_20  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_21  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_22  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_23  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_24  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 5  syscall  sw $v0 -76($fp)  li $t0 1  li $t1 0  beq $t0 $t1 \_\_tLABEL4  li $t0 1  j \_\_tLABEL5  \_\_tLABEL4:  li $t0 0  \_\_tLABEL5:  bne $t0 1 \_LABEL\_4  \_LABEL\_4:  li $t0 0  li $t1 0  beq $t0 $t1 \_\_tLABEL6  li $t0 1  j \_\_tLABEL7  \_\_tLABEL6:  li $t0 0  \_\_tLABEL7:  bne $t0 1 \_LABEL\_5  \_LABEL\_5:  li $t0 1  li $t1 0  beq $t0 $t1 \_\_tLABEL8  li $t0 1  j \_\_tLABEL9  \_\_tLABEL8:  li $t0 0  \_\_tLABEL9:  bne $t0 1 \_LABEL\_6  \_LABEL\_6:  lw $t0 -76($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL10  li $t0 1  j \_\_tLABEL11  \_\_tLABEL10:  li $t0 0  \_\_tLABEL11:  bne $t0 1 \_LABEL\_7  li $t0 1  li $t1 0  beq $t0 $t1 \_\_tLABEL12  li $t0 1  j \_\_tLABEL13  \_\_tLABEL12:  li $t0 0  \_\_tLABEL13:  bne $t0 1 \_LABEL\_8  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 49  syscall  li $v0 11  li $a0 34  syscall  li $t0 2  li $t1 1  mul $t0 $t0 $t1  sw $t0 -8($fp)  li $t0 0  lw $t1 -8($fp)  add $t0 $t0 $t1  sw $t0 -12($fp)  lw $t0 -12($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL14  li $t0 1  j \_\_tLABEL15  \_\_tLABEL14:  li $t0 0  \_\_tLABEL15:  bne $t0 1 \_LABEL\_9  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 50  syscall  li $v0 11  li $a0 34  syscall  li $t0 3  li $t1 1  mul $t0 $t0 $t1  sw $t0 -16($fp)  lw $t0 -16($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL16  li $t0 1  j \_\_tLABEL17  \_\_tLABEL16:  li $t0 0  \_\_tLABEL17:  bne $t0 1 \_LABEL\_10  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 51  syscall  li $v0 11  li $a0 34  syscall  li $t0 1  li $t1 4  mul $t0 $t0 $t1  sw $t0 -20($fp)  lw $t0 -20($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL18  li $t0 1  j \_\_tLABEL19  \_\_tLABEL18:  li $t0 0  \_\_tLABEL19:  bne $t0 1 \_LABEL\_11  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 52  syscall  li $v0 11  li $a0 34  syscall  li $t0 5  li $t1 1  div $t0 $t0 $t1  sw $t0 -24($fp)  lw $t0 -24($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL20  li $t0 1  j \_\_tLABEL21  \_\_tLABEL20:  li $t0 0  \_\_tLABEL21:  bne $t0 1 \_LABEL\_12  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 53  syscall  li $v0 11  li $a0 34  syscall  li $t0 6  li $t1 0  add $t0 $t0 $t1  sw $t0 -28($fp)  lw $t0 -28($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL22  li $t0 1  j \_\_tLABEL23  \_\_tLABEL22:  li $t0 0  \_\_tLABEL23:  bne $t0 1 \_LABEL\_13  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 54  syscall  li $v0 11  li $a0 34  syscall  li $t0 1  li $t1 7  mul $t0 $t0 $t1  sw $t0 -32($fp)  li $t0 0  lw $t1 -32($fp)  add $t0 $t0 $t1  sw $t0 -36($fp)  lw $t0 -36($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL24  li $t0 1  j \_\_tLABEL25  \_\_tLABEL24:  li $t0 0  \_\_tLABEL25:  bne $t0 1 \_LABEL\_14  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 55  syscall  li $v0 11  li $a0 34  syscall  li $t0 8  li $t1 1  div $t0 $t0 $t1  sw $t0 -40($fp)  lw $t0 -40($fp)  li $t1 0  add $t0 $t0 $t1  sw $t0 -44($fp)  lw $t0 -44($fp)  li $t1 0  sub $t0 $t0 $t1  sw $t0 -48($fp)  lw $t0 -48($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL26  li $t0 1  j \_\_tLABEL27  \_\_tLABEL26:  li $t0 0  \_\_tLABEL27:  bne $t0 1 \_LABEL\_15  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 56  syscall  li $v0 11  li $a0 34  syscall  li $t0 9  li $t1 9  add $t0 $t0 $t1  sw $t0 -52($fp)  li $t0 9  li $t1 1  mul $t0 $t0 $t1  sw $t0 -56($fp)  lw $t0 -52($fp)  lw $t1 -56($fp)  sub $t0 $t0 $t1  sw $t0 -60($fp)  lw $t0 -60($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL28  li $t0 1  j \_\_tLABEL29  \_\_tLABEL28:  li $t0 0  \_\_tLABEL29:  bne $t0 1 \_LABEL\_16  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 57  syscall  li $v0 11  li $a0 34  syscall  li $t0 1000  li $t1 0  mul $t0 $t0 $t1  sw $t0 -64($fp)  li $t0 10  li $t1 1  div $t0 $t0 $t1  sw $t0 -68($fp)  lw $t0 -64($fp)  lw $t1 -68($fp)  add $t0 $t0 $t1  sw $t0 -72($fp)  lw $t0 -72($fp)  li $t1 0  beq $t0 $t1 \_\_tLABEL30  li $t0 1  j \_\_tLABEL31  \_\_tLABEL30:  li $t0 0  \_\_tLABEL31:  bne $t0 1 \_LABEL\_17  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 49  syscall  li $v0 11  li $a0 48  syscall  li $v0 11  li $a0 34  syscall  \_LABEL\_17:  \_LABEL\_16:  \_LABEL\_15:  \_LABEL\_14:  \_LABEL\_13:  \_LABEL\_12:  \_LABEL\_11:  \_LABEL\_10:  \_LABEL\_9:  \_LABEL\_8:  \_LABEL\_7:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 33  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 107  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -76($fp)  syscall  \_\_FEND\_LAB\_3:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  loop\_test:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_25  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_26  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_27  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  lw $t0 12($fp)  sw $t0 -20($fp)  lw $t0 -20($fp)  sw $t0 -24($fp)  \_LABEL\_18:  lw $t0 -20($fp)  lw $t1 8($fp)  add $t0 $t0 $t1  sw $t0 -8($fp)  lw $t0 -8($fp)  sw $t0 -20($fp)  lw $t0 4($fp)  li $t1 1  sub $t0 $t0 $t1  sw $t0 -12($fp)  lw $t0 -12($fp)  sw $t0 4($fp)  lw $t0 4($fp)  li $t1 0  slt $t0 $t1 $t0  bne $t0 1 \_LABEL\_19  j \_LABEL\_18  \_LABEL\_19:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 121  syscall  li $v0 11  li $a0 44  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -20($fp)  syscall  \_LABEL\_20:  lw $t0 -24($fp)  li $t1 1  sub $t0 $t0 $t1  sw $t0 -16($fp)  lw $t0 -16($fp)  sw $t0 -24($fp)  lw $t0 -24($fp)  li $t1 0  slt $t0 $t1 $t0  bne $t0 1 \_LABEL\_21  j \_LABEL\_20  \_LABEL\_21:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 121  syscall  li $v0 11  li $a0 44  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 106  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -24($fp)  syscall  lw $v0 -20($fp)  j \_\_FEND\_LAB\_4  \_\_FEND\_LAB\_4:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  mytoupper:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  lw $t0 4($fp)  li $t1 97  bne $t0 $t1 \_\_tLABEL32  li $t0 1  j \_\_tLABEL33  \_\_tLABEL32:  li $t0 0  \_\_tLABEL33:  bne $t0 1 \_LABEL\_22  li $v0 65  j \_\_FEND\_LAB\_5  \_LABEL\_22:  lw $t0 4($fp)  li $t1 98  bne $t0 $t1 \_\_tLABEL34  li $t0 1  j \_\_tLABEL35  \_\_tLABEL34:  li $t0 0  \_\_tLABEL35:  bne $t0 1 \_LABEL\_23  li $v0 66  j \_\_FEND\_LAB\_5  \_LABEL\_23:  lw $t0 4($fp)  li $t1 99  bne $t0 $t1 \_\_tLABEL36  li $t0 1  j \_\_tLABEL37  \_\_tLABEL36:  li $t0 0  \_\_tLABEL37:  bne $t0 1 \_LABEL\_24  li $v0 67  j \_\_FEND\_LAB\_5  \_LABEL\_24:  lw $t0 4($fp)  li $t1 100  bne $t0 $t1 \_\_tLABEL38  li $t0 1  j \_\_tLABEL39  \_\_tLABEL38:  li $t0 0  \_\_tLABEL39:  bne $t0 1 \_LABEL\_25  li $v0 68  j \_\_FEND\_LAB\_5  \_LABEL\_25:  lw $t0 4($fp)  li $t1 101  bne $t0 $t1 \_\_tLABEL40  li $t0 1  j \_\_tLABEL41  \_\_tLABEL40:  li $t0 0  \_\_tLABEL41:  bne $t0 1 \_LABEL\_26  li $v0 69  j \_\_FEND\_LAB\_5  \_LABEL\_26:  \_\_FEND\_LAB\_5:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  case\_test:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_28  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_29  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_30  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_31  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_32  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_33  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_34  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_35  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_36  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_37  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_38  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_39  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_40  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_41  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_42  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 40  syscall  li $v0 11  li $a0 48  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 57  syscall  li $v0 11  li $a0 41  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 38  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 40  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 41  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 5  syscall  sw $v0 -68($fp)  li $v0 12  syscall  sw $v0 -72($fp)  \_LABEL\_27:  lw $t0 -68($fp)  li $t1 0  bne $t0 $t1 \_\_tLABEL42  li $t0 1  j \_\_tLABEL43  \_\_tLABEL42:  li $t0 0  \_\_tLABEL43:  bne $t0 1 \_LABEL\_29  j \_LABEL\_30  \_LABEL\_30:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -8($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 48  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -8($fp)  syscall  j \_LABEL\_28  \_LABEL\_29:  lw $t0 -68($fp)  li $t1 1  bne $t0 $t1 \_\_tLABEL44  li $t0 1  j \_\_tLABEL45  \_\_tLABEL44:  li $t0 0  \_\_tLABEL45:  bne $t0 1 \_LABEL\_31  j \_LABEL\_32  \_LABEL\_32:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -12($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 49  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -12($fp)  syscall  j \_LABEL\_28  \_LABEL\_31:  lw $t0 -68($fp)  li $t1 2  bne $t0 $t1 \_\_tLABEL46  li $t0 1  j \_\_tLABEL47  \_\_tLABEL46:  li $t0 0  \_\_tLABEL47:  bne $t0 1 \_LABEL\_33  j \_LABEL\_34  \_LABEL\_34:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -16($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 50  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -16($fp)  syscall  j \_LABEL\_28  \_LABEL\_33:  lw $t0 -68($fp)  li $t1 3  bne $t0 $t1 \_\_tLABEL48  li $t0 1  j \_\_tLABEL49  \_\_tLABEL48:  li $t0 0  \_\_tLABEL49:  bne $t0 1 \_LABEL\_35  j \_LABEL\_36  \_LABEL\_36:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -20($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 51  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -20($fp)  syscall  j \_LABEL\_28  \_LABEL\_35:  lw $t0 -68($fp)  li $t1 4  bne $t0 $t1 \_\_tLABEL50  li $t0 1  j \_\_tLABEL51  \_\_tLABEL50:  li $t0 0  \_\_tLABEL51:  bne $t0 1 \_LABEL\_37  j \_LABEL\_38  \_LABEL\_38:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -24($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 52  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -24($fp)  syscall  j \_LABEL\_28  \_LABEL\_37:  lw $t0 -68($fp)  li $t1 5  bne $t0 $t1 \_\_tLABEL52  li $t0 1  j \_\_tLABEL53  \_\_tLABEL52:  li $t0 0  \_\_tLABEL53:  bne $t0 1 \_LABEL\_39  j \_LABEL\_40  \_LABEL\_40:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -28($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 53  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -28($fp)  syscall  j \_LABEL\_28  \_LABEL\_39:  lw $t0 -68($fp)  li $t1 6  bne $t0 $t1 \_\_tLABEL54  li $t0 1  j \_\_tLABEL55  \_\_tLABEL54:  li $t0 0  \_\_tLABEL55:  bne $t0 1 \_LABEL\_41  j \_LABEL\_42  \_LABEL\_42:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -32($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 54  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -32($fp)  syscall  j \_LABEL\_28  \_LABEL\_41:  lw $t0 -68($fp)  li $t1 7  bne $t0 $t1 \_\_tLABEL56  li $t0 1  j \_\_tLABEL57  \_\_tLABEL56:  li $t0 0  \_\_tLABEL57:  bne $t0 1 \_LABEL\_43  j \_LABEL\_44  \_LABEL\_44:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -36($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 55  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -36($fp)  syscall  j \_LABEL\_28  \_LABEL\_43:  lw $t0 -68($fp)  li $t1 8  bne $t0 $t1 \_\_tLABEL58  li $t0 1  j \_\_tLABEL59  \_\_tLABEL58:  li $t0 0  \_\_tLABEL59:  bne $t0 1 \_LABEL\_45  j \_LABEL\_46  \_LABEL\_46:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -40($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 56  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -40($fp)  syscall  j \_LABEL\_28  \_LABEL\_45:  lw $t0 -68($fp)  li $t1 9  bne $t0 $t1 \_\_tLABEL60  li $t0 1  j \_\_tLABEL61  \_\_tLABEL60:  li $t0 0  \_\_tLABEL61:  bne $t0 1 \_LABEL\_47  j \_LABEL\_48  \_LABEL\_48:  lw $t0 -68($fp)  lw $t1 -72($fp)  add $t0 $t0 $t1  sw $t0 -44($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 57  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -44($fp)  syscall  j \_LABEL\_28  \_LABEL\_28:  \_LABEL\_47:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 109  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 33  syscall  li $v0 11  li $a0 34  syscall  \_LABEL\_49:  lw $t0 -72($fp)  li $t1 97  bne $t0 $t1 \_\_tLABEL62  li $t0 1  j \_\_tLABEL63  \_\_tLABEL62:  li $t0 0  \_\_tLABEL63:  bne $t0 1 \_LABEL\_51  j \_LABEL\_52  \_LABEL\_52:  lw $t0 -72($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal mytoupper  nop  sw $v0 -48($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -48($fp)  syscall  j \_LABEL\_50  \_LABEL\_51:  lw $t0 -72($fp)  li $t1 98  bne $t0 $t1 \_\_tLABEL64  li $t0 1  j \_\_tLABEL65  \_\_tLABEL64:  li $t0 0  \_\_tLABEL65:  bne $t0 1 \_LABEL\_53  j \_LABEL\_54  \_LABEL\_54:  lw $t0 -72($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal mytoupper  nop  sw $v0 -52($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 98  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -52($fp)  syscall  j \_LABEL\_50  \_LABEL\_53:  lw $t0 -72($fp)  li $t1 99  bne $t0 $t1 \_\_tLABEL66  li $t0 1  j \_\_tLABEL67  \_\_tLABEL66:  li $t0 0  \_\_tLABEL67:  bne $t0 1 \_LABEL\_55  j \_LABEL\_56  \_LABEL\_56:  lw $t0 -72($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal mytoupper  nop  sw $v0 -56($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -56($fp)  syscall  j \_LABEL\_50  \_LABEL\_55:  lw $t0 -72($fp)  li $t1 100  bne $t0 $t1 \_\_tLABEL68  li $t0 1  j \_\_tLABEL69  \_\_tLABEL68:  li $t0 0  \_\_tLABEL69:  bne $t0 1 \_LABEL\_57  j \_LABEL\_58  \_LABEL\_58:  lw $t0 -72($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal mytoupper  nop  sw $v0 -60($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -60($fp)  syscall  j \_LABEL\_50  \_LABEL\_57:  lw $t0 -72($fp)  li $t1 101  bne $t0 $t1 \_\_tLABEL70  li $t0 1  j \_\_tLABEL71  \_\_tLABEL70:  li $t0 0  \_\_tLABEL71:  bne $t0 1 \_LABEL\_59  j \_LABEL\_60  \_LABEL\_60:  lw $t0 -72($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal mytoupper  nop  sw $v0 -64($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 39  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  lw $a0 -64($fp)  syscall  j \_LABEL\_50  \_LABEL\_50:  \_LABEL\_59:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 95  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 33  syscall  li $v0 11  li $a0 34  syscall  \_\_FEND\_LAB\_6:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  setaverage:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_43  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_44  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_45  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_46  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_47  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0  sw $t0 -28($fp)  li $t0 0  sw $t0 -36($fp)  \_LABEL\_61:  lw $t1 -36($fp)  mul $t1 $t1 4  addi $t1 $t1 2147479460  lw $t1 0($t1)  sw $t1 -8($fp)  lw $t0 -28($fp)  lw $t1 -8($fp)  add $t0 $t0 $t1  sw $t0 -12($fp)  lw $t0 -12($fp)  sw $t0 -28($fp)  lw $t0 -36($fp)  li $t1 1  add $t0 $t0 $t1  sw $t0 -16($fp)  lw $t0 -16($fp)  sw $t0 -36($fp)  lw $t0 -36($fp)  li $t1 10  slt $t0 $t0 $t1  bne $t0 1 \_LABEL\_62  j \_LABEL\_61  \_LABEL\_62:  lw $t0 -28($fp)  li $t1 10  div $t0 $t0 $t1  sw $t0 -20($fp)  lw $t0 -20($fp)  sw $t0 -32($fp)  li $t0 0  sw $t0 -36($fp)  \_LABEL\_63:  lw $t1 -36($fp)  mul $t1 $t1 4  addi $t1 $t1 2147479504  lw $t0 -32($fp)  sw $t0 0($t1)  lw $t0 -36($fp)  li $t1 1  add $t0 $t0 $t1  sw $t0 -24($fp)  lw $t0 -24($fp)  sw $t0 -36($fp)  lw $t0 -36($fp)  li $t1 10  slt $t0 $t0 $t1  bne $t0 1 \_LABEL\_64  j \_LABEL\_63  \_LABEL\_64:  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 34  syscall  \_\_FEND\_LAB\_7:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  array\_test:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_48  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_49  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 121  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  li $t0 0  sw $t0 -16($fp)  li $t0 0  sw $t0 -20($fp)  li $t0 48  sw $t0 -24($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 49  syscall  li $v0 11  li $a0 48  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  \_LABEL\_65:  li $v0 12  syscall  sw $v0 -24($fp)  lw $t1 -16($fp)  mul $t1 $t1 4  addi $t1 $t1 2147479460  lw $t0 -24($fp)  sw $t0 0($t1)  lw $t0 -16($fp)  li $t1 1  add $t0 $t0 $t1  sw $t0 -8($fp)  lw $t0 -8($fp)  sw $t0 -16($fp)  lw $t0 -16($fp)  li $t1 10  slt $t0 $t0 $t1  bne $t0 1 \_LABEL\_66  j \_LABEL\_65  \_LABEL\_66:  jal setaverage  nop  lw $t0 -44($t9)  sw $t0 -12($fp)  lw $t0 -12($fp)  sw $t0 -20($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -20($fp)  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 121  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 33  syscall  li $v0 11  li $a0 34  syscall  \_\_FEND\_LAB\_8:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  jr $ra  \_\_main:  #Save Register  sw $t0 0($t8)  sw $t1 4($t8)  sw $t2 8($t8)  sw $t3 12($t8)  sw $t4 16($t8)  sw $t5 20($t8)  sw $t6 24($t8)  sw $t7 28($t8)  sw $fp ($sp)  add $fp $sp $0  subi $sp $sp 4  sw $ra ($sp)  subi $sp $sp 4  #Save Register Done!  li $t0 0 #$\_50  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #$\_51  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  li $t0 0 #  sw $t0 ($sp)  subi $sp $sp 4  jal init  nop  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 98  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  lw $t0 0($t9)  sw $t0 ($sp)  subi $sp $sp 4  jal fib  nop  sw $v0 -8($fp)  li $v0 1  lw $a0 -8($fp)  syscall  jal if\_test  nop  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 51  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 40  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 51  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 109  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 62  syscall  li $v0 11  li $a0 48  syscall  li $v0 11  li $a0 41  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 34  syscall  li $v0 5  syscall  sw $v0 -16($fp)  li $v0 5  syscall  sw $v0 -20($fp)  li $v0 5  syscall  sw $v0 -24($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -16($fp)  syscall  lw $t0 -16($fp)  sw $t0 ($sp)  subi $sp $sp 4  lw $t0 -20($fp)  sw $t0 ($sp)  subi $sp $sp 4  lw $t0 -24($fp)  sw $t0 ($sp)  subi $sp $sp 4  jal loop\_test  nop  sw $v0 -12($fp)  lw $t0 -12($fp)  sw $t0 -16($fp)  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 102  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 44  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 105  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 118  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 108  syscall  li $v0 11  li $a0 117  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 99  syscall  li $v0 11  li $a0 104  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 115  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 116  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 58  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 34  syscall  li $v0 1  lw $a0 -16($fp)  syscall  jal case\_test  nop  jal array\_test  nop  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 65  syscall  li $v0 11  li $a0 76  syscall  li $v0 11  li $a0 76  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 84  syscall  li $v0 11  li $a0 69  syscall  li $v0 11  li $a0 83  syscall  li $v0 11  li $a0 84  syscall  li $v0 11  li $a0 83  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 70  syscall  li $v0 11  li $a0 73  syscall  li $v0 11  li $a0 78  syscall  li $v0 11  li $a0 73  syscall  li $v0 11  li $a0 83  syscall  li $v0 11  li $a0 72  syscall  li $v0 11  li $a0 69  syscall  li $v0 11  li $a0 68  syscall  li $v0 11  li $a0 33  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 34  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 112  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 111  syscall  li $v0 11  li $a0 103  syscall  li $v0 11  li $a0 114  syscall  li $v0 11  li $a0 97  syscall  li $v0 11  li $a0 109  syscall  li $v0 11  li $a0 32  syscall  li $v0 11  li $a0 101  syscall  li $v0 11  li $a0 110  syscall  li $v0 11  li $a0 100  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 45  syscall  li $v0 11  li $a0 34  syscall  \_\_FEND\_LAB\_9:  lw $ra -4($fp)  add $sp $fp $0  lw $fp ($fp)  lw $t0 0($t8)  lw $t1 4($t8)  lw $t2 8($t8)  lw $t3 12($t8)  lw $t4 16($t8)  lw $t5 20($t8)  lw $t6 24($t8)  lw $t7 28($t8)  li $v0 10  syscall |
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