EECS 645 Report Contents

Gary J. Minden

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This document describes the content of your report for the CacheModel for EECS 645, Spring 2017.

Your report shall be comprehensive so that a peer senior in EECS could replicate, modify, improve, or repair your project. Write your report for someone not familiar with our class discussions. Include in your report all assumptions and discussions we have had in class. Your report shall be in PDF format as a single file. One report is to be submitted per group.

1. Cover Page – Title of report; Names of team members; date; copyright notice;
2. All pages except for the cover page shall have page numbers.
3. Revision History – State the version of the report; state when the report was issued; state significant changes from previous versions of the report; See TI manuals for examples;
4. Table of Contents – list each section of your report to three levels; include page numbers;
5. List of Figures – List each figure number; include a short caption; include page numbers for each figure;
6. List of Tables – List each table number, include a short caption; include page numbers for each table;
7. Abstract – Briefly describe the project and your results (about 1-2 short paragraphs); the abstract should be designed to help the reader decide, "Do I want to read this report?"
8. Principles of Operation (POP) – The POP shall describe the architecture and design of your task; it should start with a overall prose description of the task and goals including a top-level block diagram;
9. Data Structure Descriptions – Describe each significant data structure in your project; for example, in the work I presented in class I would describe the Cache structure; you do not need to document FreeRTOS structures or simple char, int, or float structures;
10. Function Descriptions – Describe each function in your project; include pseudo-code; list each external function you reference, e.g. UARTStdio_Initialization();
11. Parameters – Describe each parameter and it's value in your project, e.g. the address size, block size, and cache size; Provide a rationale for the value(s) you selected;

12. Testing – Describe how you tested the modules and operation of your project;
13. Results – Describe the results of executing your cache model with each of the address traces and the two associativity values you selected.
14. Lessons Learn – Describe what you learned; describe what you would do differently next time around; provide advice to future students in EECS 645;
15. Program Listing – Provide a neat source code listing as an appendix to your report;

The overarching goal is to provide a complete document which describes your work in sufficient detail that a senior in Computer Engineering or Computer Science could replicate your work.