

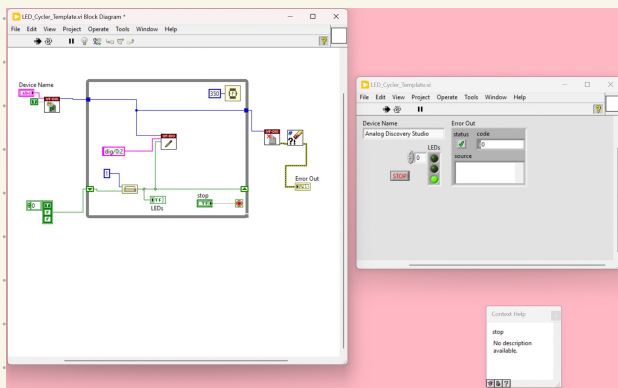
# Physics IIIA

## Lab 10

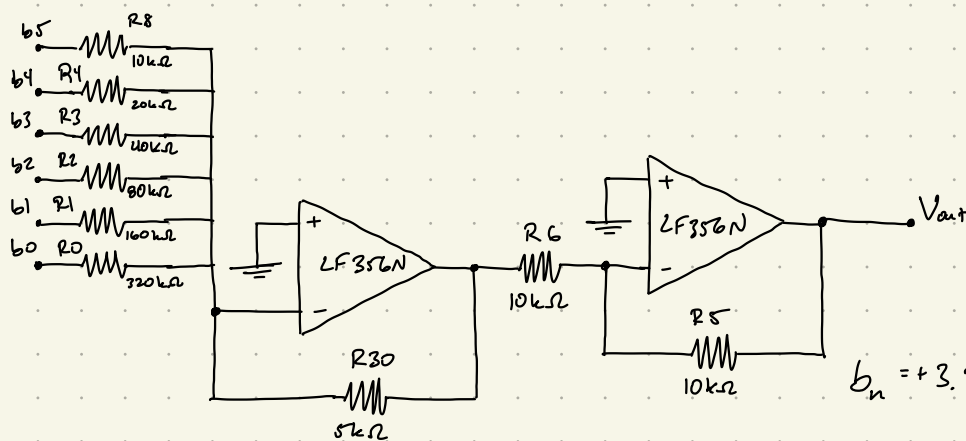
### Problem R10.1:

- a) With ramp interpolation, the sampled wave is a triangle wave. It's somewhat reasonable. When the phase is set to  $45^\circ$  the sampled wave is now a trapezoidal wave instead of triangular. This wave is more reasonable. With a sample rate of 4.05 the ramp interpolation is closer to the sine wave by showing some curvature. With an expanded time axis it is harder to account for the qualitative differences.
- b) The sampled wave is a better facsimile of the original wave when done with flat interpolation. It is an almost perfect reconstruction, only difference being a small phase shift. When phase is shifted by  $45^\circ$ , it is a perfect reconstruction. When the sample frequency is set to 4.05, the sampled wave is a worse facsimile since on occasion it will sample a point during the rise or falling portion, creating a step instead a square.
- c) The sampling rate is of comparable value to the original waveform frequency resulting in an inadequate number of sample points to reconstruct the wave. Sine wave is constantly changing so a lot of information can be lost between sample points.
- d) The square wave is easier to reconstruct at lower sample rates since it's not changing constantly so as long as the sample rate is double the frequency of the original wave, there will be adequate enough data to create an accurate facsimile.
- e) For a sine wave, sample rate of 6 Hz produces a reasonable facsimile using ramp interpolation. 2 Hz is good for a square wave using flat interpolation. They are different due to information lost between samples. Due to sine wave changing there is lost data points while square waves are constant so you can get away with less sample points.
- f) No, a square and sine wave would both appear like triangle waves at the Nyquist frequency.
- g) Two-Tone - 5 Hz  
AM - 6 Hz  
FM - 4 Hz
- h) You can use lower sample rates to recreate an accurate sine wave, but the amplitude would be incorrect.
- i) Yes, the filter greatly improves the accuracy of the sampled wave.
- j) No interpolation method helps produce an accurate facsimile of the original wave with a realistic low pass. With an ideal low-pass filter, comb interpolation produces the best wave. The lowest we can go is 2.003 Hz for the sampling frequency.
- k) The ideal and realistic low-pass filters both produce poor facsimiles of the original wave.
- l) Two-tone: 2.003 Hz, AM: 2.0 Hz, FM: 1.9 Hz. A square wave is partially composed of high frequency waves which end up filtered out.

## Problem R10.2:



## Problem R10.3:



a)  $b_0$  corresponds to the lowest order bit

$$R_8 - 1 \text{ } 10\text{k}\Omega$$

$$R_4 - 2 \text{ } 10\text{k}\Omega$$

$$R_3 - 4 \text{ } 10\text{k}\Omega$$

$$R_2 - 1 \text{ } 75\text{k}\Omega, 1 \text{ } 3\text{k}\Omega, 1 \text{ } 2\text{k}\Omega$$

$$R_1 - 1 \text{ } 150\text{k}\Omega, 1 \text{ } 10\text{k}\Omega$$

$$R_0 - 1 \text{ } 220\text{k}\Omega, 1 \text{ } 120\text{k}\Omega$$

$$b_n = +3.3\text{V}, I_{\text{total}} = \sum_{i=0}^5 \left(\frac{1}{2}\right)^i \frac{3.3\text{V}}{10\text{k}\Omega} = 6.497 \times 10^{-4}\text{A}$$

$$-V_{\text{out}_1} = I_{\text{tot}} (5\text{k}\Omega) = 3.248\text{V} \Rightarrow \underline{V_{\text{out}} = 3.248\text{V}}$$

b) The DAC works for up to requested voltages of 3.25V. That is its limit.

c) We made the subvis.

d) The WriteDAC and ReadADC.vi still works.

## Problem R10.4:

a) The program runs as expected

b) Turning on  $b_5$  causes the biggest steps in the data. This bit change were the most likely to cause trouble since it is the most significant bit that can be represent and supplies the greatest current to the summer. Yes, the steps are compatible with the accuracy of the resistors.

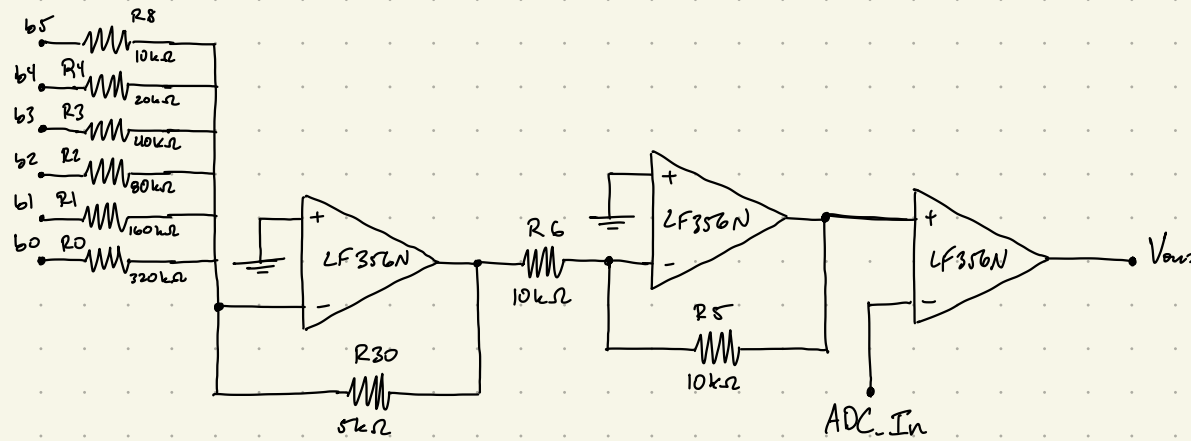
c) $b_n$	$V_{\text{out}} (\text{V})$
0	3.29
1	3.29
2	3.29
3	3.28
4	3.26
5	3.21

The offset for the higher bits is greater due to them having much lower input impedance, with  $b_5$  being much less and resulting in a greater offset.

## Problem R10.5:

- We recreated CalculateResiduals.vi
- We substituted our subvis into PlotResidualScan.vi
- PlotResidualScan.vi works the same as it did with the original subvis.

## Problem R10.6:



a) The comparator was built and integrated into the DAC circuit.

b) The ADC.vi works and correctly gives the necessary outputs.

c)

Target Voltage (V)	Final ADC Voltage (V)
0.1	0
0.5	0.413
1.0	0.929
1.2	1.135
1.5	1.444
2.0	1.960
2.3	2.270
2.5	2.476
3	2.992
3.25	3.198

## Problem R10.7:

