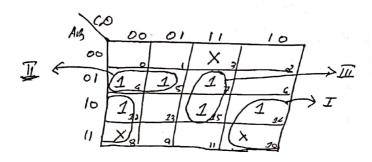
Name! Zubayer Ahmed Zidhan Kaskar, Roll: - 2007, 1000 7062
Branch: Computer Science & Engineering, Subject: Digital Systems.

CIE-2

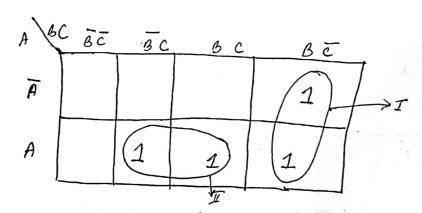
3. @ Sol! Crivens

F (A,B,C,D) = Zm (4,5,7,12,14,15)+Zd(308,20)



 $\circ \circ f(A,B,C,D) = I + II + II$ $\Rightarrow f(A,B,C,D) = AD + \overline{A}B\overline{C} + BCD_{p}$

3. B Sol »: Coiver, AB + ABC + ABC + BC



$$\stackrel{\circ}{\circ} f(A,B,C) = I + II$$

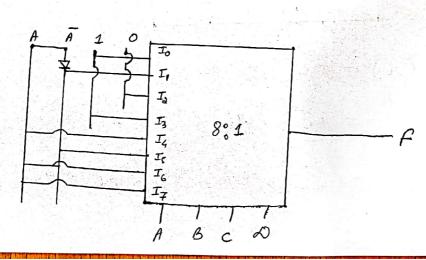
$$= BC + AC$$

o° AB+ ABC + ABC + BC = BC + AC

1) Sout = Criven, $f = \sum_{m} (0,1,3,5,8,11,12,14,2r)$

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	•
A 6 2 2 3 4 0	5 7
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	I_{7}



4. Ars:

Half - Addes

Full-Addes

1) Produces sum of two impute i.e. adds two 1-624 digits.

2) Revious carry is not used.

à, S = A DB Co = A.B

1) Adds three 1-bit bimany numbers and c'e produce sum of three imputs and a carry value.

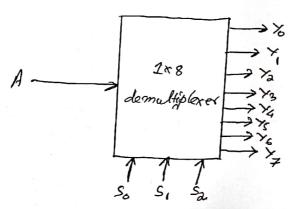
2) Are vious carry is used.

3) Logical expression for half-adds 3) Logical expression for full adder is, S = ABBBCin

2. Ans: - I demultiplexer is the opposite of a multiplexer. A Demux has I imput and 2" outputs, for this reason, it is also called a data distributor.

1-line to 8-line demultiplexer :-

In this type of demux, there are are a total of eight outputs from % to Y 3 and three selection lines So, S, and S2, and it takes a single inputs A. The block-diagram of a 1x8 demux à as follows:



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					1	X	1 /3	Ya	1 %		
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