

NUCLEO64 STM32G4

MB1367

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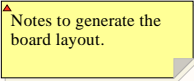
Sheet 6: Power

Sheet 7: ST-Link/V3E

Sheet 8: Mechanical

Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.



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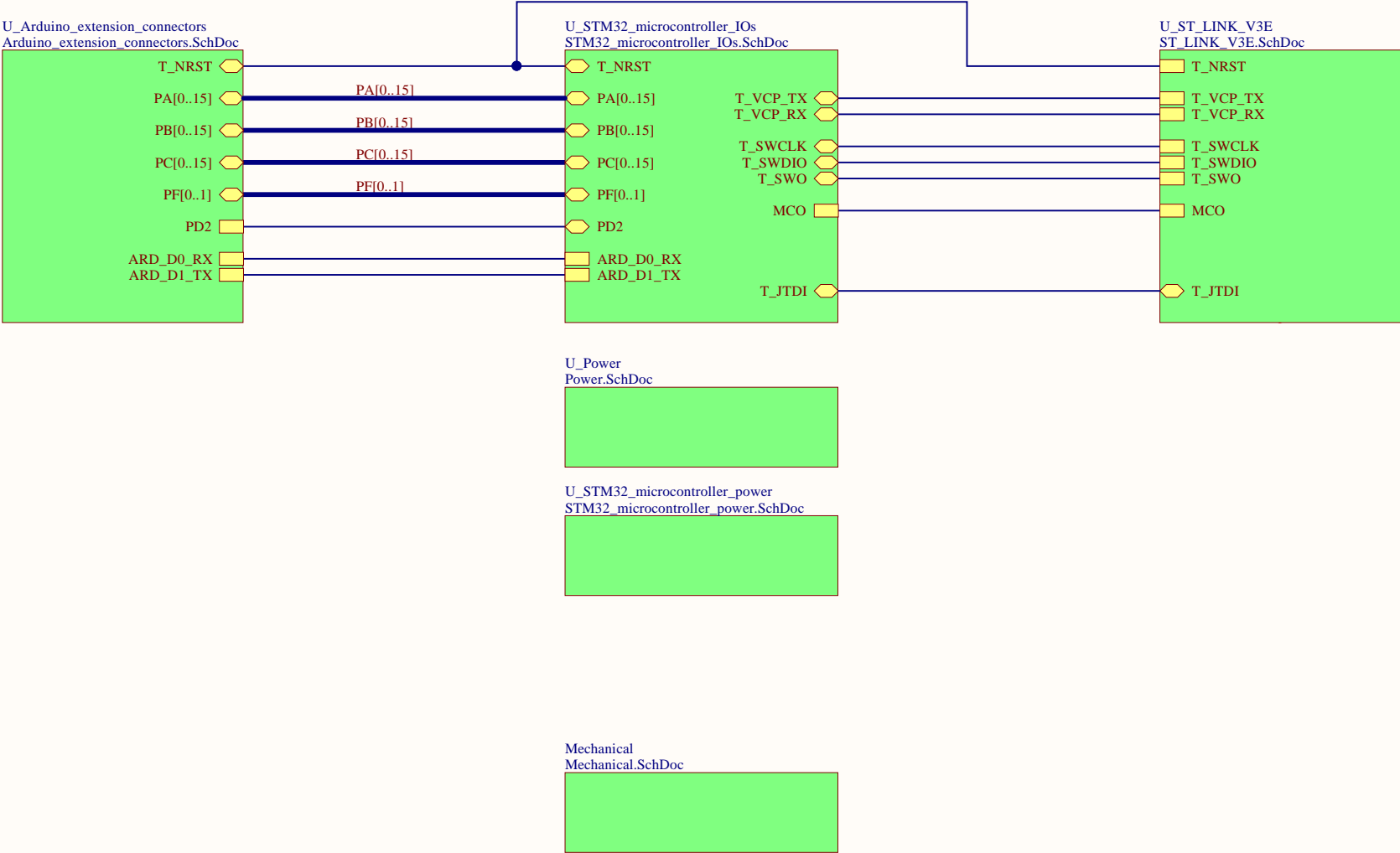
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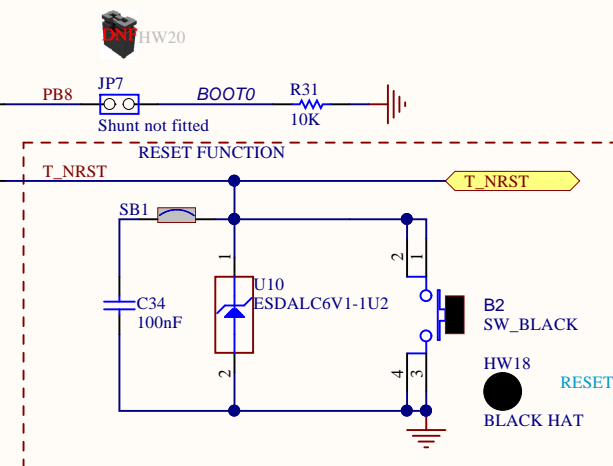
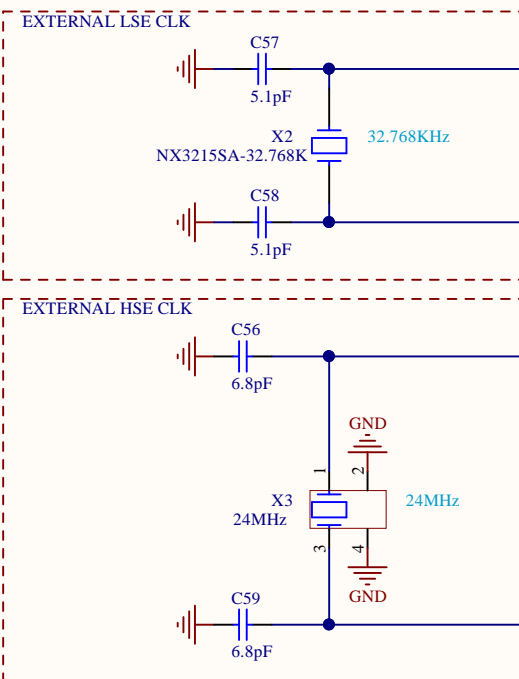
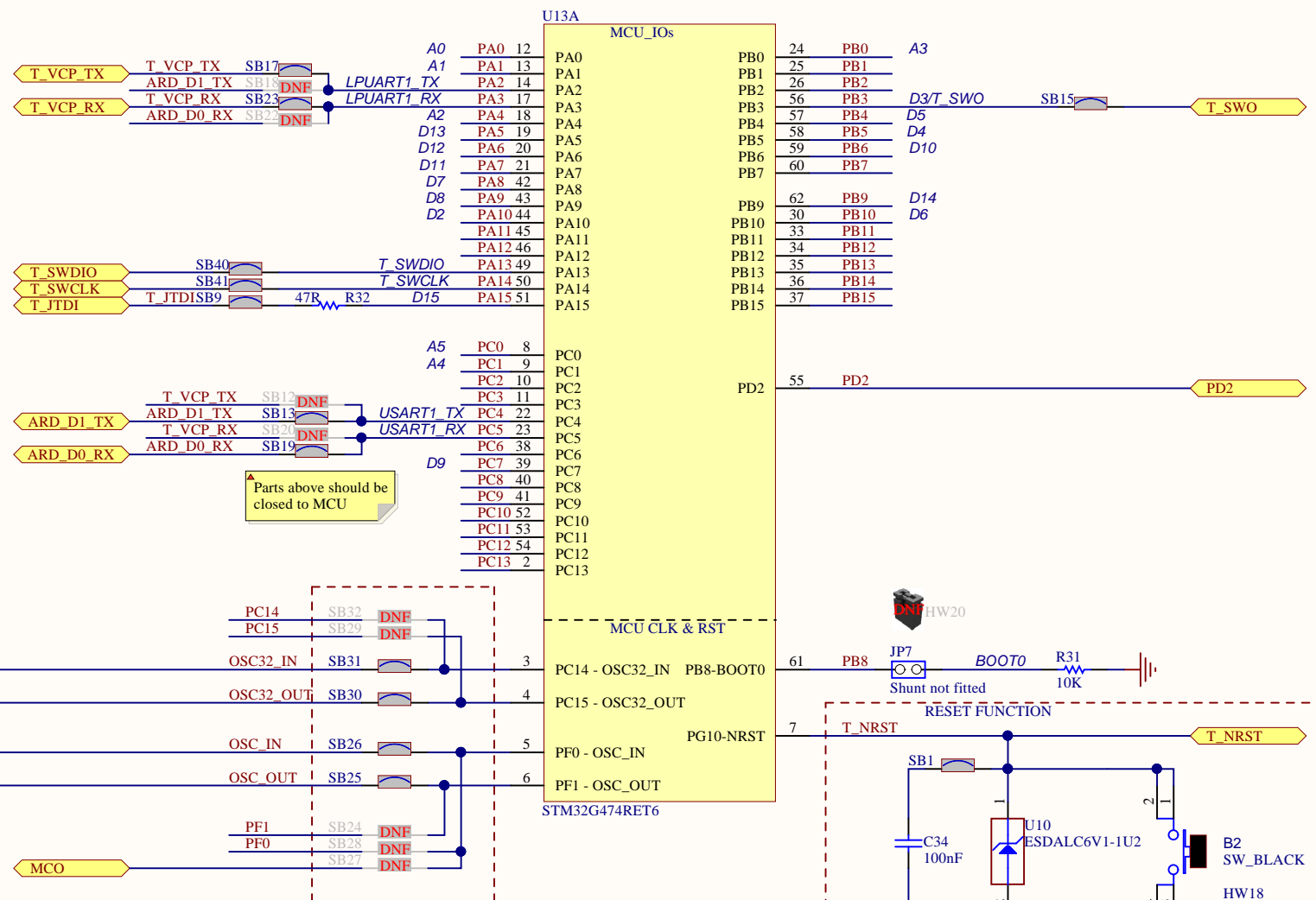
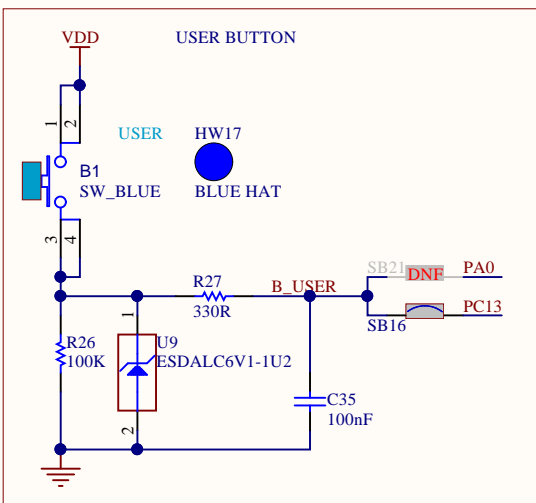
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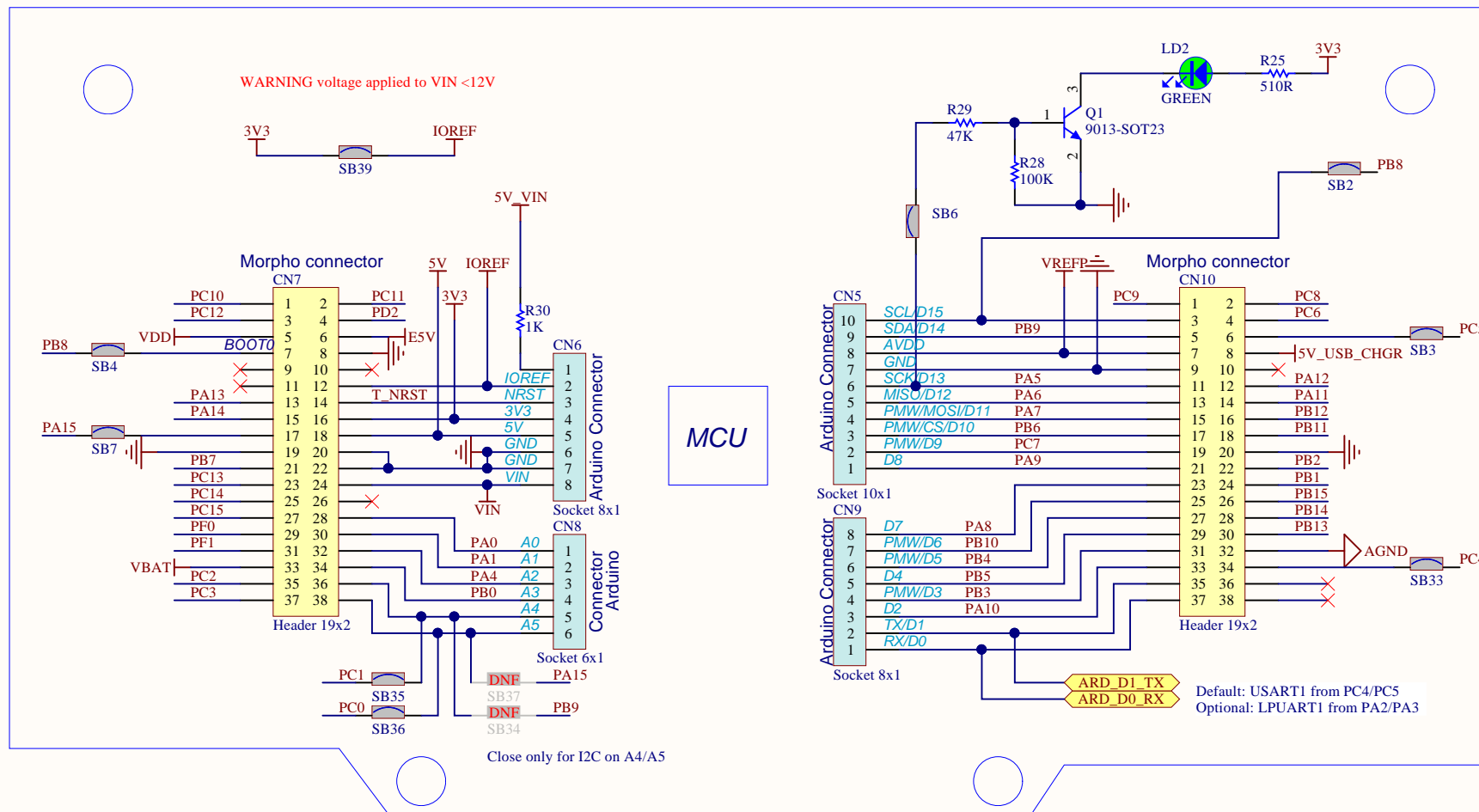
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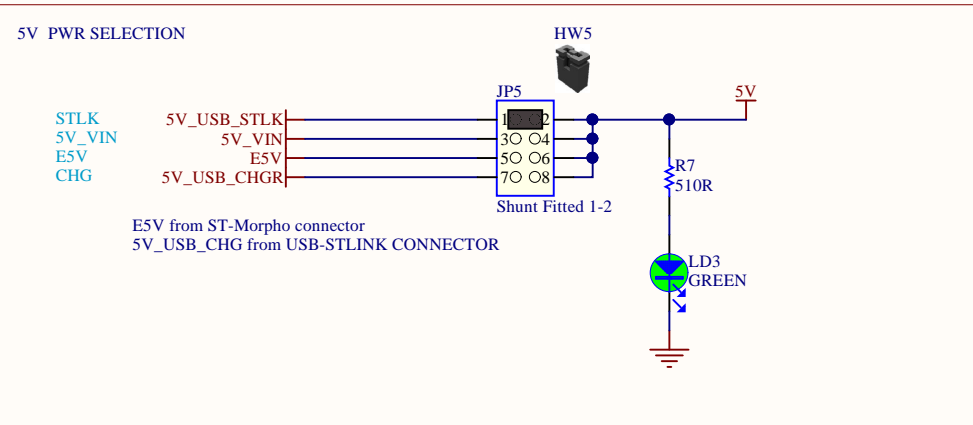






PA[0..15] PA[0..15]
 PB[0..15] PB[0..15]
 PC[0..15] PC[0..15]
 PD2 PD2
 T_NRST T_NRST
 PF[0..1] PF[0..1]

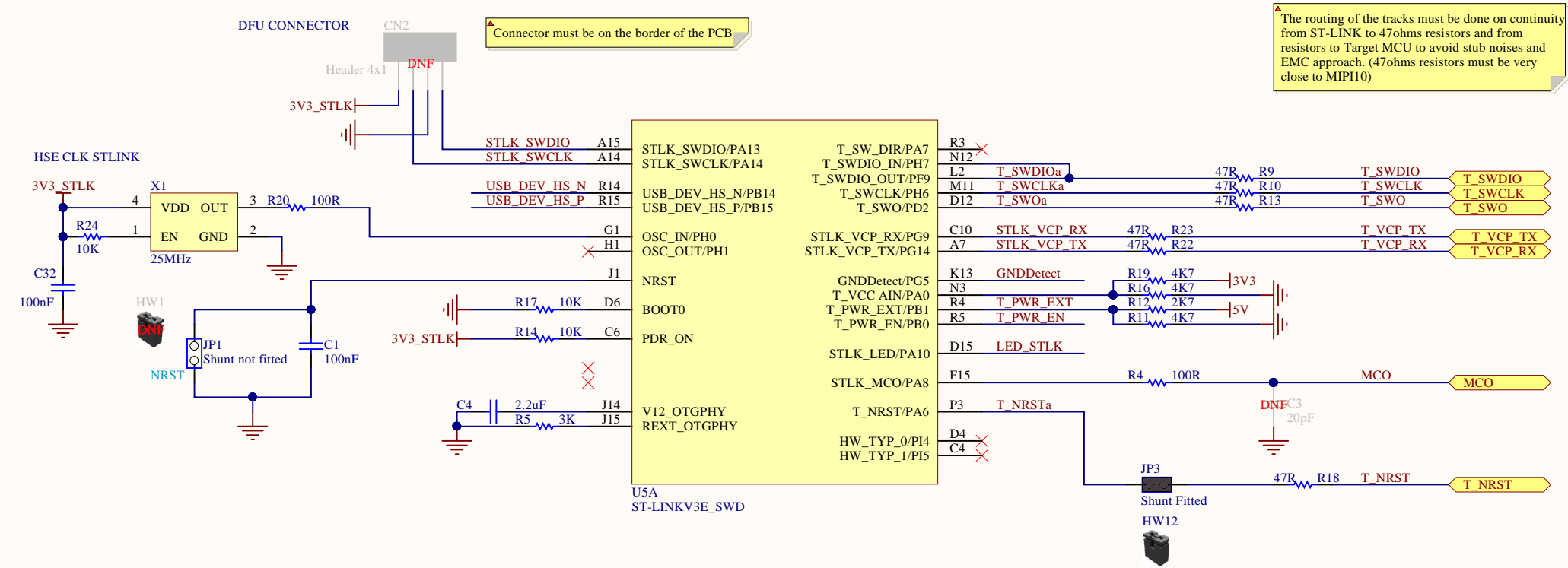


[illegible]

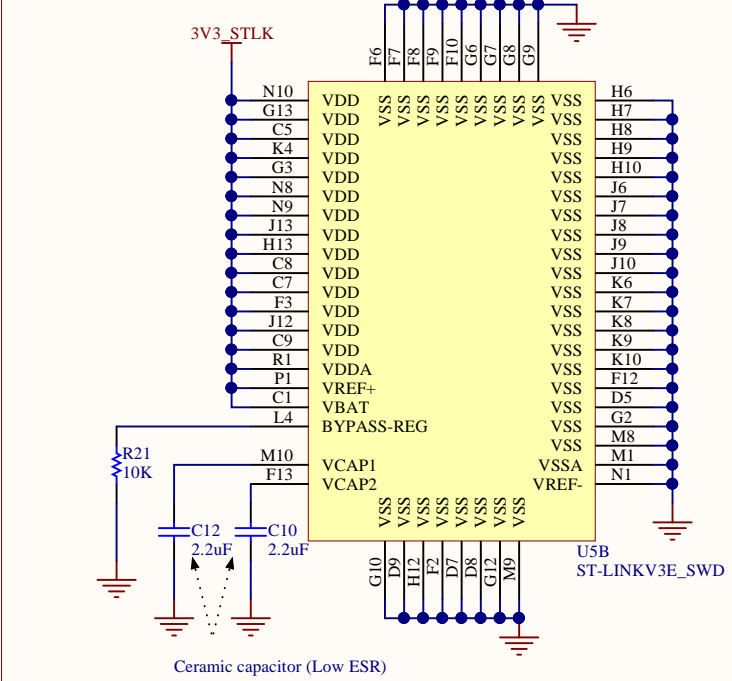
The diagram illustrates the VREF PWR circuit. A 5V supply is connected to a network of components. A resistor R35 (180R) is connected to the 5V supply. A capacitor C51 (100pF) is connected to the 5V supply. A transistor Q2 (9013-SOT23) is connected to the 5V supply. A diode U14 (TL1431CL5T) is connected to the 5V supply. The output VREF is connected to a network of resistors (R33, R34), capacitors (C50, C46), and a diode (SB14). A note indicates that SB14 is not fitted in the default configuration.

To have VREF at 3,25V, then SB14 is not fitted. This is the default configuration.
To have VREF at 2,5V, then SB14 is fitted and R34 is not fitted

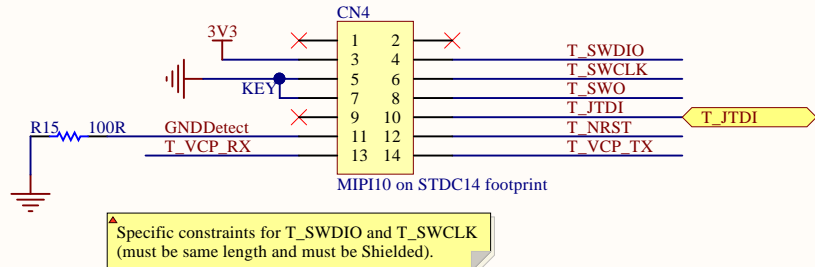
STLINK_MCU



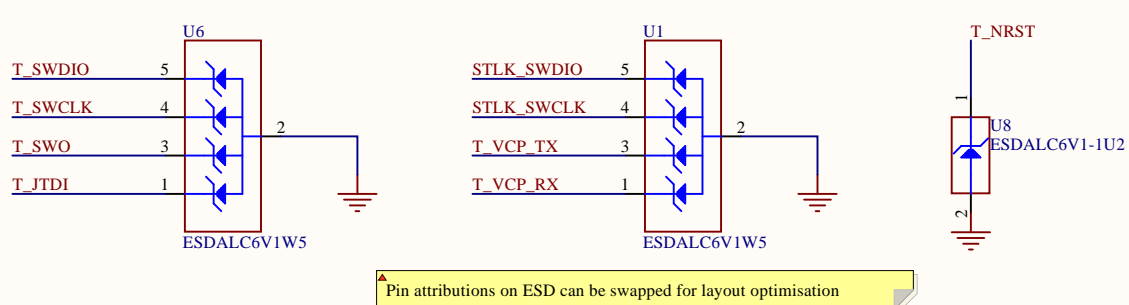
ST-LINK POWER



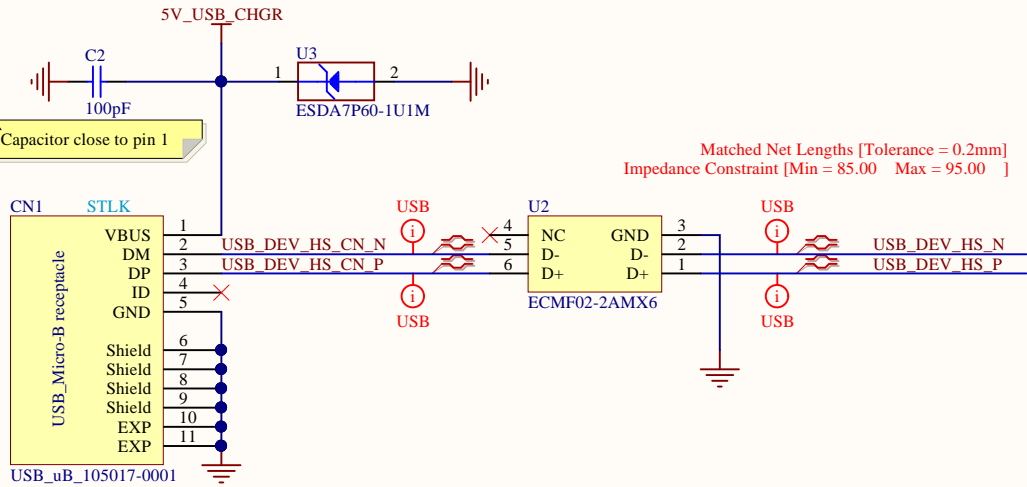
STDC14 Receiver



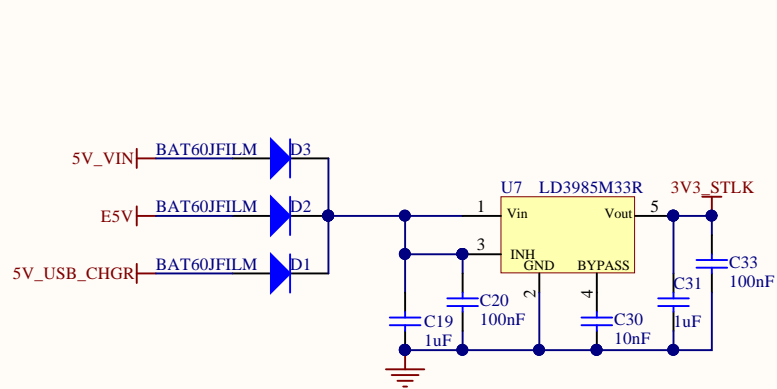
ESD PROTECTIONS



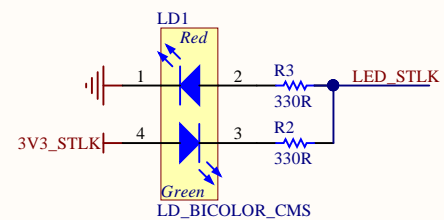
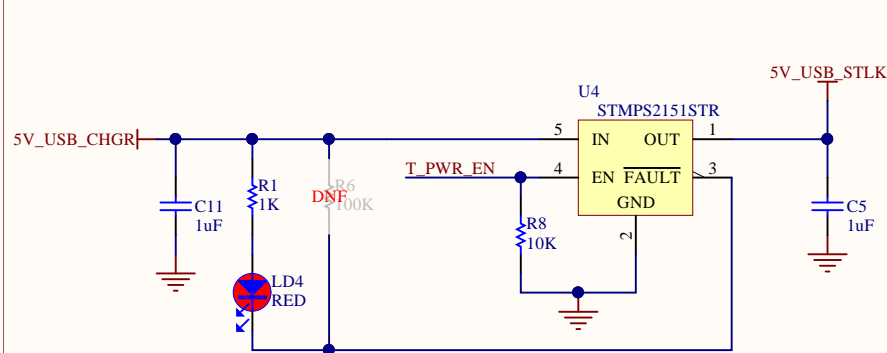
ST-LINK USB CONNECTOR



ST-LINK POWER 3V3 / 150mA



5V ST-LINK PROTECTION



HW101

BOARD REF

MB1367C-03 syywwxxxxx

HW102

BOARD CPN

NUCLEO-G4xxRx

HW108

ES

ES

HW100

PCB

MB1367C

HW103

LOGO NUCLEO

HW106

LOGO ST

HW104

LOGO CE

HW107

LOGO ESD

HW105

LOGO ROHS