

VLSI for Beginners

FPGA PRACTICE EXERCISE

Implement the 4 bit Up Counter on FPGA use system Clock as input clk, A8 as the RST, and 4 Color LEDs as counter output.

The code is discussed in the demo video in the LMS, you need to practice the same and download the result on the board assigned to you and take screenshot and share with us, and the screenshot should contain the camera view.

